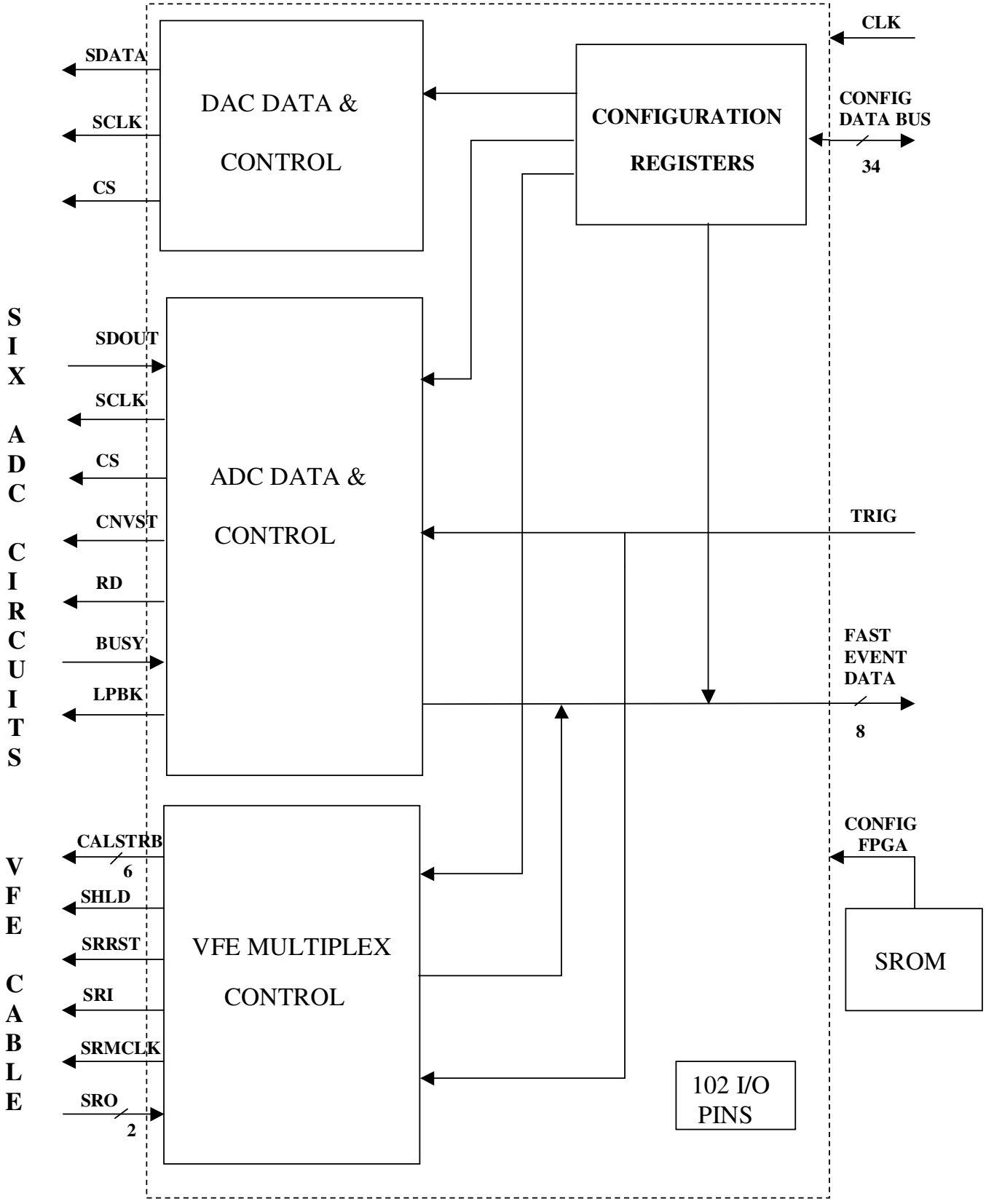
SDATA
SCLK
CS
DAC DATA &
CONTROL
CONFIGURATION
REGISTERS
CLK
CONFIG
DATA BUS
34
SIX ADC CIRCUITS
SDOUT
SCLK
CS
CNVST
RD
BUSY
LPBK
ADC DATA &
CONTROL
TRIG
FAST
EVENT
DATA
8
VFE CABLE
CALSTRB
6
SHLD
SRRST
SRI
SRMCLK
SRO
2
VFE MULTIPLEX
CONTROL
CONFIG
FPGA
SROM
102 I/O
PINS