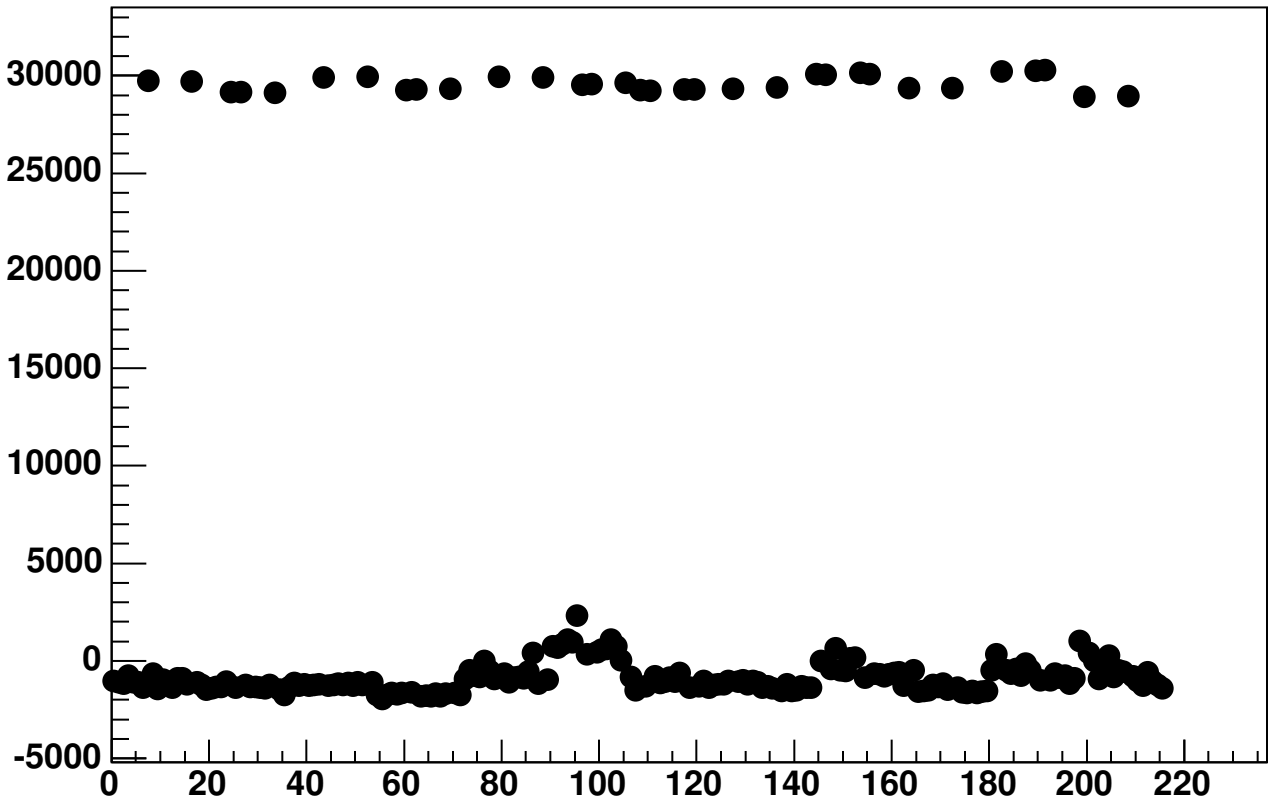
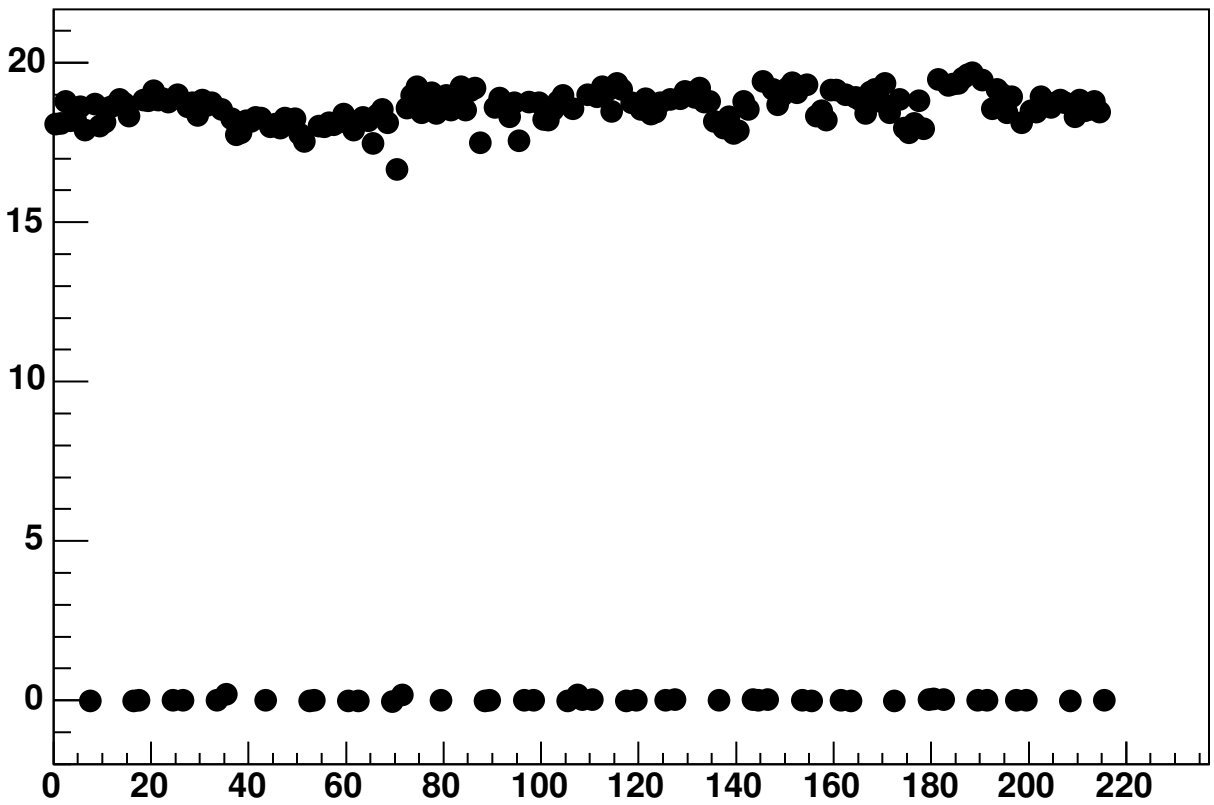
Channel Enabled, Hold=30, Fit Intercept vs 18*Chip+Chan



Channel Enabled, Hold=30, Fit Slope vs 18*Chip+Chan



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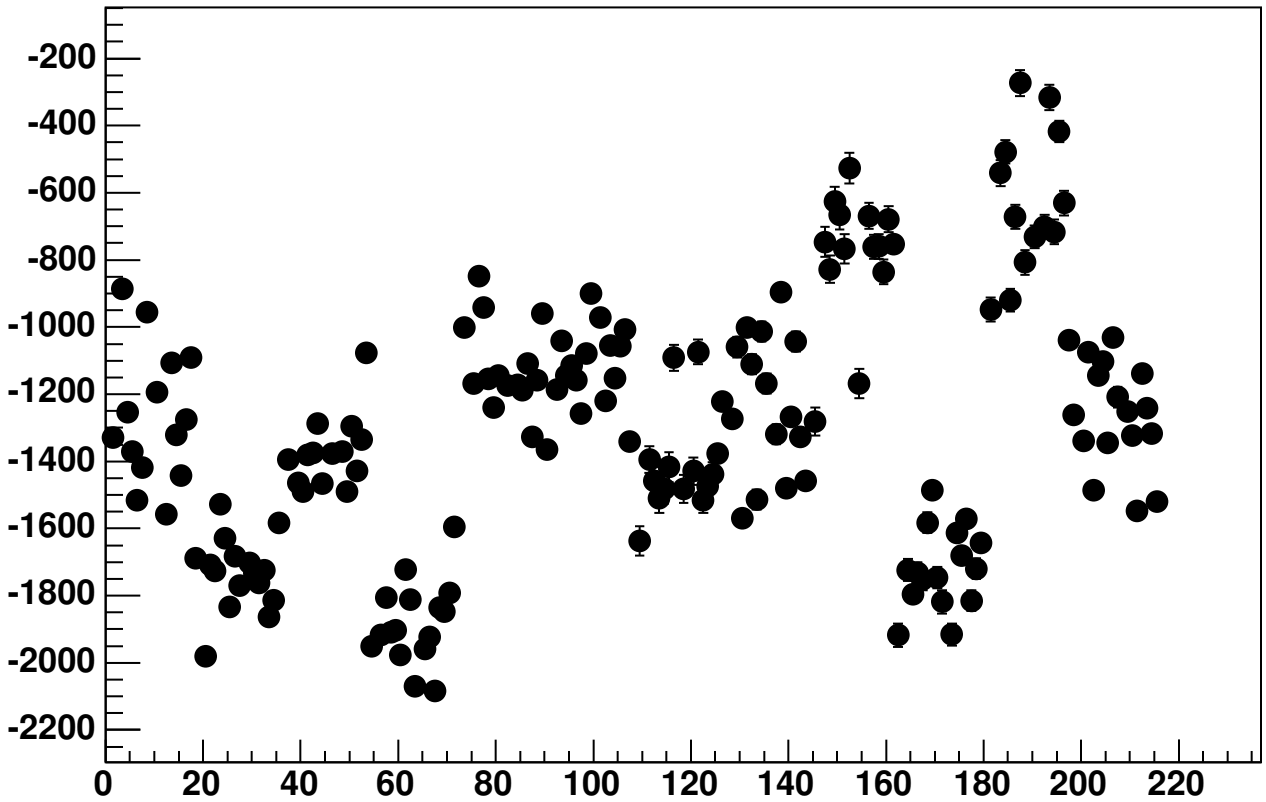
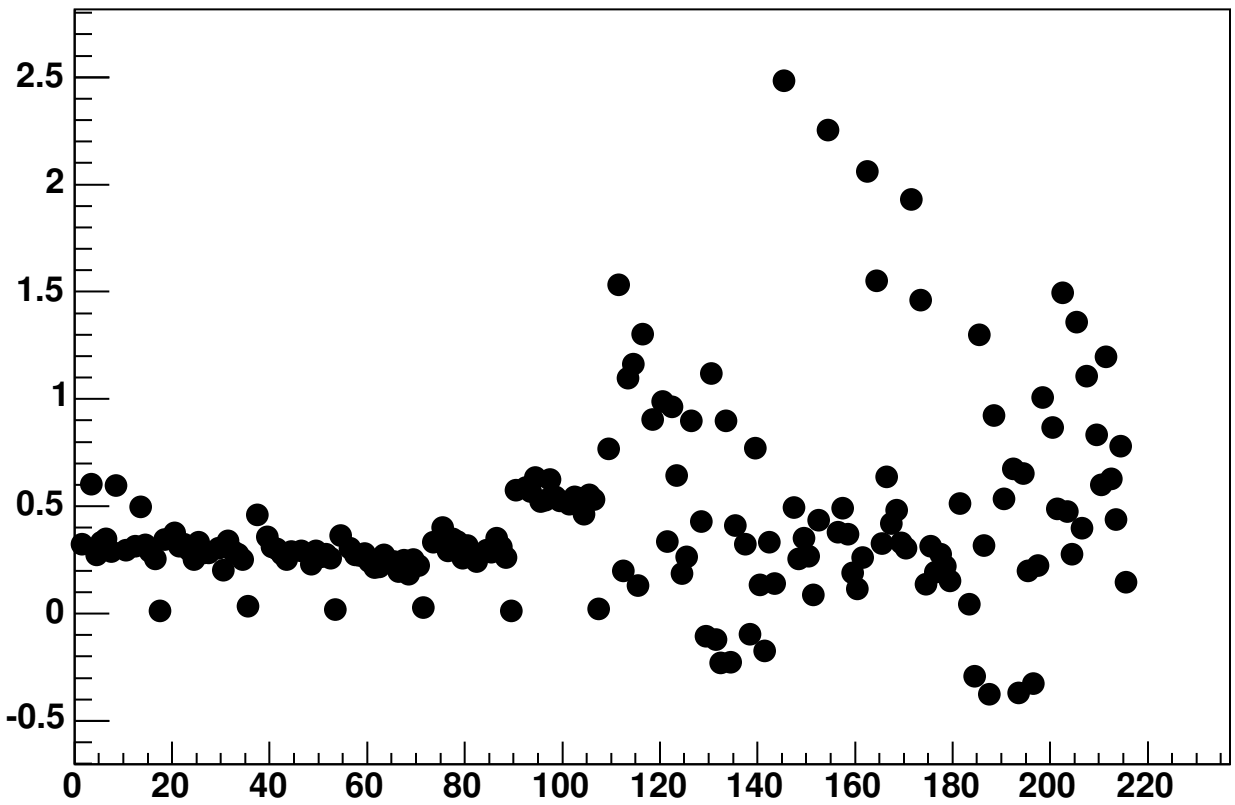
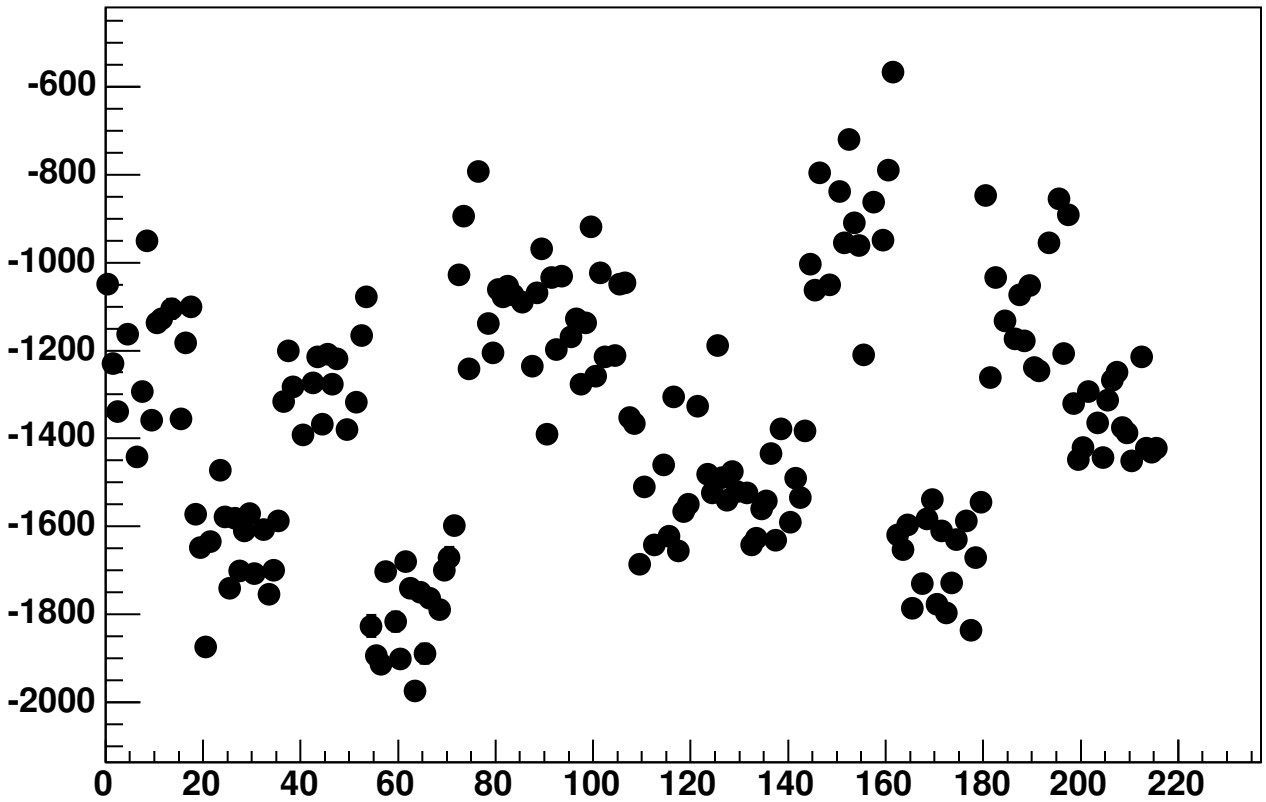


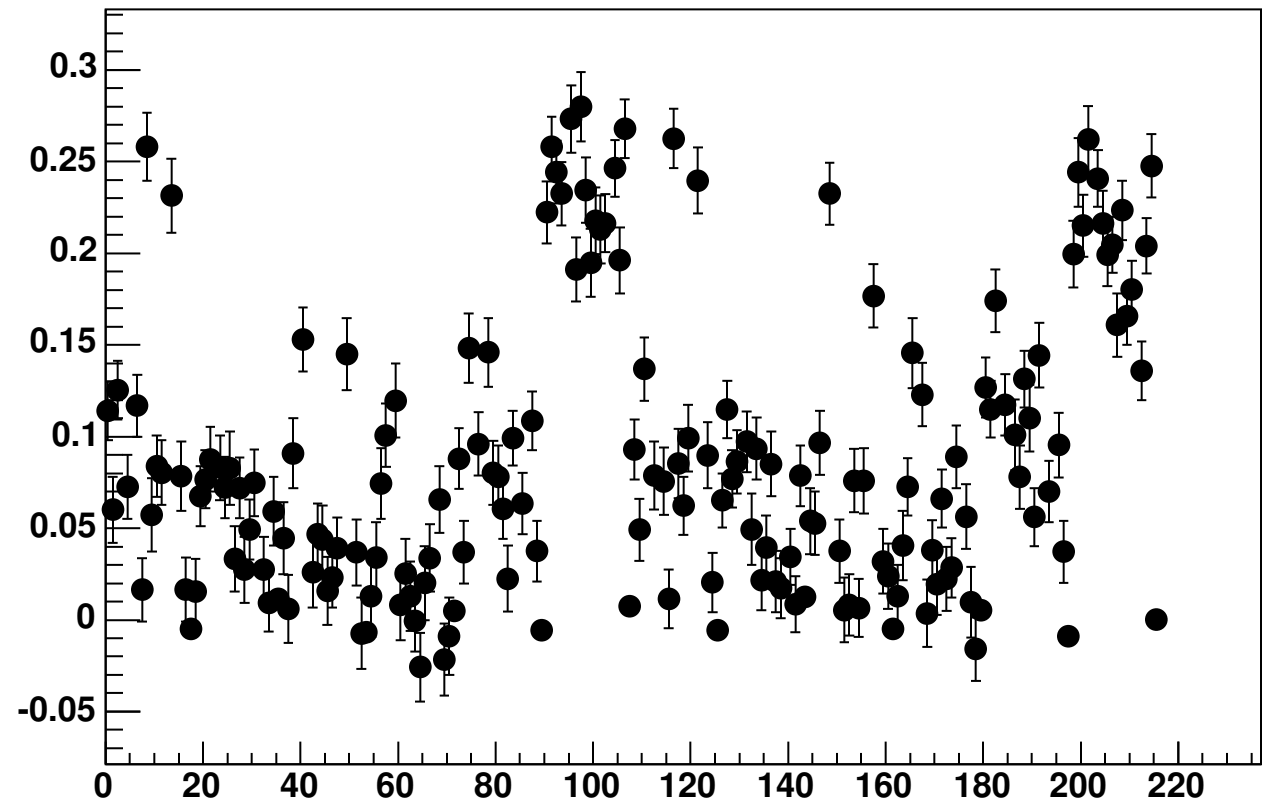
Disabled 0, Hold=30, Fit Slope vs 18*Chip+Chan



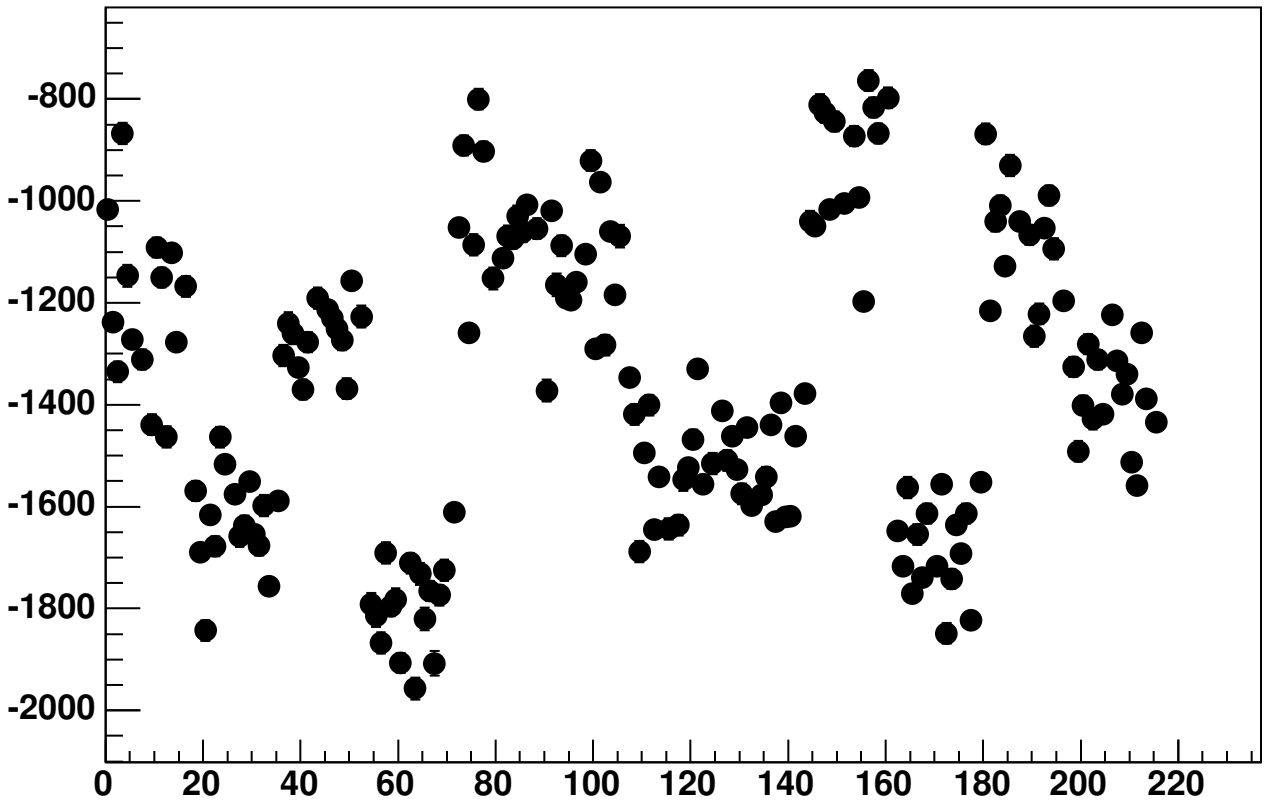
Disabled 1, Hold=30, Fit Intercept vs 18*Chip+Chan



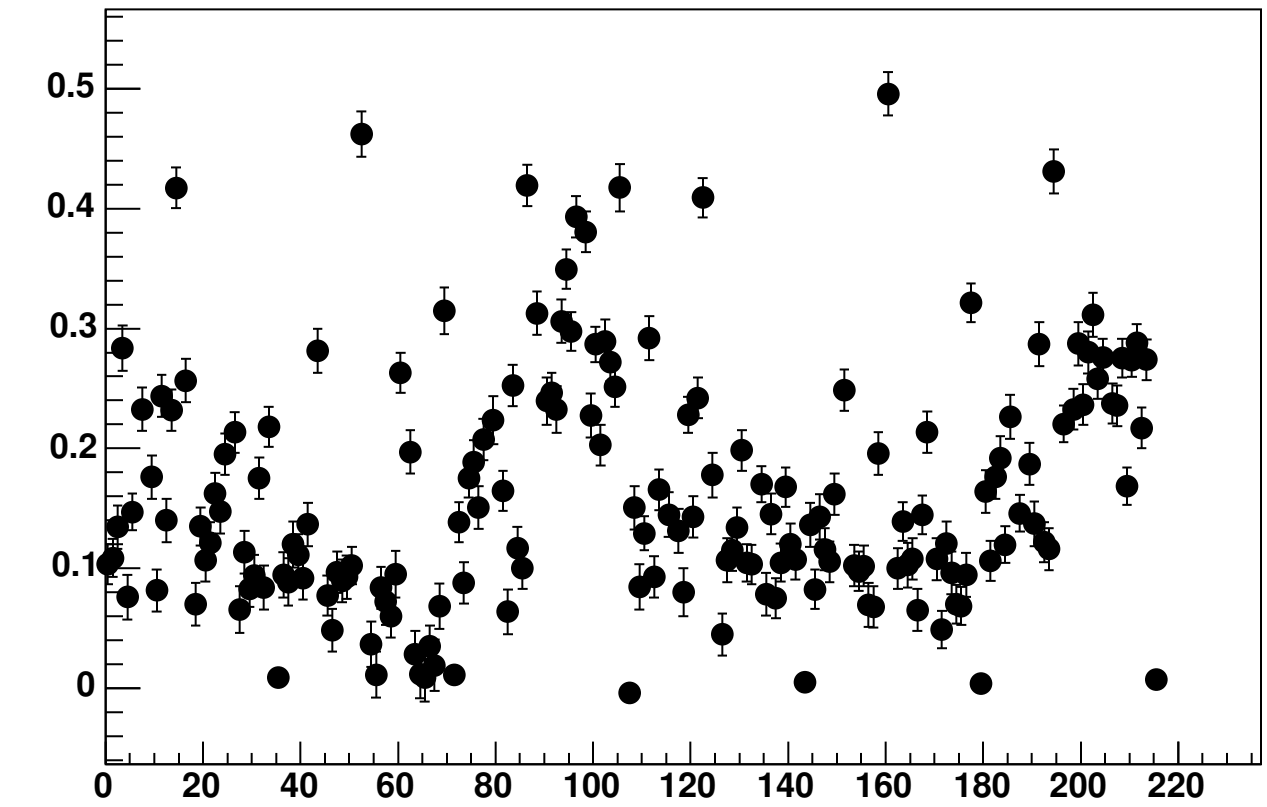
Disabled 1, Hold=30, Fit Slope vs 18*Chip+Chan



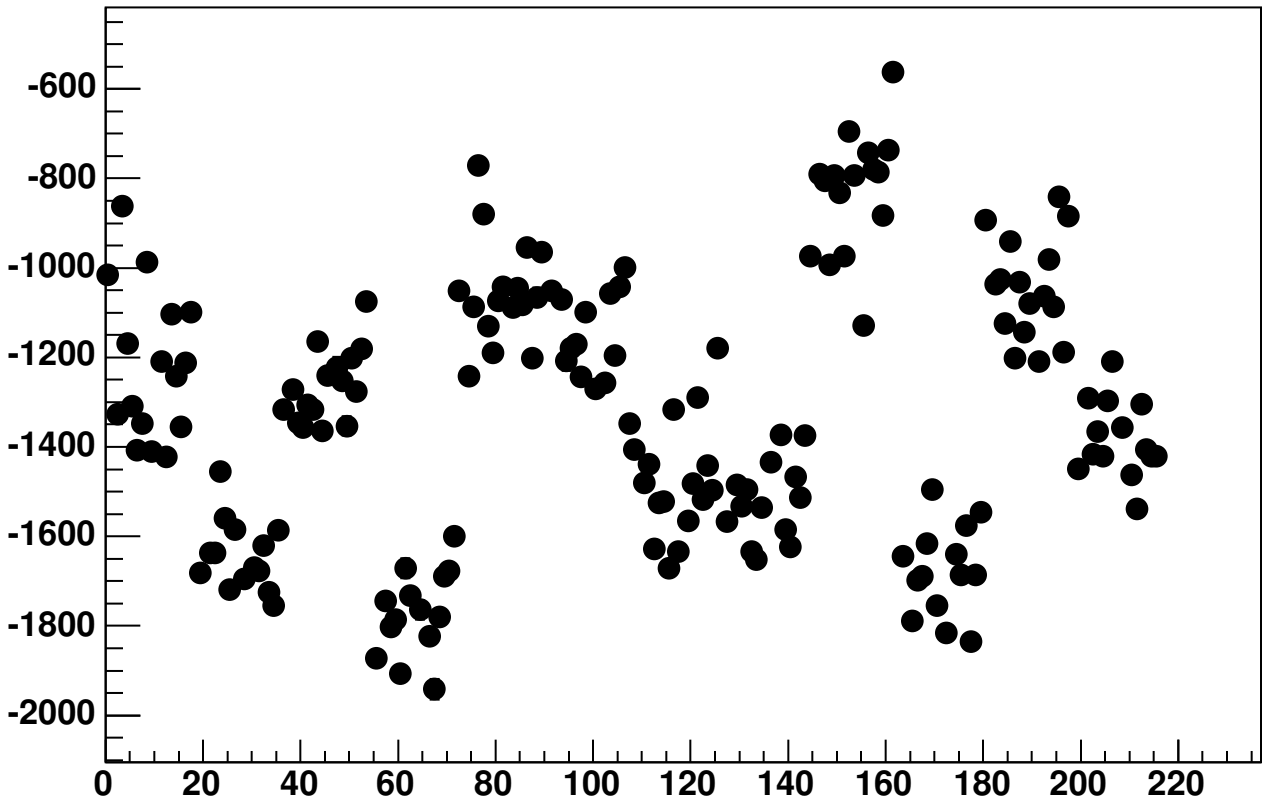
Disabled 2, Hold=30, Fit Intercept vs 18*Chip+Chan



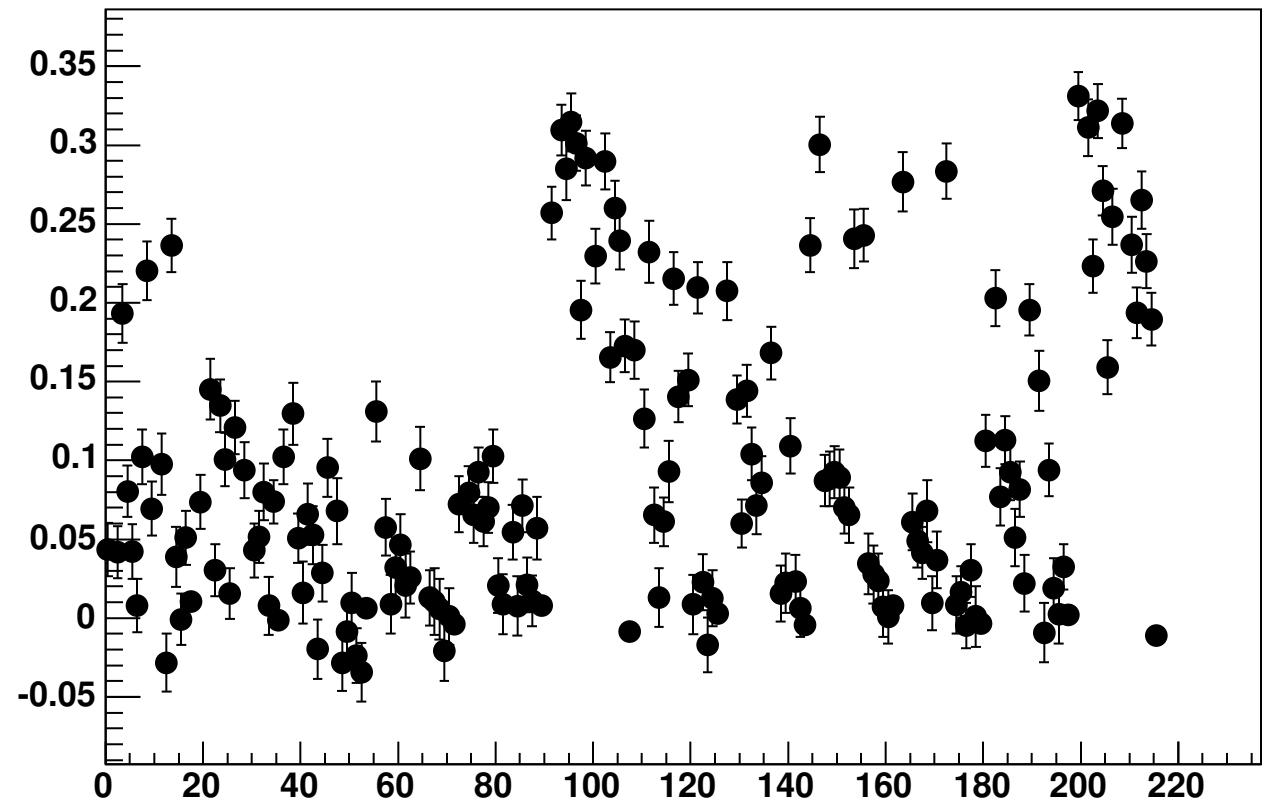
Disabled 2, Hold=30, Fit Slope vs 18*Chip+Chan



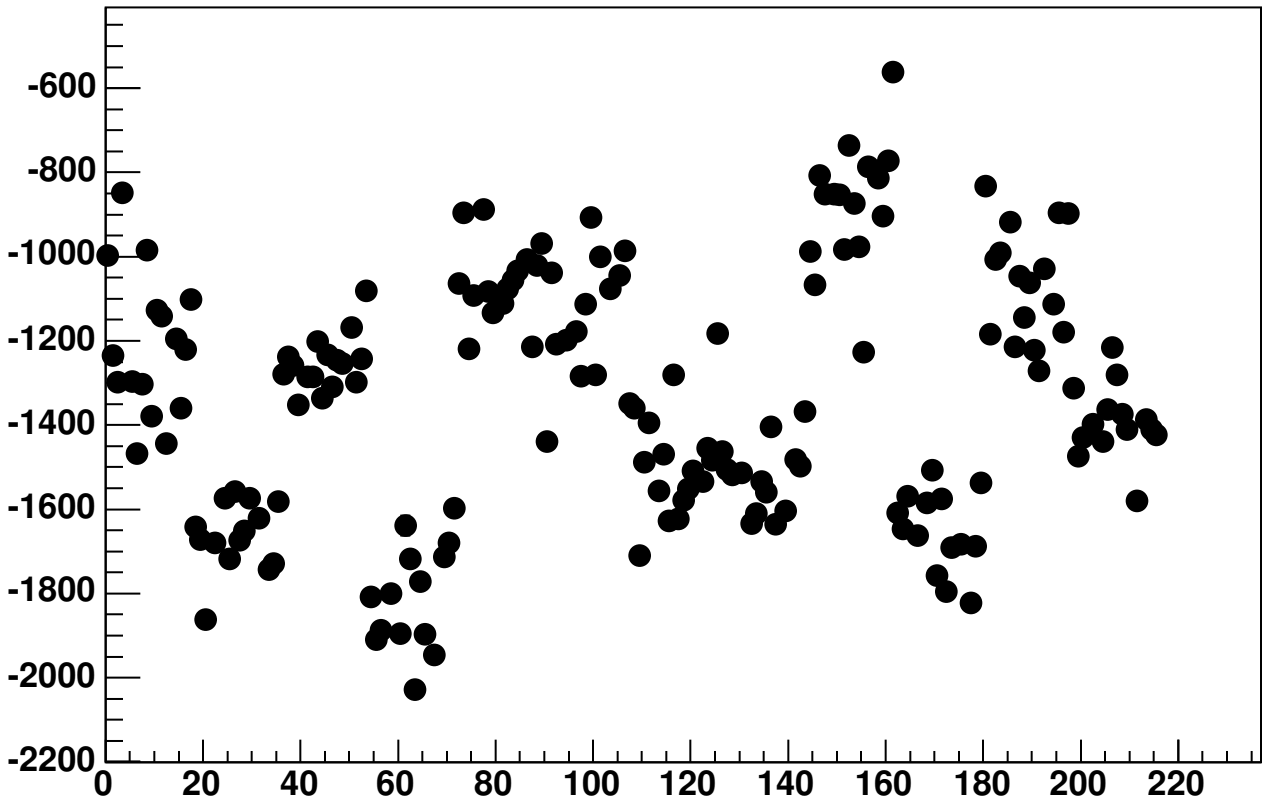
Disabled 3, Hold=30, Fit Intercept vs 18*Chip+Chan



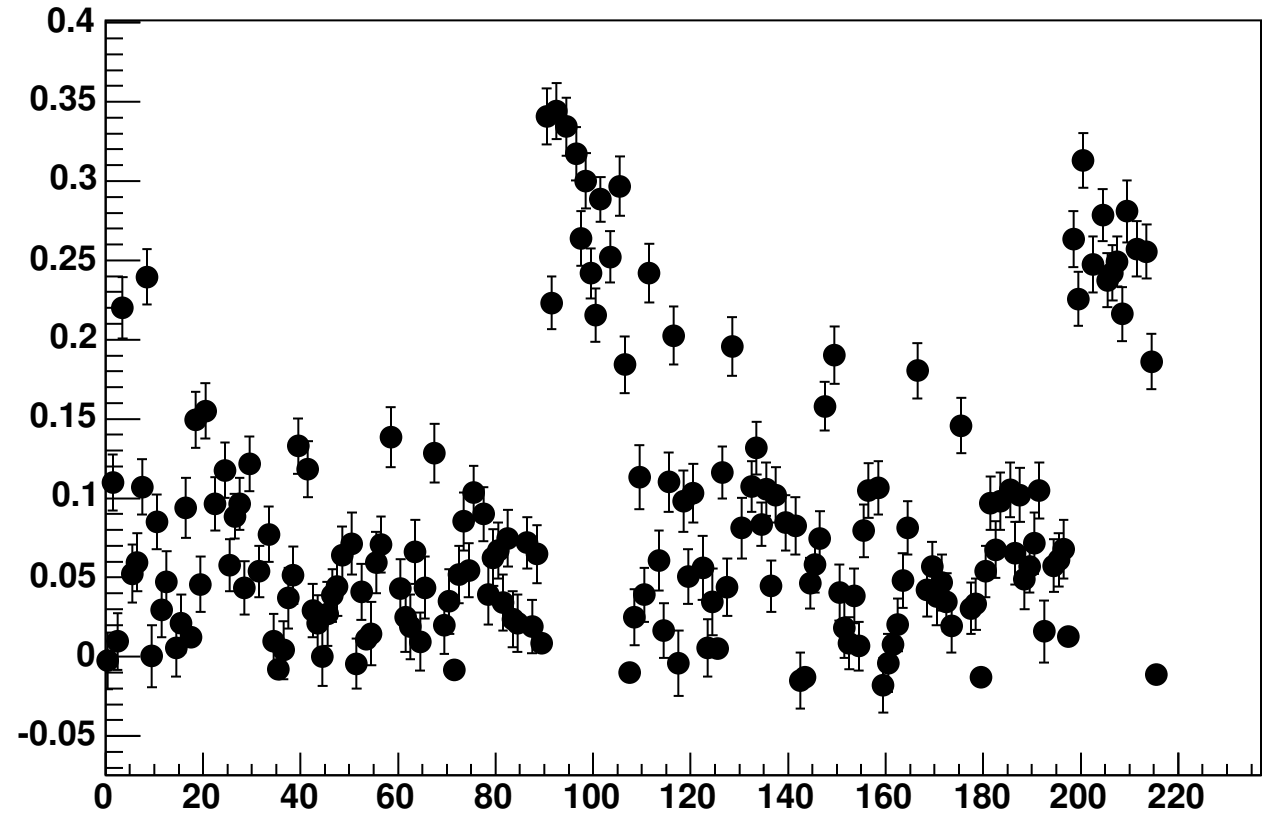
Disabled 3, Hold=30, Fit Slope vs 18*Chip+Chan



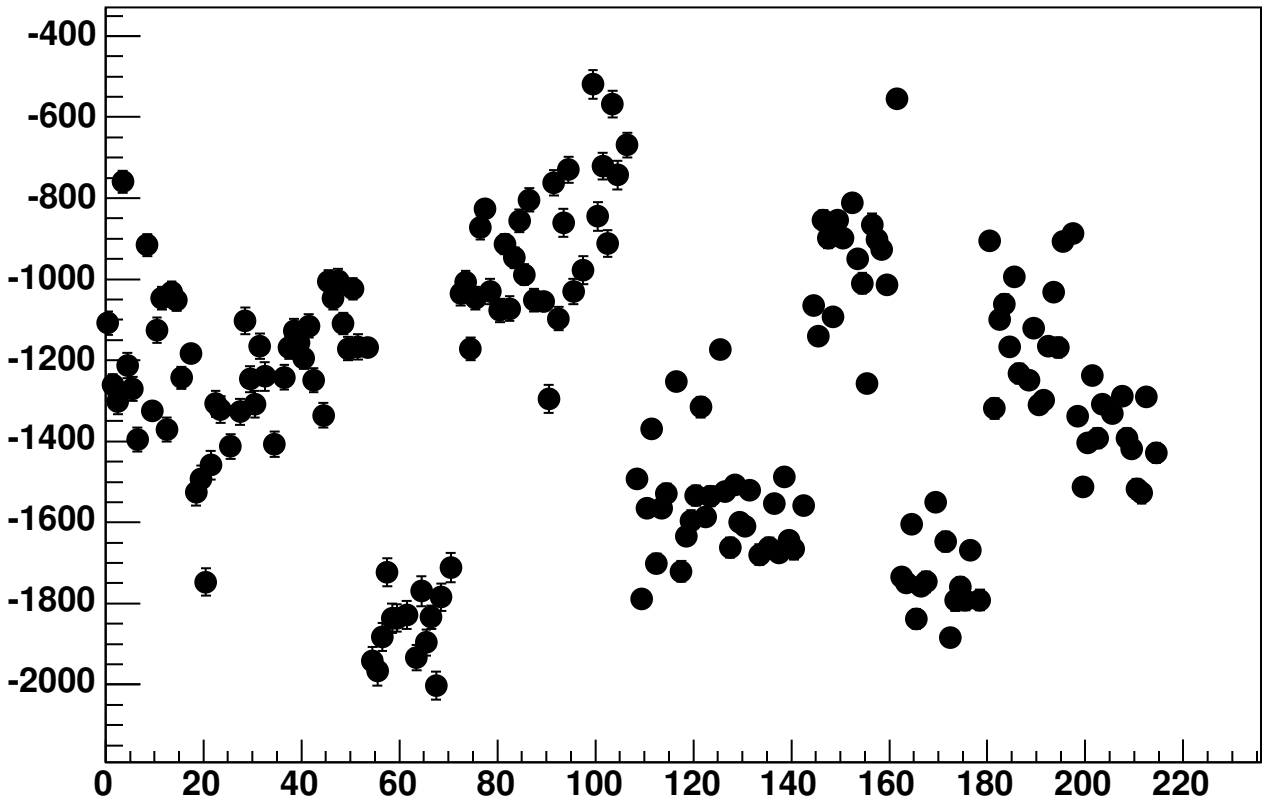
Disabled 4, Hold=30, Fit Intercept vs 18*Chip+Chan



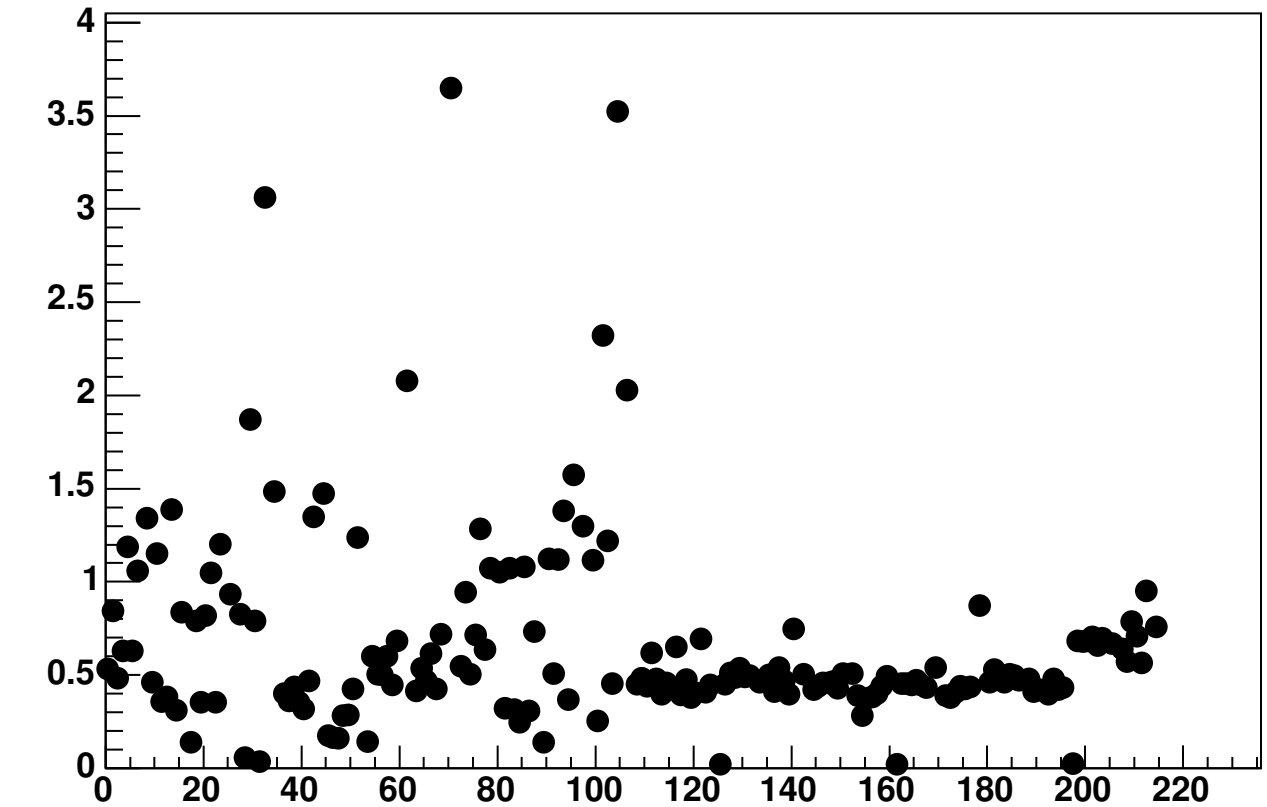
Disabled 4, Hold=30, Fit Slope vs 18*Chip+Chan



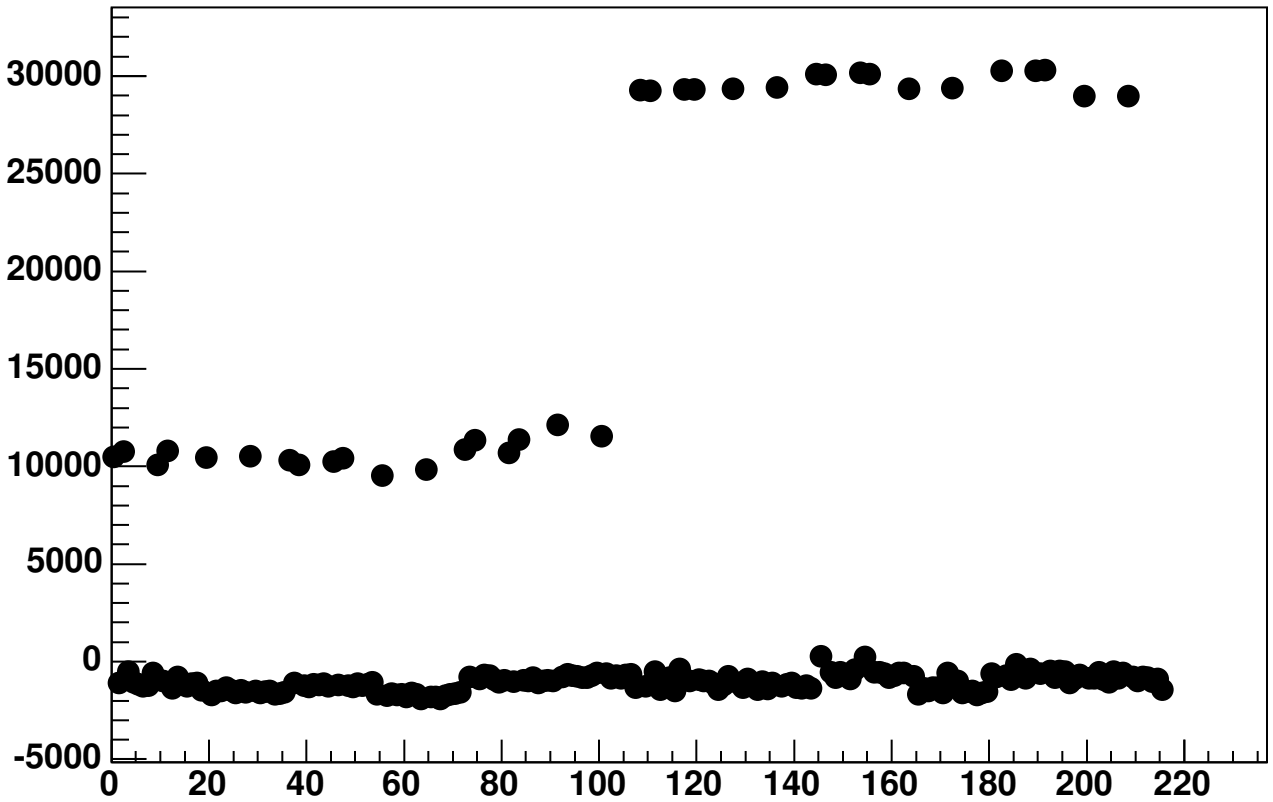
Disabled 5, Hold=30, Fit Intercept vs 18*Chip+Chan



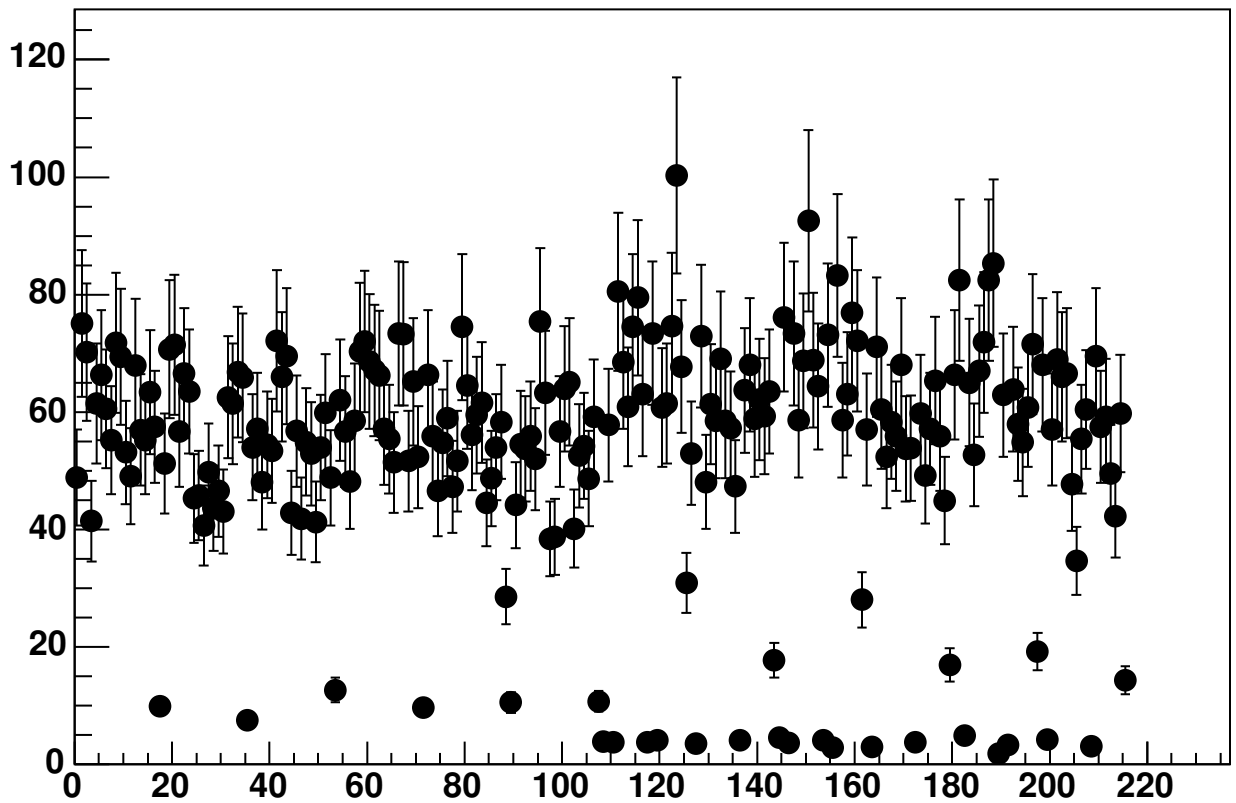
Disabled 5, Hold=30, Fit Slope vs 18*Chip+Chan



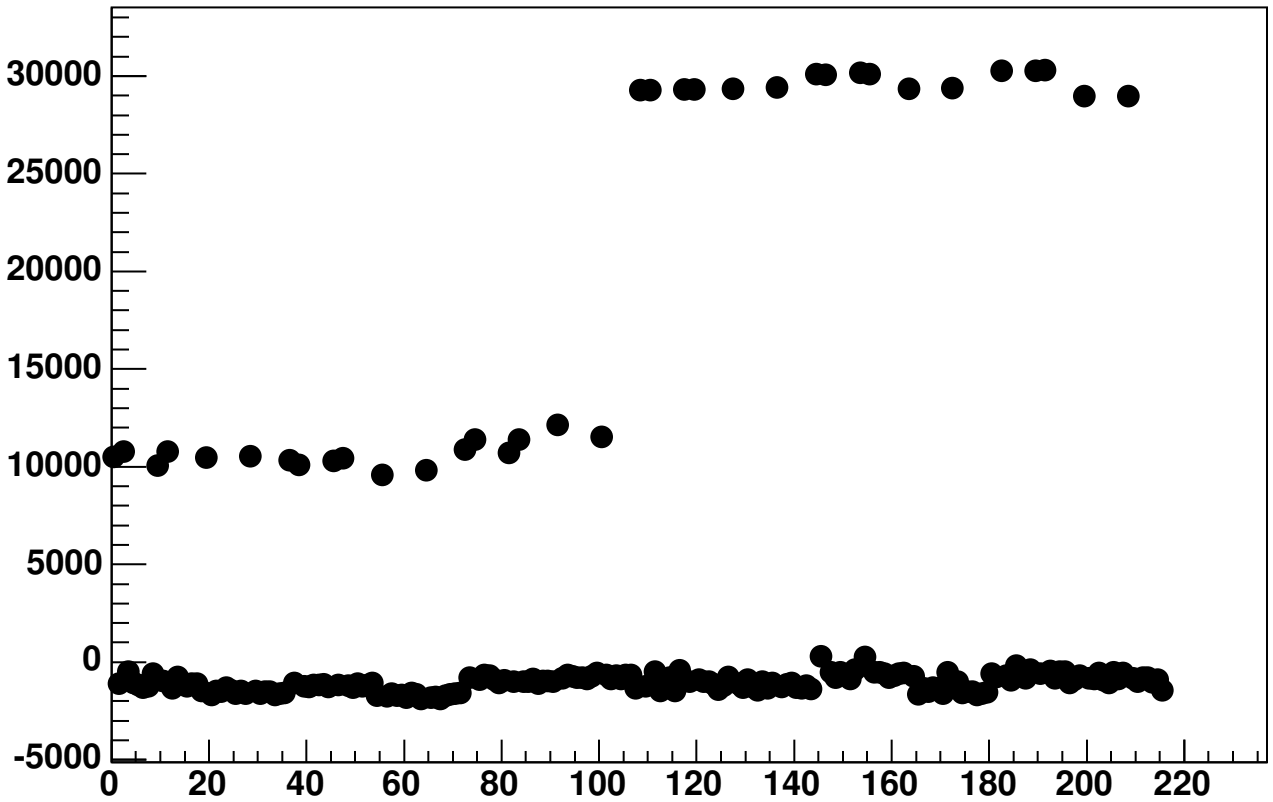
Enable 0, Hold=30, DAC=0, ADC Mean vs 18*Chip+Chan



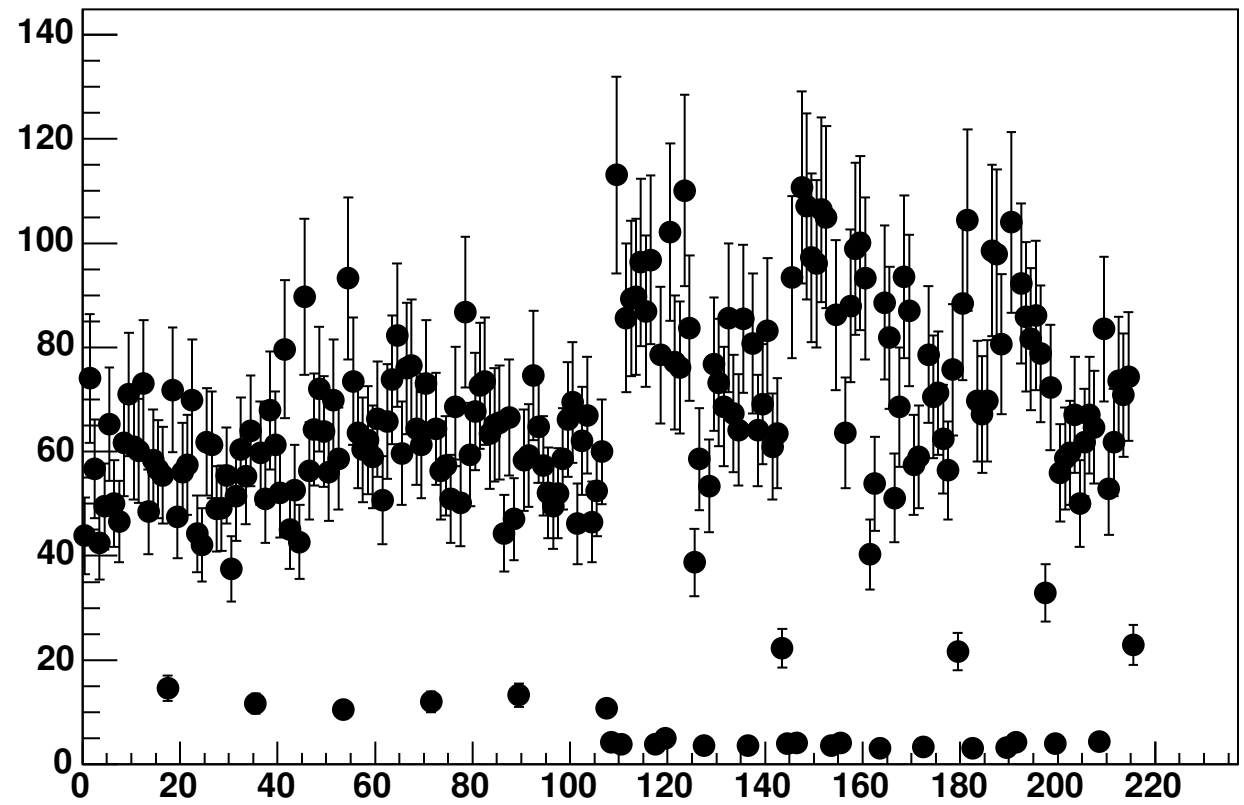
Enable 0, Hold=30, DAC=0, ADC Noise vs 18*Chip+Chan



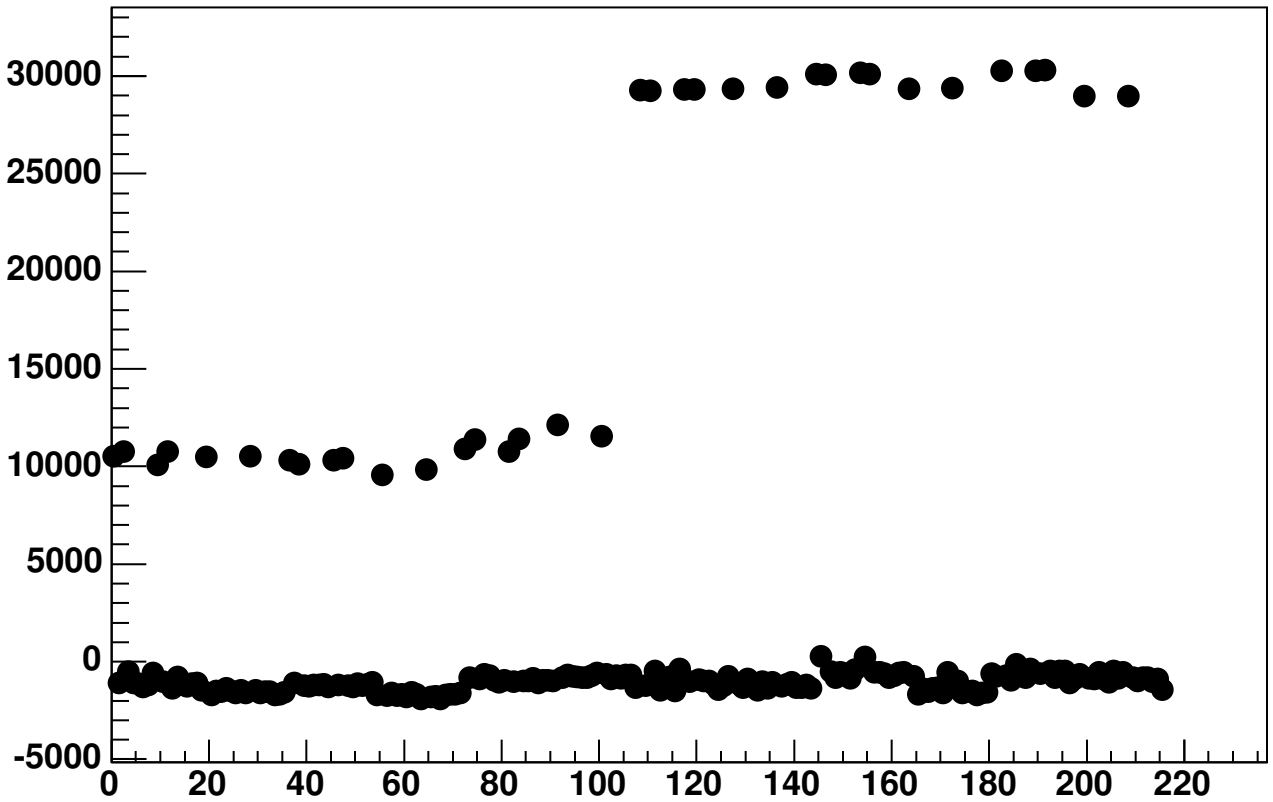
Enable 0, Hold=30, DAC=50, ADC Mean vs 18*Chip+Chan



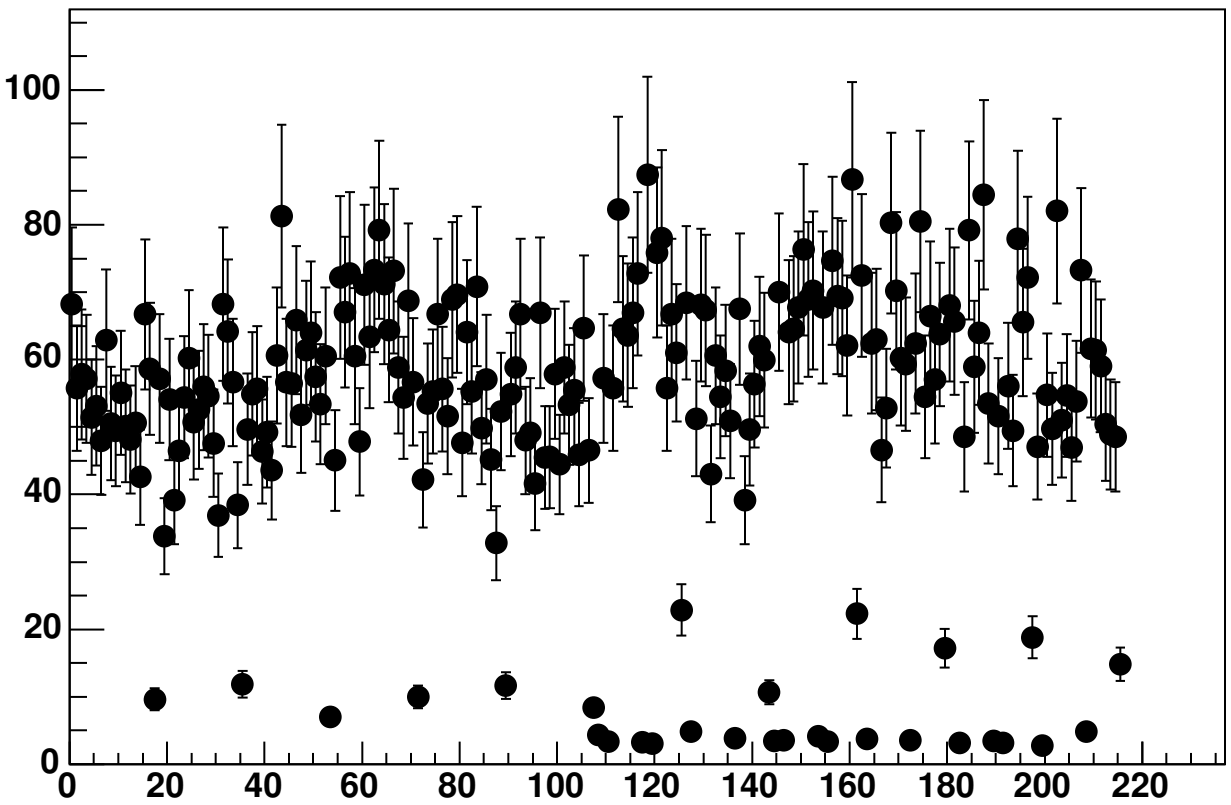
Enable 0, Hold=30, DAC=50, ADC Noise vs 18*Chip+Chan



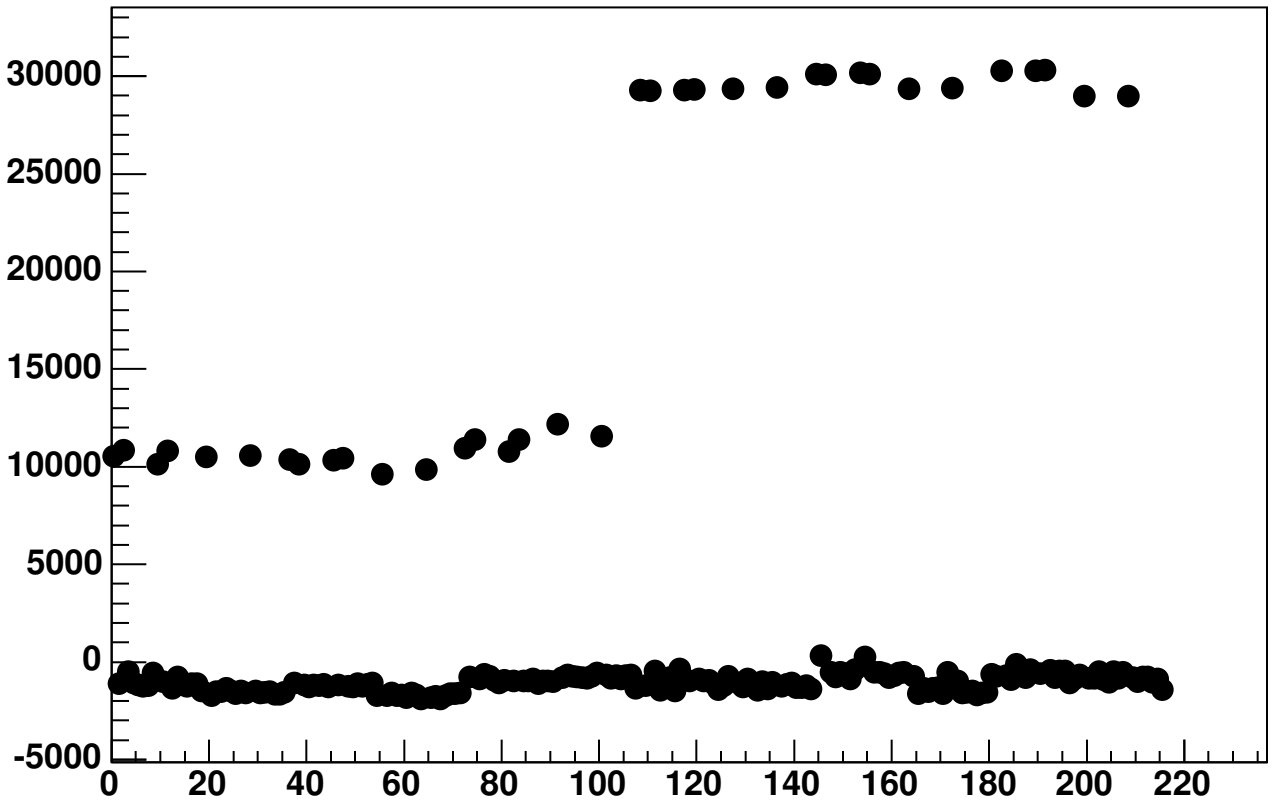
Enable 0, Hold=30, DAC=100, ADC Mean vs 18*Chip+Chan



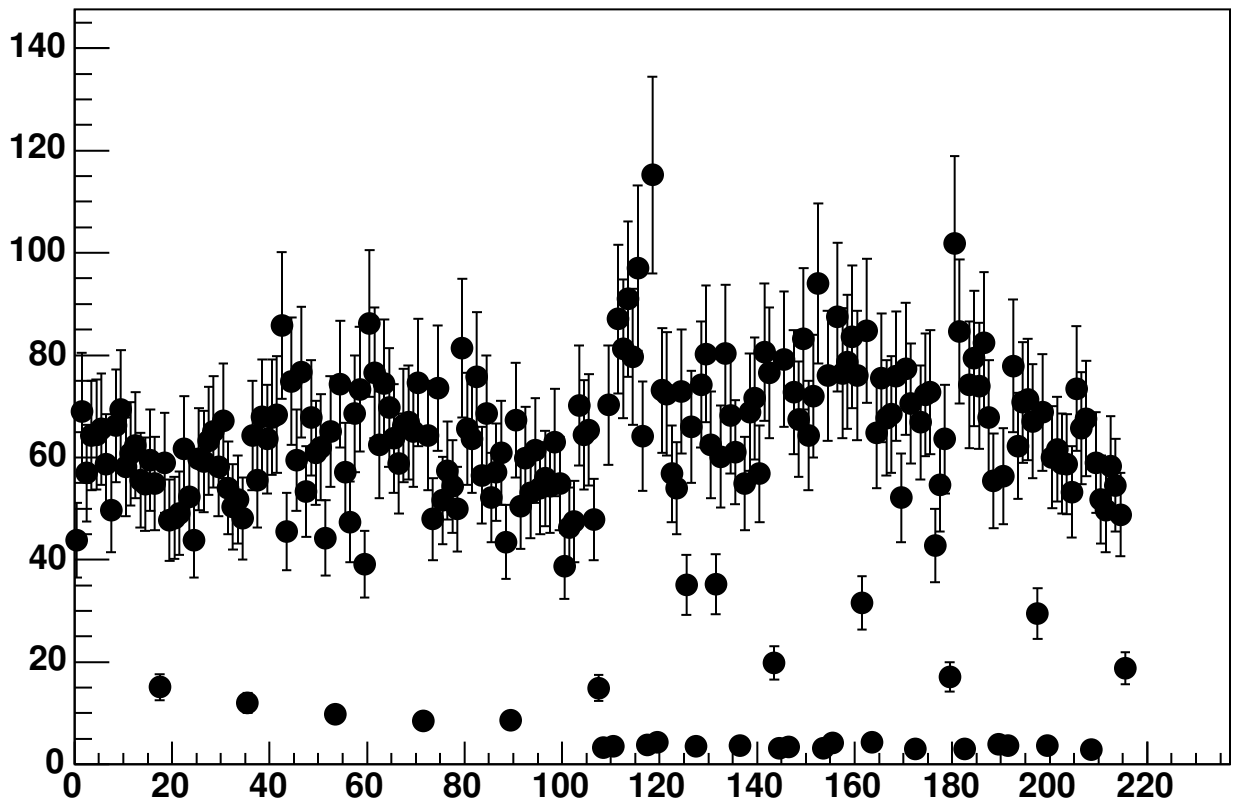
Enable 0, Hold=30, DAC=100, ADC Noise vs 18*Chip+Chan



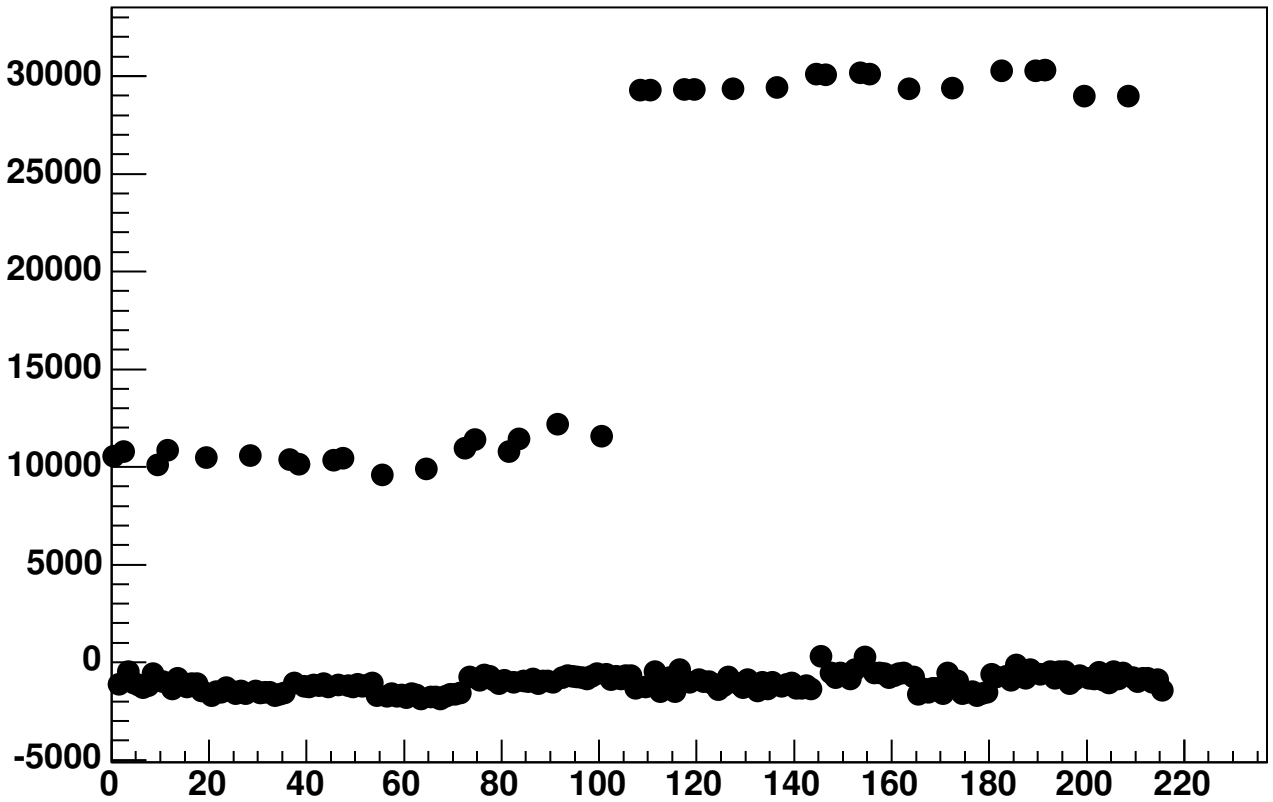
Enable 0, Hold=30, DAC=150, ADC Mean vs 18*Chip+Chan



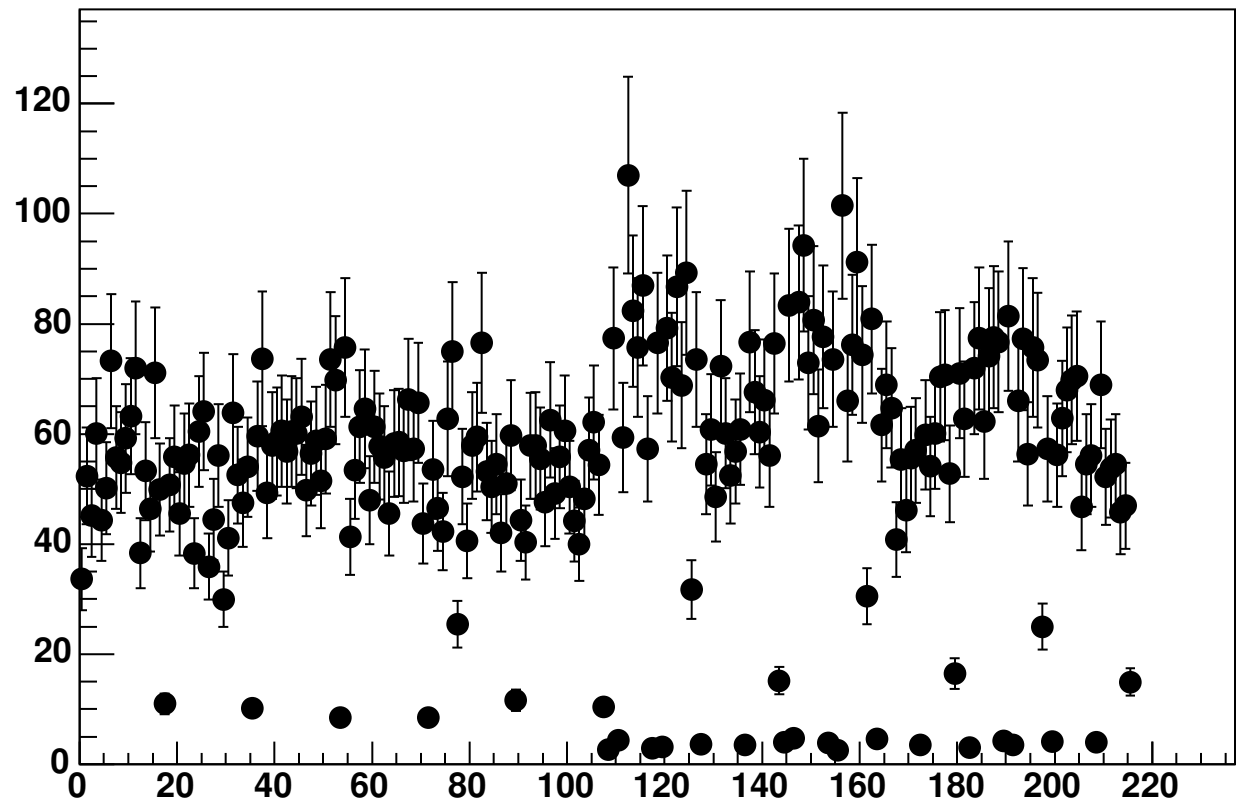
Enable 0, Hold=30, DAC=150, ADC Noise vs 18*Chip+Chan



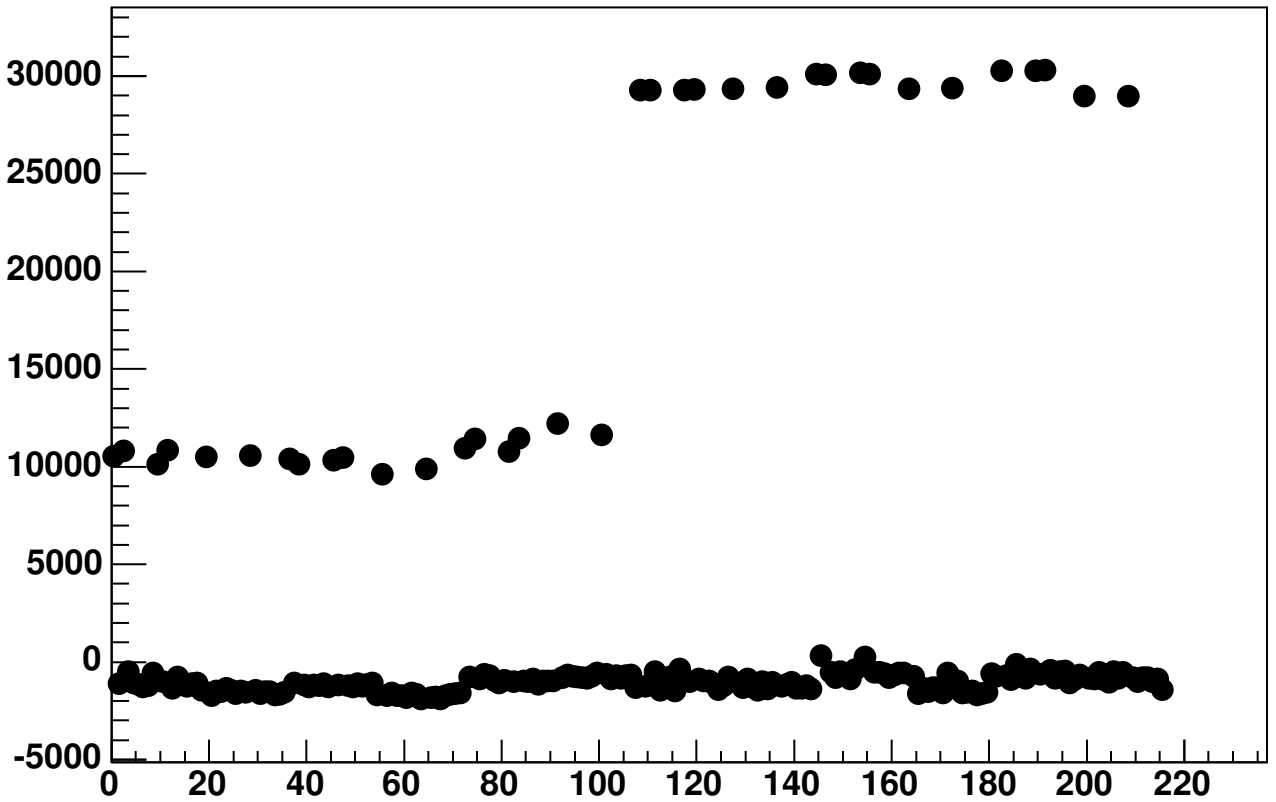
Enable 0, Hold=30, DAC=200, ADC Mean vs 18*Chip+Chan



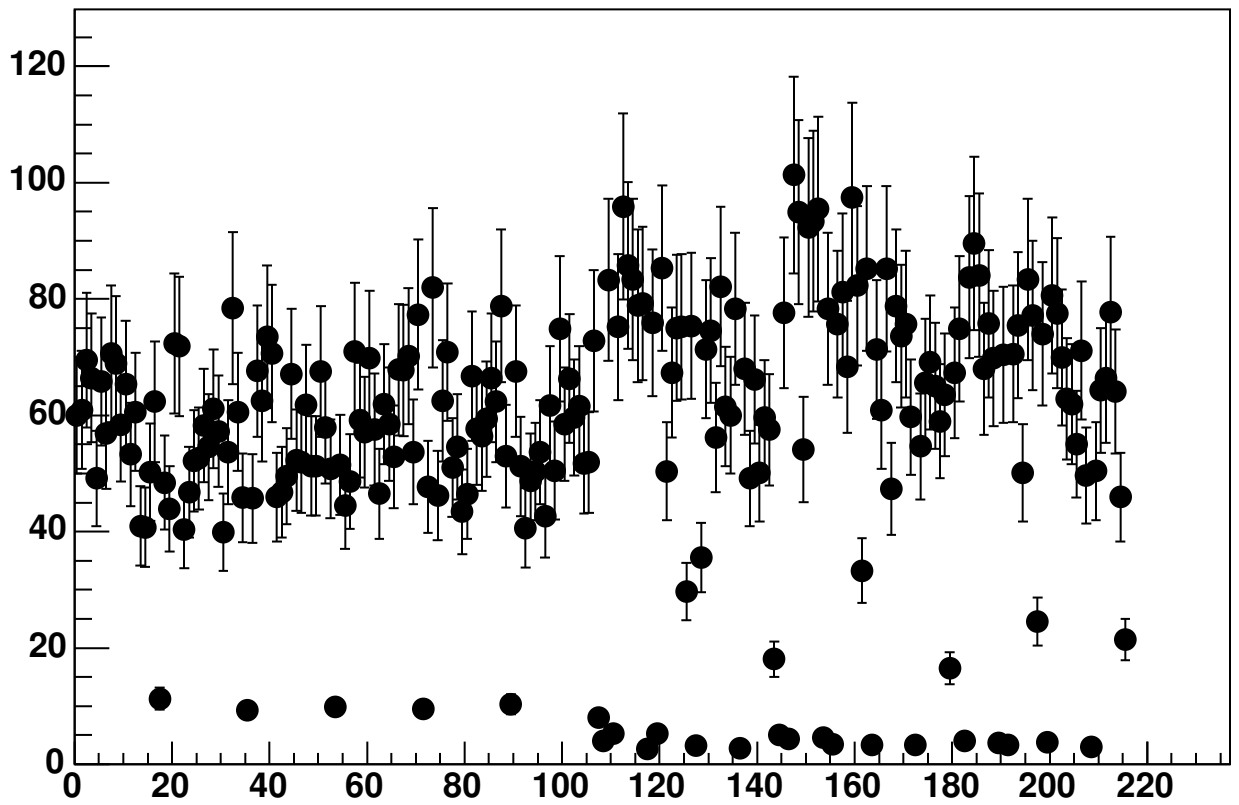
Enable 0, Hold=30, DAC=200, ADC Noise vs 18*Chip+Chan



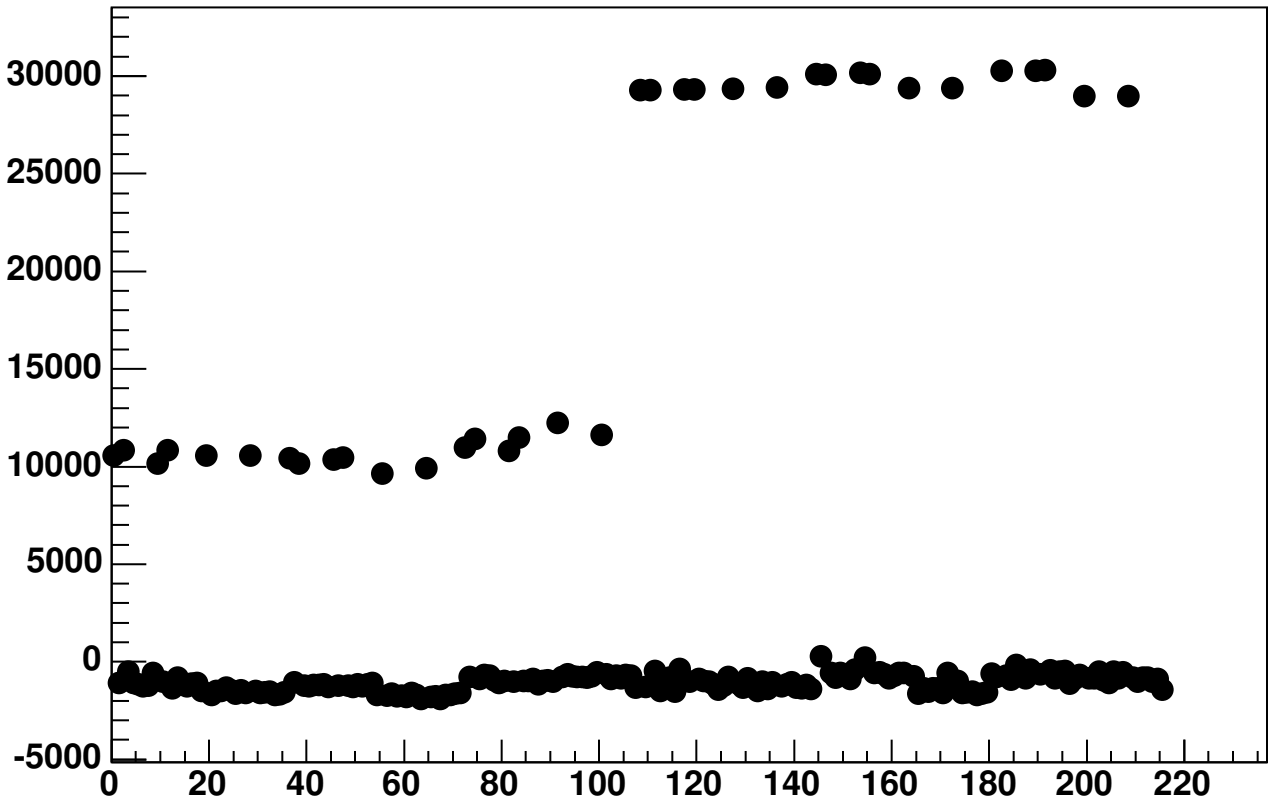
Enable 0, Hold=30, DAC=250, ADC Mean vs 18*Chip+Chan



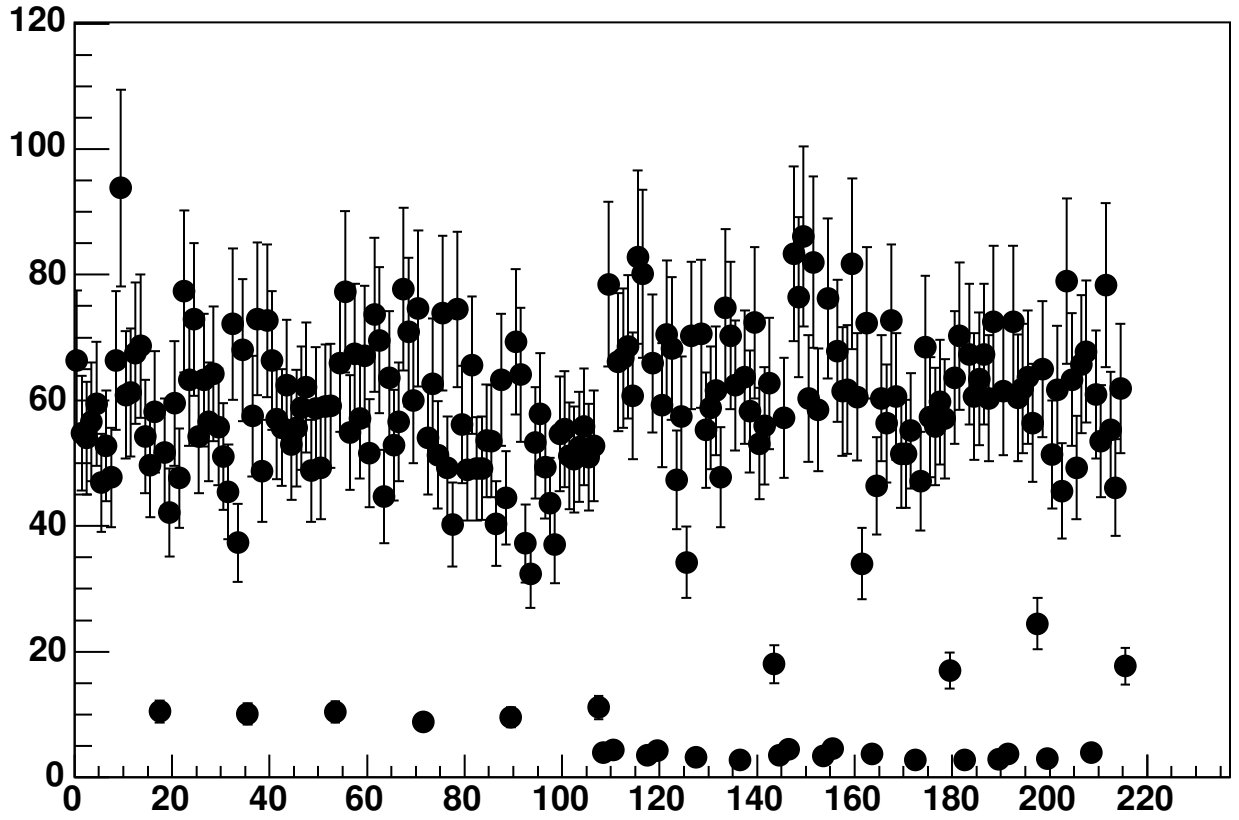
Enable 0, Hold=30, DAC=250, ADC Noise vs 18*Chip+Chan



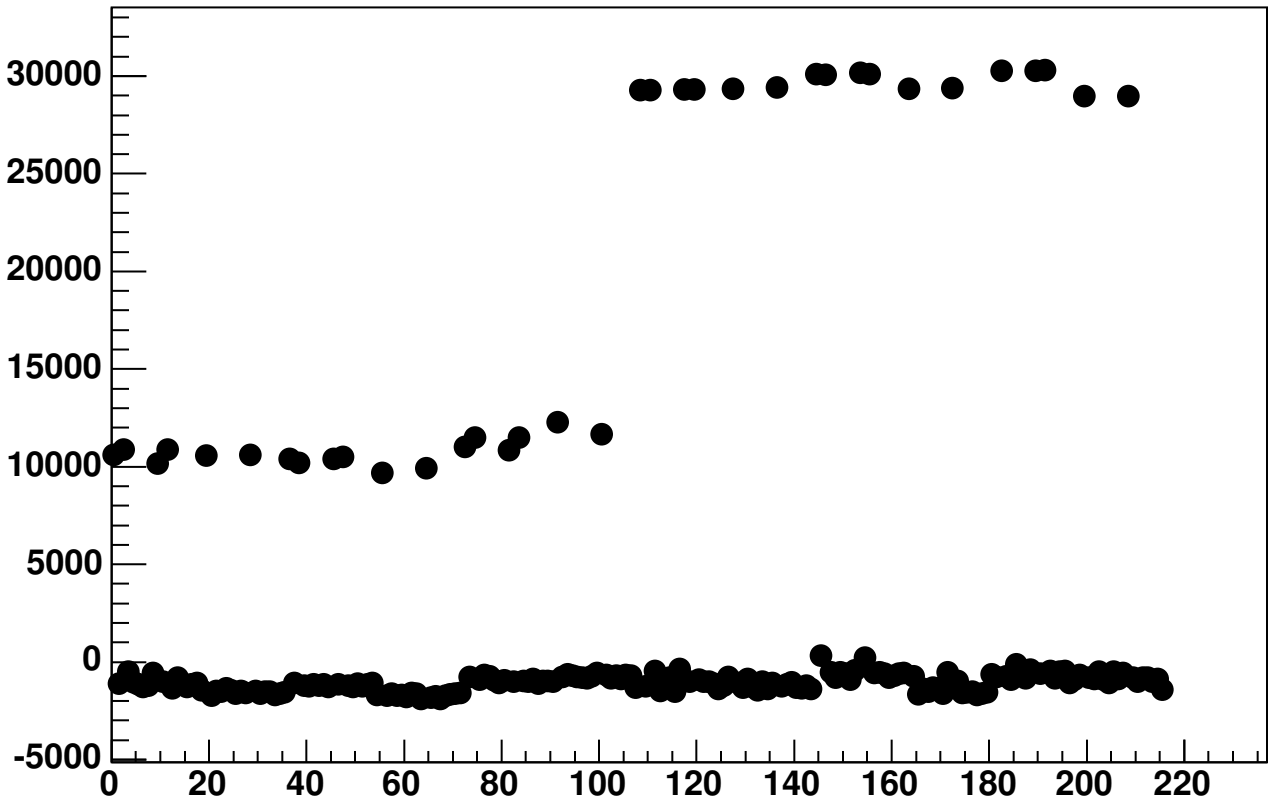
Enable 0, Hold=30, DAC=300, ADC Mean vs 18*Chip+Chan



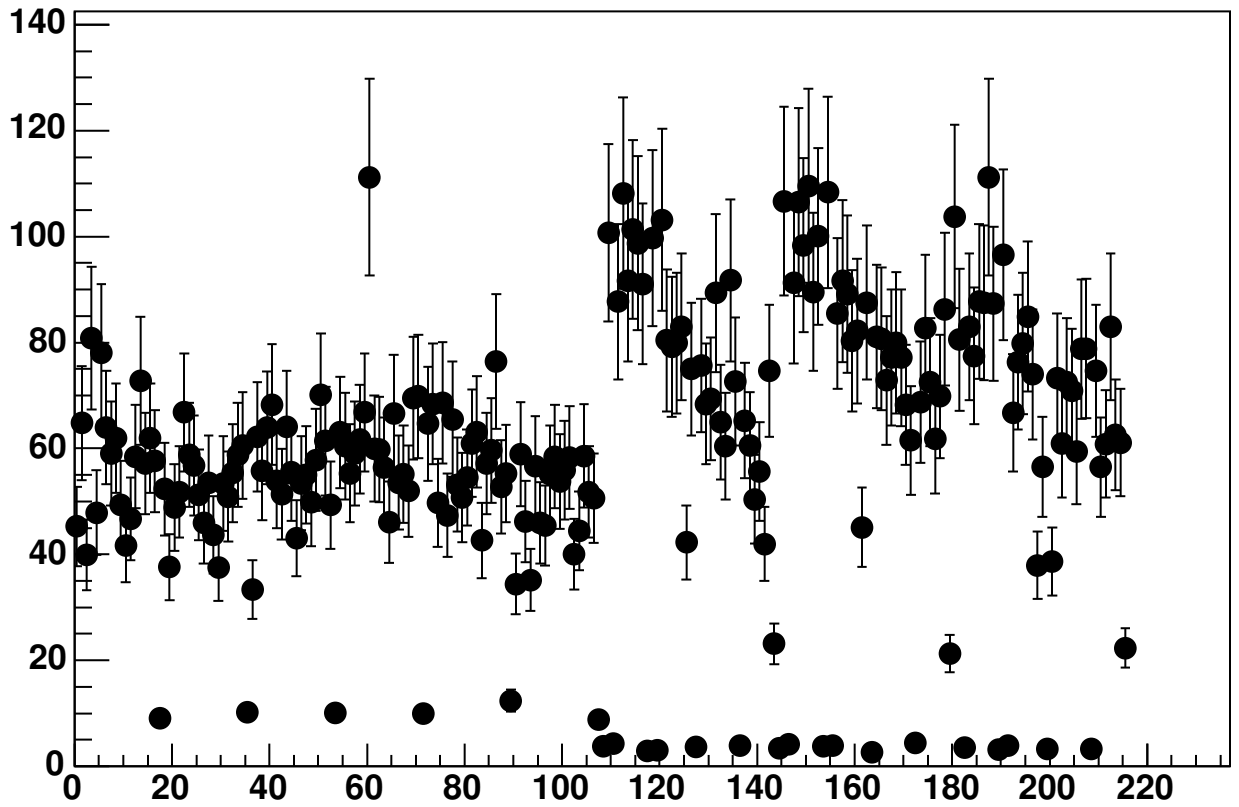
Enable 0, Hold=30, DAC=300, ADC Noise vs 18*Chip+Chan



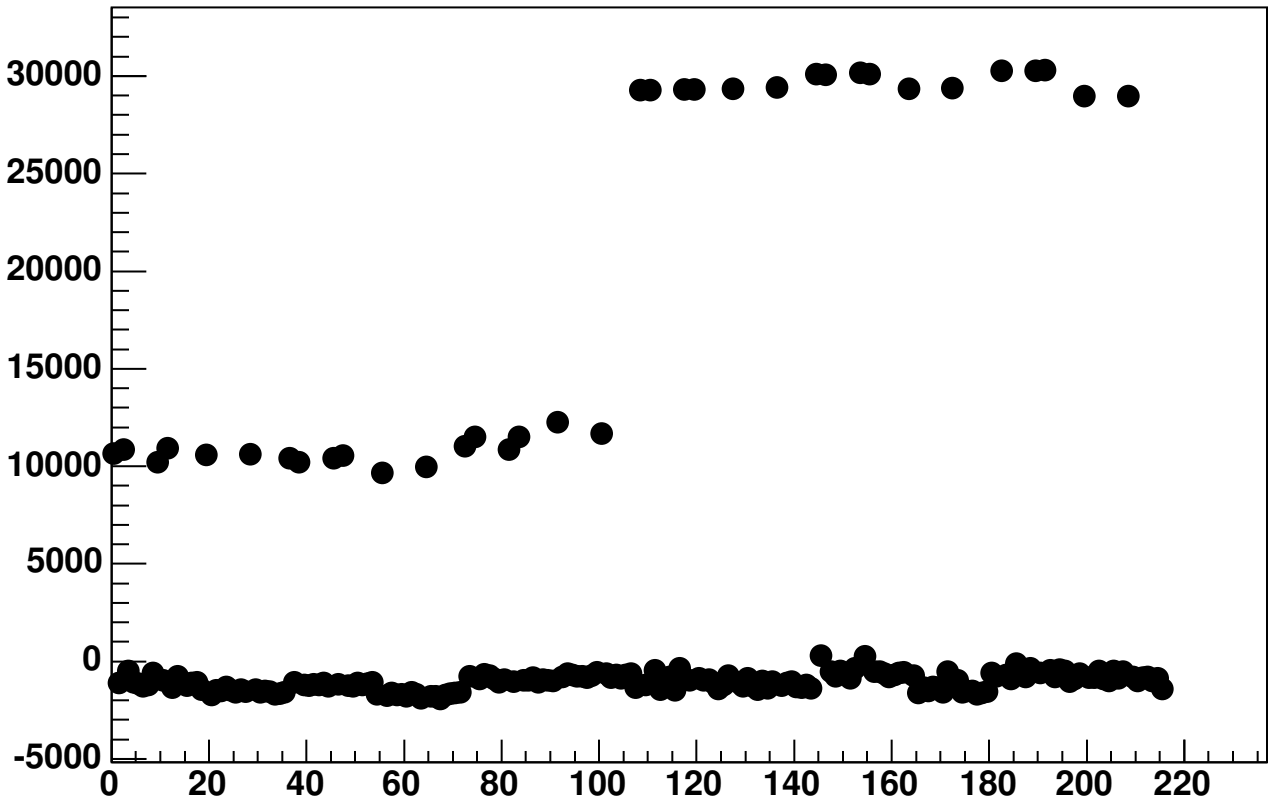
Enable 0, Hold=30, DAC=350, ADC Mean vs 18*Chip+Chan



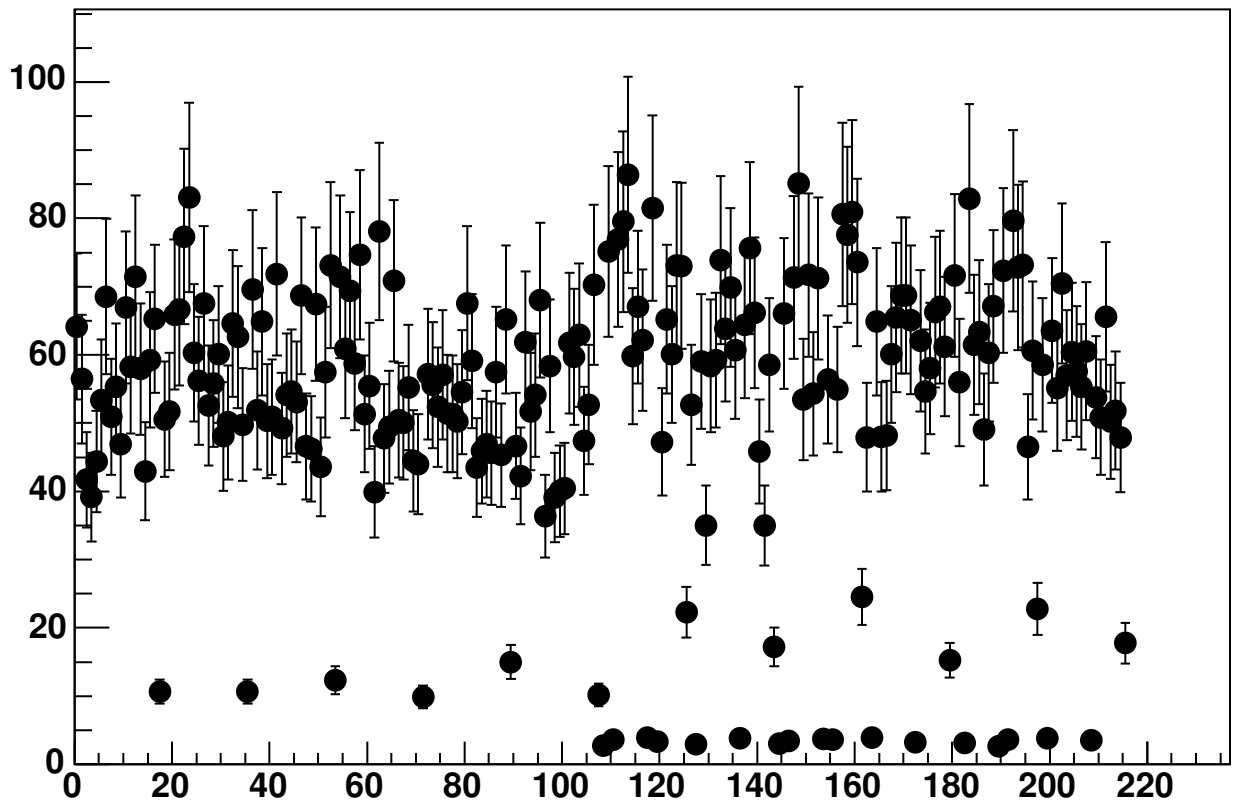
Enable 0, Hold=30, DAC=350, ADC Noise vs 18*Chip+Chan



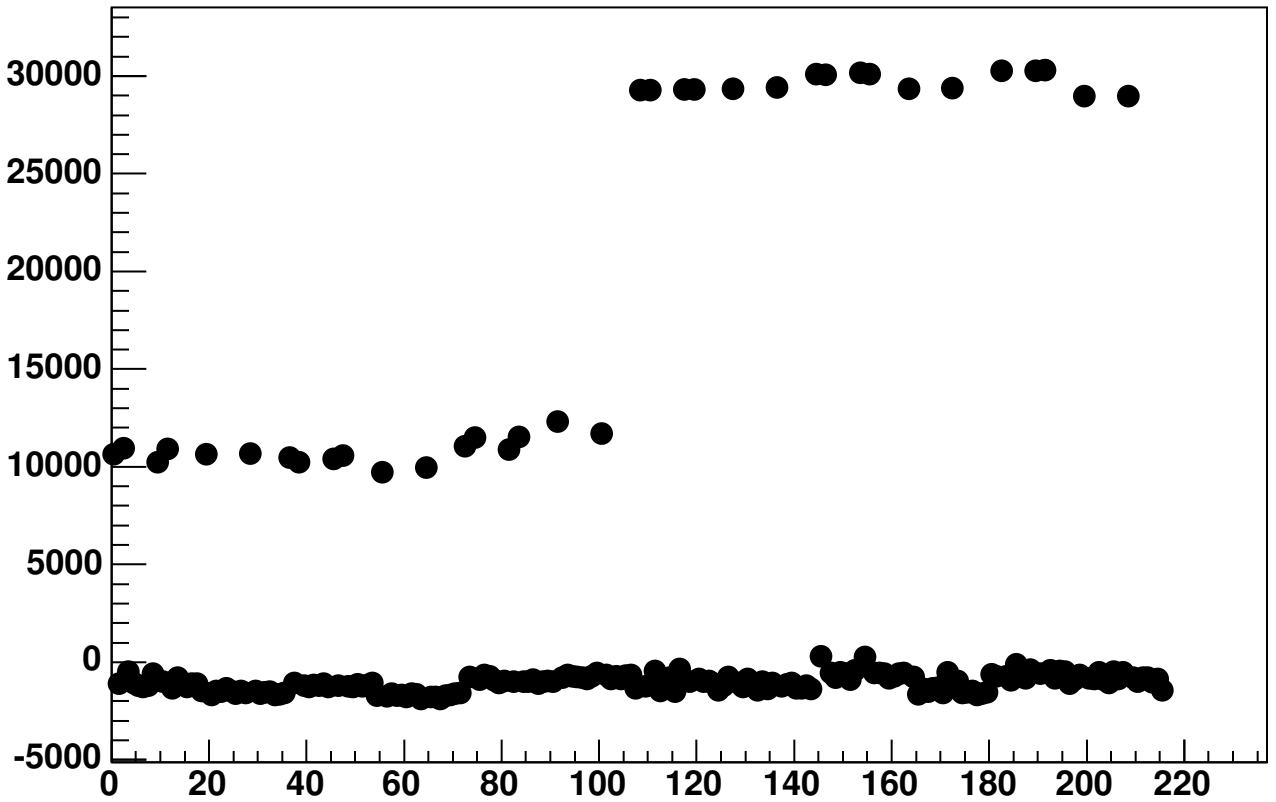
Enable 0, Hold=30, DAC=400, ADC Mean vs 18*Chip+Chan



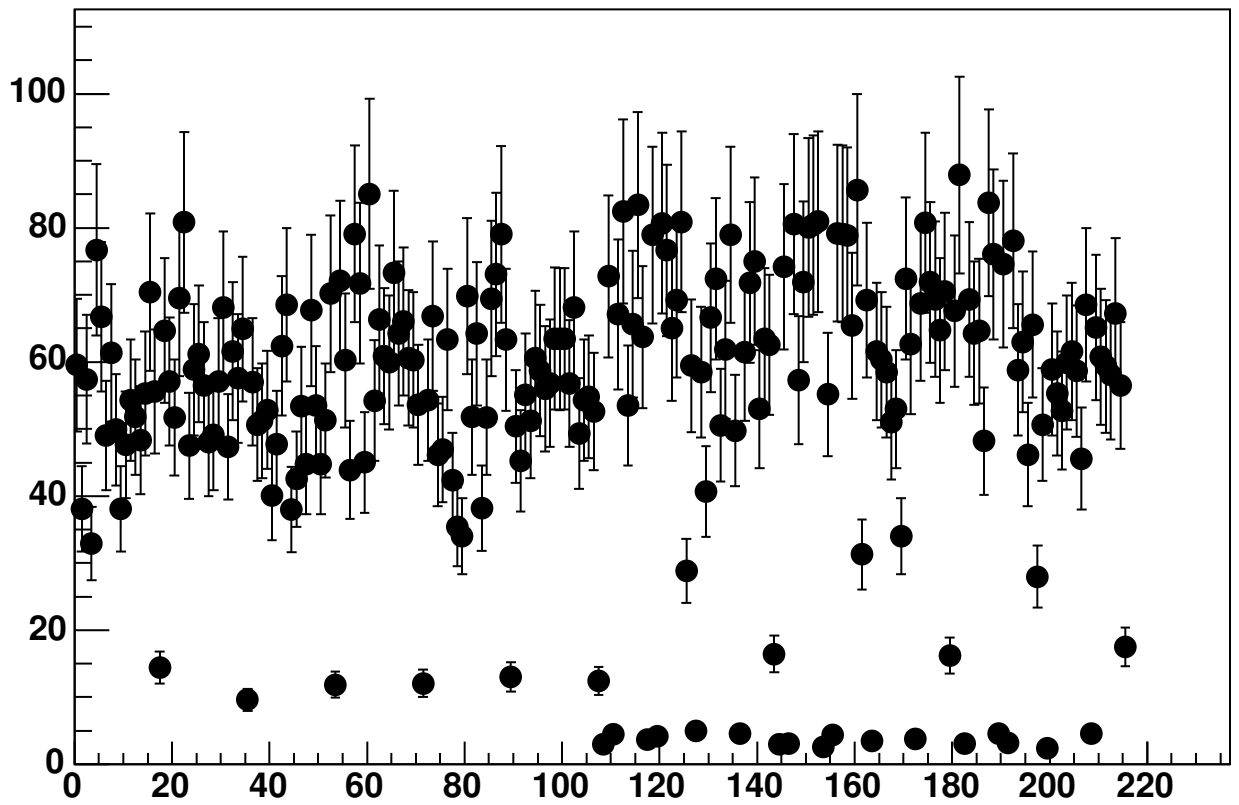
Enable 0, Hold=30, DAC=400, ADC Noise vs 18*Chip+Chan



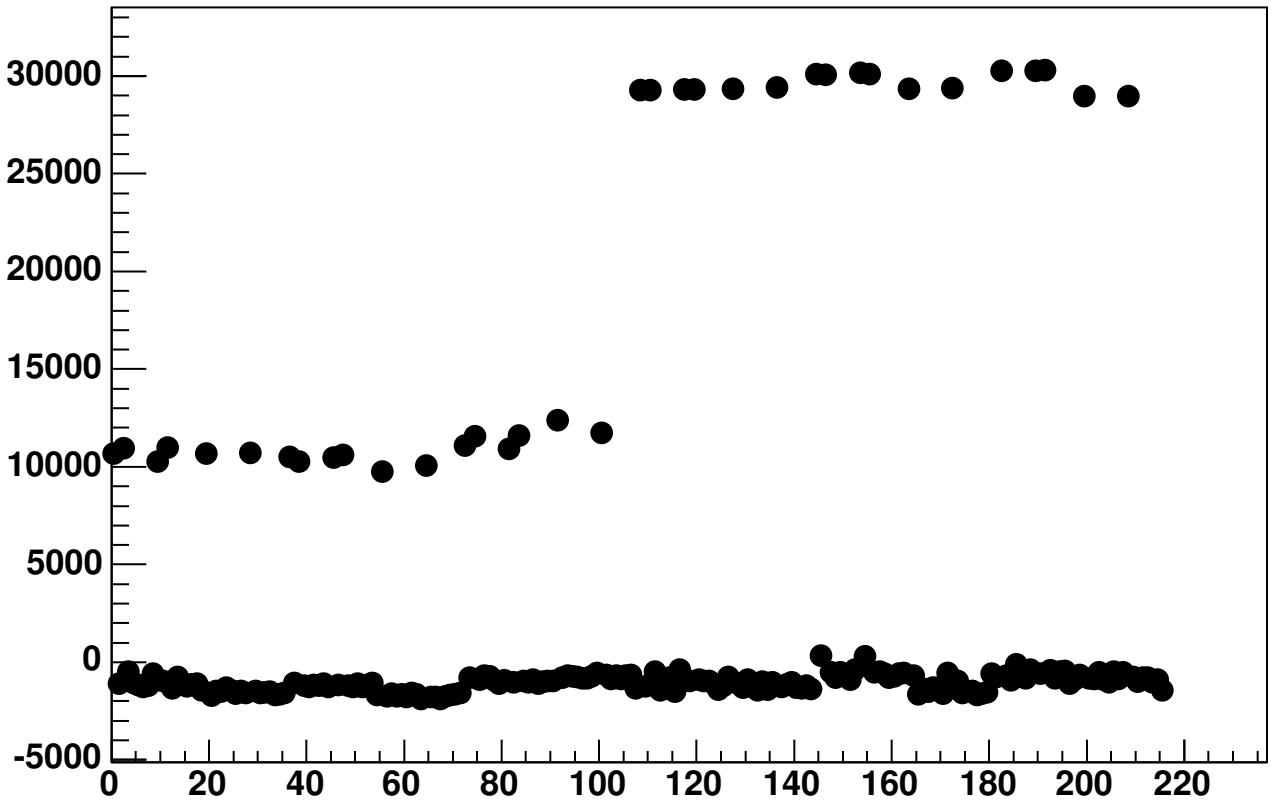
Enable 0, Hold=30, DAC=450, ADC Mean vs 18*Chip+Chan



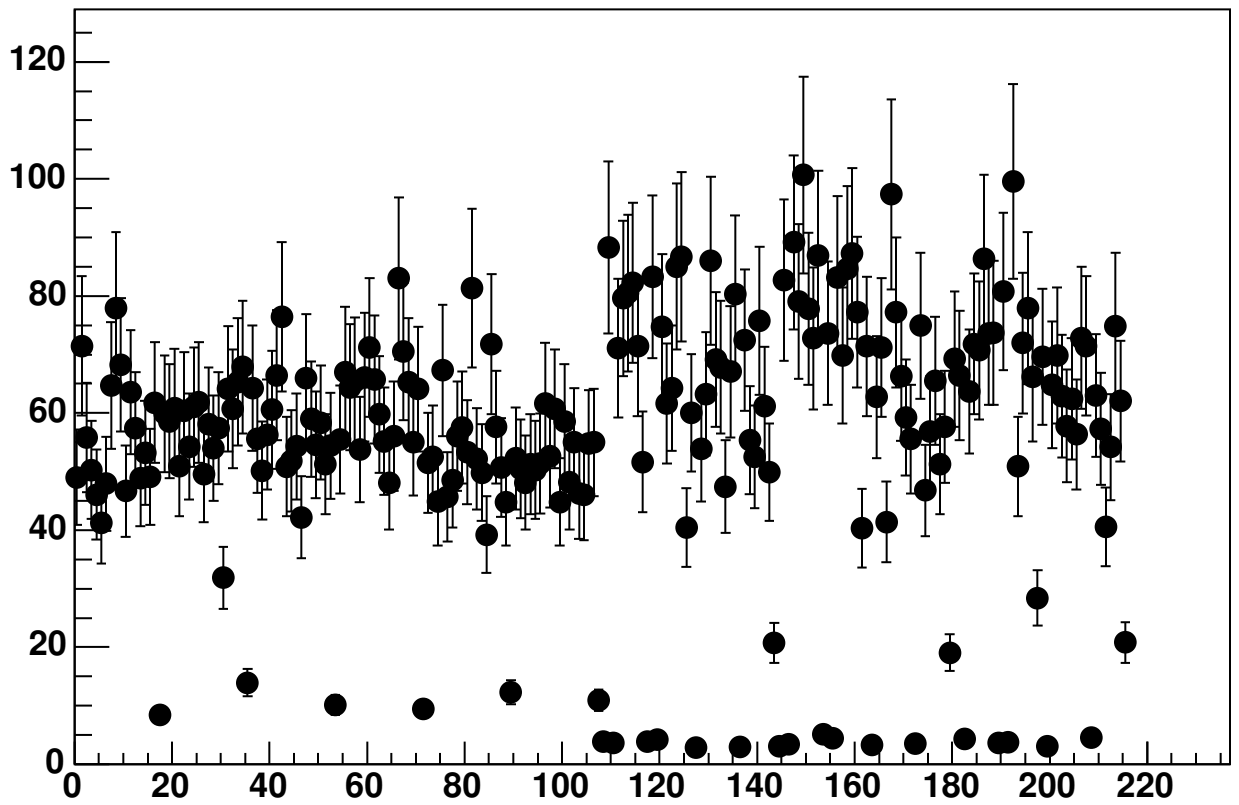
Enable 0, Hold=30, DAC=450, ADC Noise vs 18*Chip+Chan



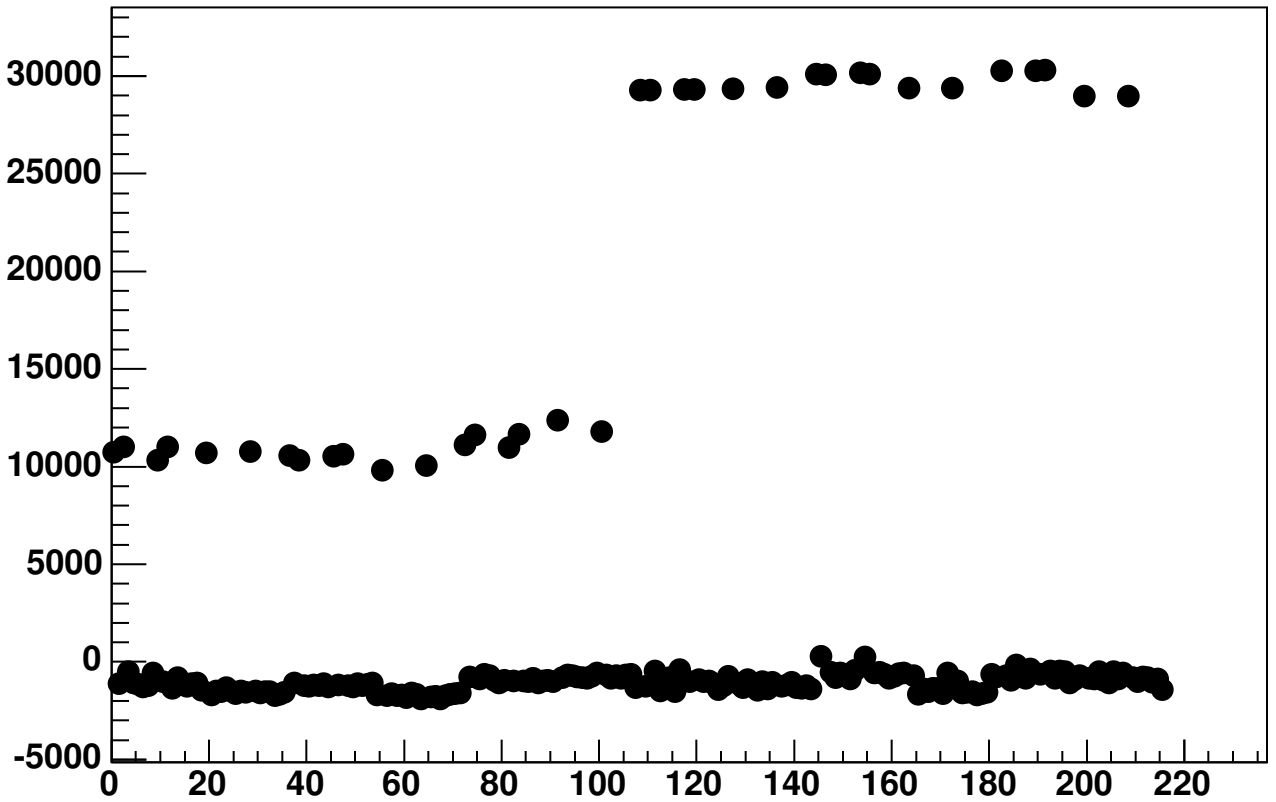
Enable 0, Hold=30, DAC=500, ADC Mean vs 18*Chip+Chan



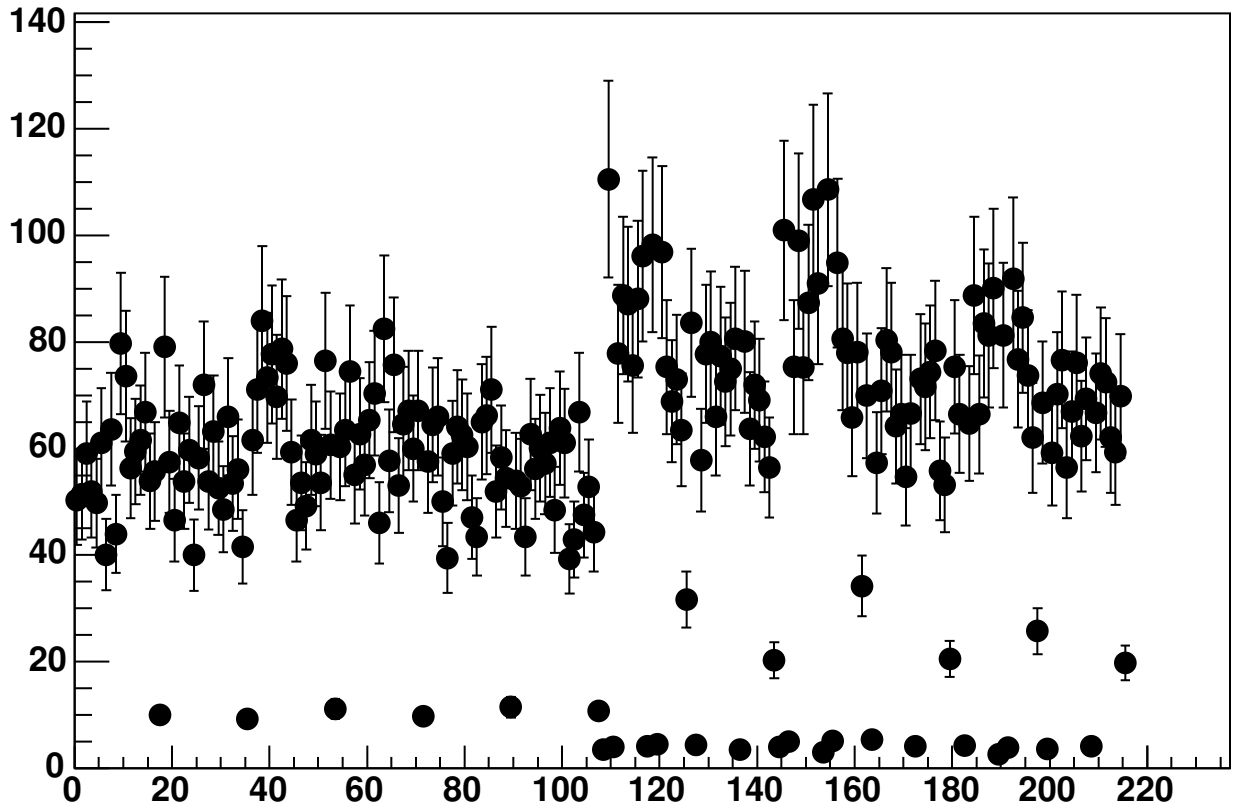
Enable 0, Hold=30, DAC=500, ADC Noise vs 18*Chip+Chan



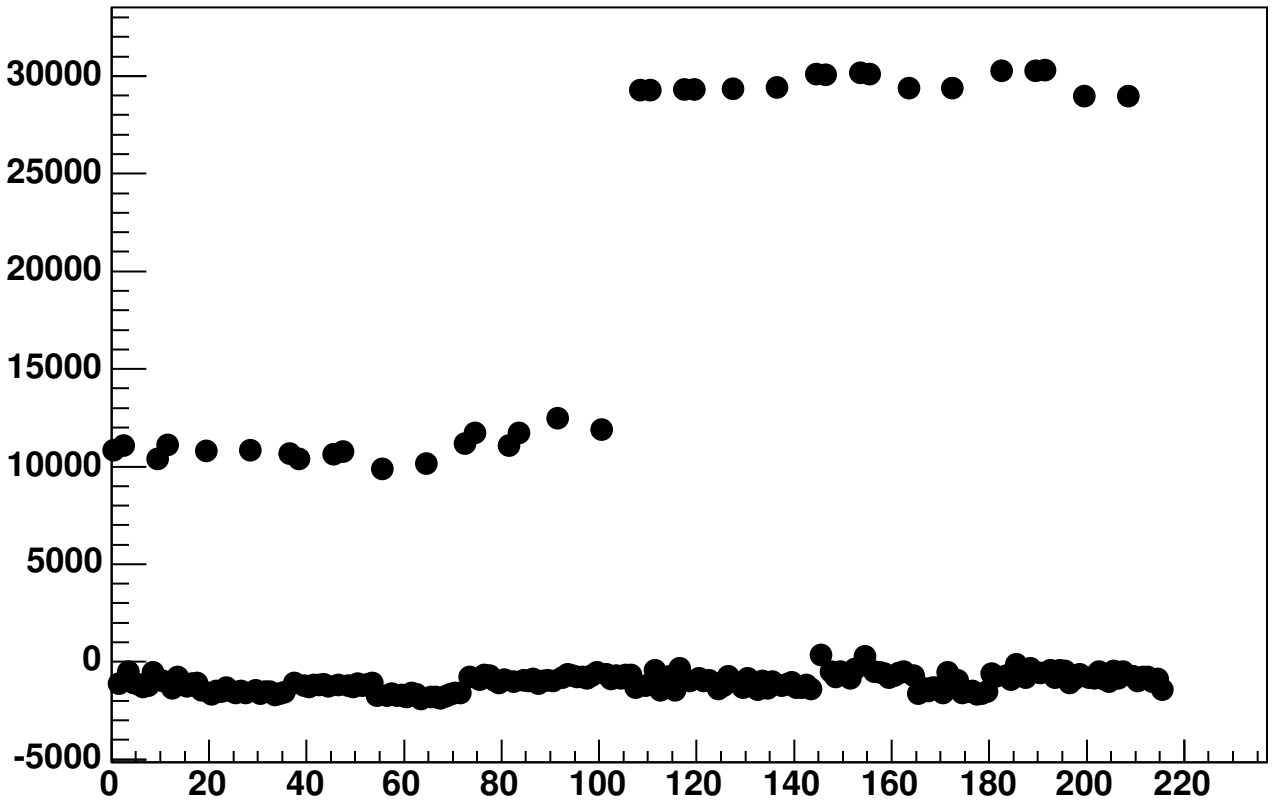
Enable 0, Hold=30, DAC=550, ADC Mean vs 18*Chip+Chan



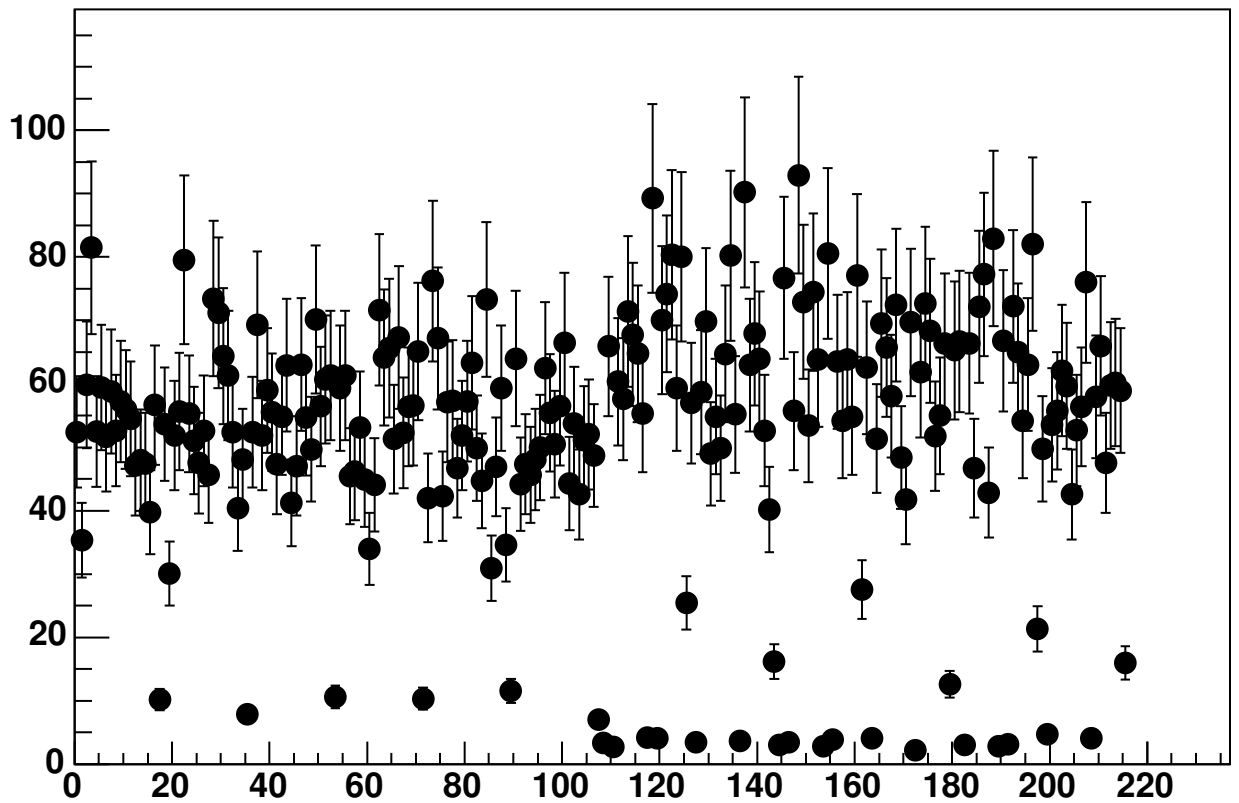
Enable 0, Hold=30, DAC=550, ADC Noise vs 18*Chip+Chan



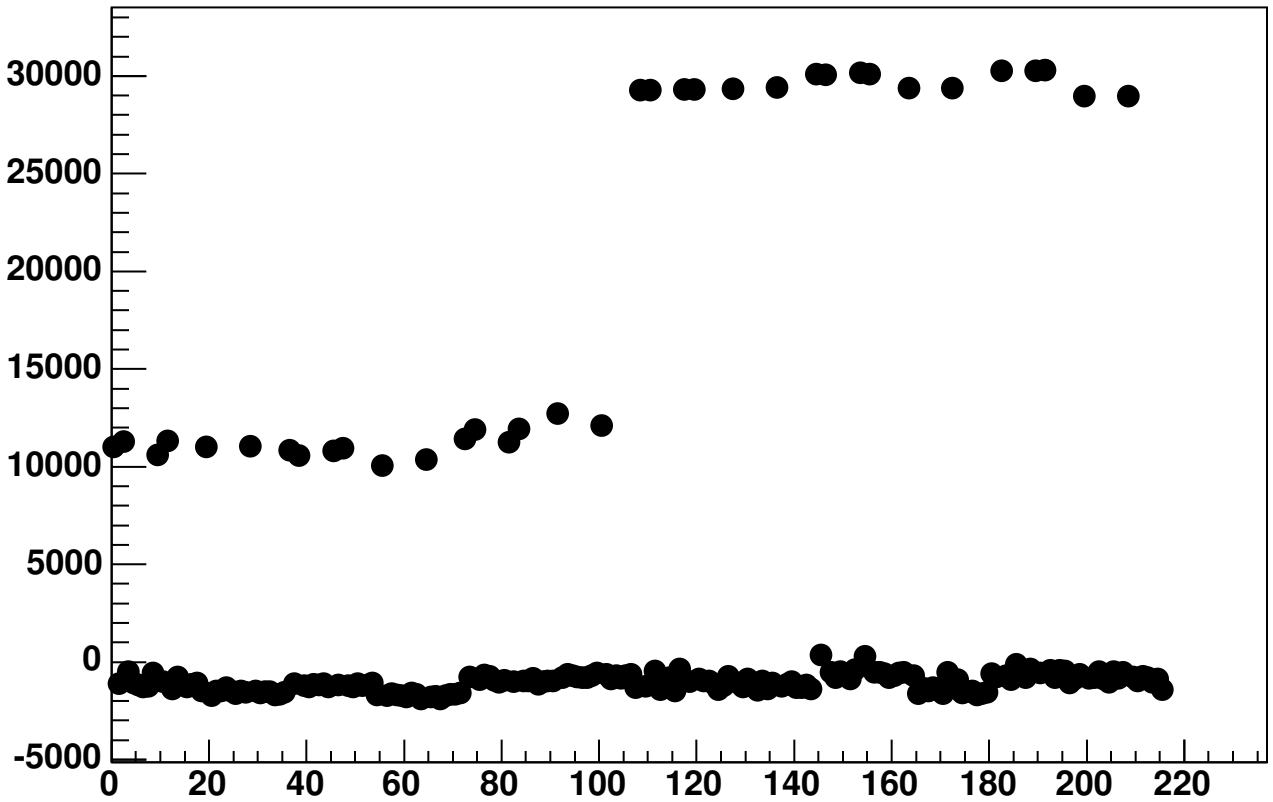
Enable 0, Hold=30, DAC=600, ADC Mean vs 18*Chip+Chan



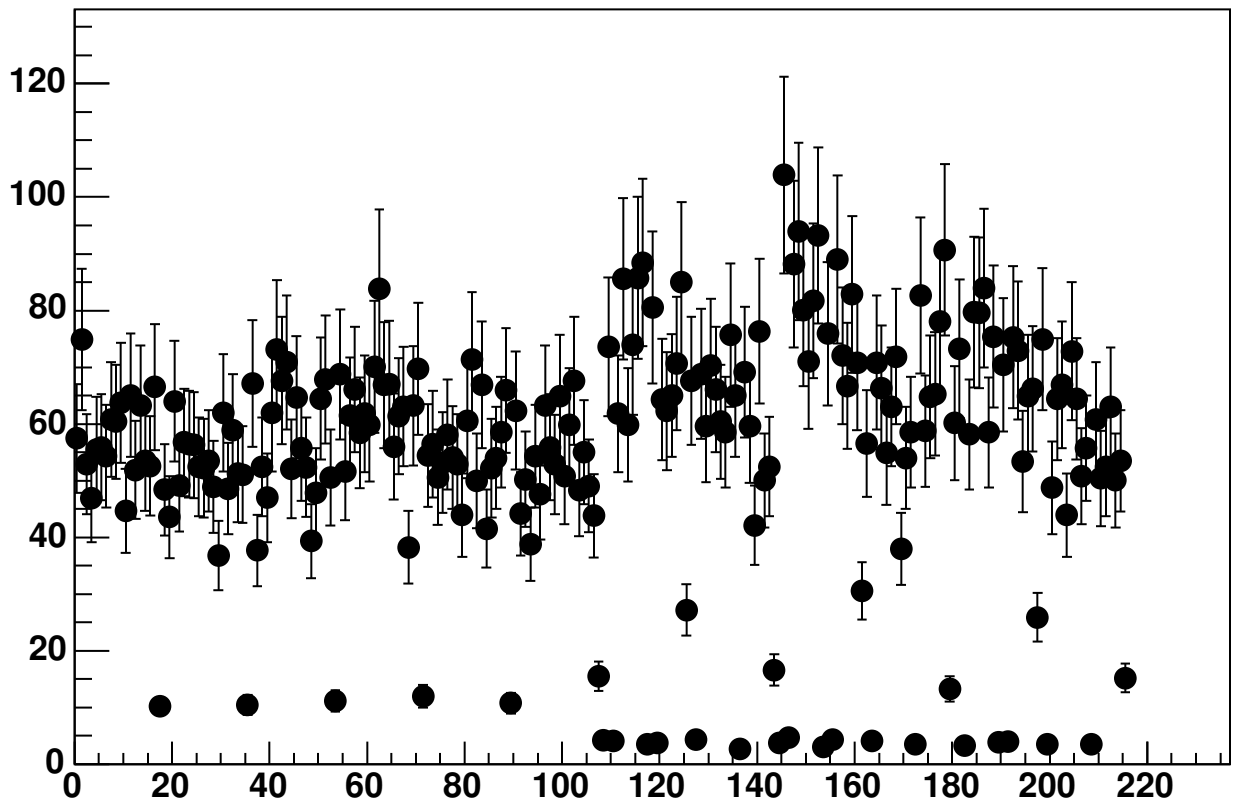
Enable 0, Hold=30, DAC=600, ADC Noise vs 18*Chip+Chan



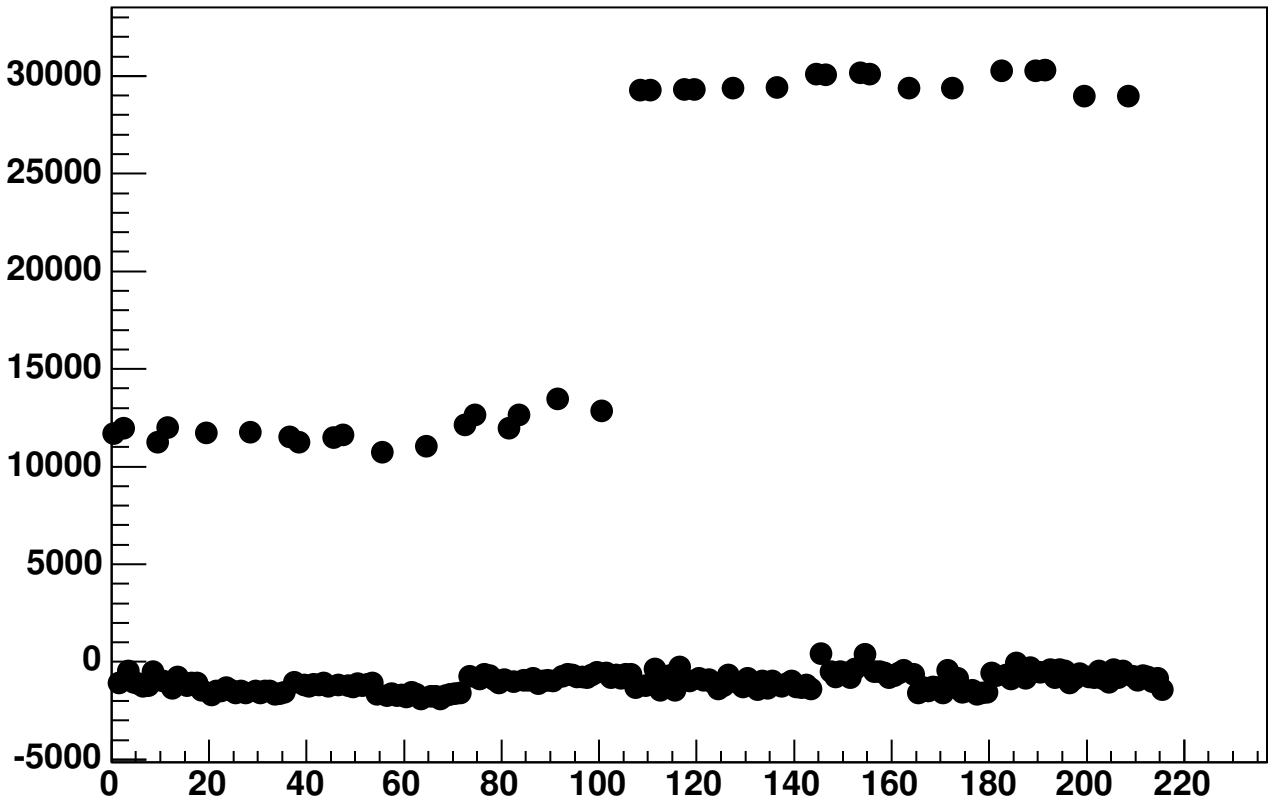
Enable 0, Hold=30, DAC=650, ADC Mean vs 18*Chip+Chan



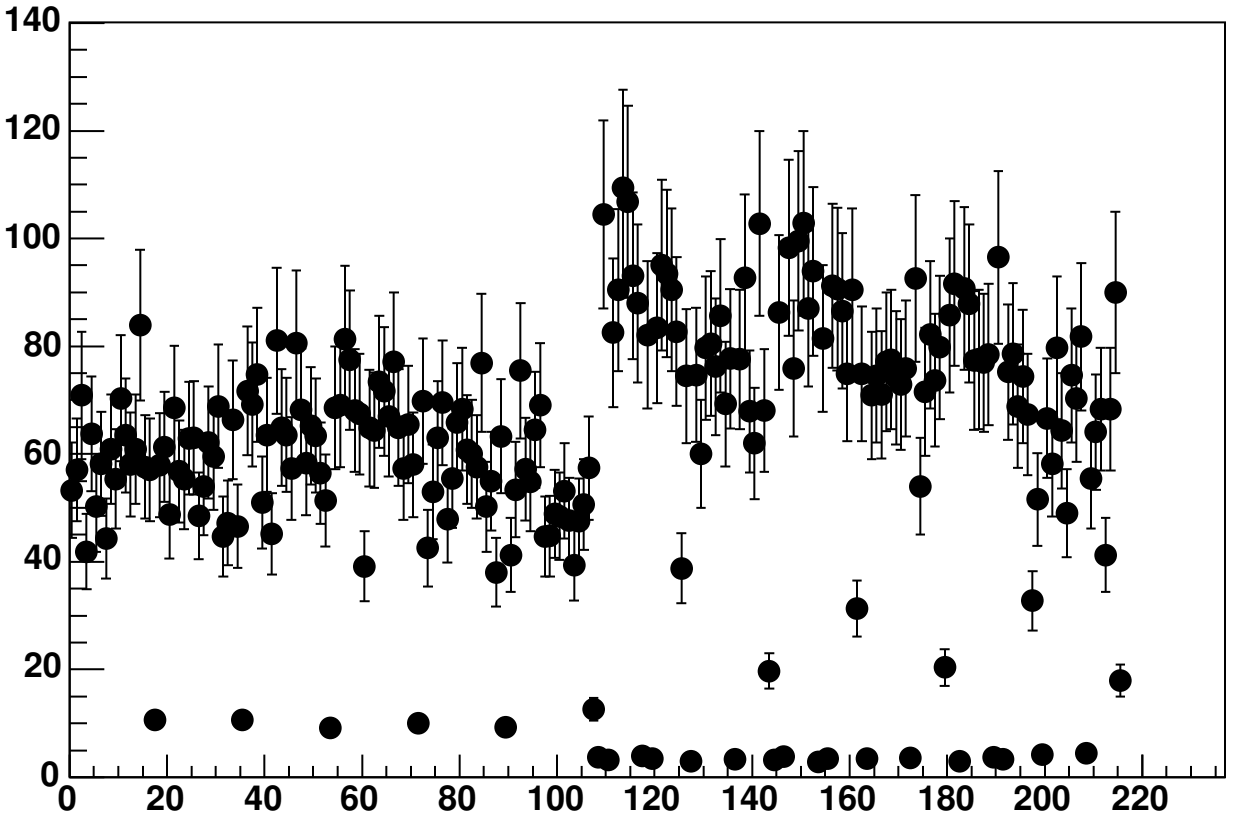
Enable 0, Hold=30, DAC=650, ADC Noise vs 18*Chip+Chan



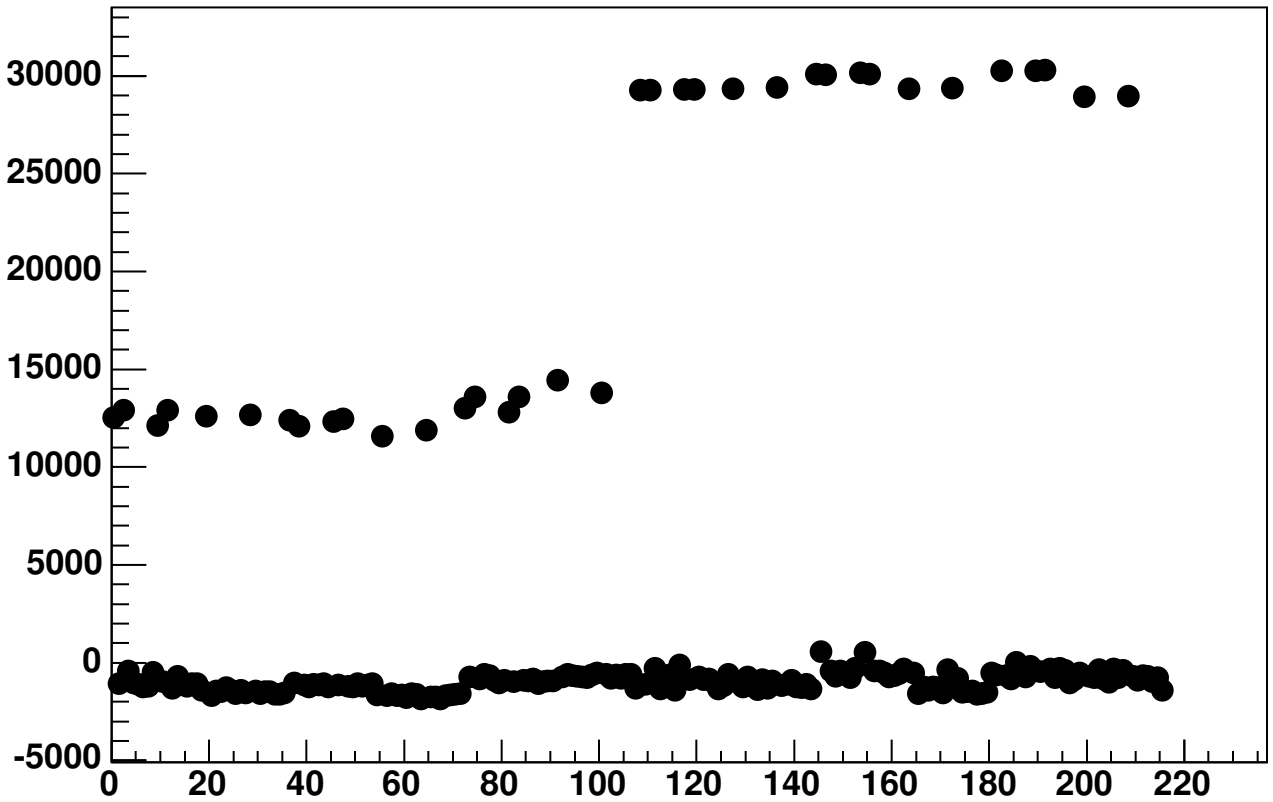
Enable 0, Hold=30, DAC=700, ADC Mean vs 18*Chip+Chan



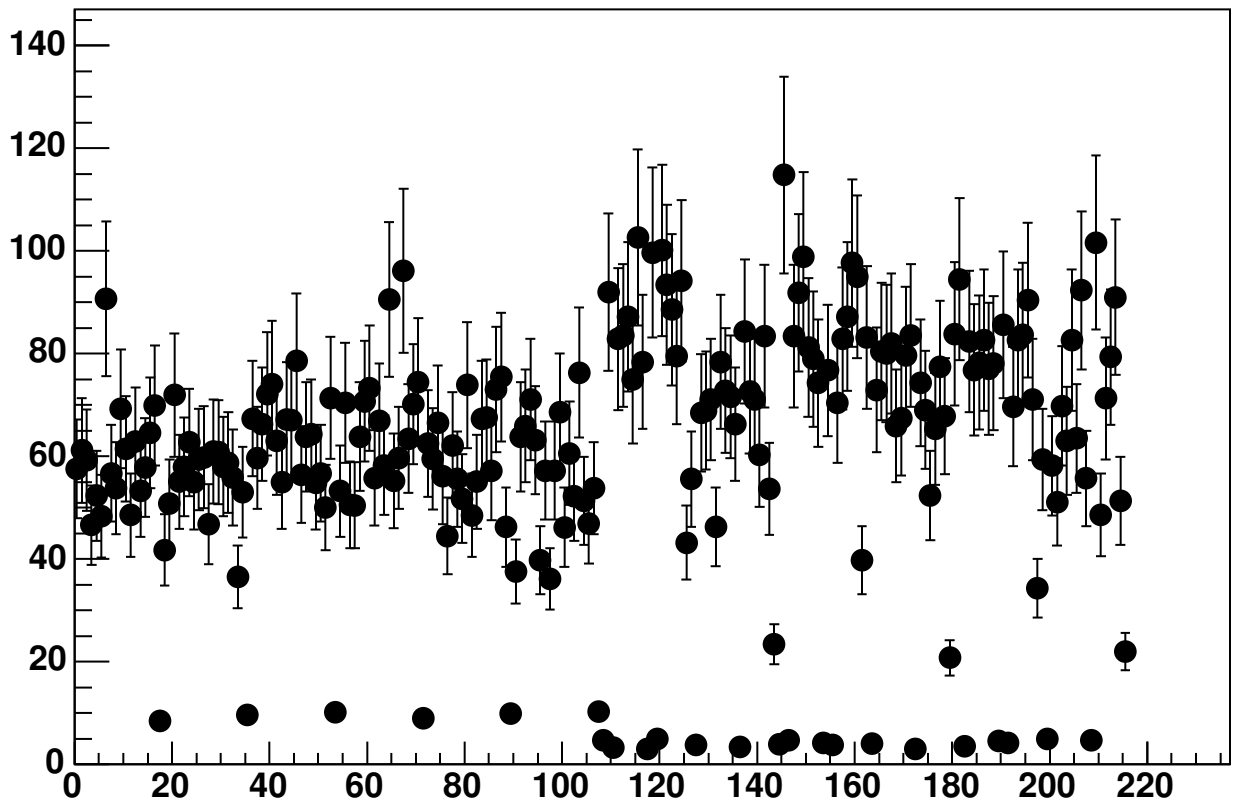
Enable 0, Hold=30, DAC=700, ADC Noise vs 18*Chip+Chan



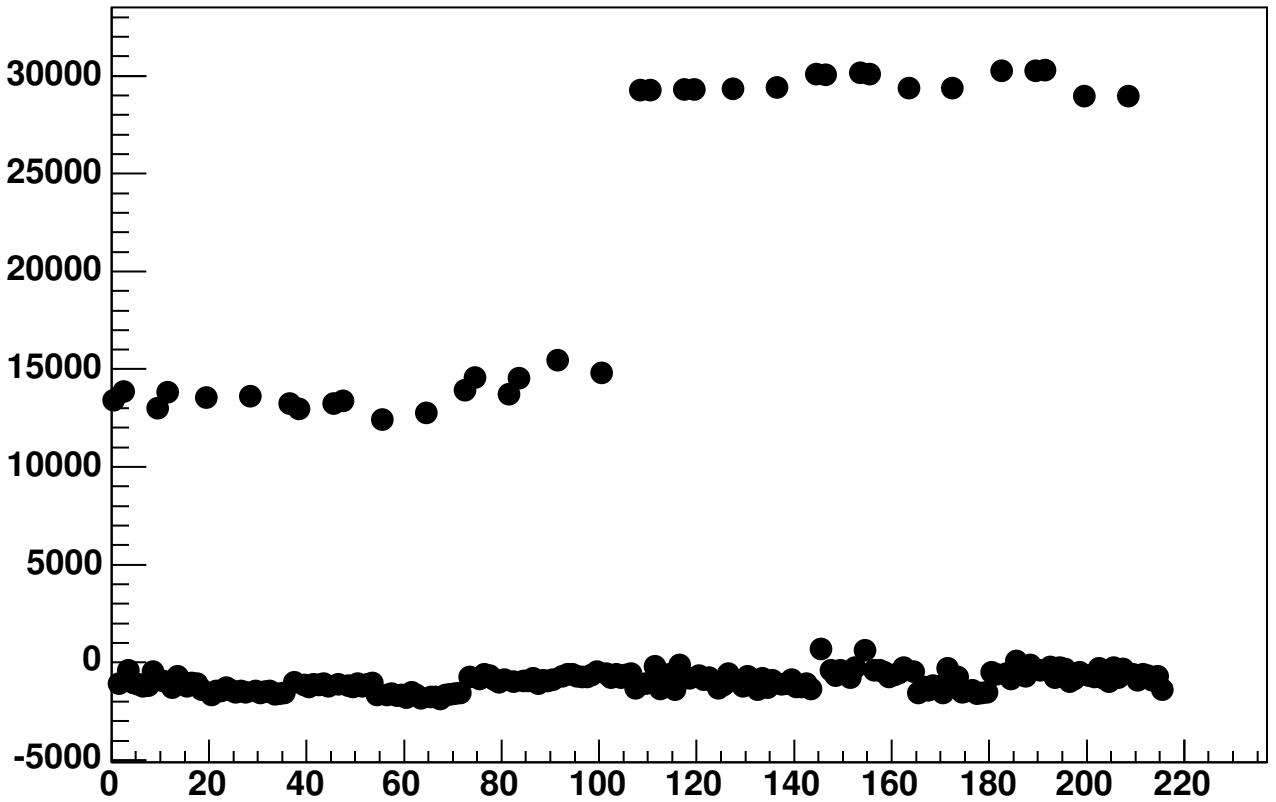
Enable 0, Hold=30, DAC=750, ADC Mean vs 18*Chip+Chan



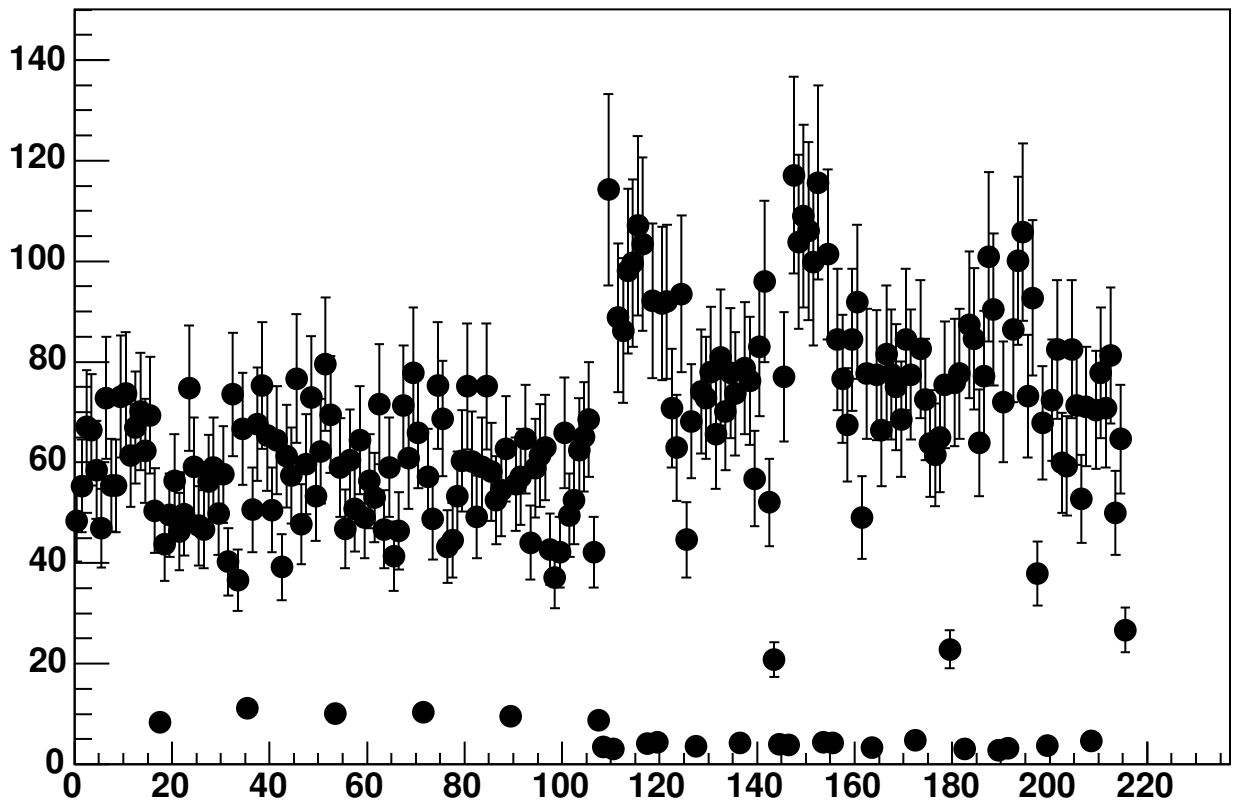
Enable 0, Hold=30, DAC=750, ADC Noise vs 18*Chip+Chan



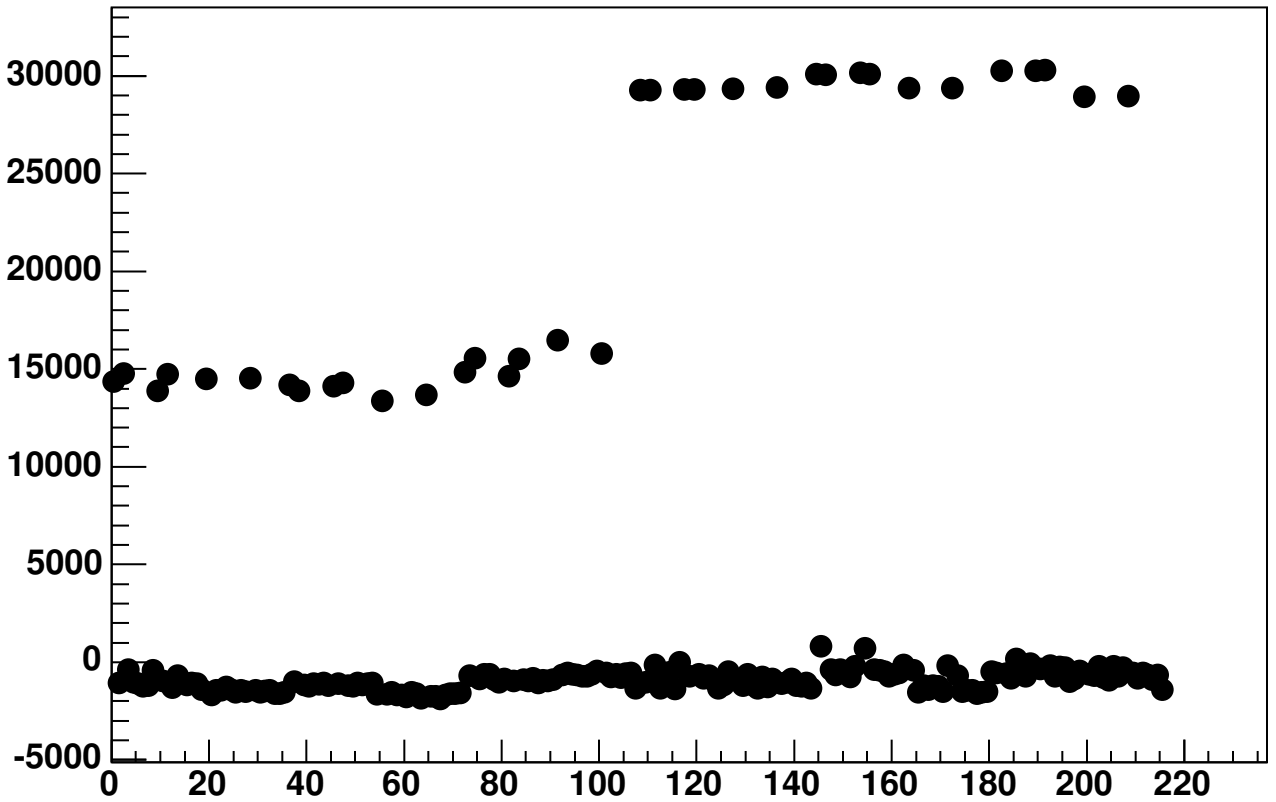
Enable 0, Hold=30, DAC=800, ADC Mean vs 18*Chip+Chan



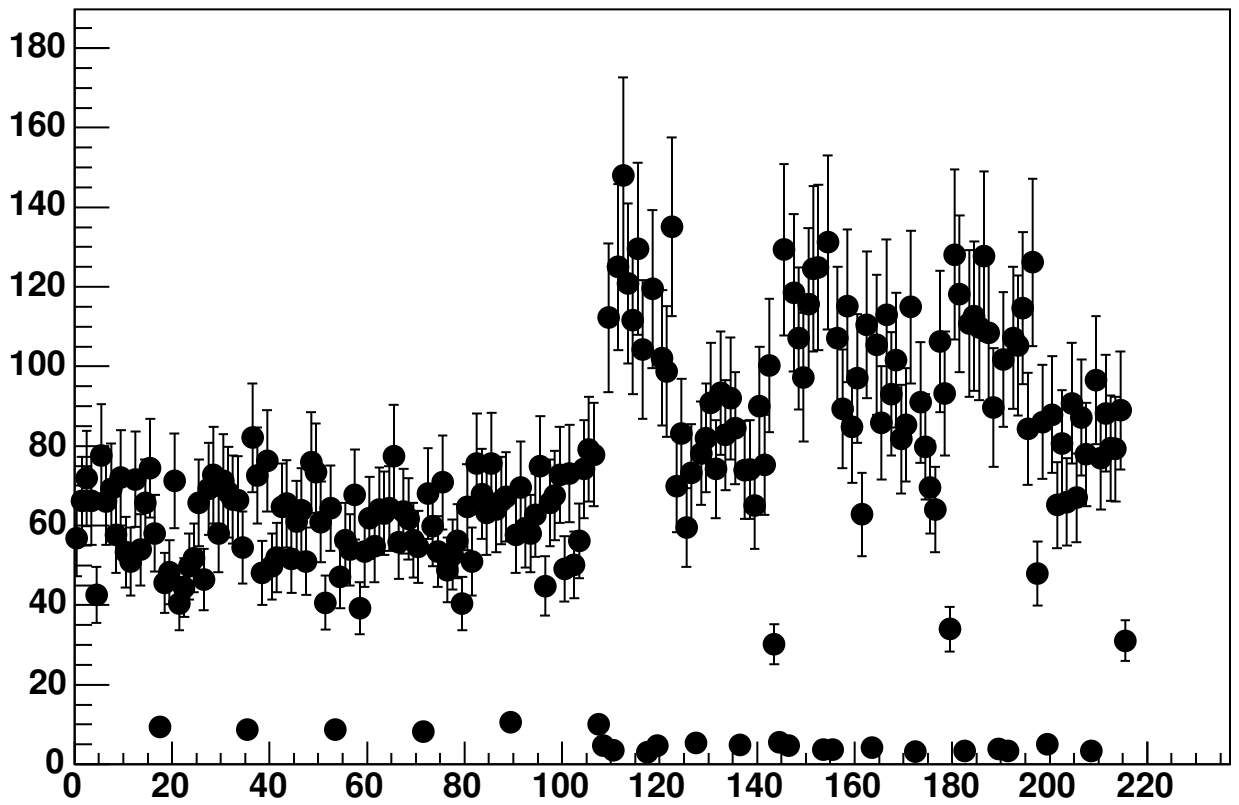
Enable 0, Hold=30, DAC=800, ADC Noise vs 18*Chip+Chan



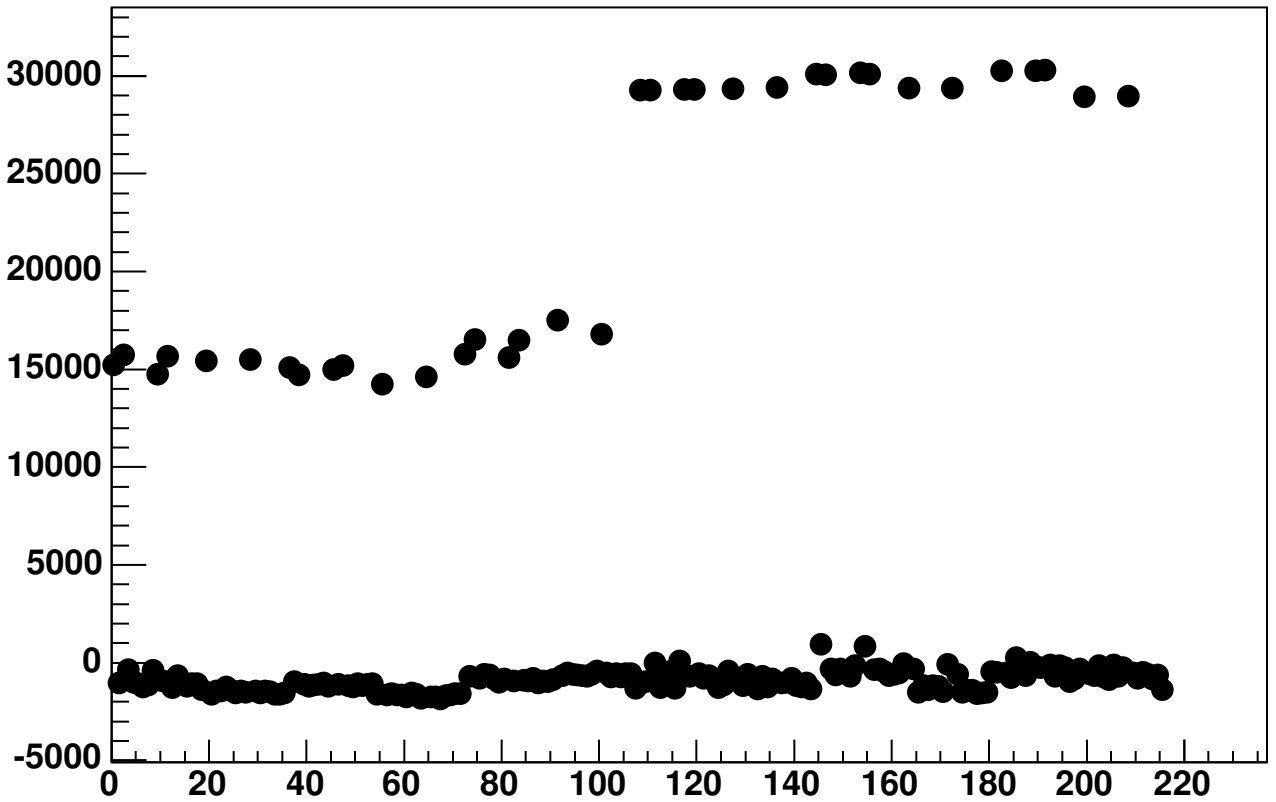
Enable 0, Hold=30, DAC=850, ADC Mean vs 18*Chip+Chan



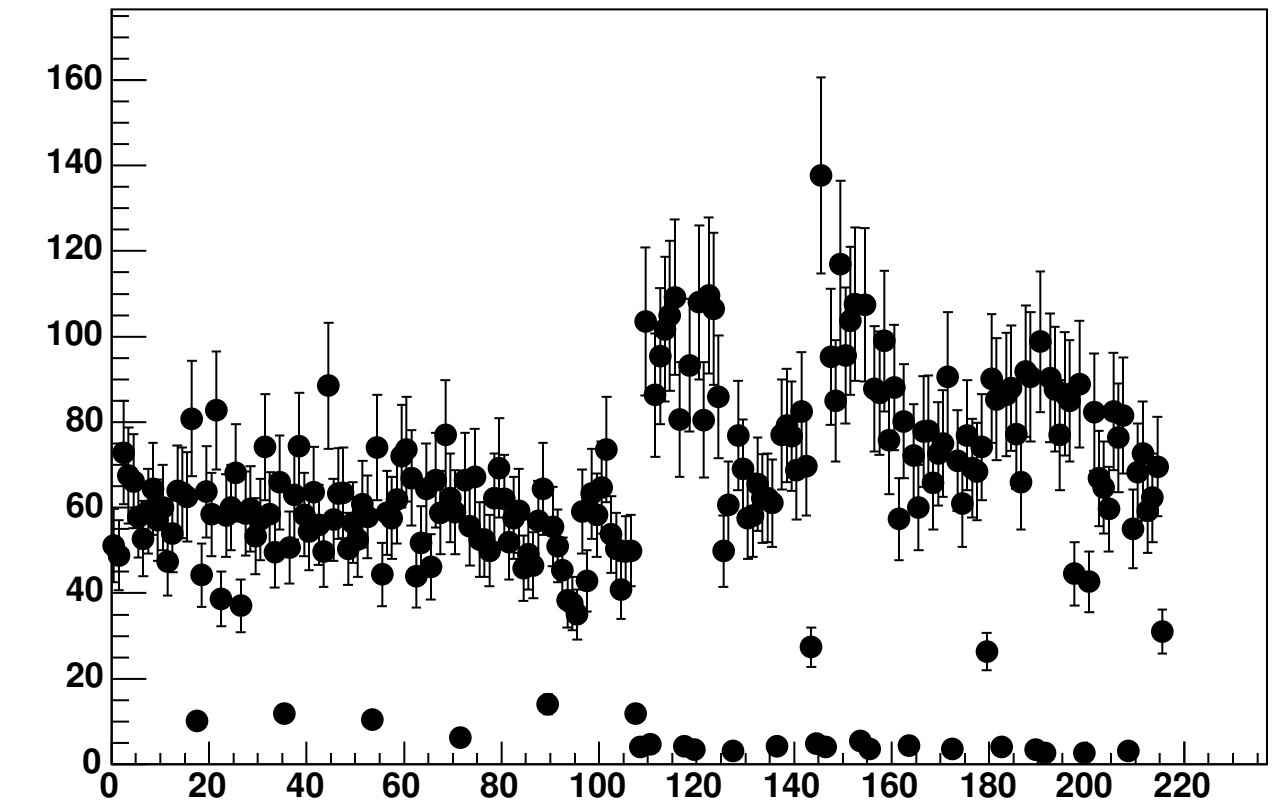
Enable 0, Hold=30, DAC=850, ADC Noise vs 18*Chip+Chan



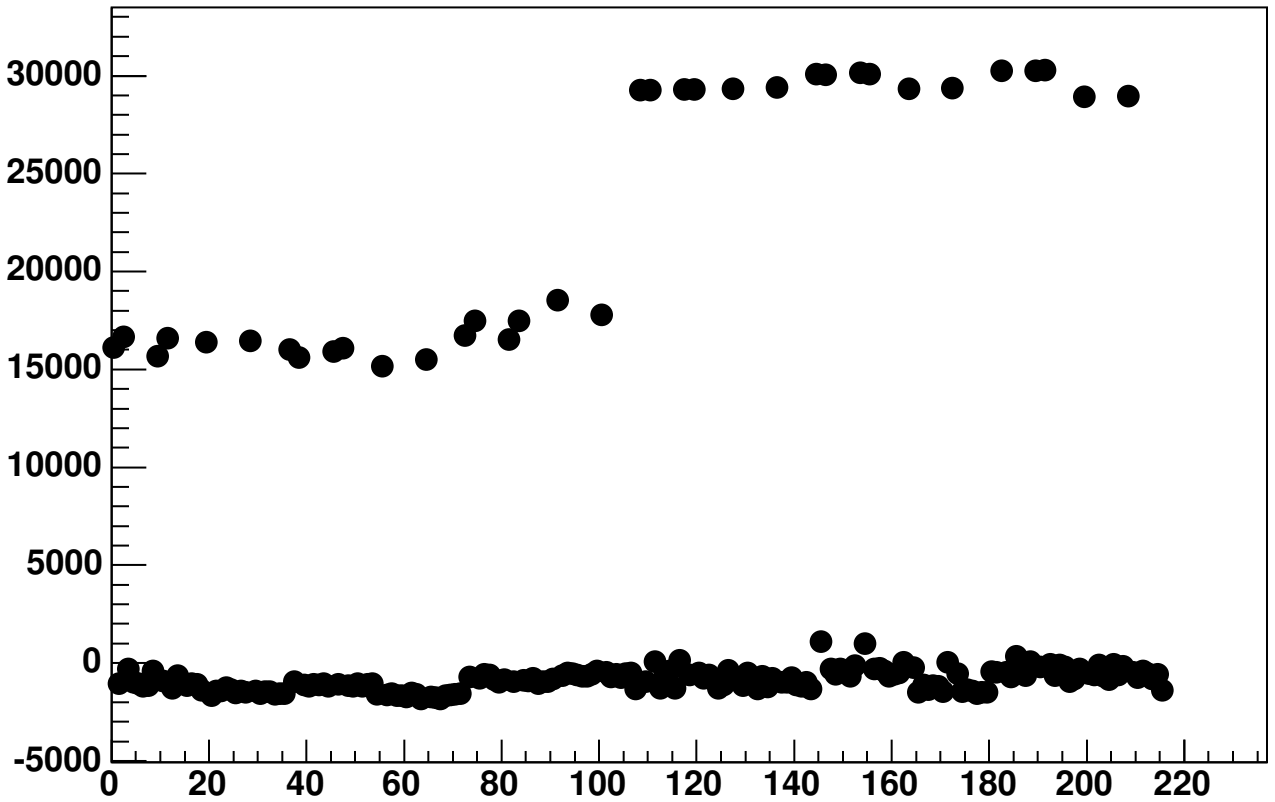
Enable 0, Hold=30, DAC=900, ADC Mean vs 18*Chip+Chan



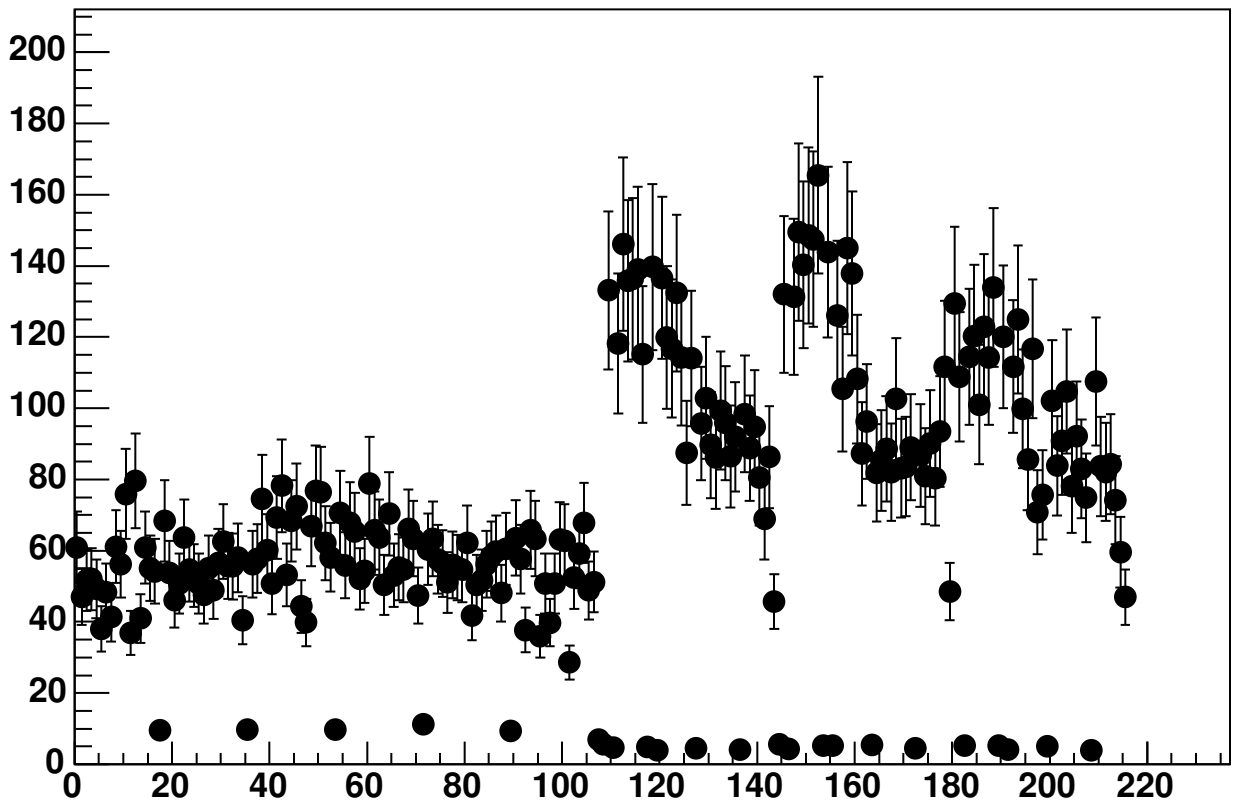
Enable 0, Hold=30, DAC=900, ADC Noise vs 18*Chip+Chan



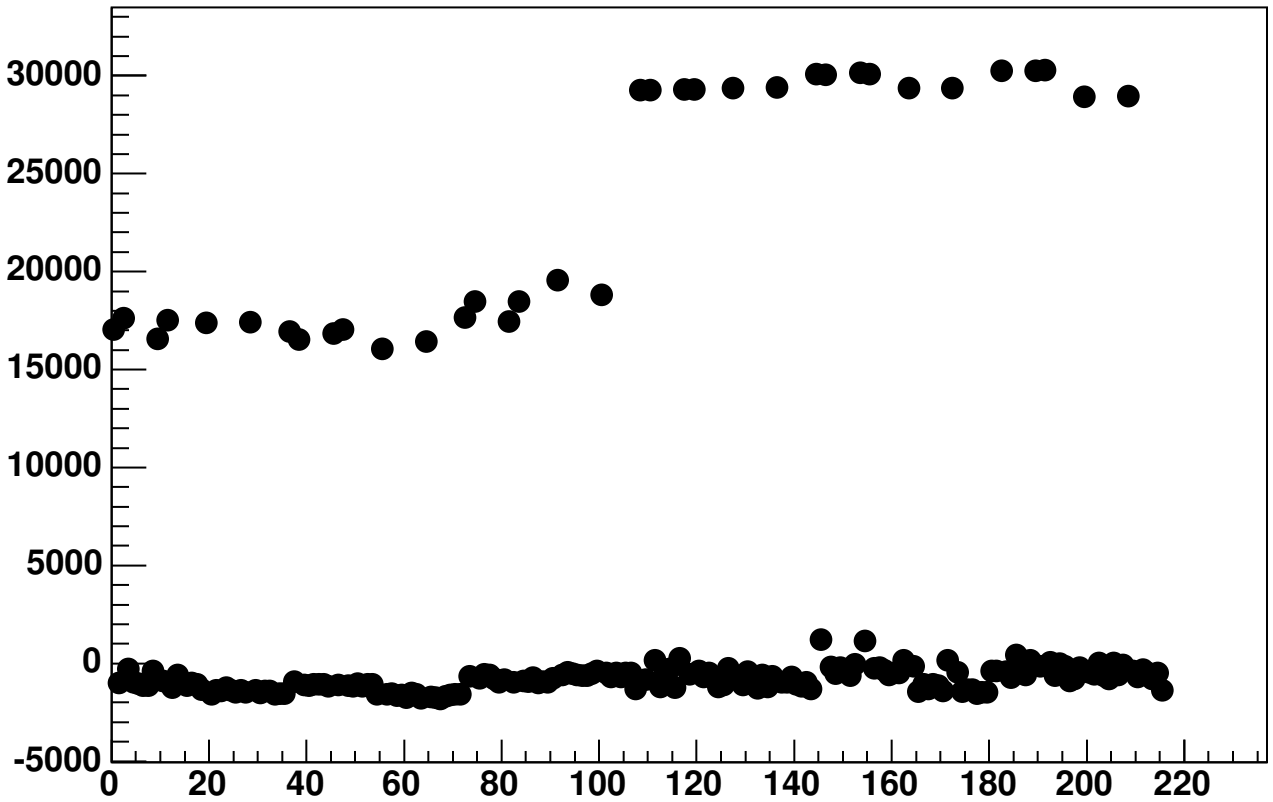
Enable 0, Hold=30, DAC=950, ADC Mean vs 18*Chip+Chan



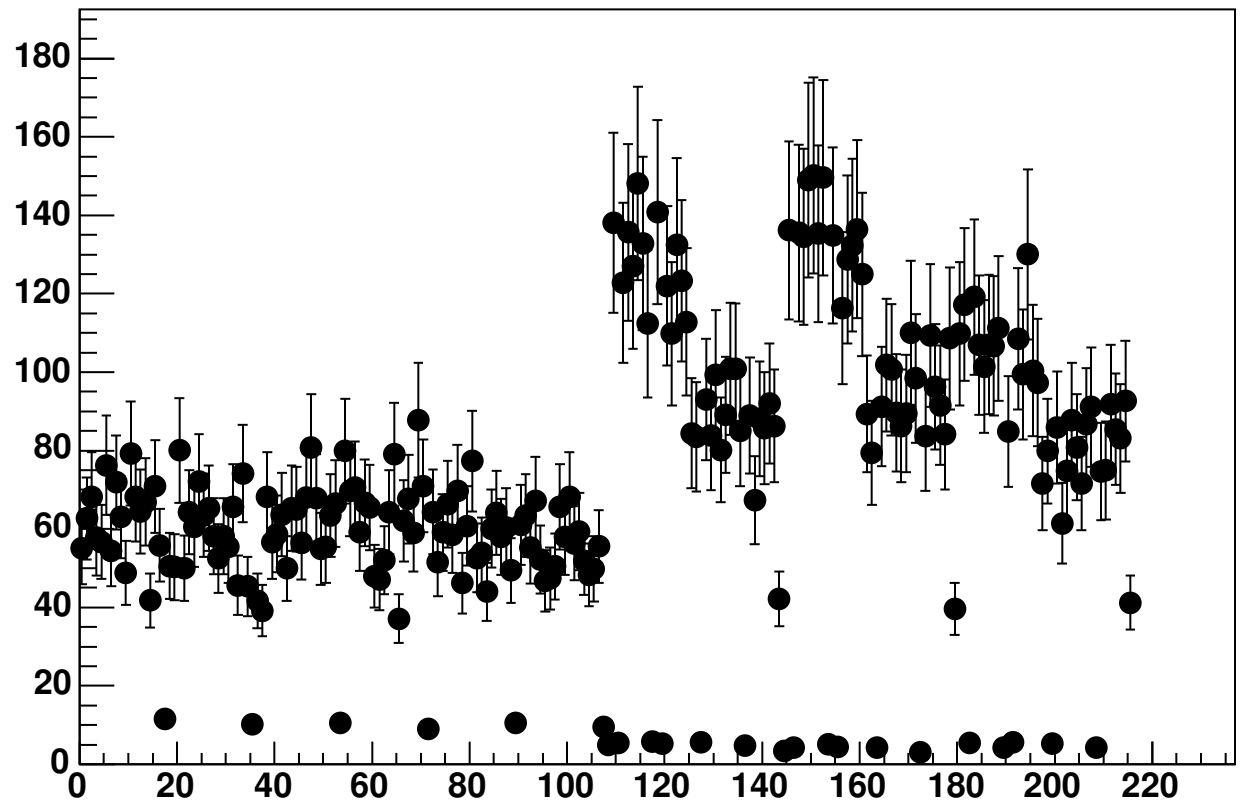
Enable 0, Hold=30, DAC=950, ADC Noise vs 18*Chip+Chan



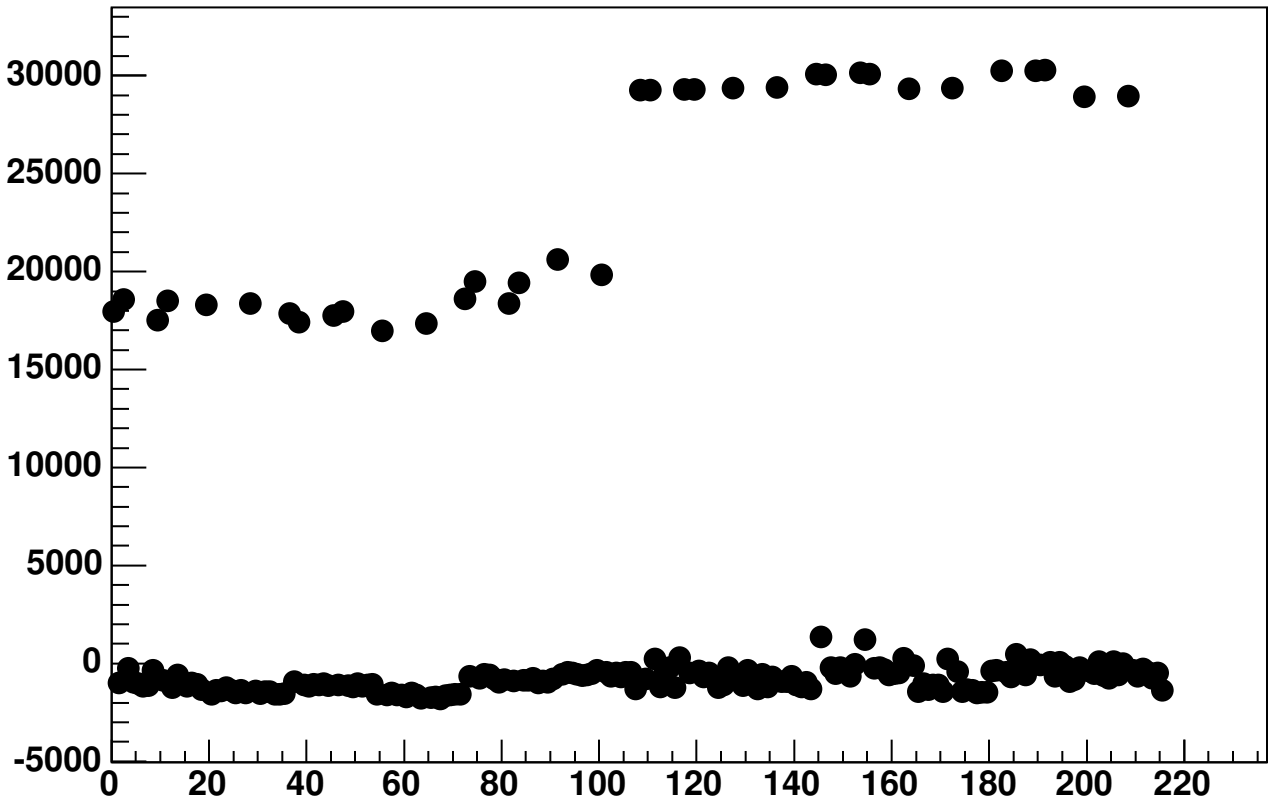
Enable 0, Hold=30, DAC=1000, ADC Mean vs 18*Chip+Chan



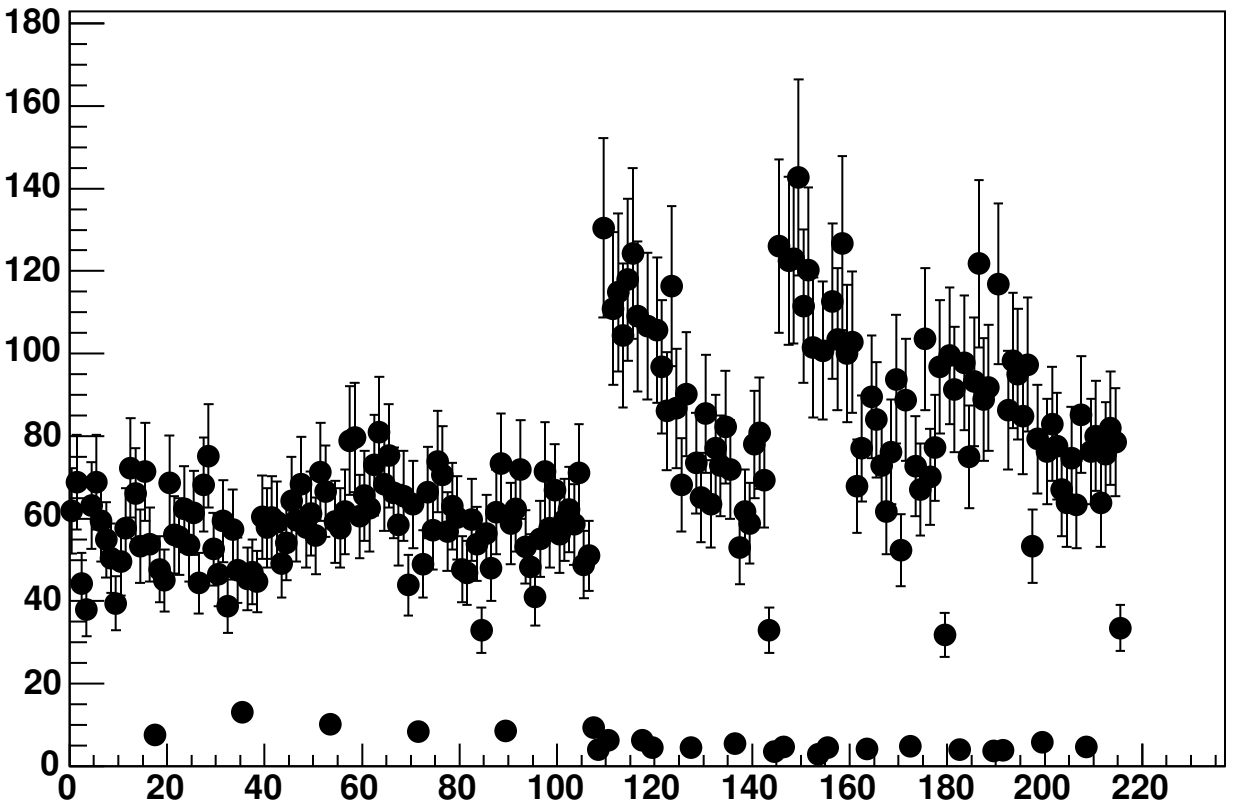
Enable 0, Hold=30, DAC=1000, ADC Noise vs 18*Chip+Chan



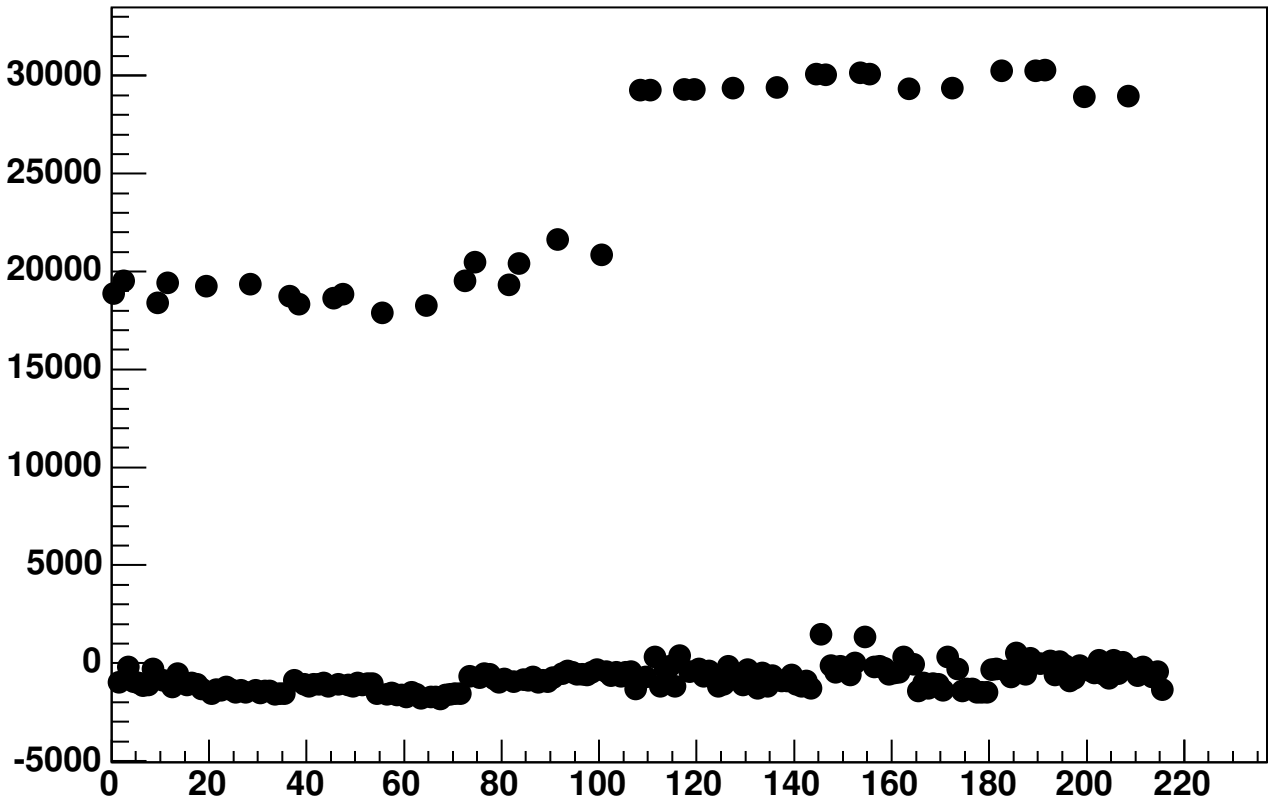
Enable 0, Hold=30, DAC=1050, ADC Mean vs 18*Chip+Chan



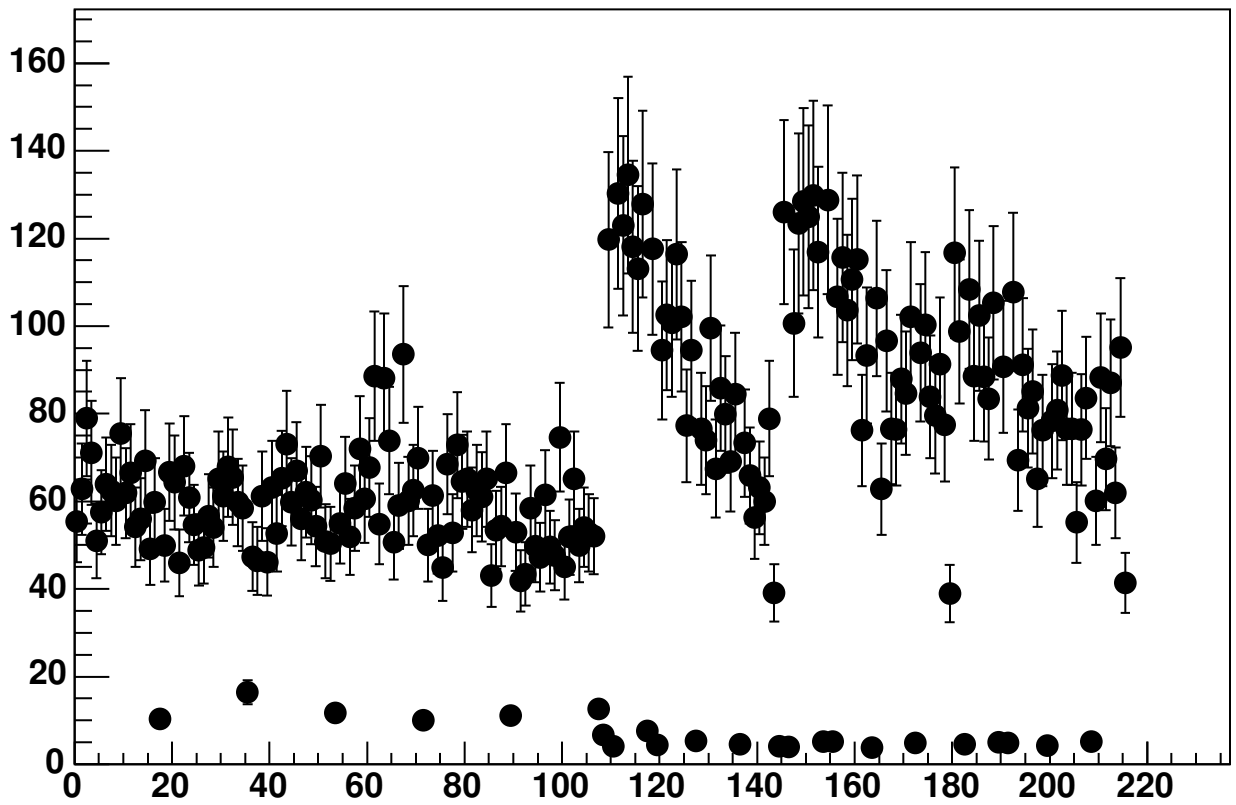
Enable 0, Hold=30, DAC=1050, ADC Noise vs 18*Chip+Chan



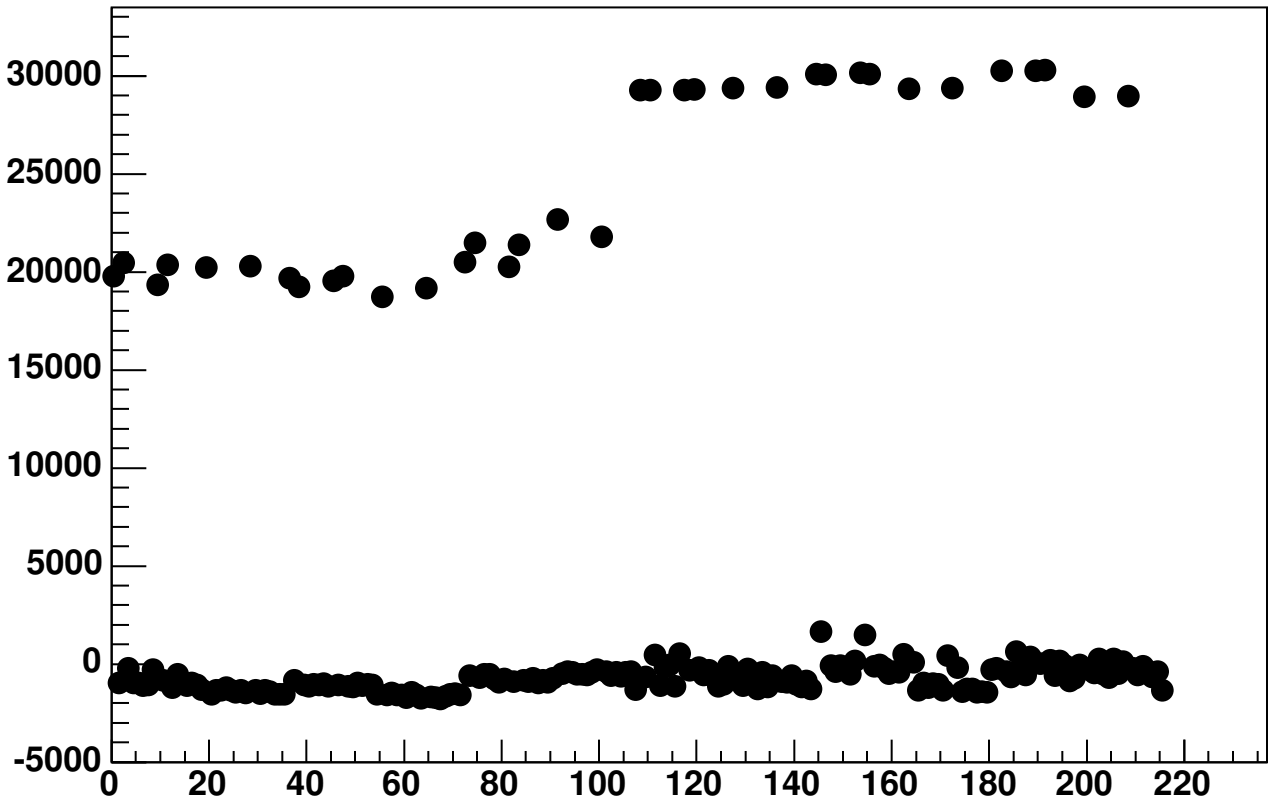
Enable 0, Hold=30, DAC=1100, ADC Mean vs 18*Chip+Chan



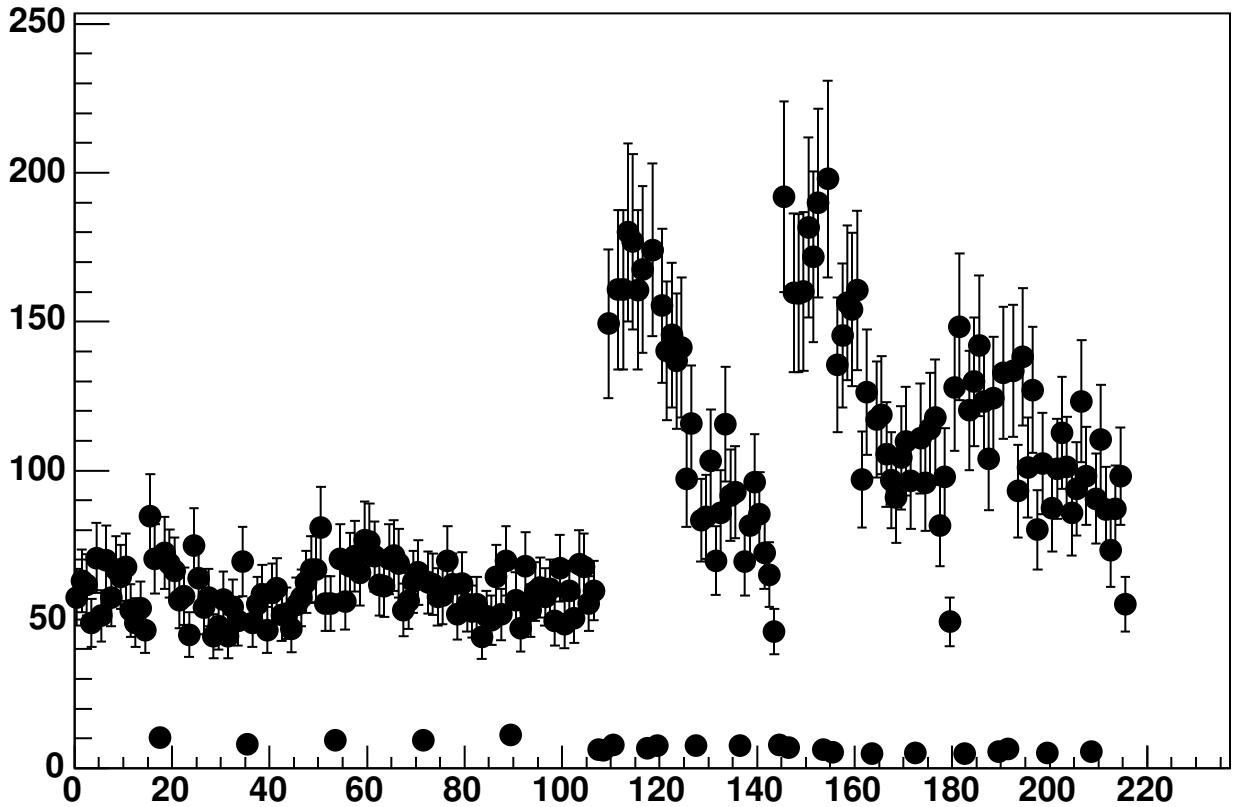
Enable 0, Hold=30, DAC=1100, ADC Noise vs 18*Chip+Chan



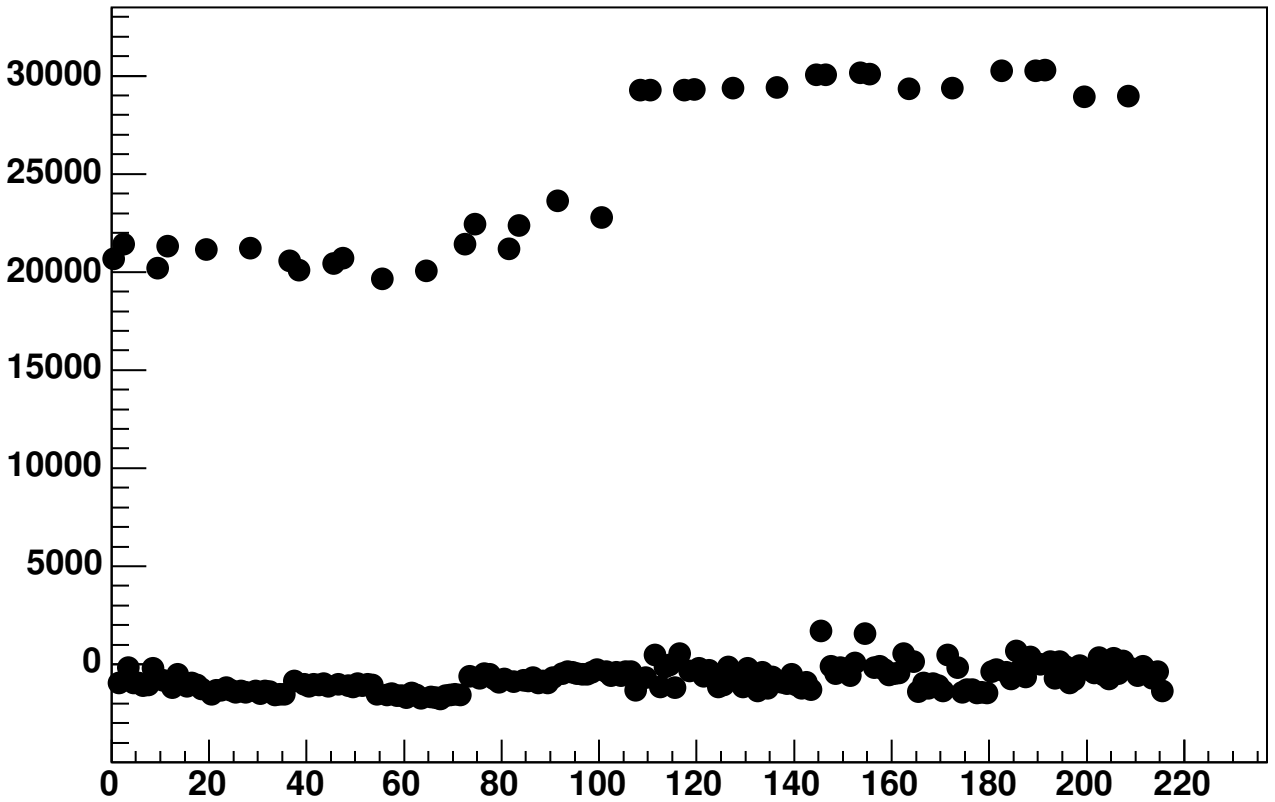
Enable 0, Hold=30, DAC=1150, ADC Mean vs 18*Chip+Chan



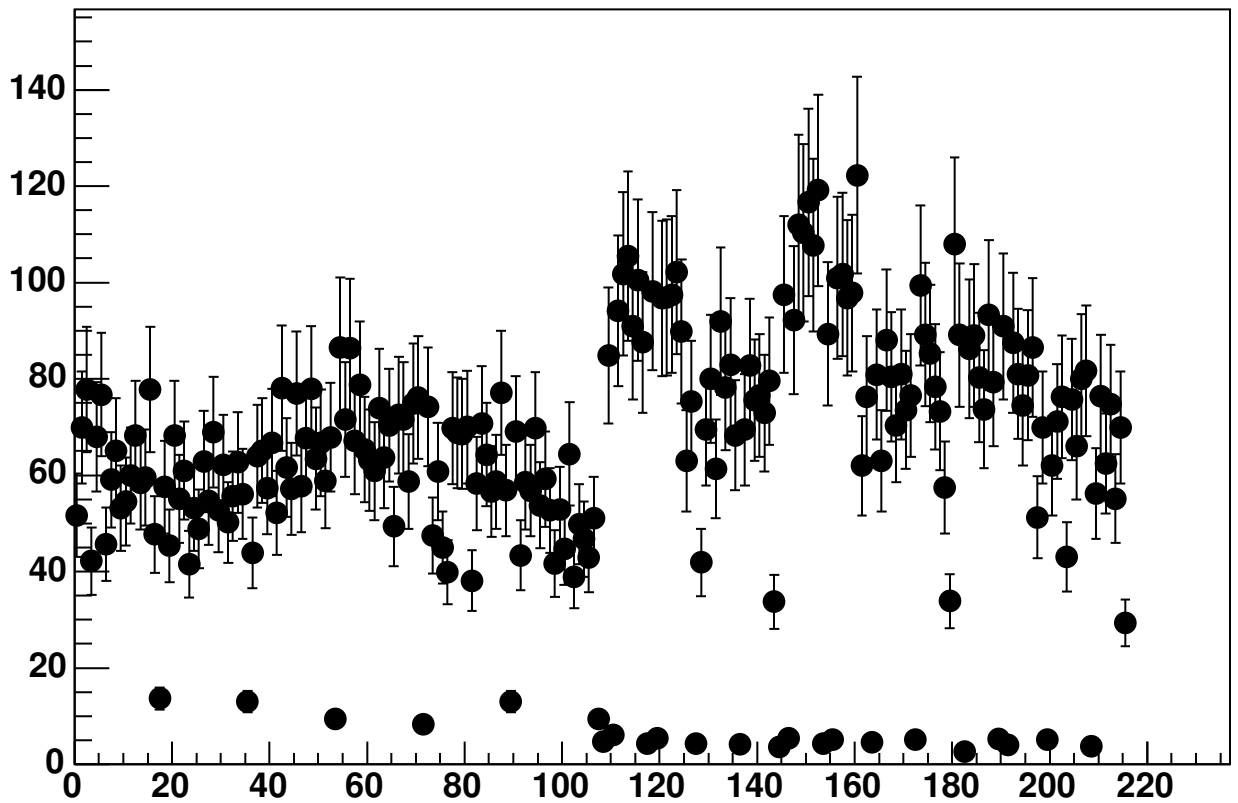
Enable 0, Hold=30, DAC=1150, ADC Noise vs 18*Chip+Chan



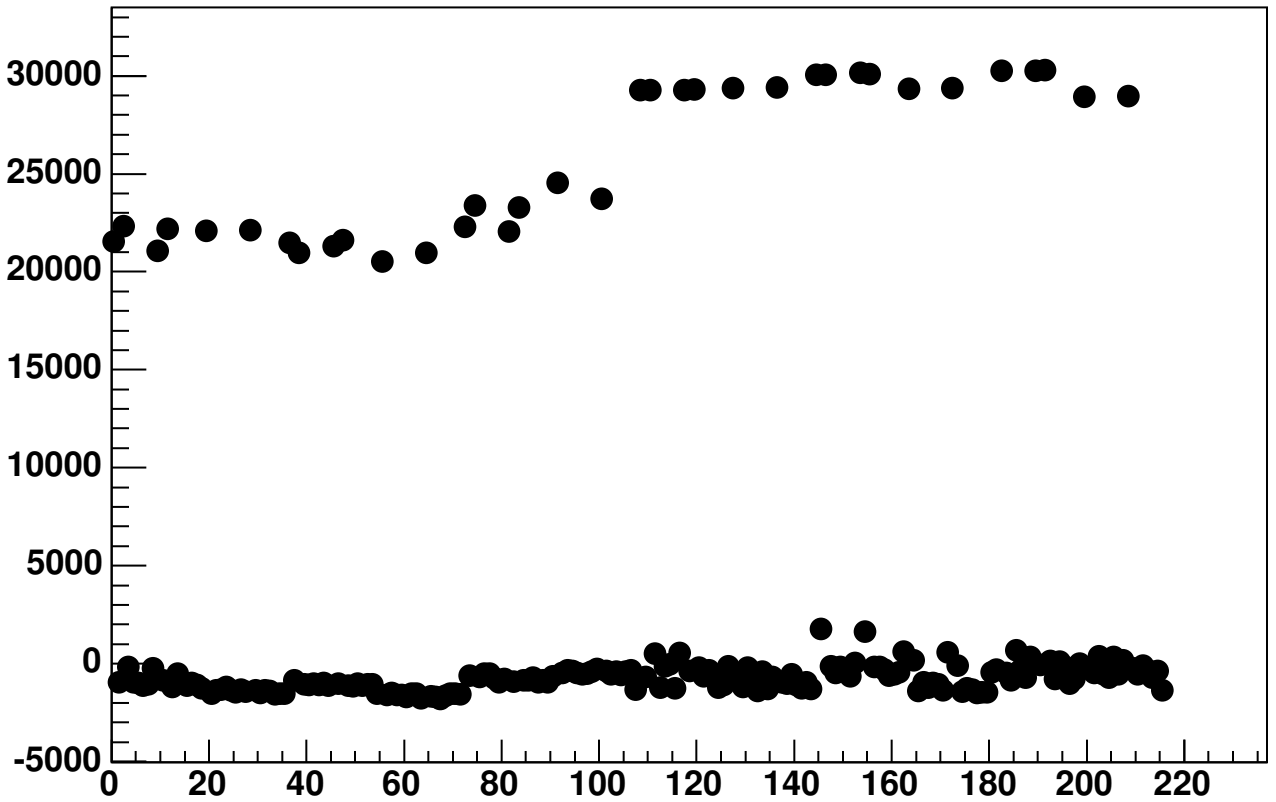
Enable 0, Hold=30, DAC=1200, ADC Mean vs 18*Chip+Chan



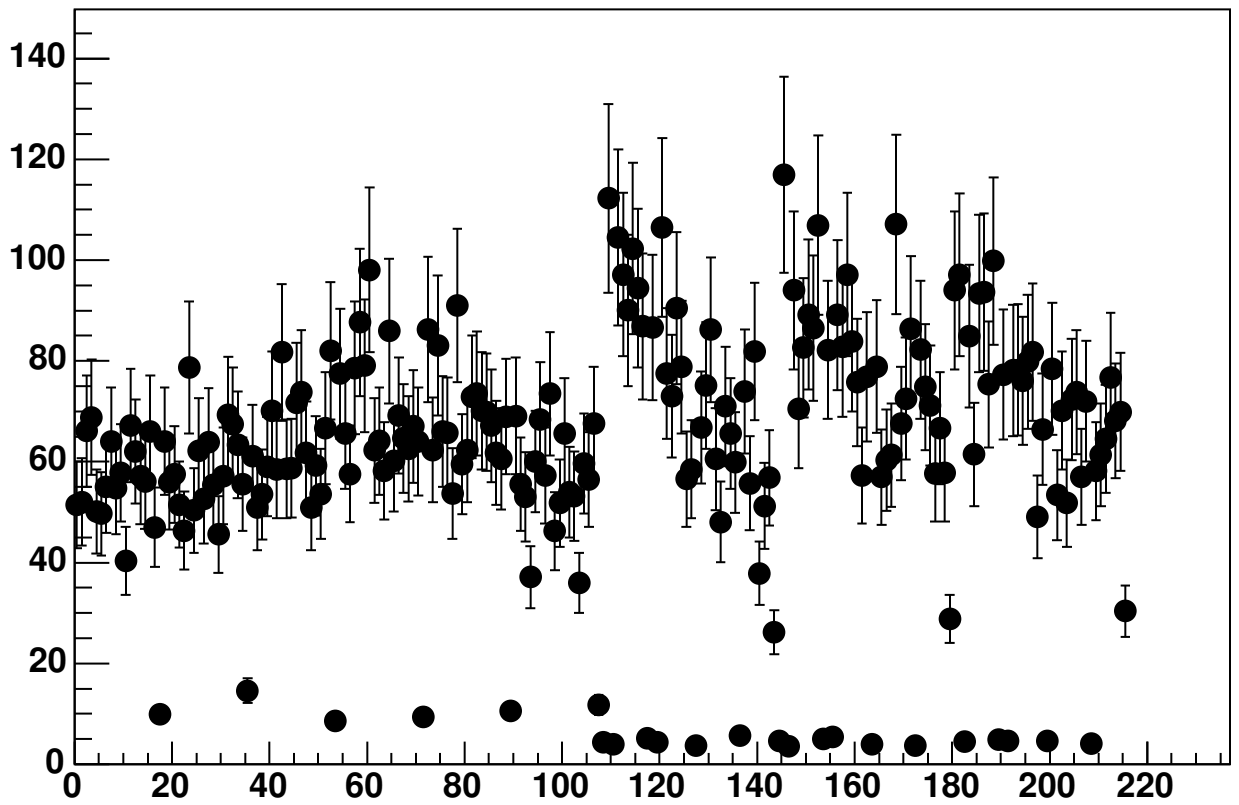
Enable 0, Hold=30, DAC=1200, ADC Noise vs 18*Chip+Chan



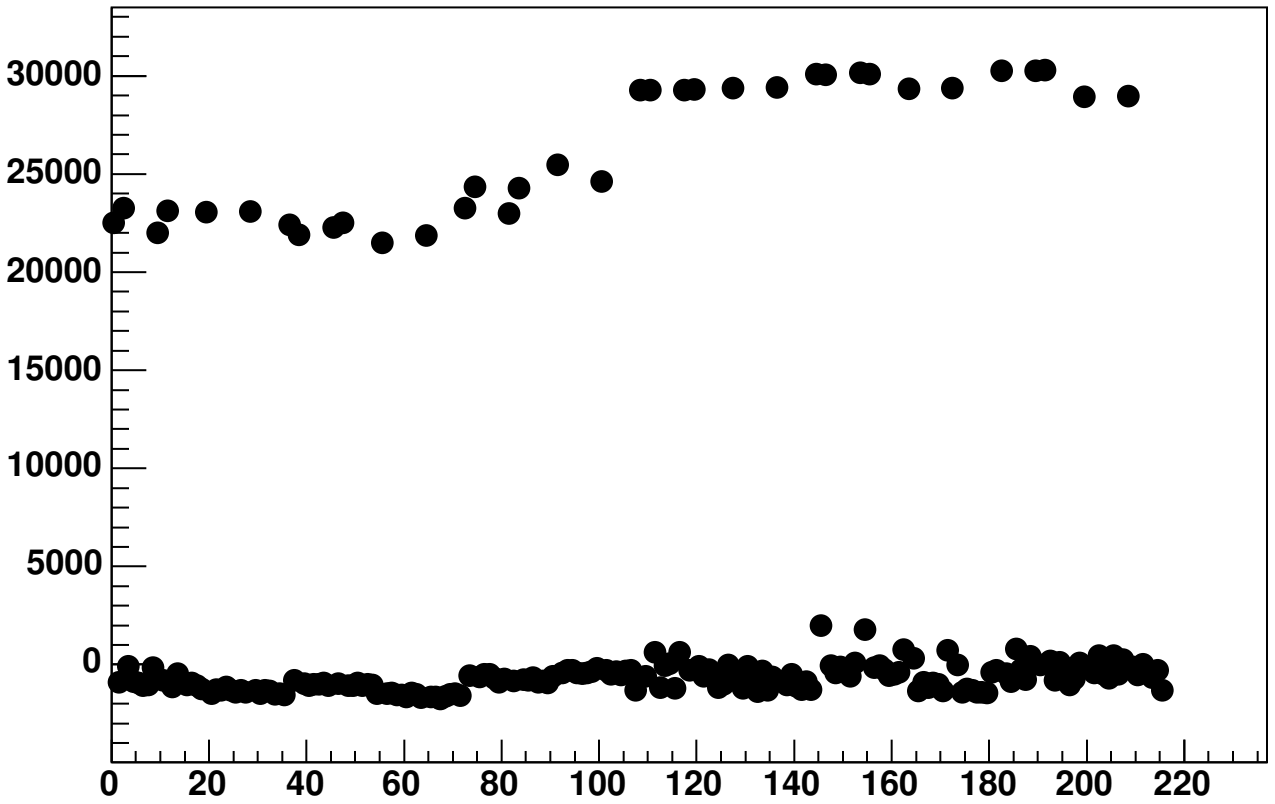
Enable 0, Hold=30, DAC=1250, ADC Mean vs 18*Chip+Chan



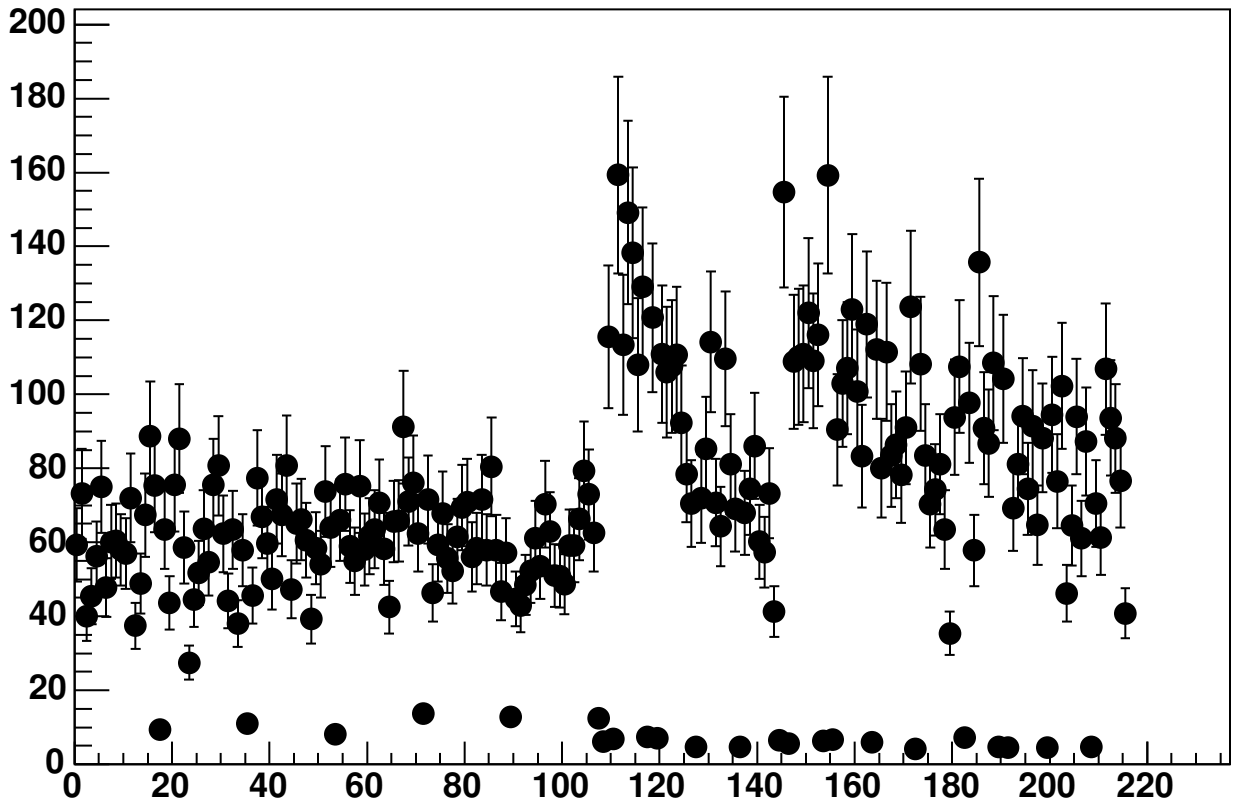
Enable 0, Hold=30, DAC=1250, ADC Noise vs 18*Chip+Chan



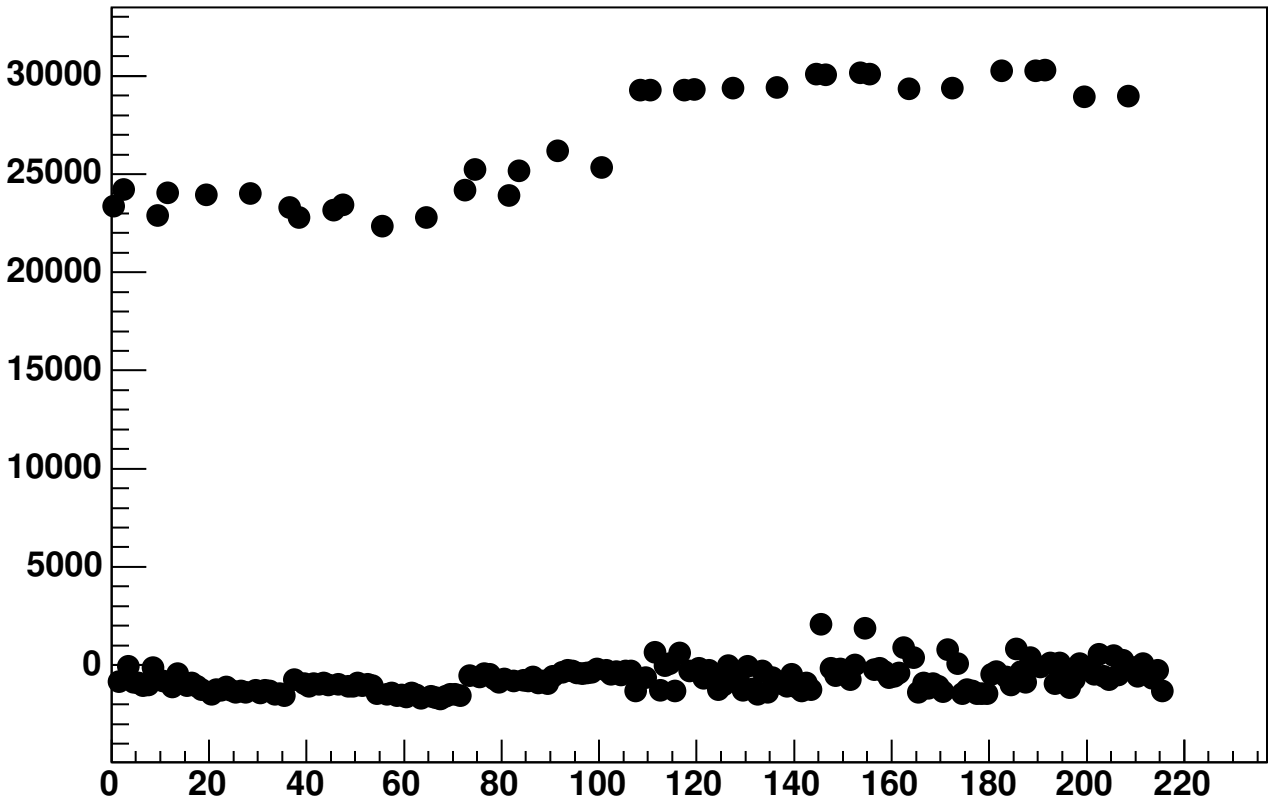
Enable 0, Hold=30, DAC=1300, ADC Mean vs 18*Chip+Chan



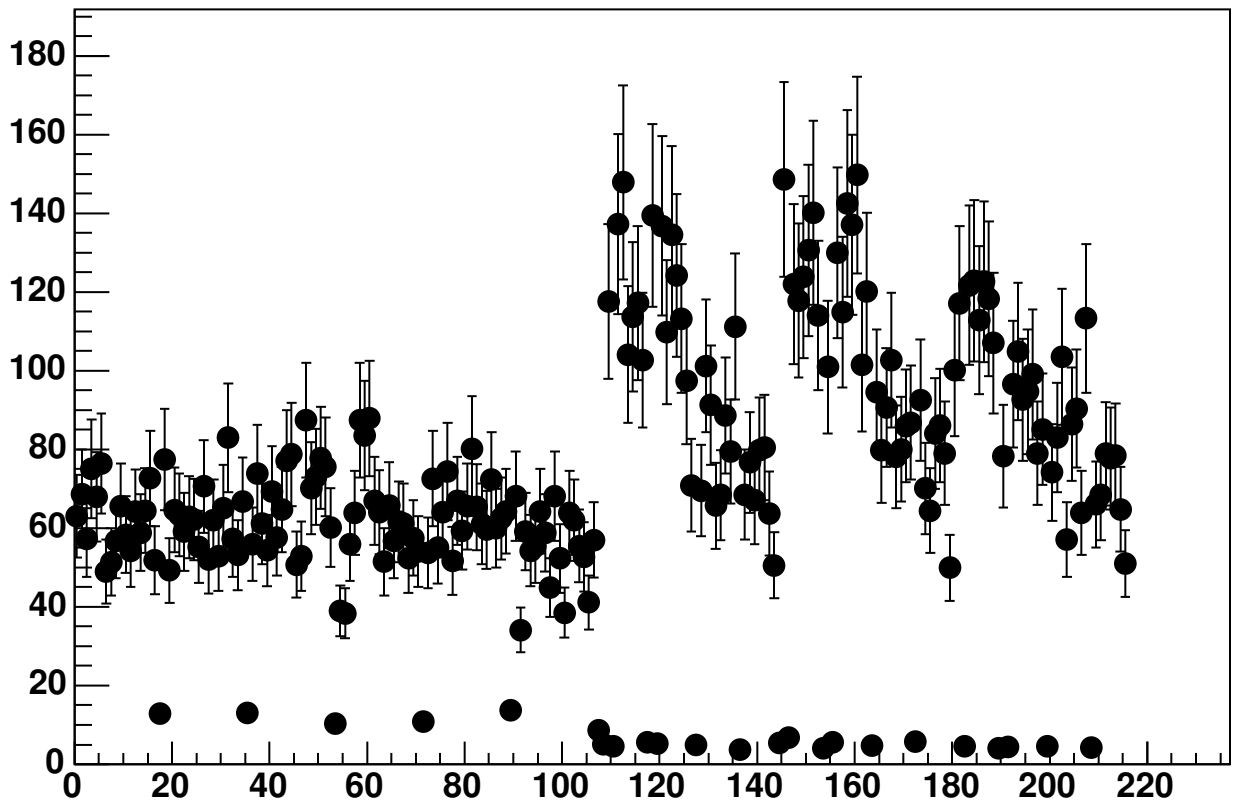
Enable 0, Hold=30, DAC=1300, ADC Noise vs 18*Chip+Chan



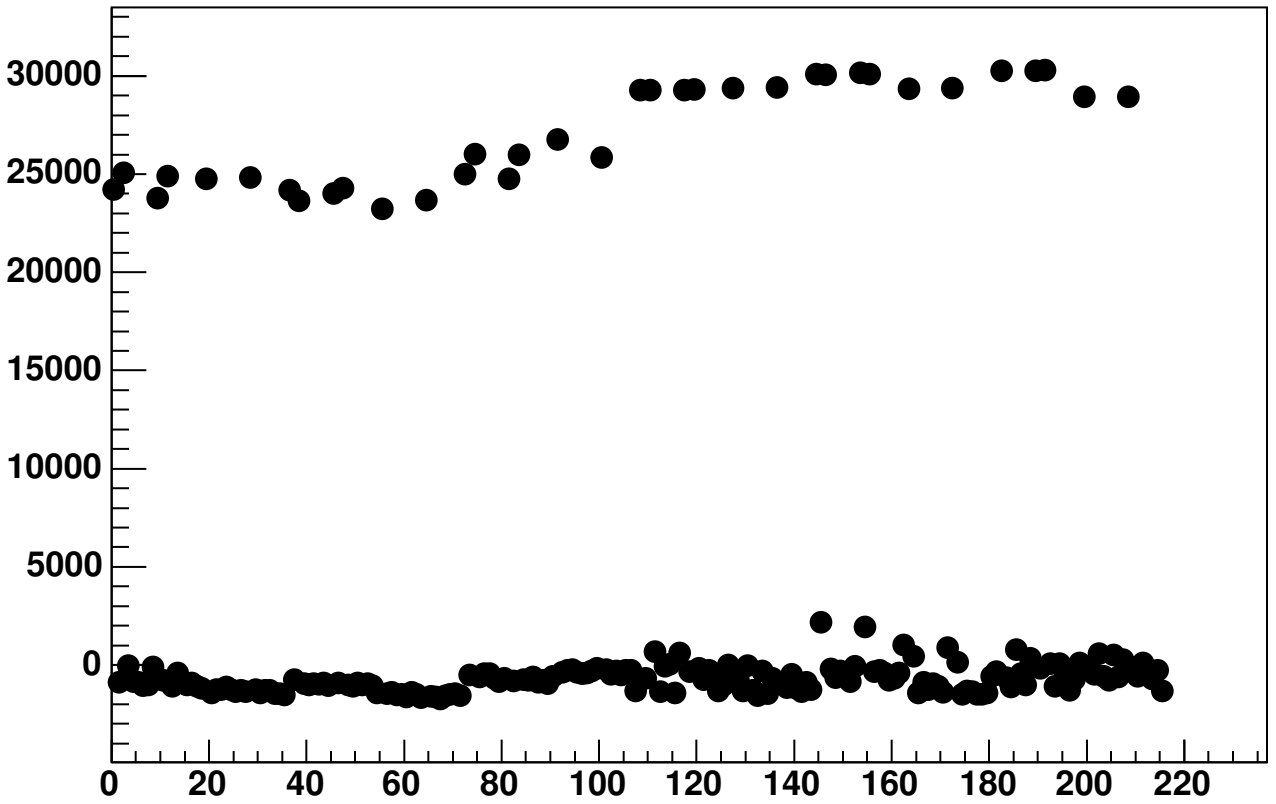
Enable 0, Hold=30, DAC=1350, ADC Mean vs 18*Chip+Chan



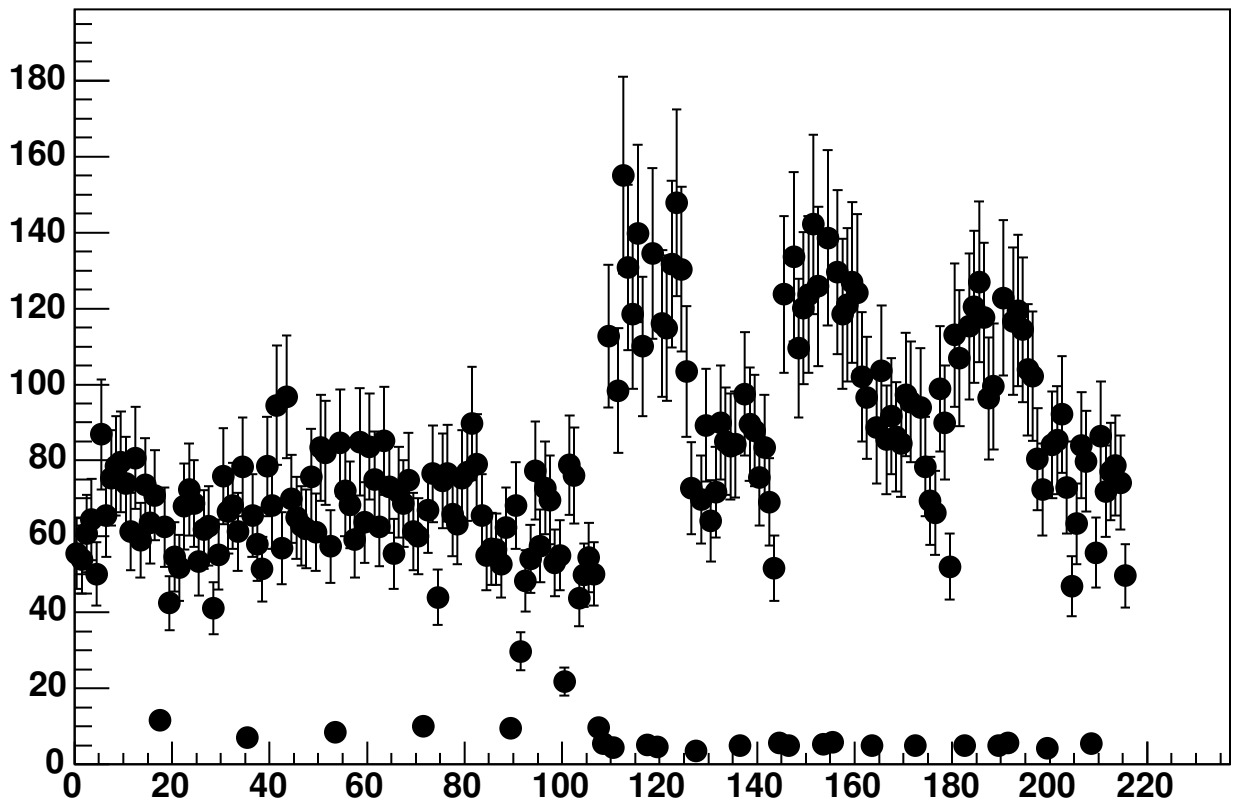
Enable 0, Hold=30, DAC=1350, ADC Noise vs 18*Chip+Chan



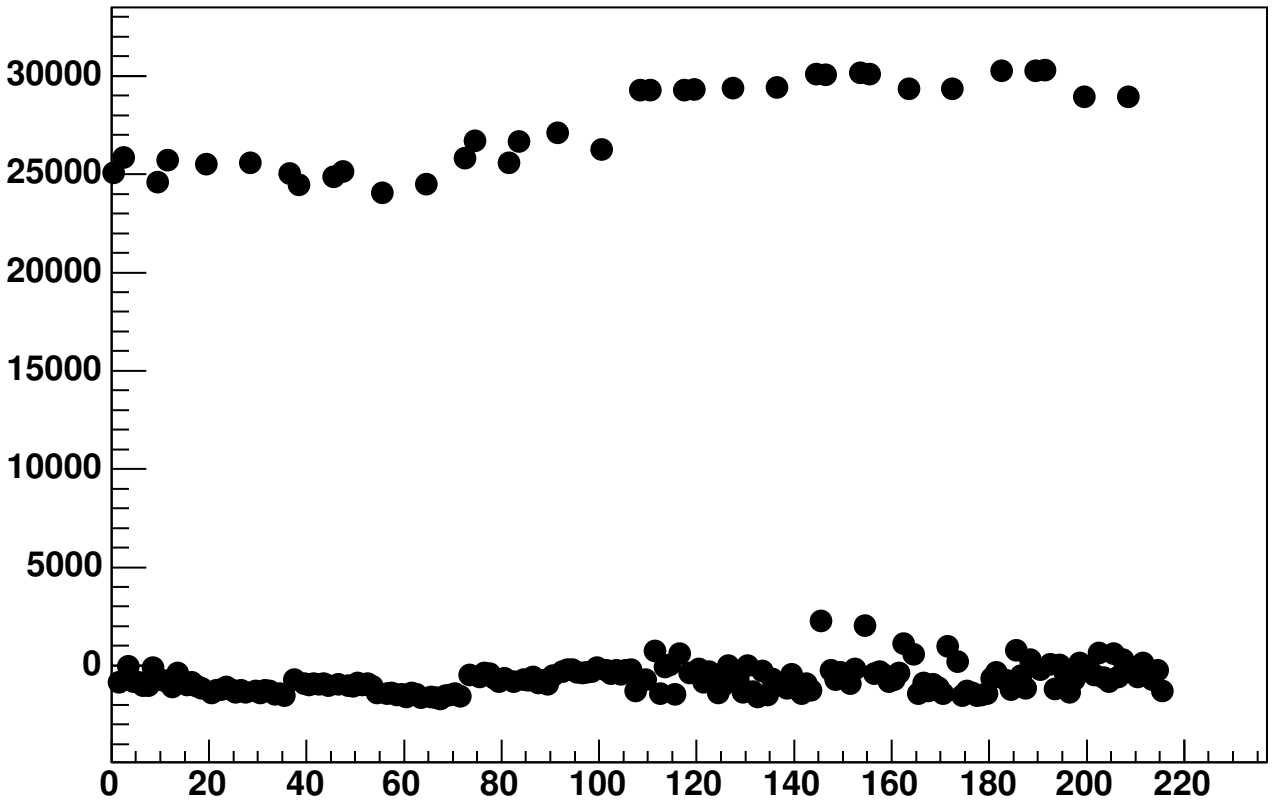
Enable 0, Hold=30, DAC=1400, ADC Mean vs 18*Chip+Chan



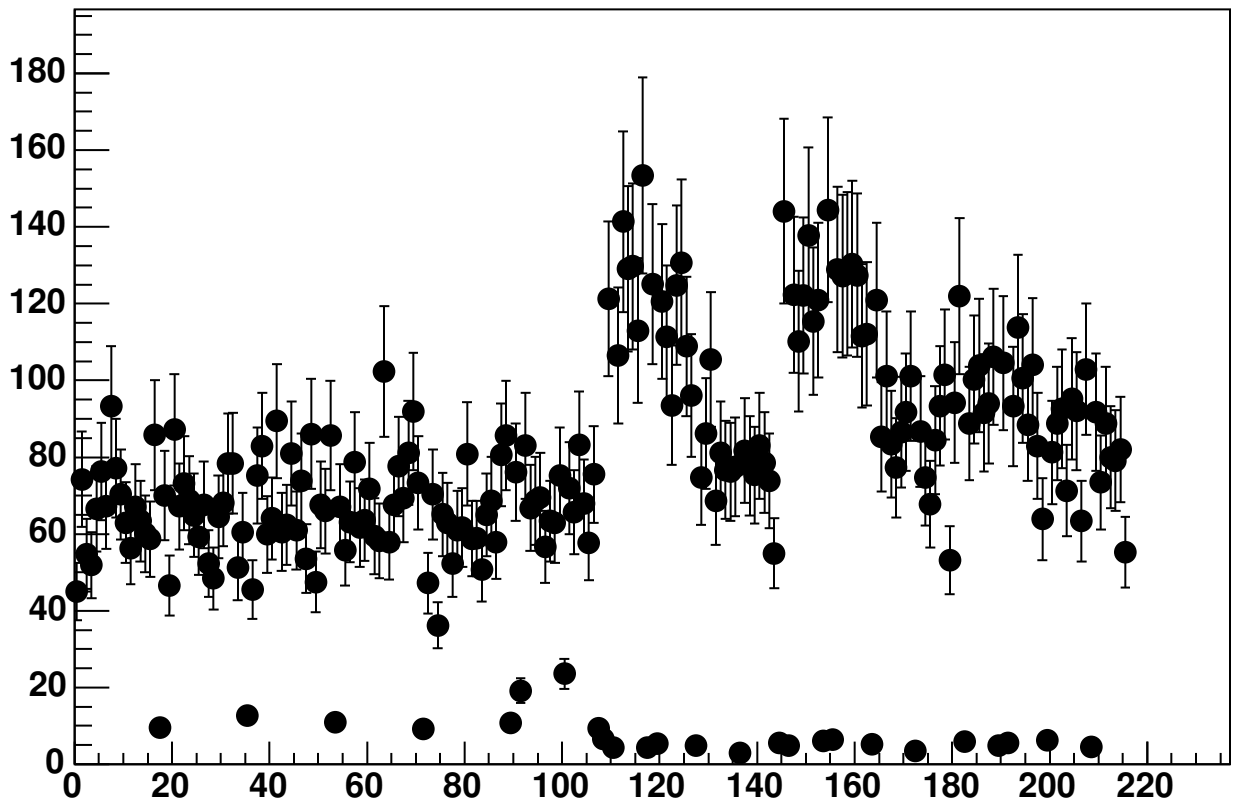
Enable 0, Hold=30, DAC=1400, ADC Noise vs 18*Chip+Chan



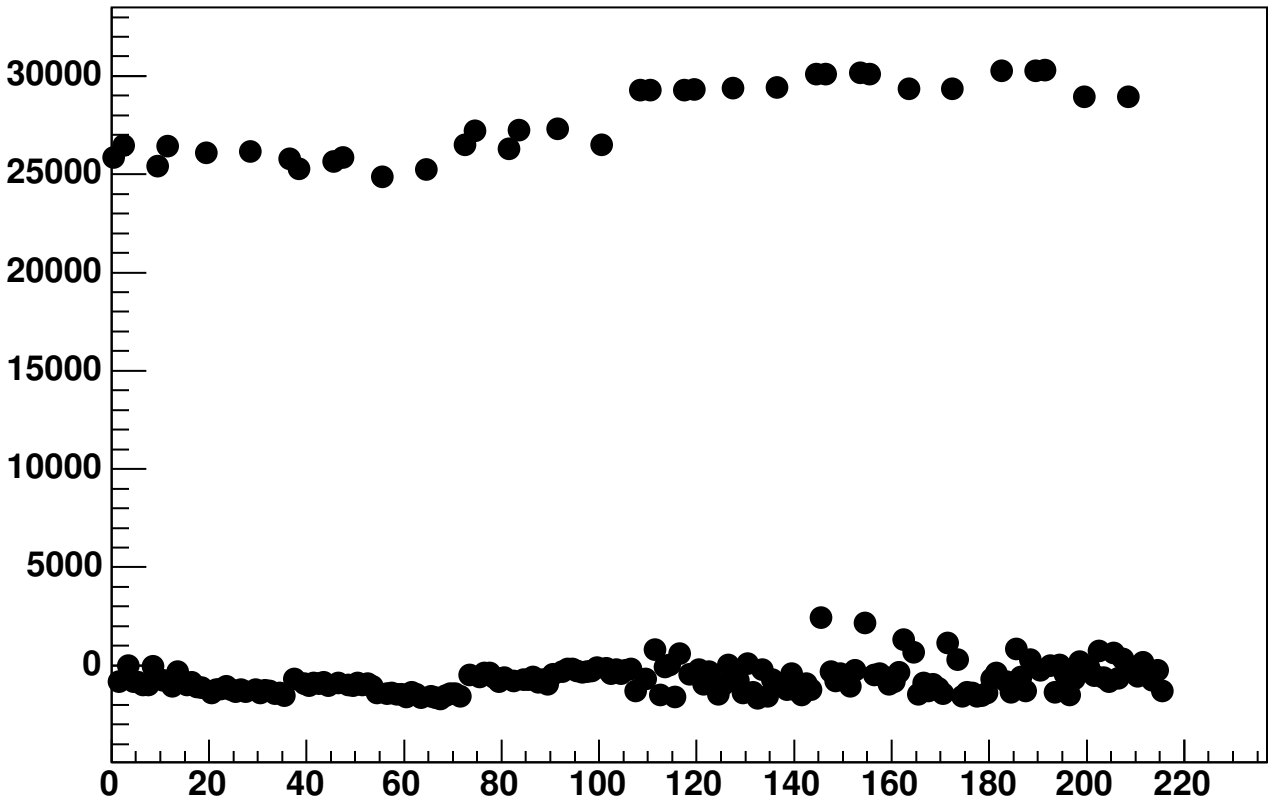
Enable 0, Hold=30, DAC=1450, ADC Mean vs 18*Chip+Chan



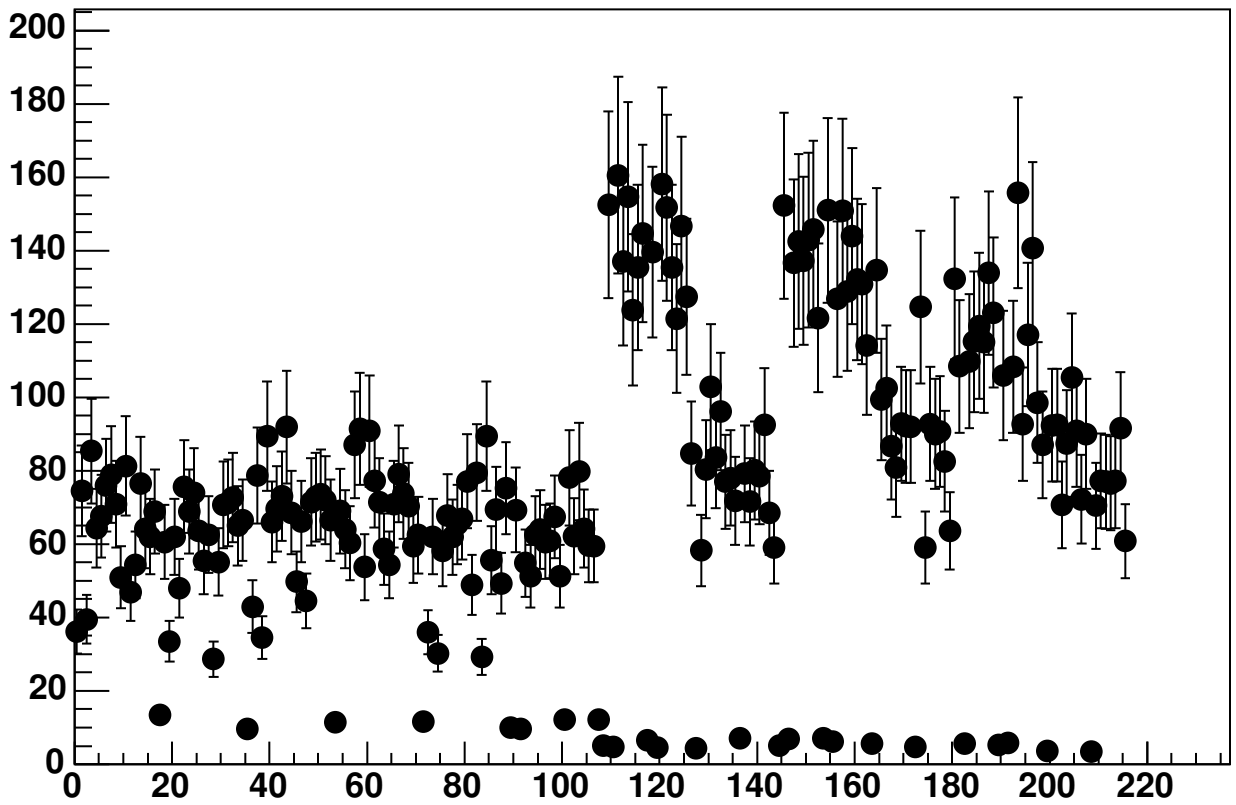
Enable 0, Hold=30, DAC=1450, ADC Noise vs 18*Chip+Chan



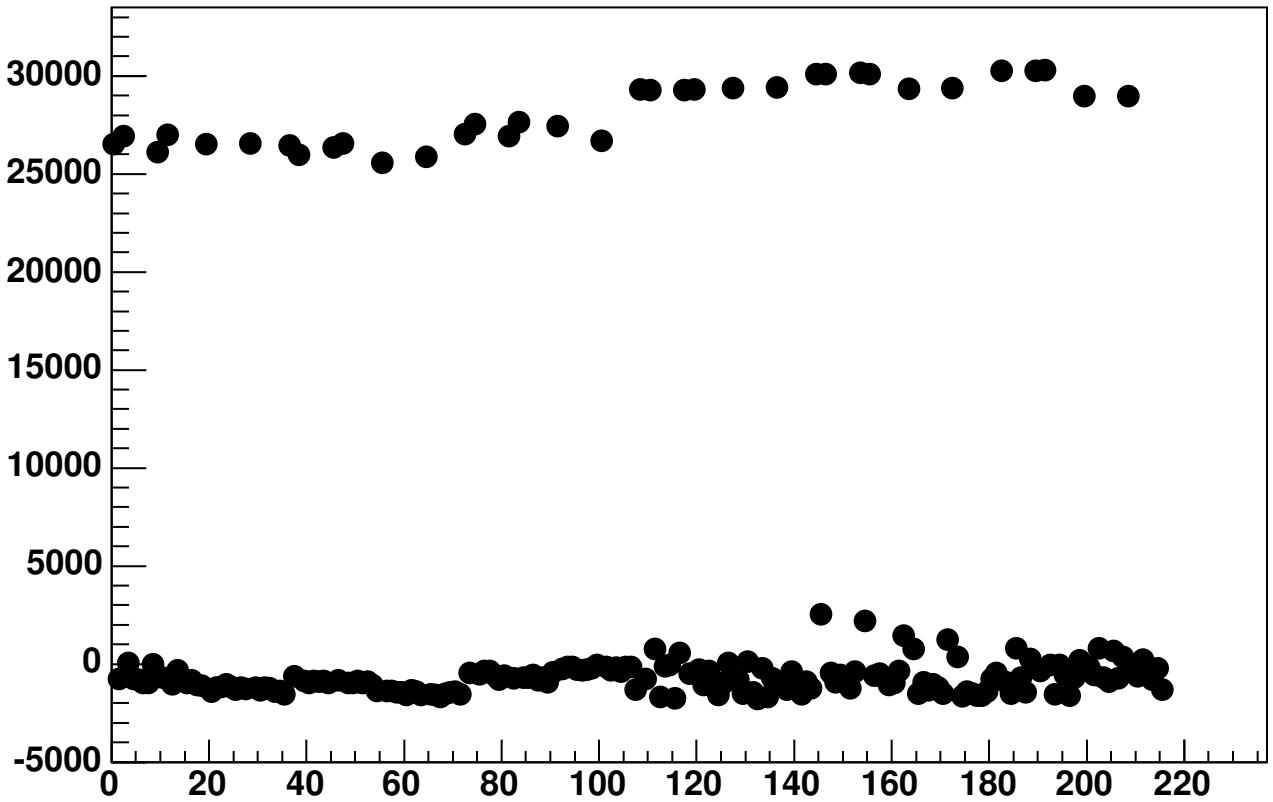
Enable 0, Hold=30, DAC=1500, ADC Mean vs 18*Chip+Chan



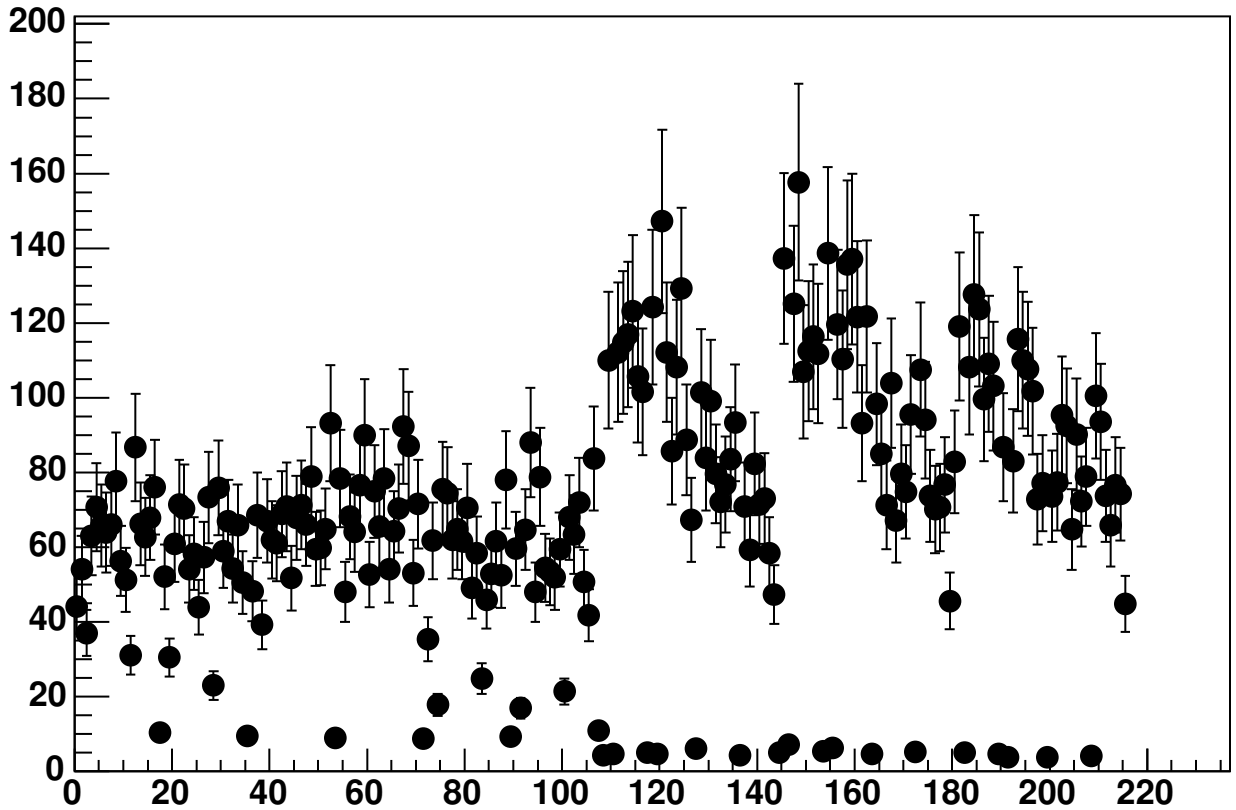
Enable 0, Hold=30, DAC=1500, ADC Noise vs 18*Chip+Chan



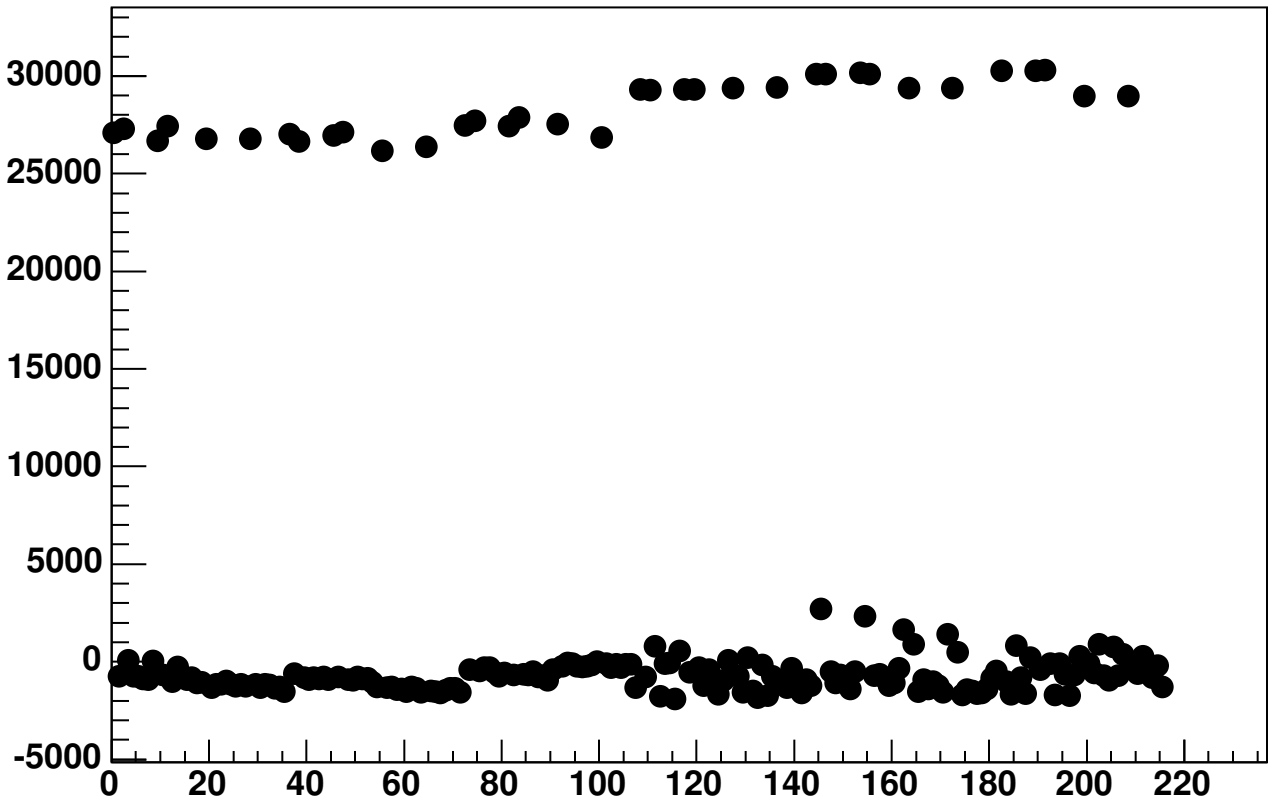
Enable 0, Hold=30, DAC=1550, ADC Mean vs 18*Chip+Chan



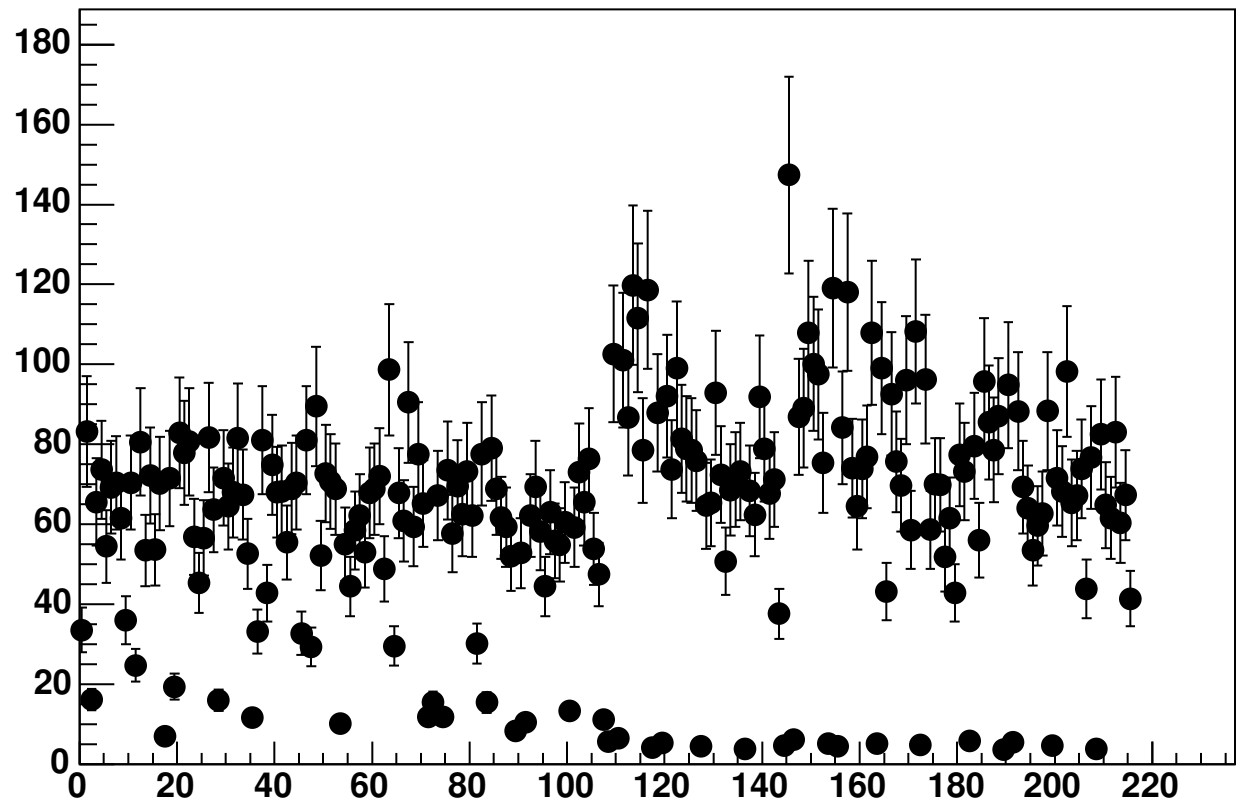
Enable 0, Hold=30, DAC=1550, ADC Noise vs 18*Chip+Chan



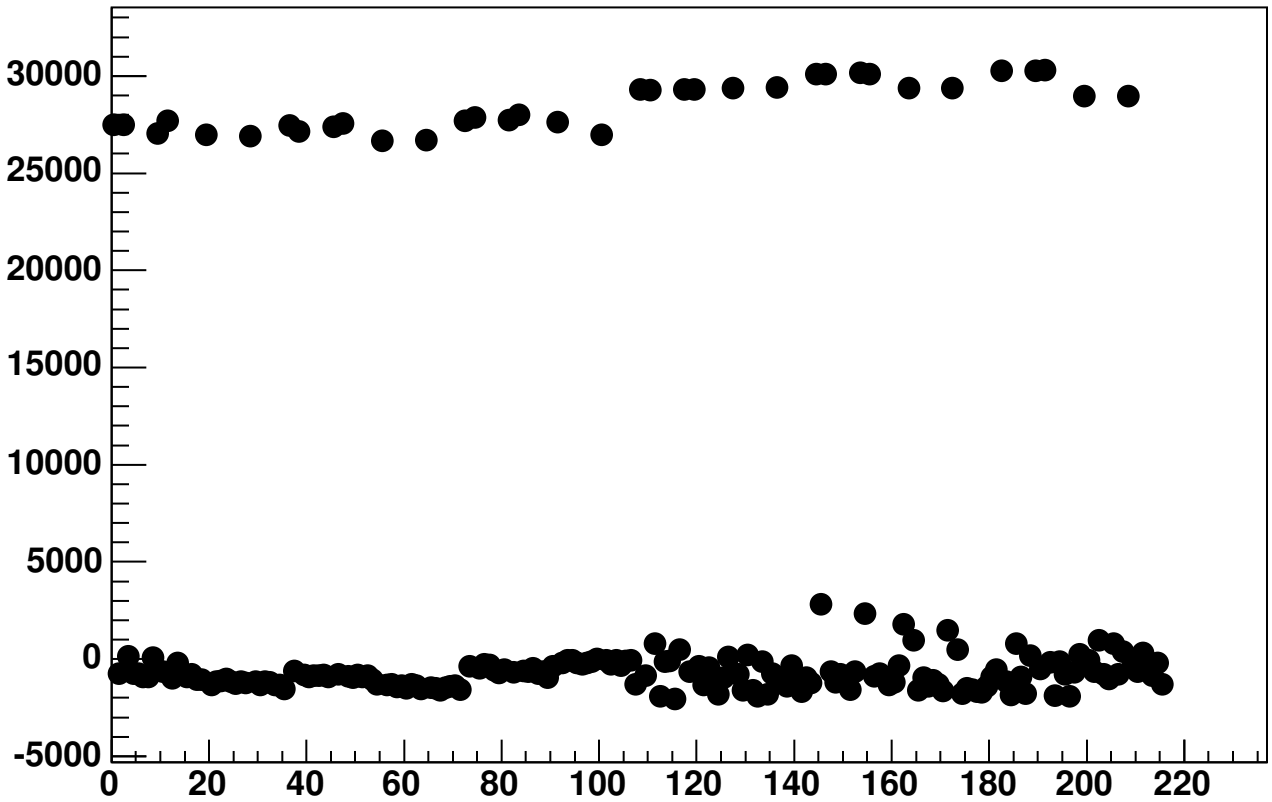
Enable 0, Hold=30, DAC=1600, ADC Mean vs 18*Chip+Chan



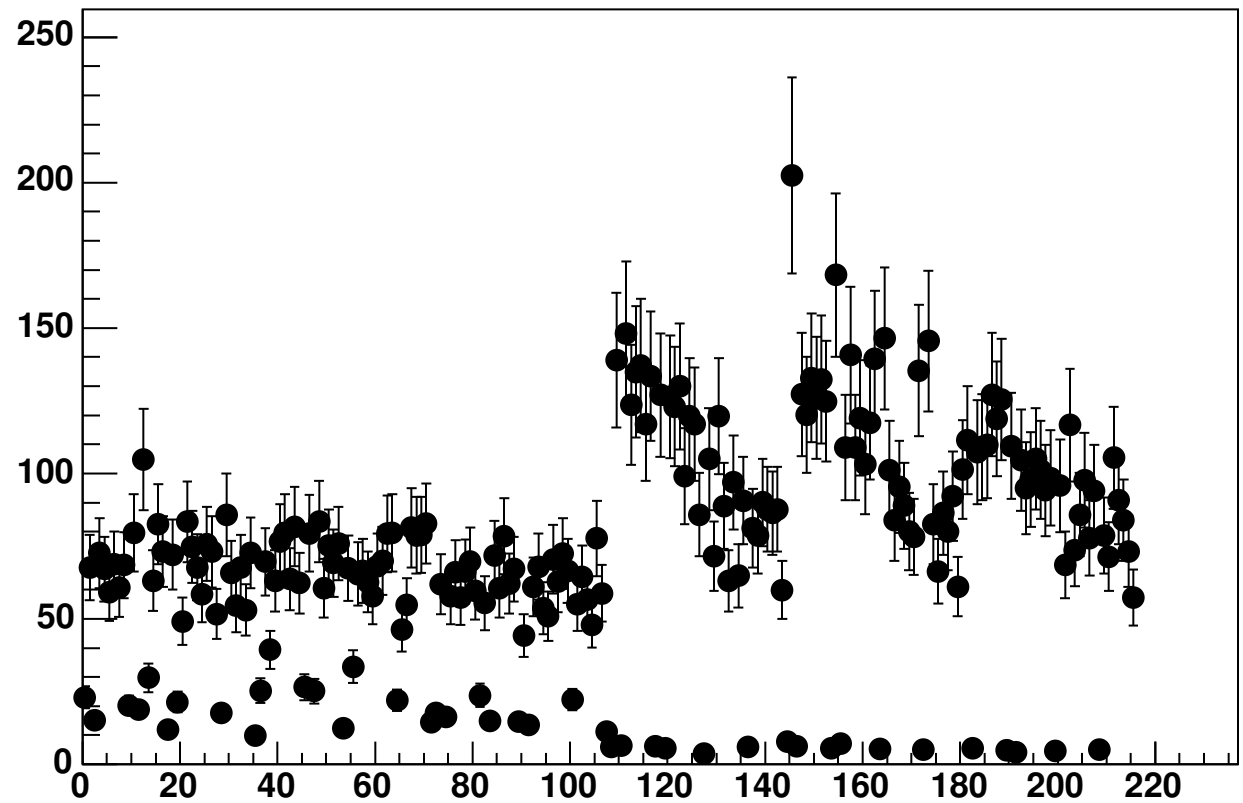
Enable 0, Hold=30, DAC=1600, ADC Noise vs 18*Chip+Chan



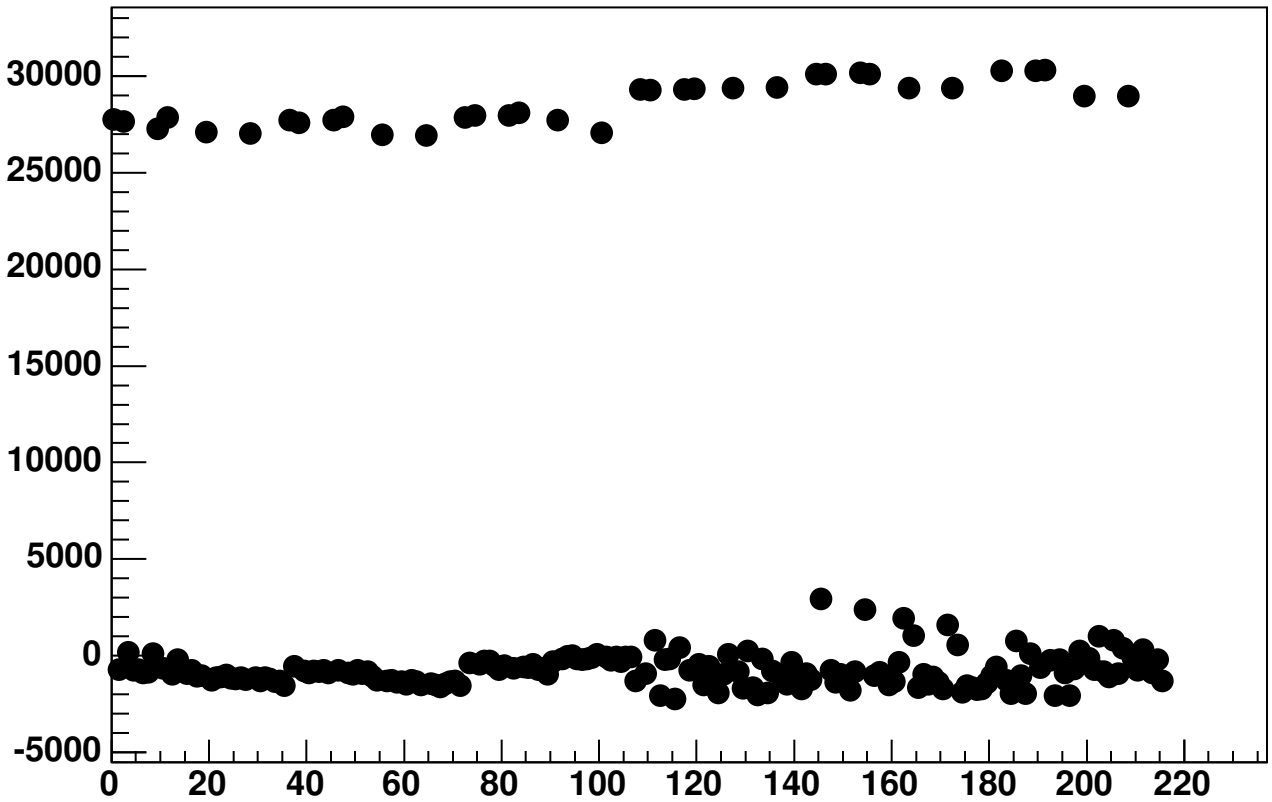
Enable 0, Hold=30, DAC=1650, ADC Mean vs 18*Chip+Chan



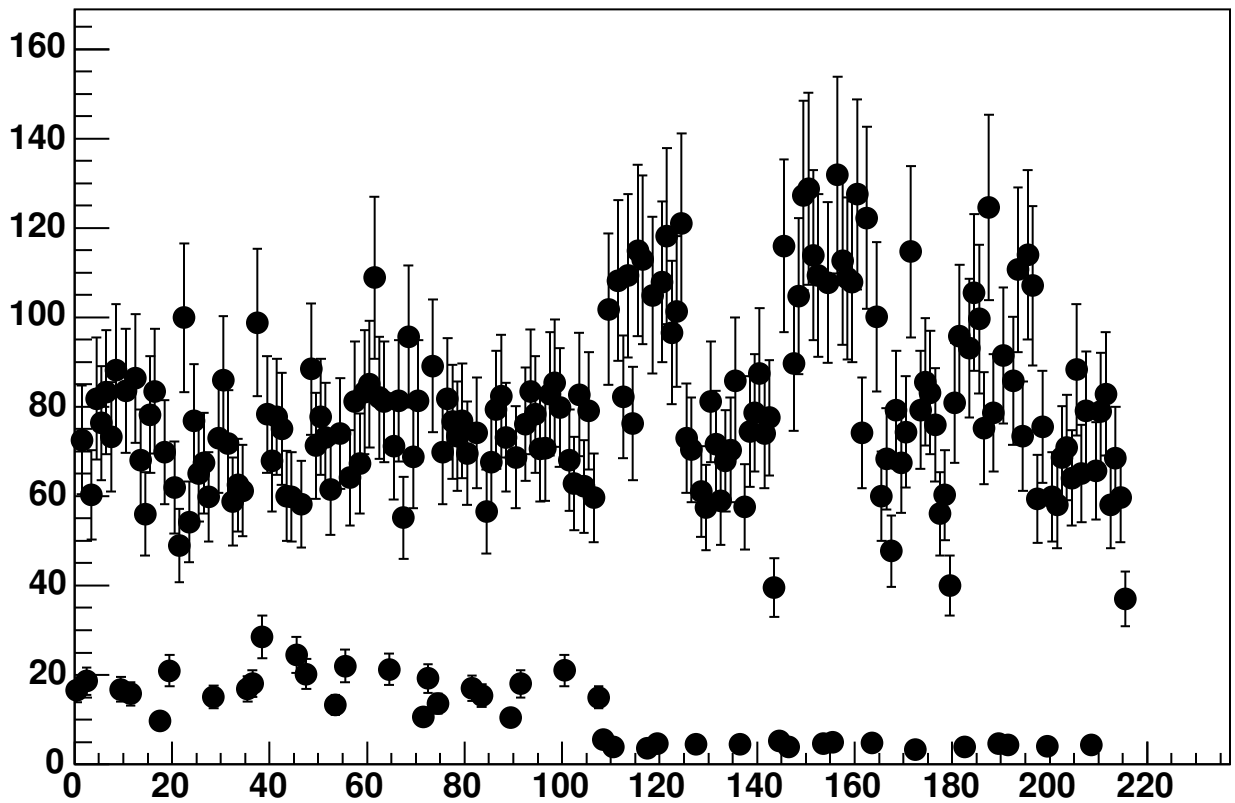
Enable 0, Hold=30, DAC=1650, ADC Noise vs 18*Chip+Chan



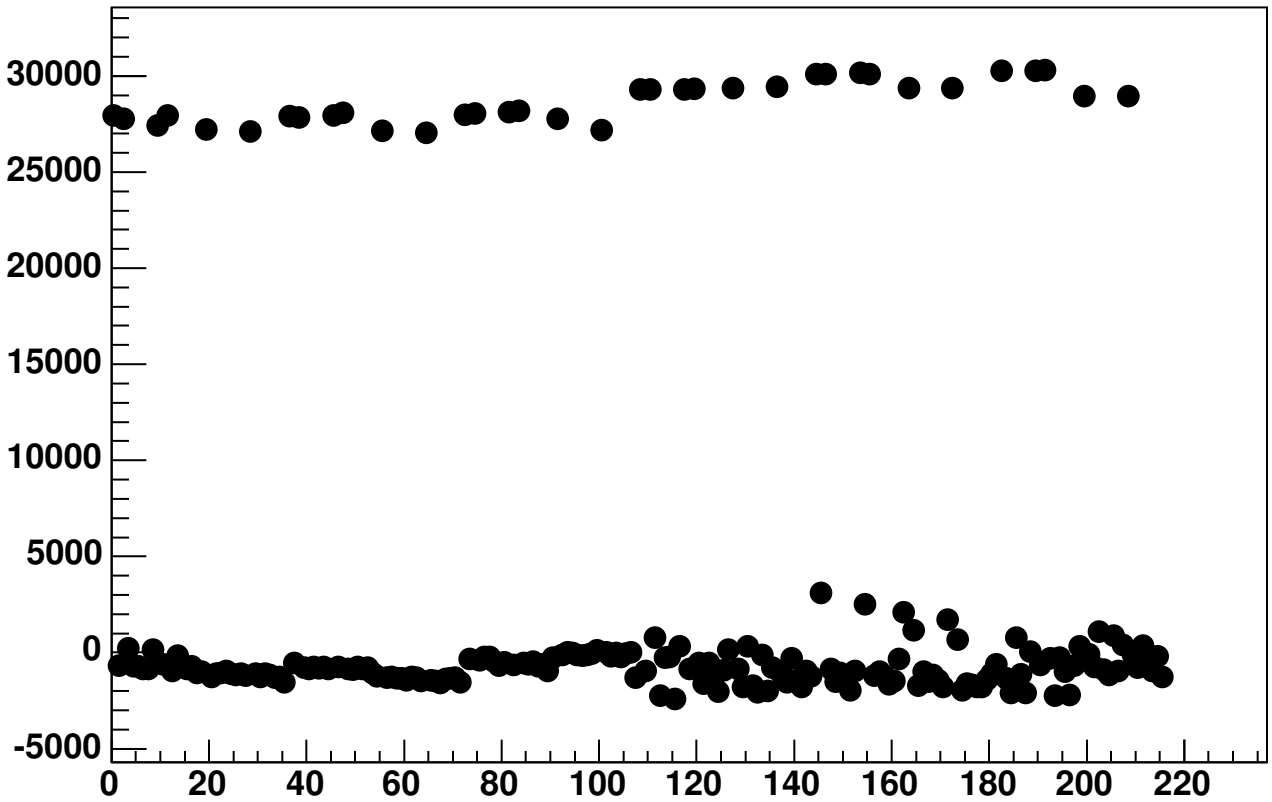
Enable 0, Hold=30, DAC=1700, ADC Mean vs 18*Chip+Chan



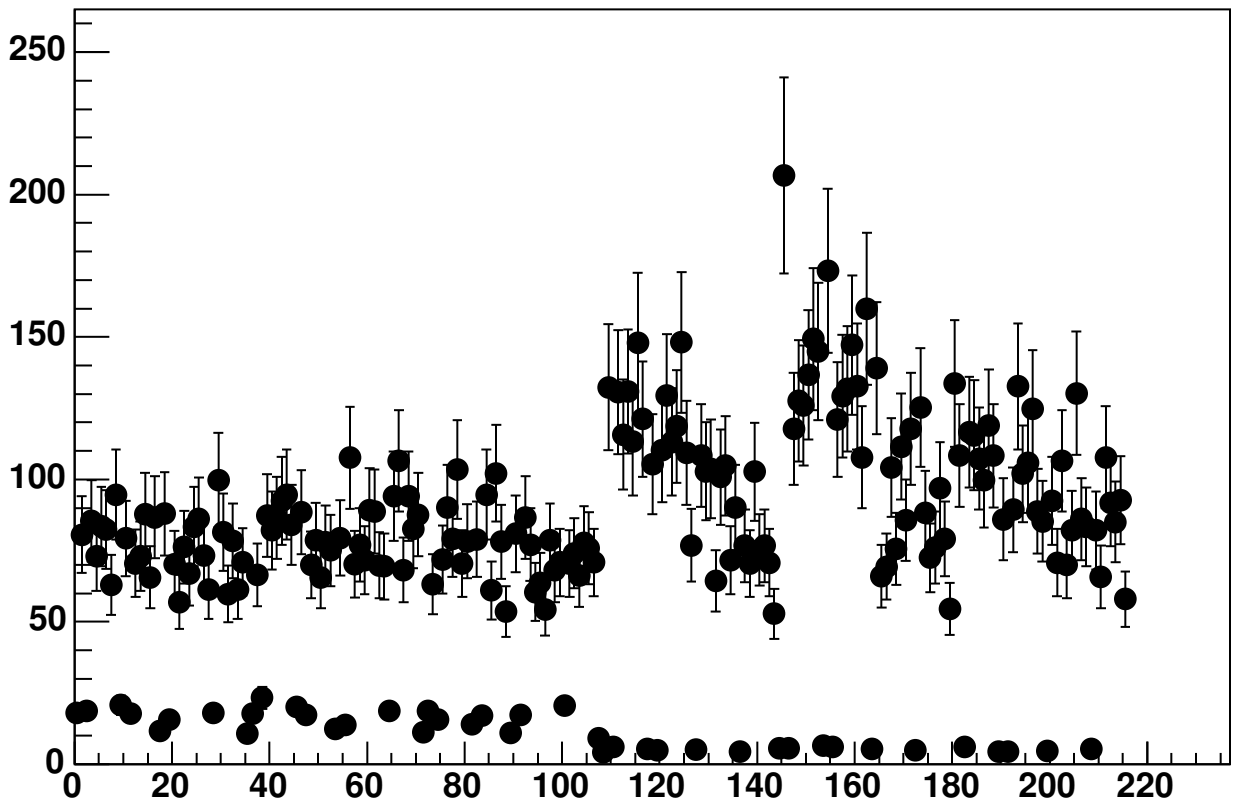
Enable 0, Hold=30, DAC=1700, ADC Noise vs 18*Chip+Chan



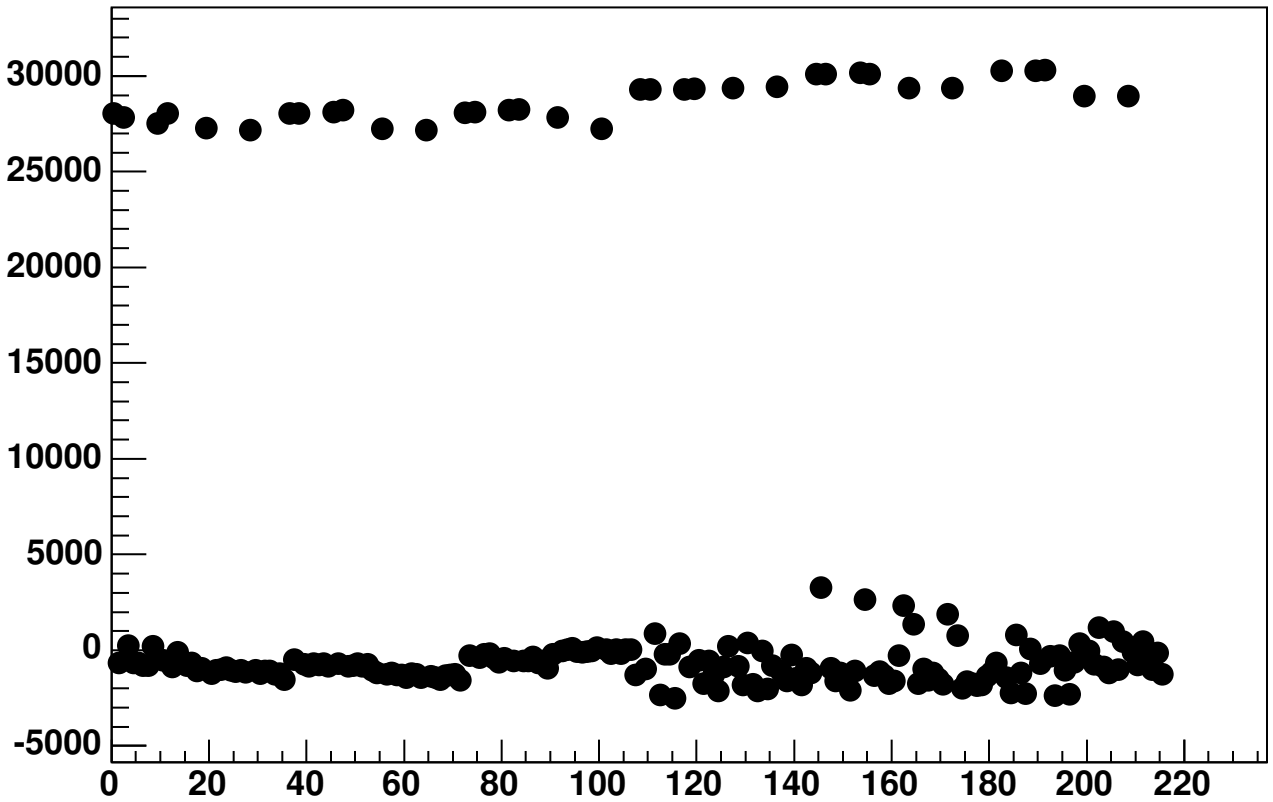
Enable 0, Hold=30, DAC=1750, ADC Mean vs 18*Chip+Chan



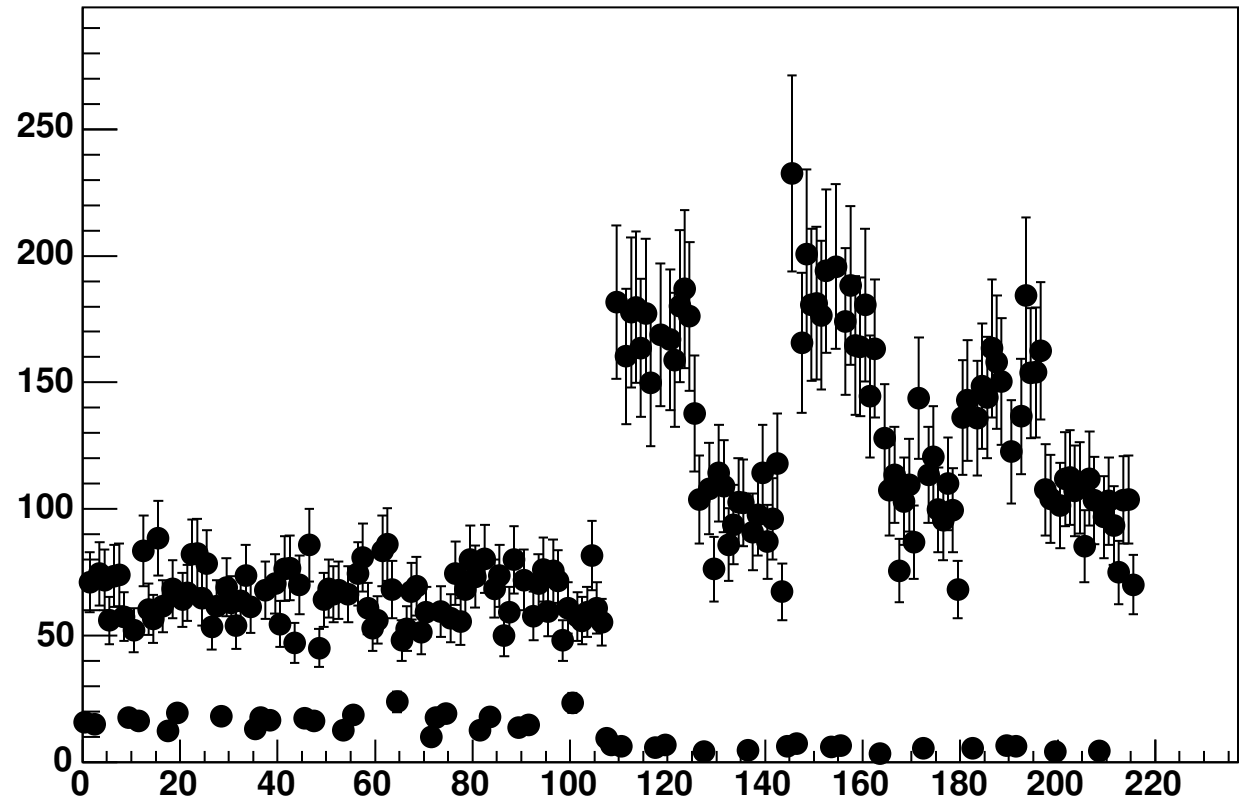
Enable 0, Hold=30, DAC=1750, ADC Noise vs 18*Chip+Chan



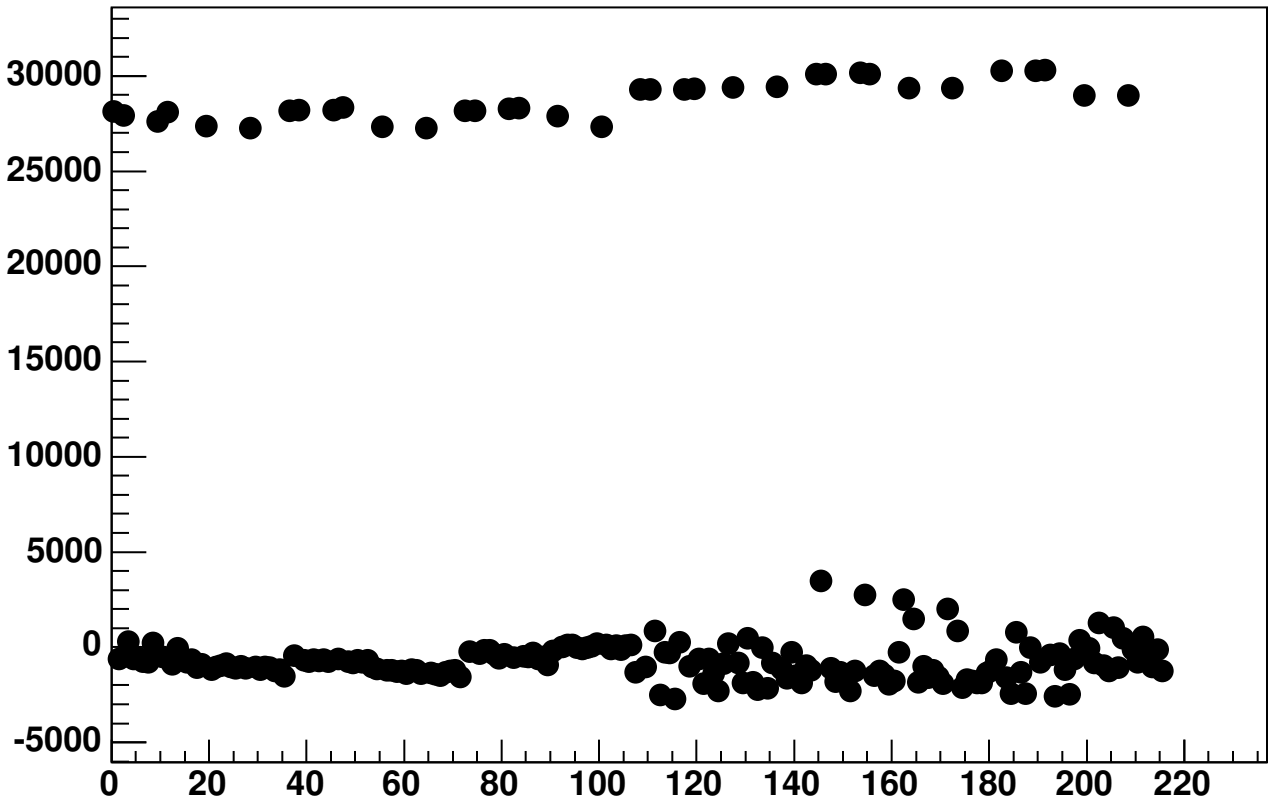
Enable 0, Hold=30, DAC=1800, ADC Mean vs 18*Chip+Chan



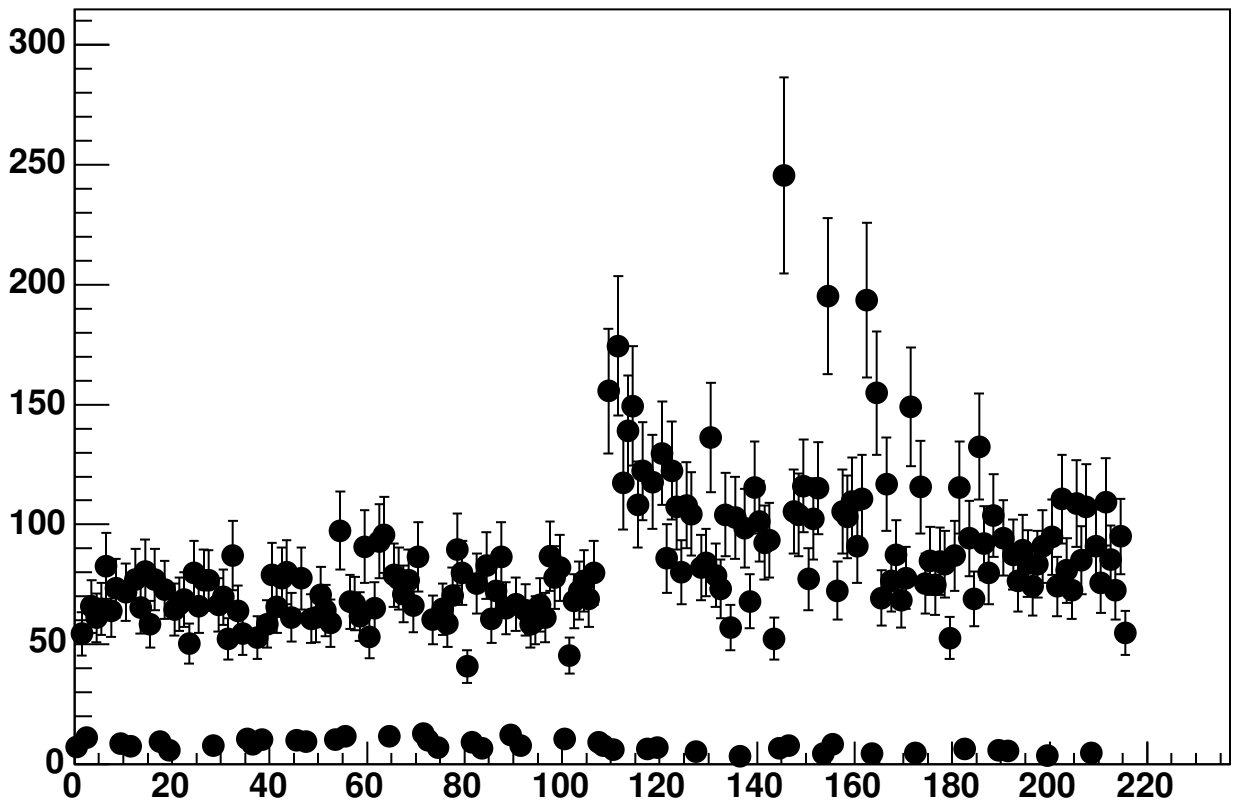
Enable 0, Hold=30, DAC=1800, ADC Noise vs 18*Chip+Chan



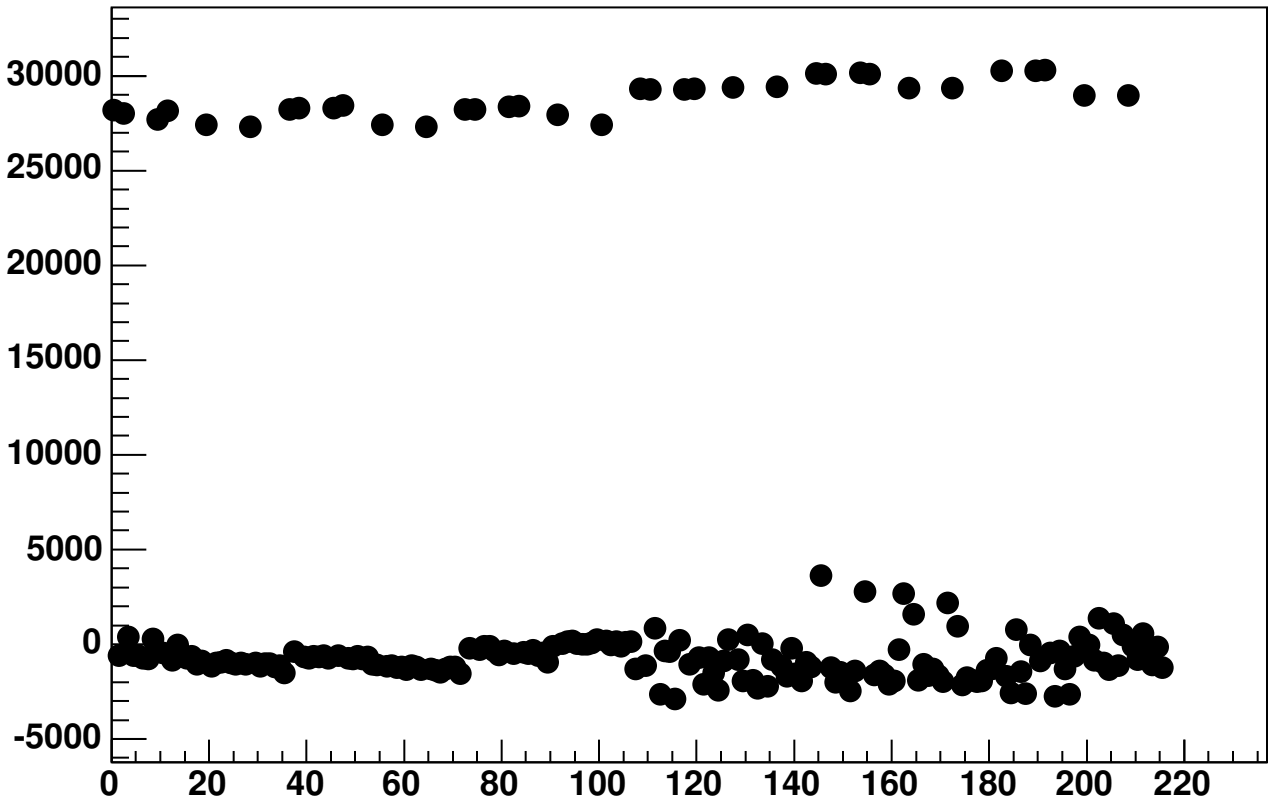
Enable 0, Hold=30, DAC=1850, ADC Mean vs 18*Chip+Chan



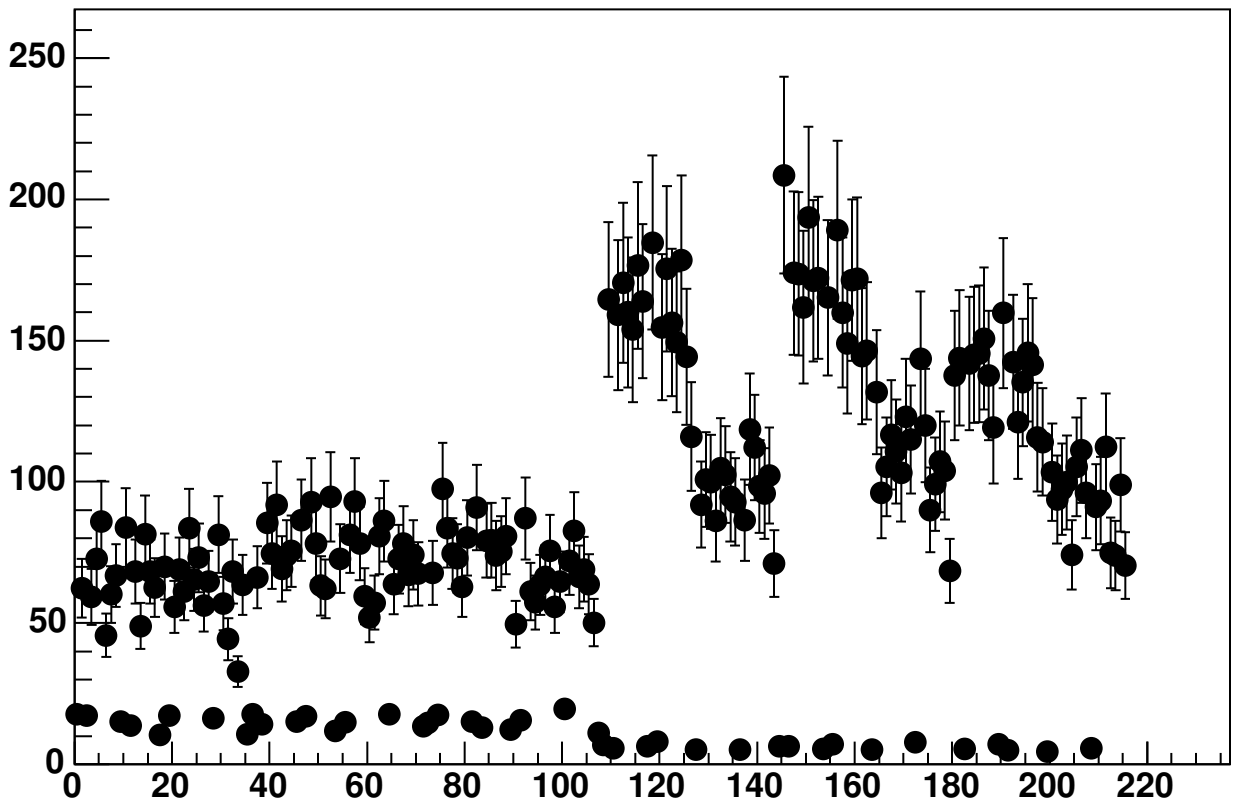
Enable 0, Hold=30, DAC=1850, ADC Noise vs 18*Chip+Chan



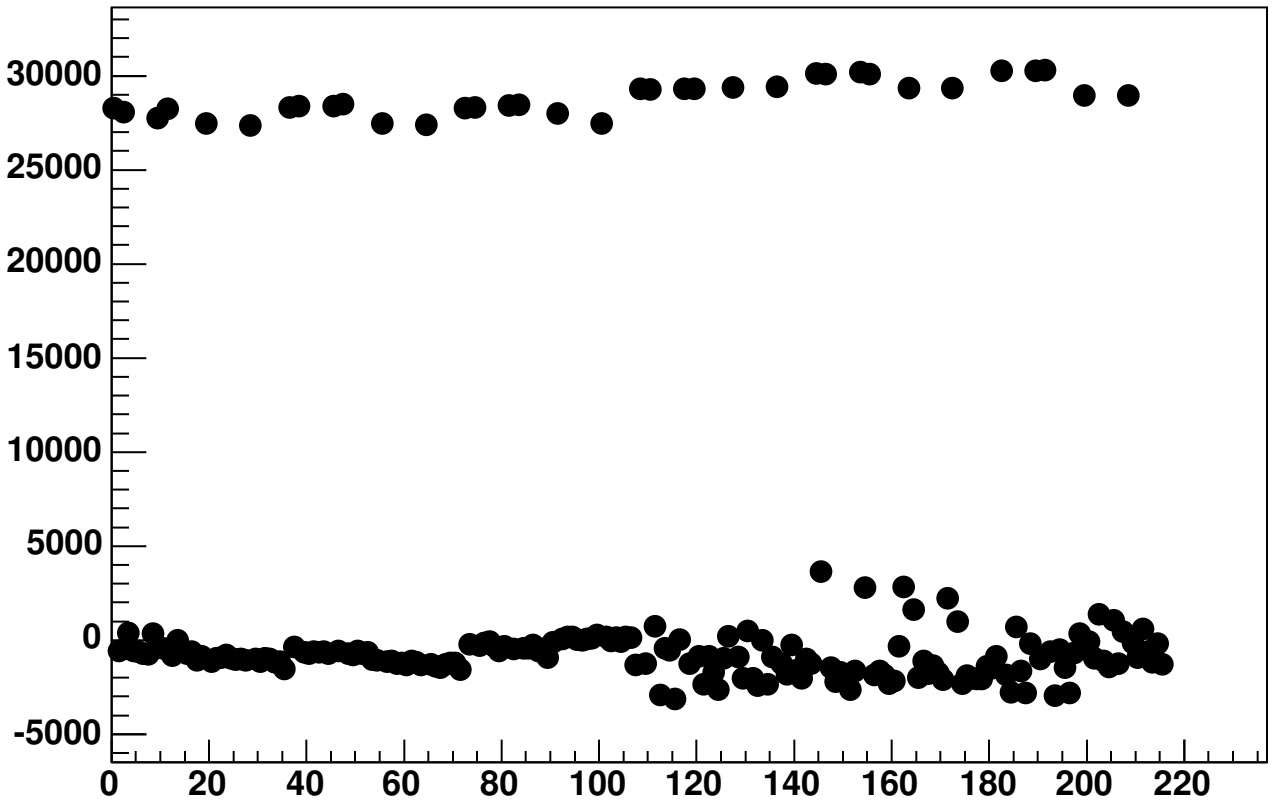
Enable 0, Hold=30, DAC=1900, ADC Mean vs 18*Chip+Chan



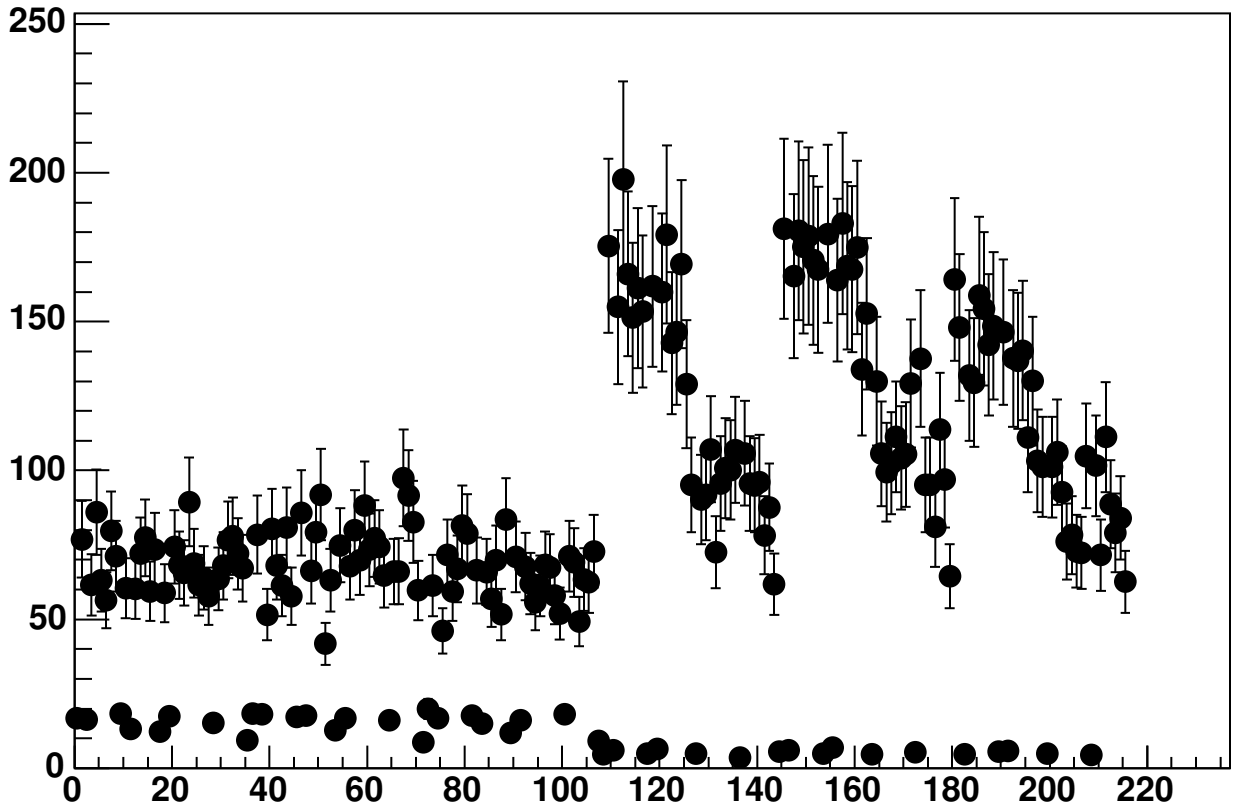
Enable 0, Hold=30, DAC=1900, ADC Noise vs 18*Chip+Chan



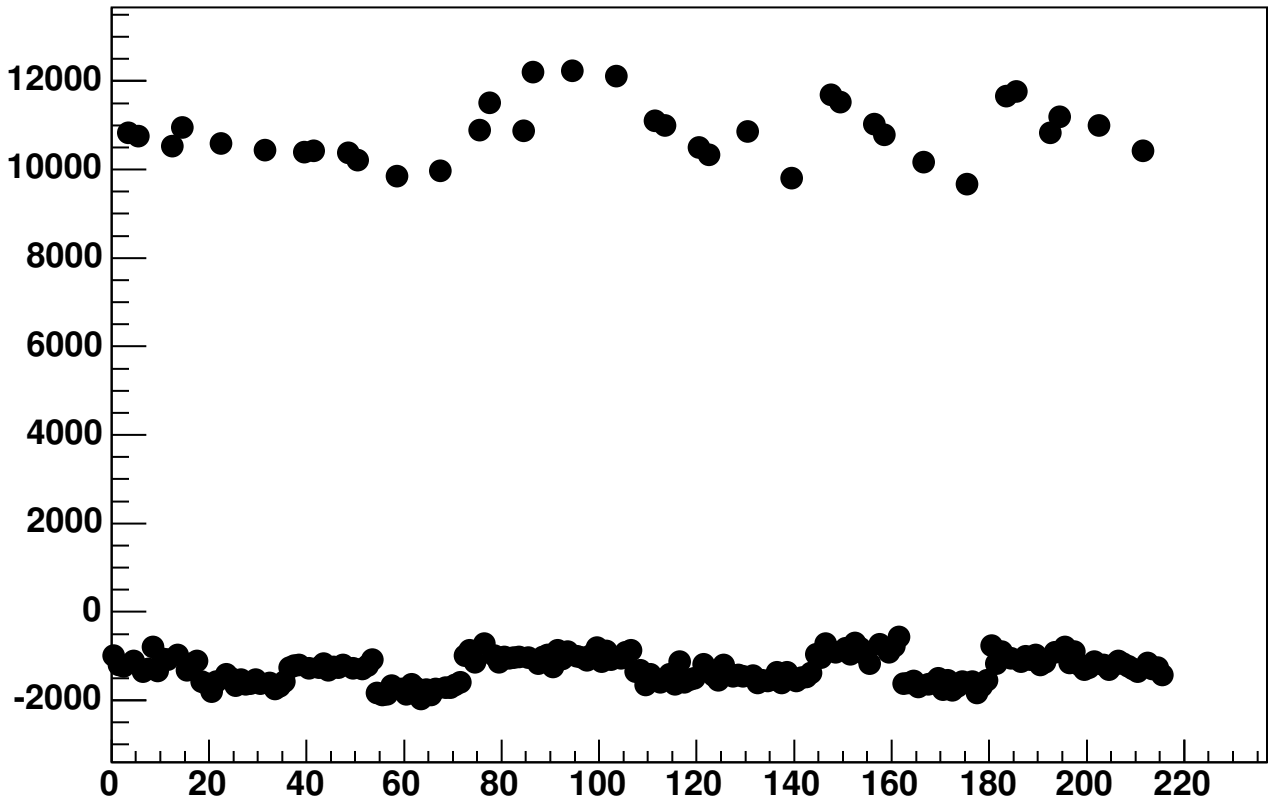
Enable 0, Hold=30, DAC=1950, ADC Mean vs 18*Chip+Chan



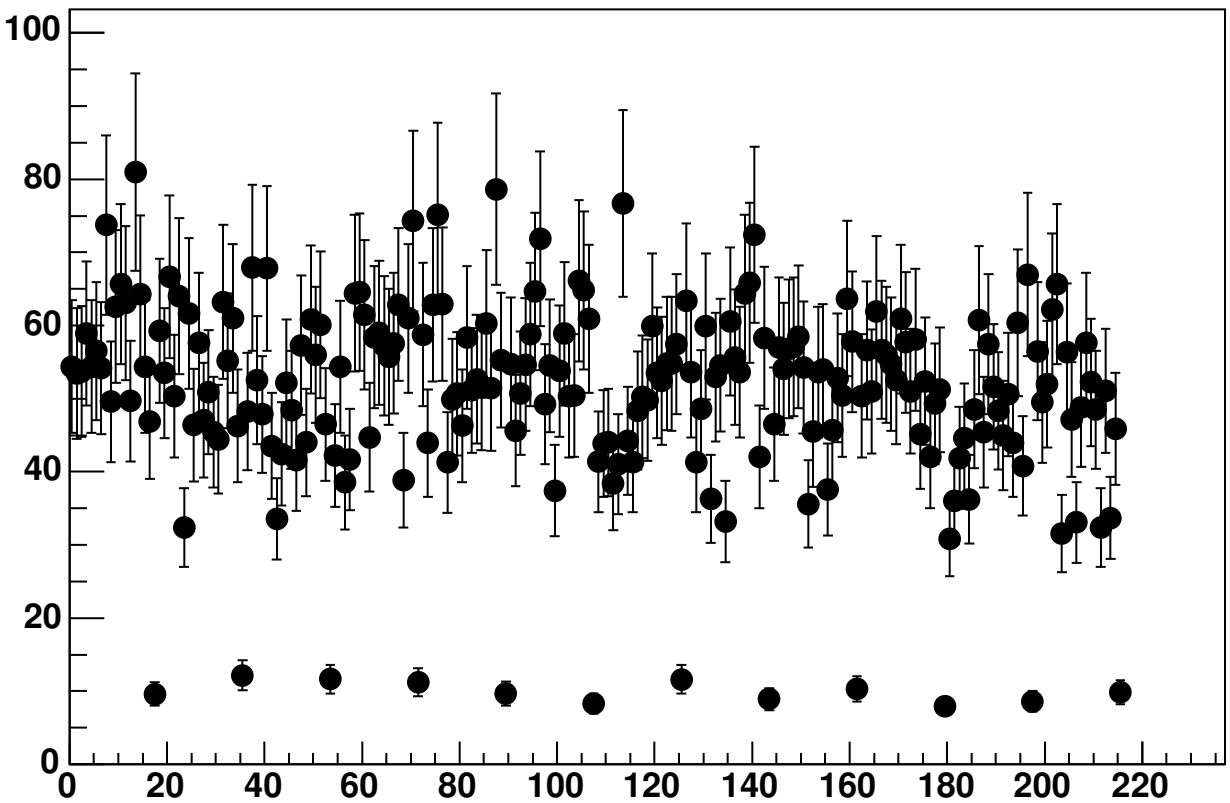
Enable 0, Hold=30, DAC=1950, ADC Noise vs 18*Chip+Chan



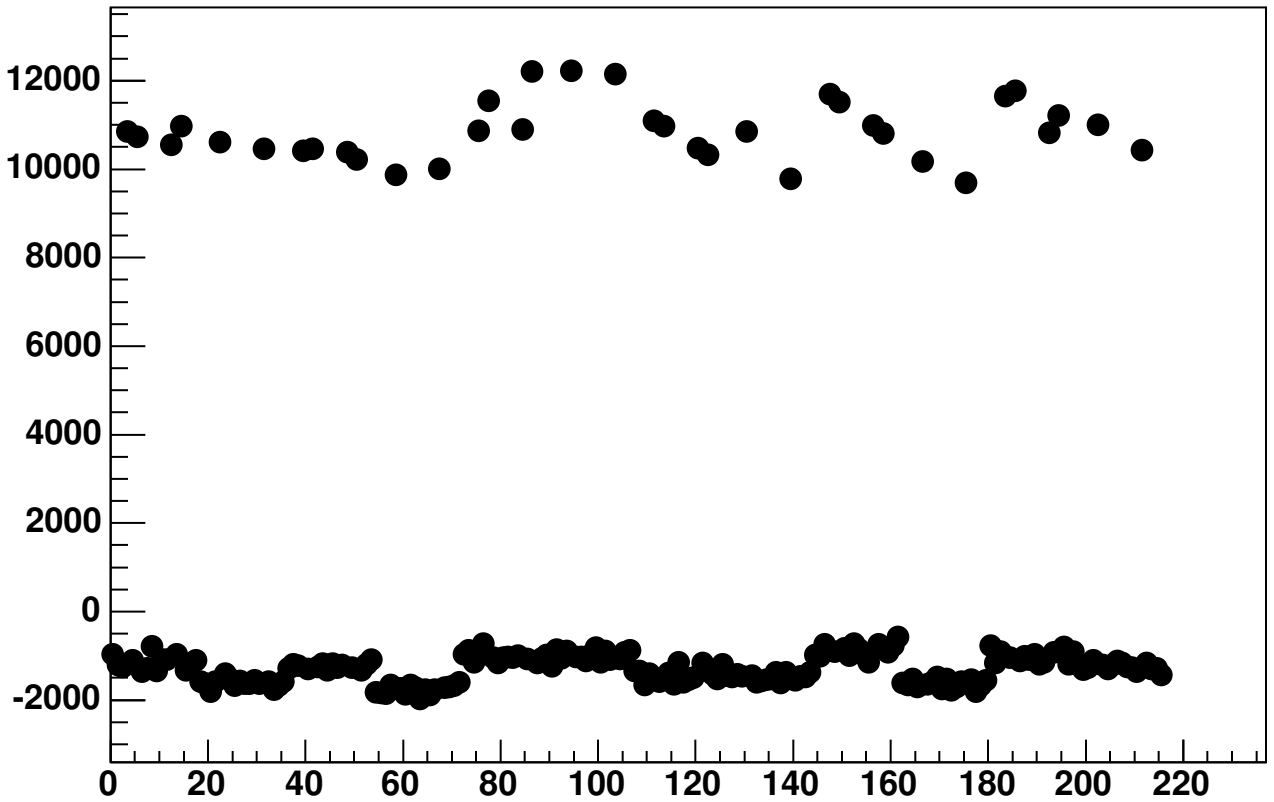
Enable 1, Hold=30, DAC=0, ADC Mean vs 18*Chip+Chan



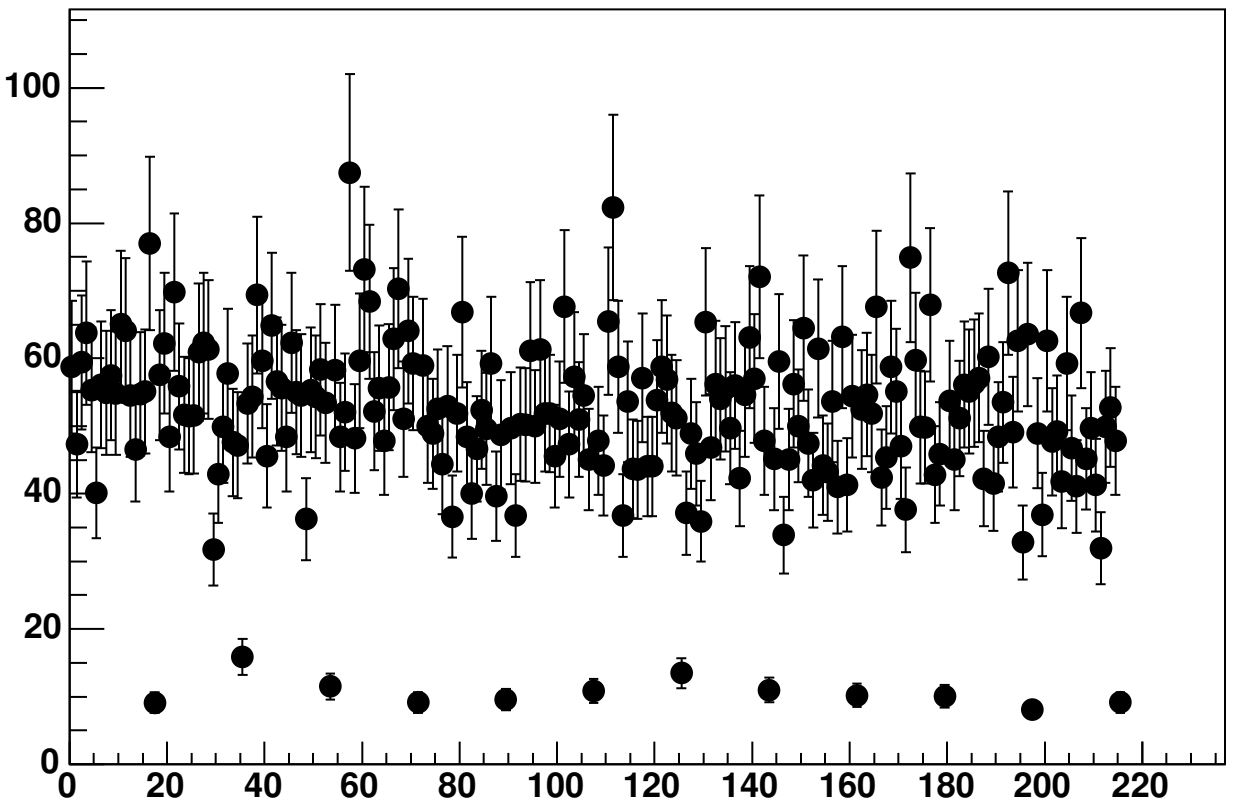
Enable 1, Hold=30, DAC=0, ADC Noise vs 18*Chip+Chan



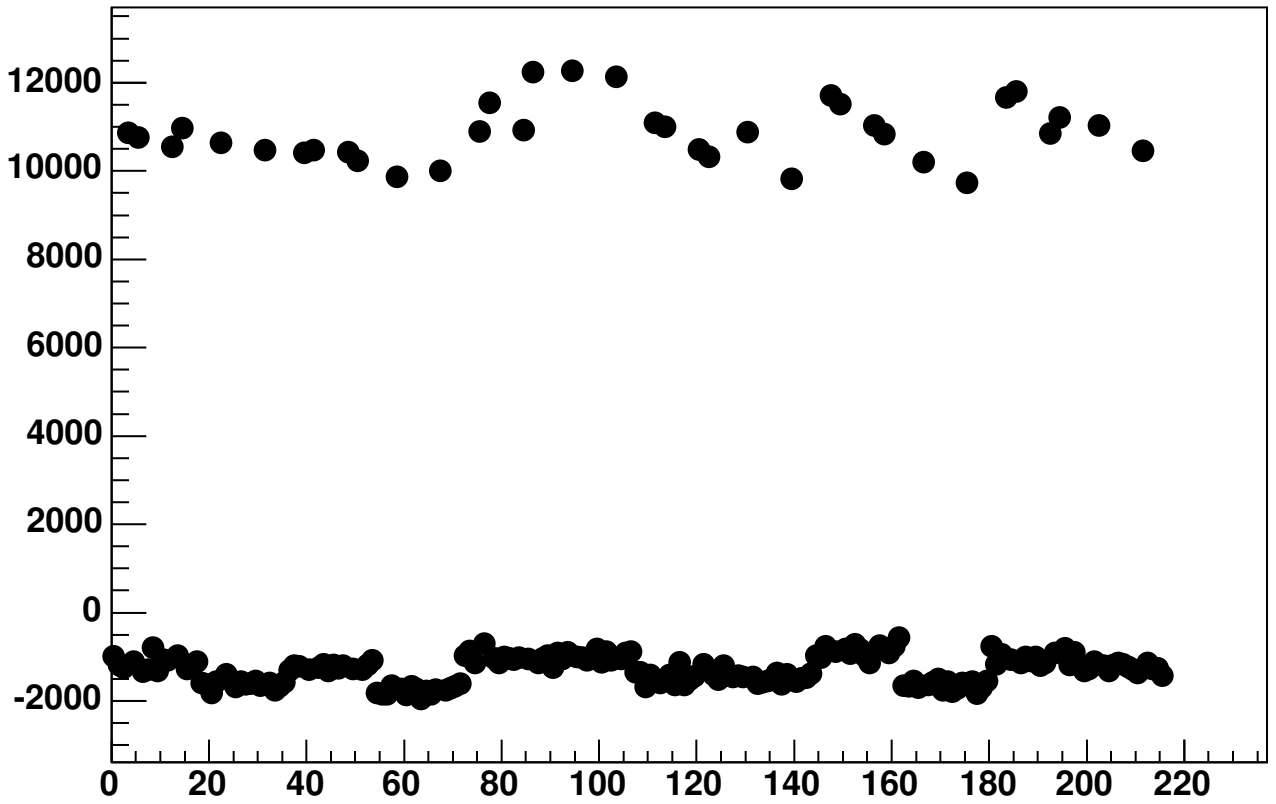
Enable 1, Hold=30, DAC=50, ADC Mean vs 18*Chip+Chan



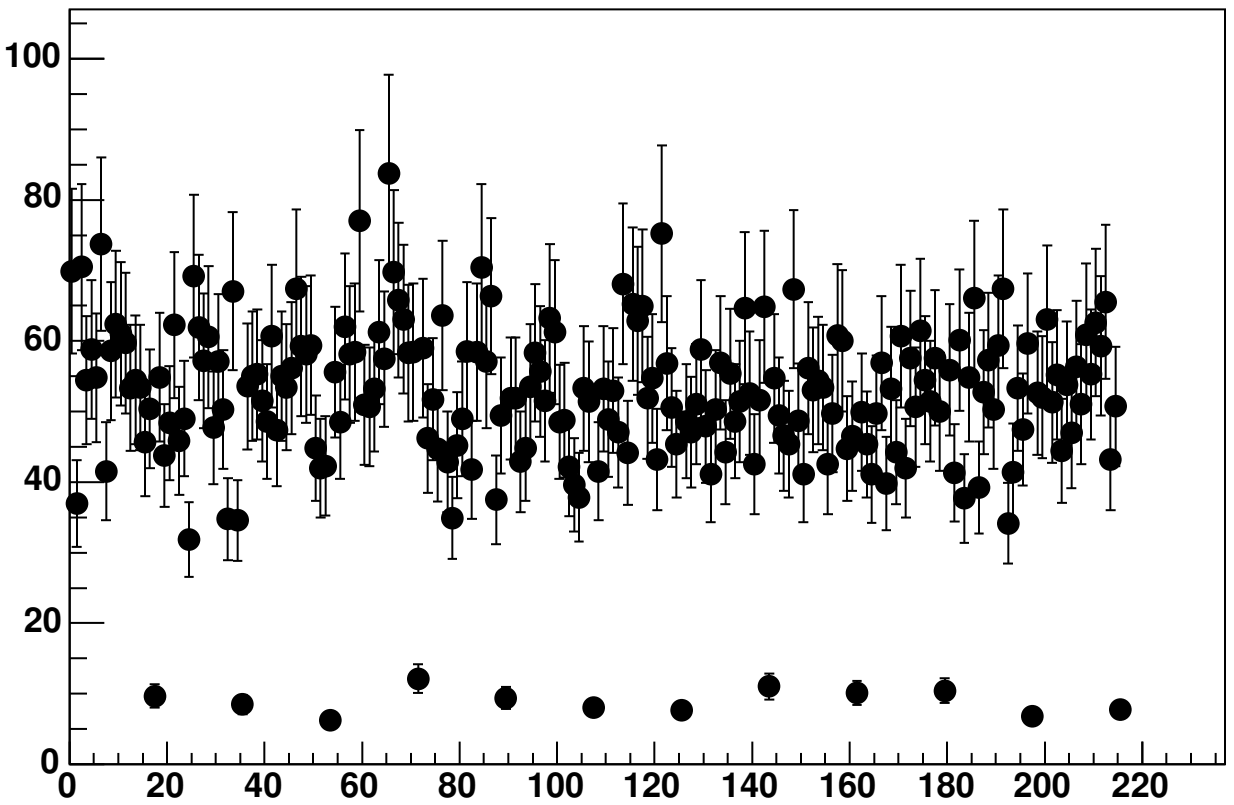
Enable 1, Hold=30, DAC=50, ADC Noise vs 18*Chip+Chan



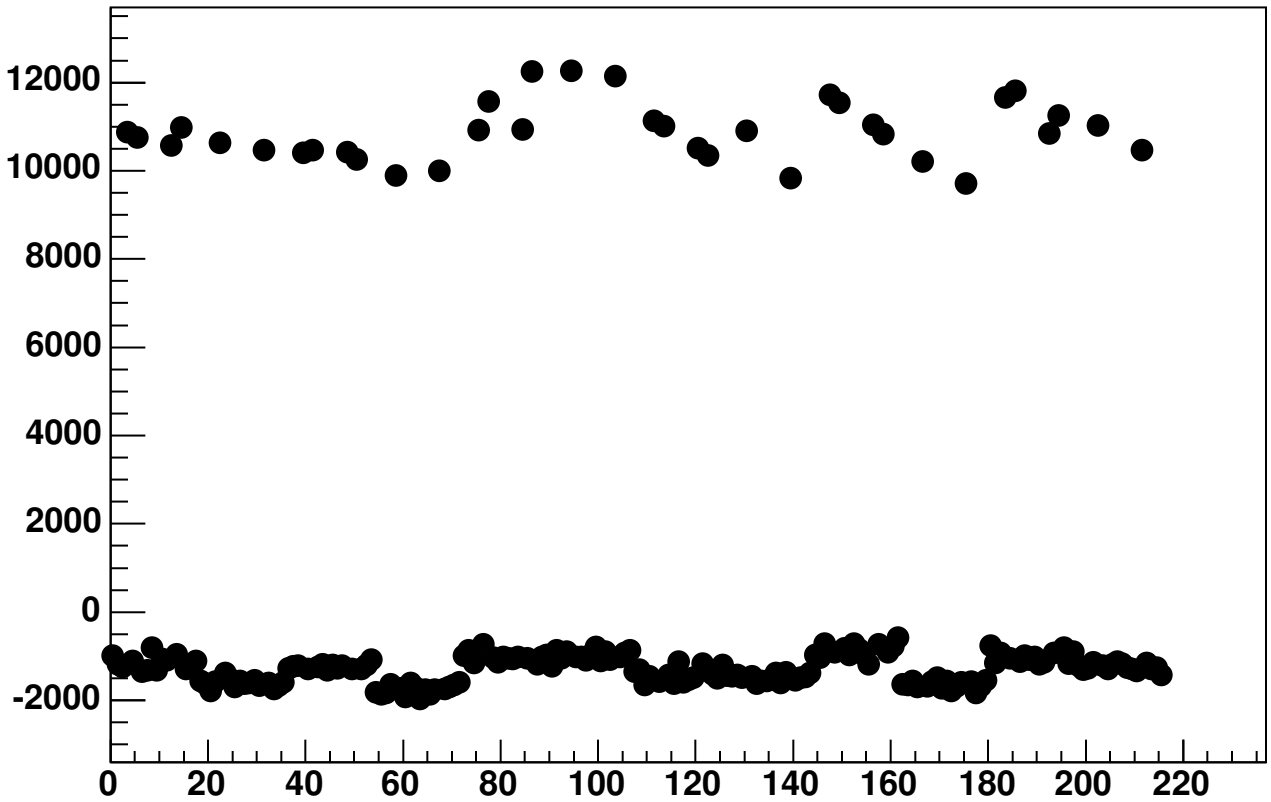
Enable 1, Hold=30, DAC=100, ADC Mean vs 18*Chip+Chan



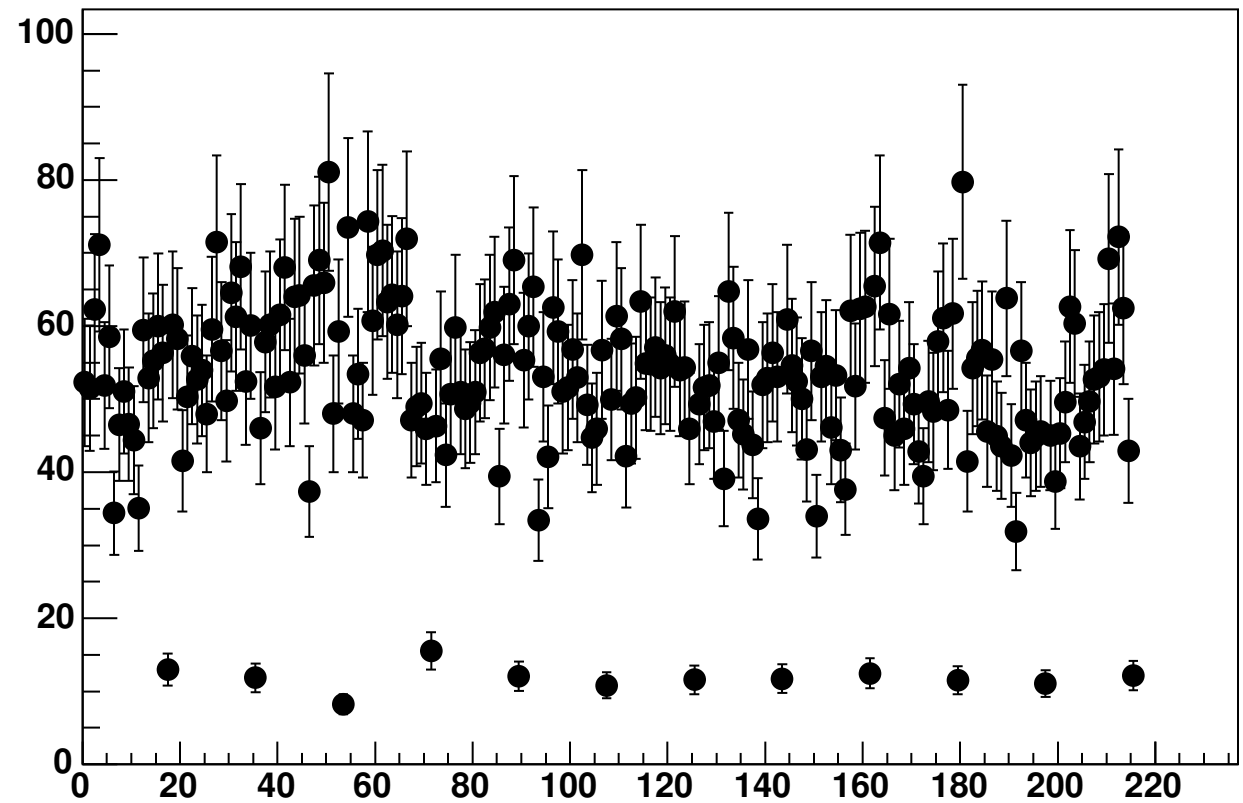
Enable 1, Hold=30, DAC=100, ADC Noise vs 18*Chip+Chan



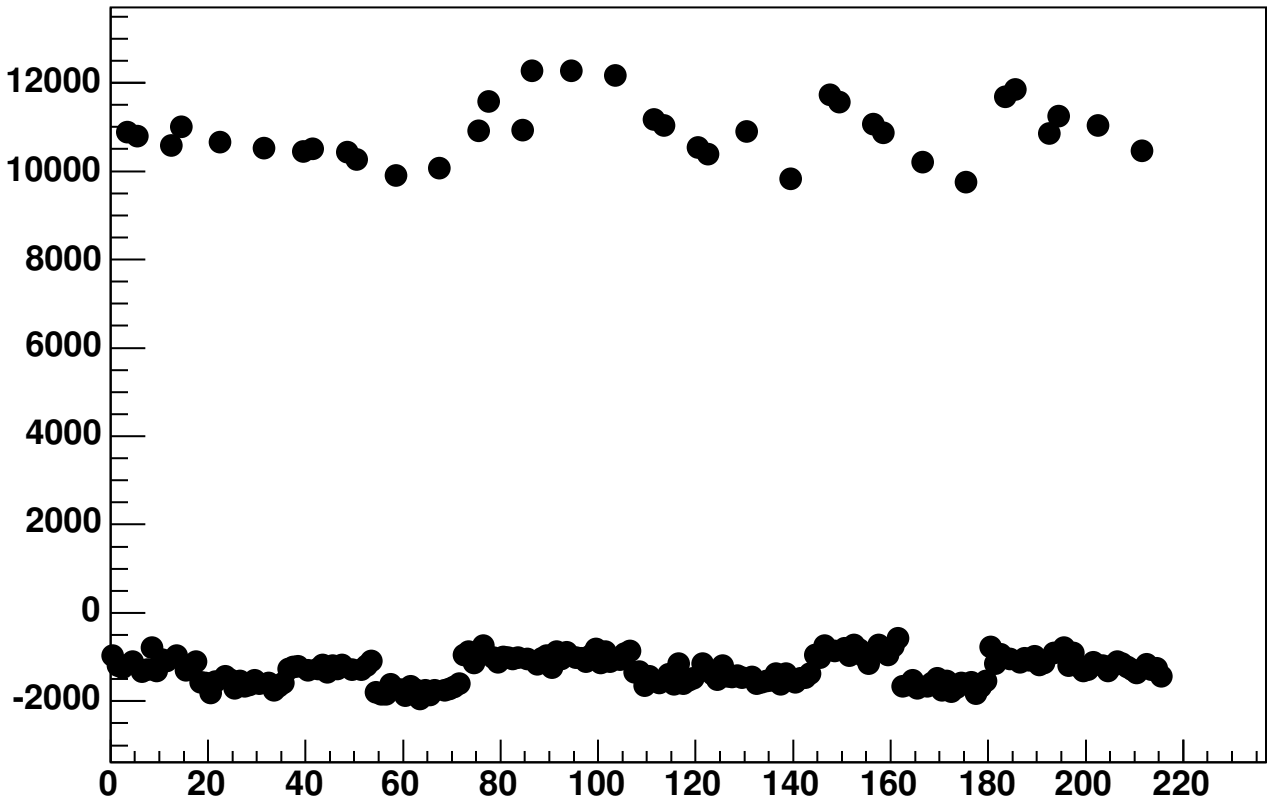
Enable 1, Hold=30, DAC=150, ADC Mean vs 18*Chip+Chan



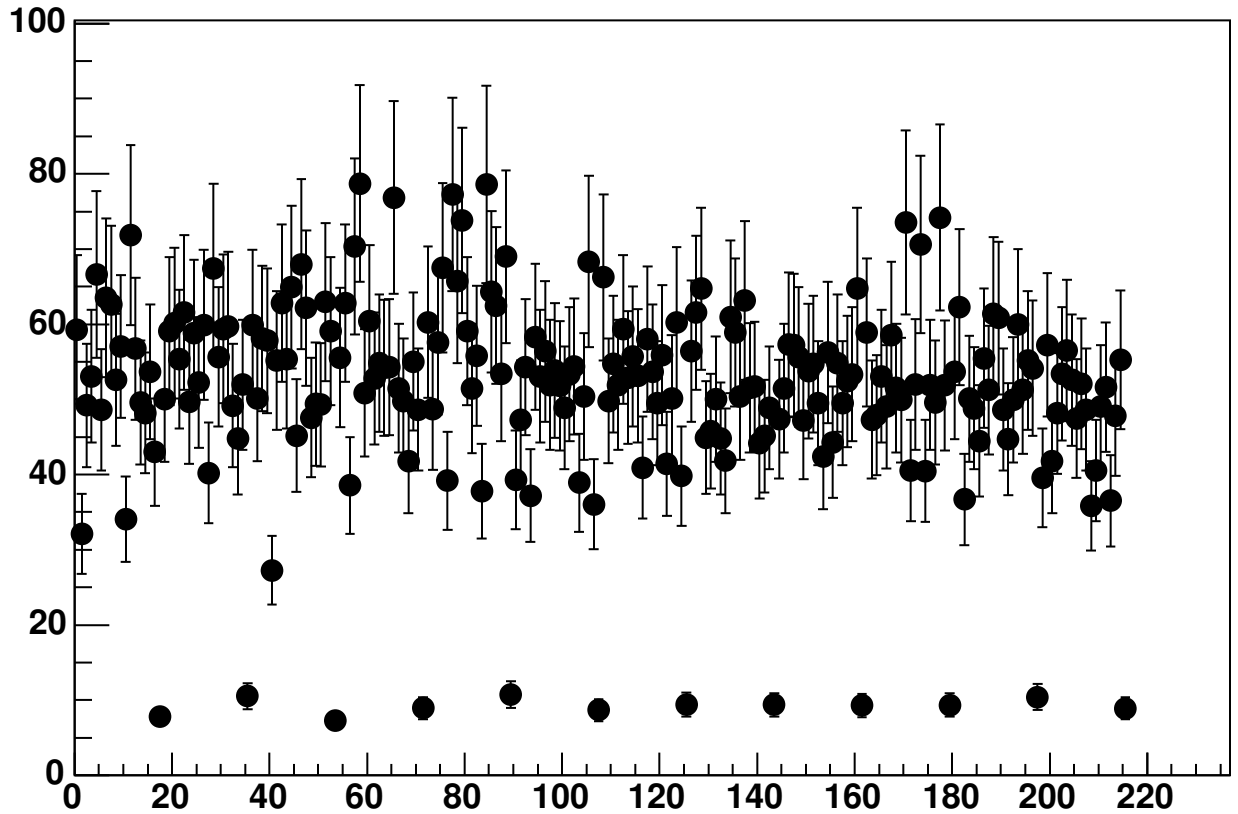
Enable 1, Hold=30, DAC=150, ADC Noise vs 18*Chip+Chan



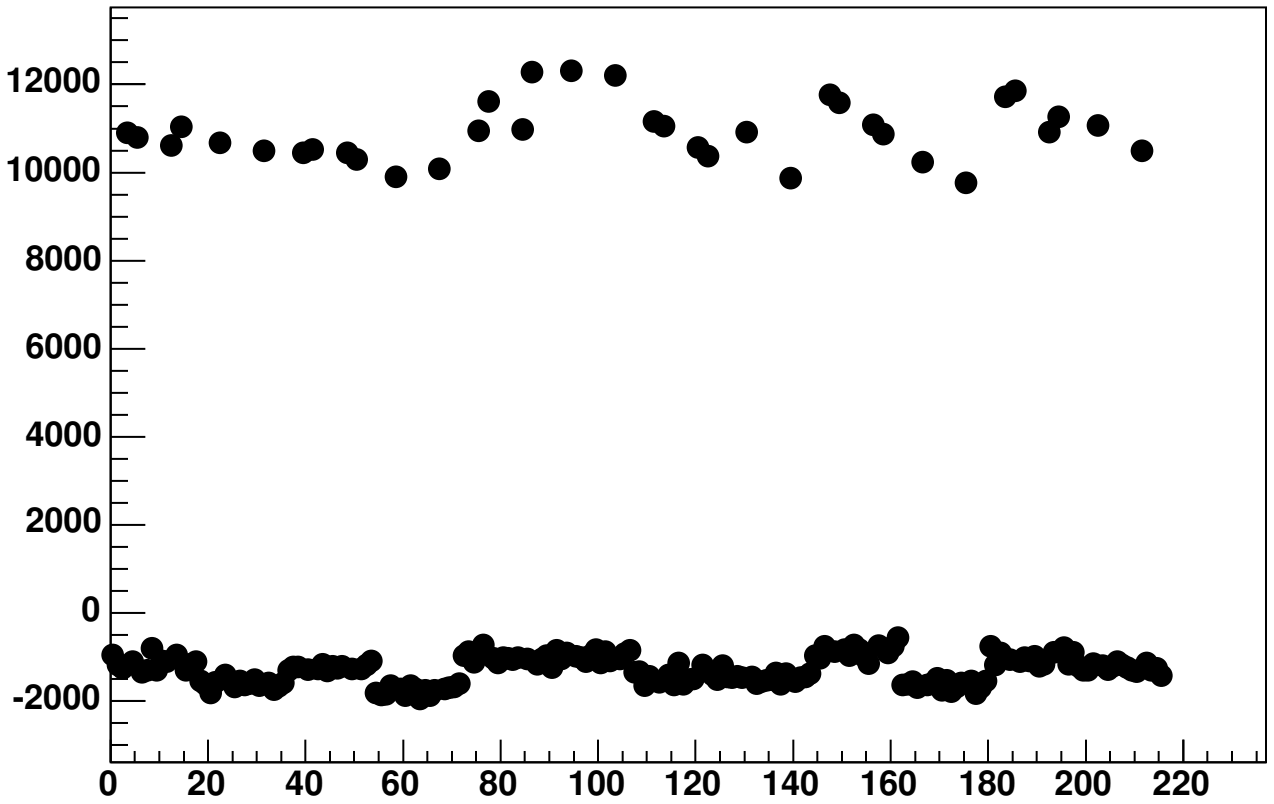
Enable 1, Hold=30, DAC=200, ADC Mean vs 18*Chip+Chan



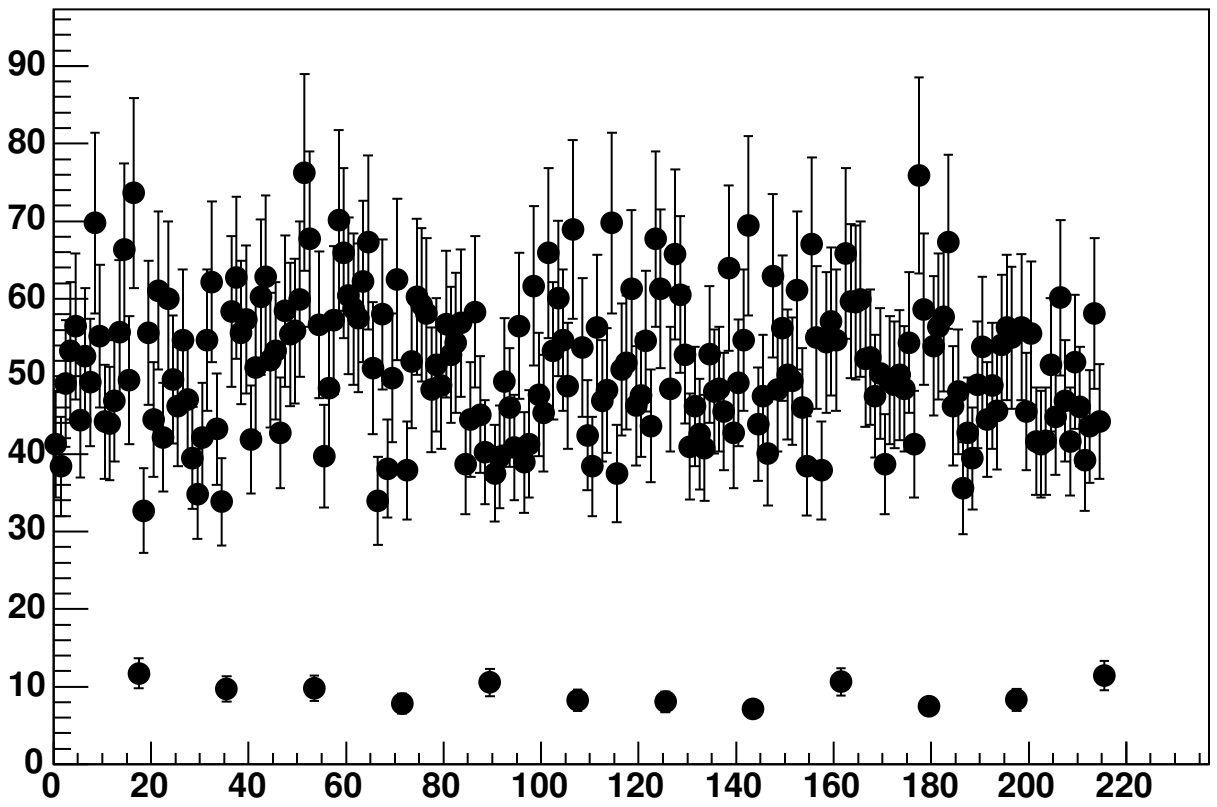
Enable 1, Hold=30, DAC=200, ADC Noise vs 18*Chip+Chan



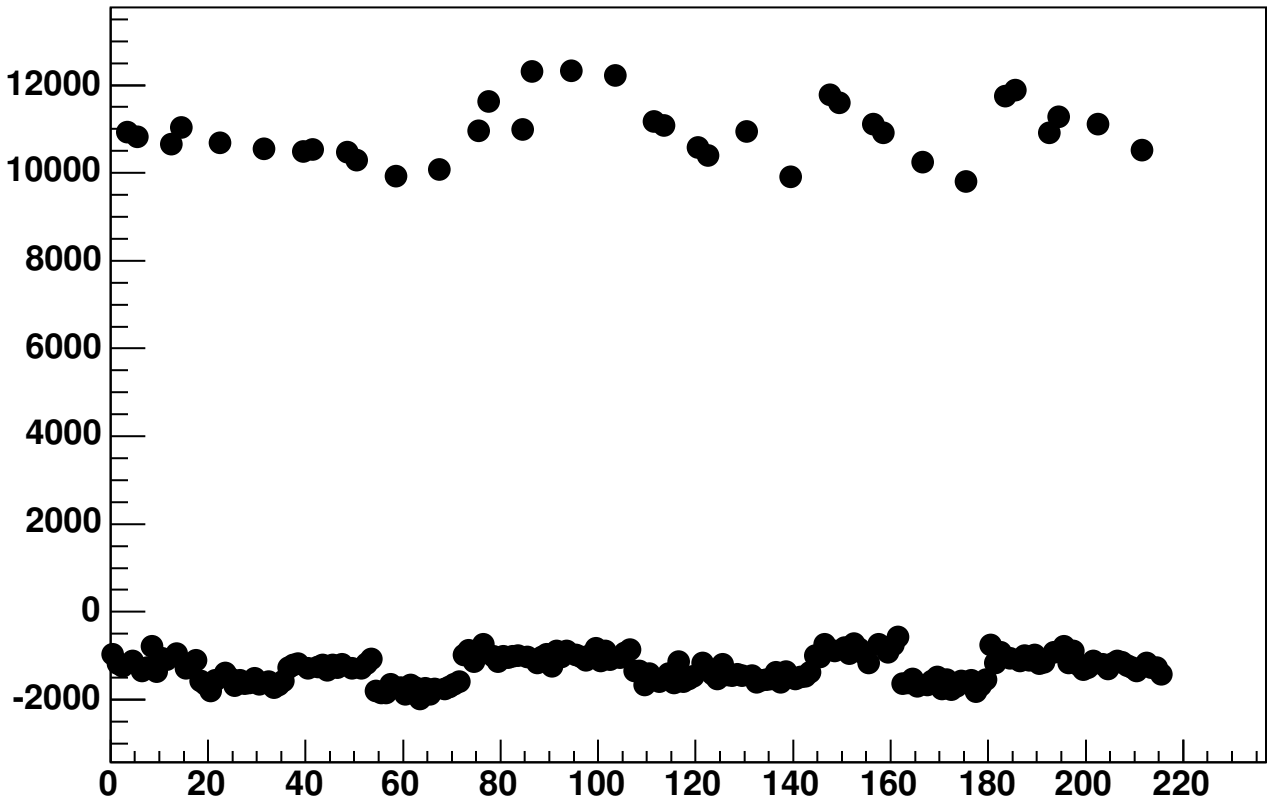
Enable 1, Hold=30, DAC=250, ADC Mean vs 18*Chip+Chan



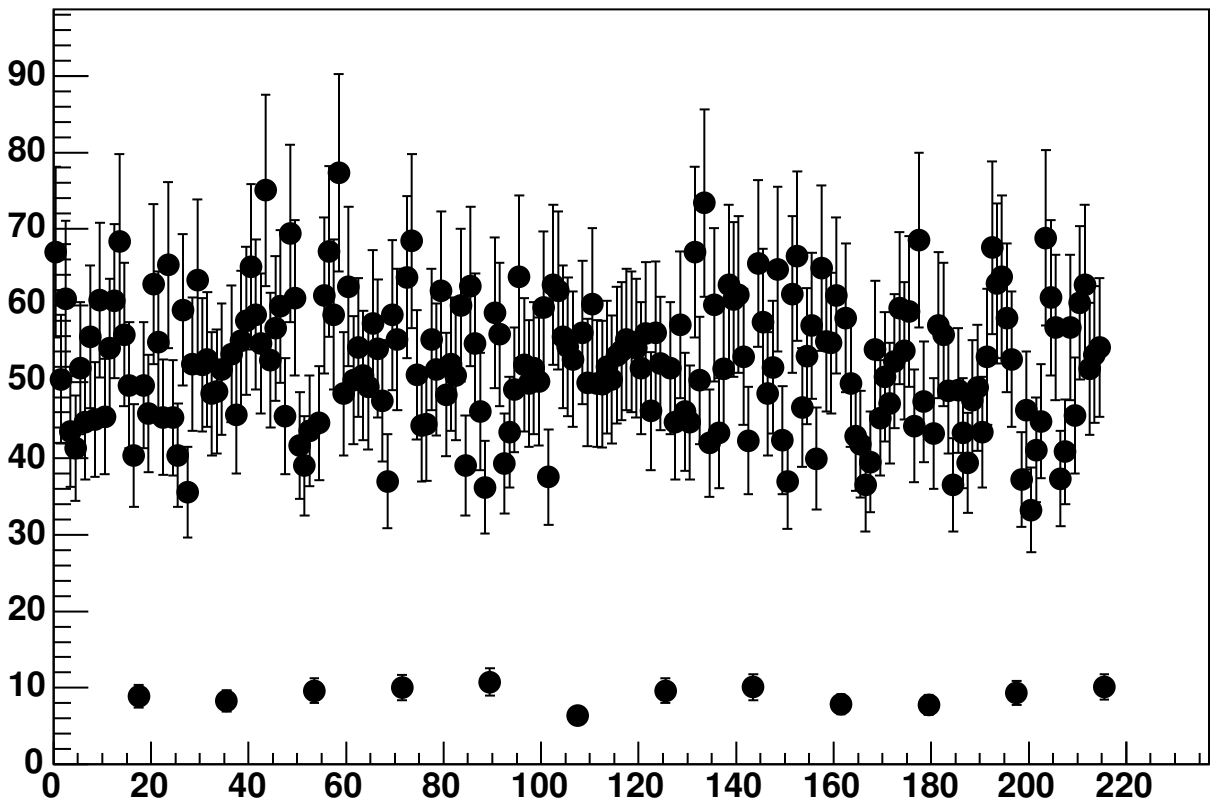
Enable 1, Hold=30, DAC=250, ADC Noise vs 18*Chip+Chan



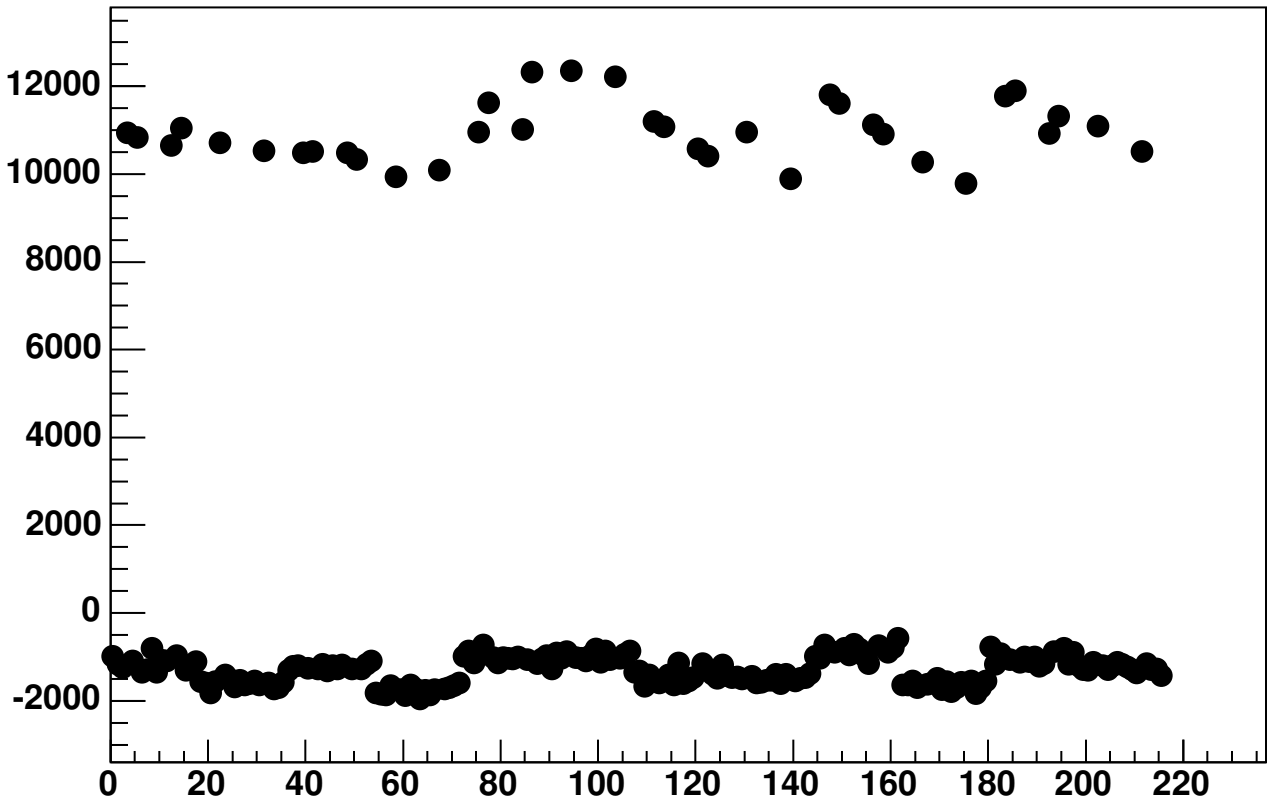
Enable 1, Hold=30, DAC=300, ADC Mean vs 18*Chip+Chan



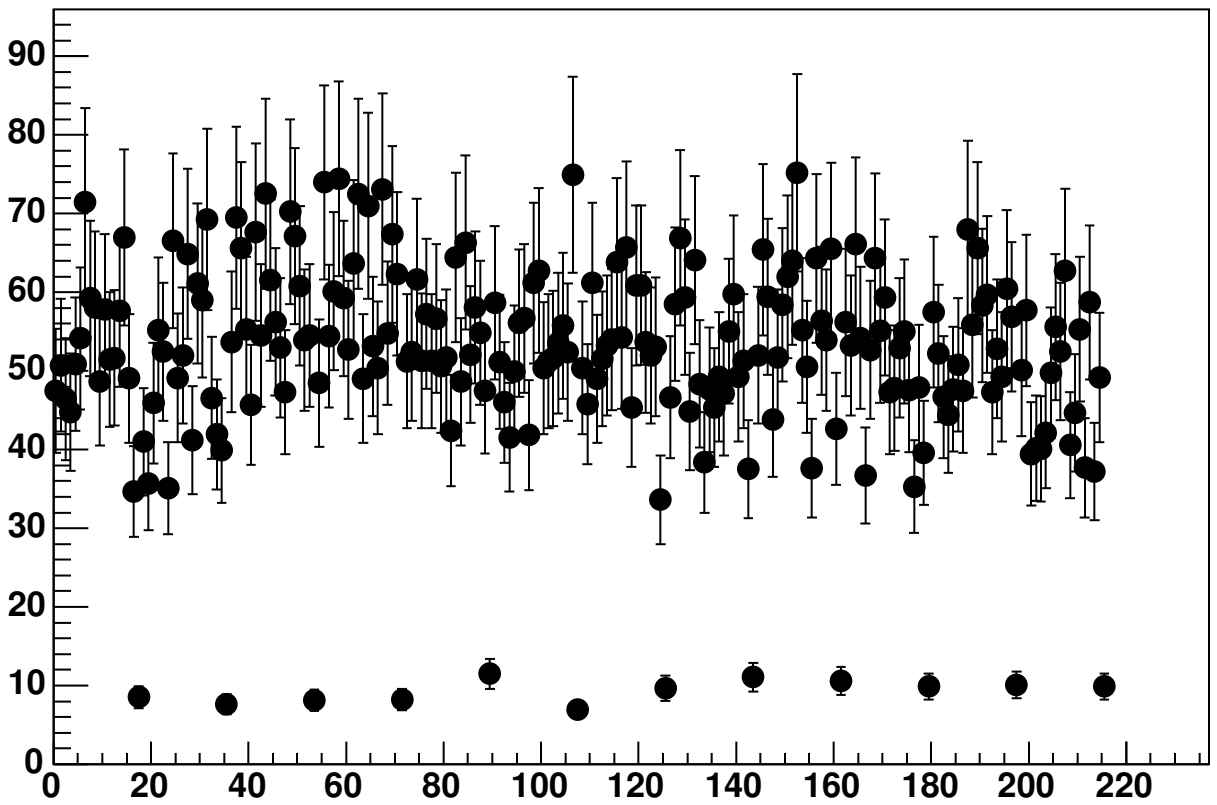
Enable 1, Hold=30, DAC=300, ADC Noise vs 18*Chip+Chan



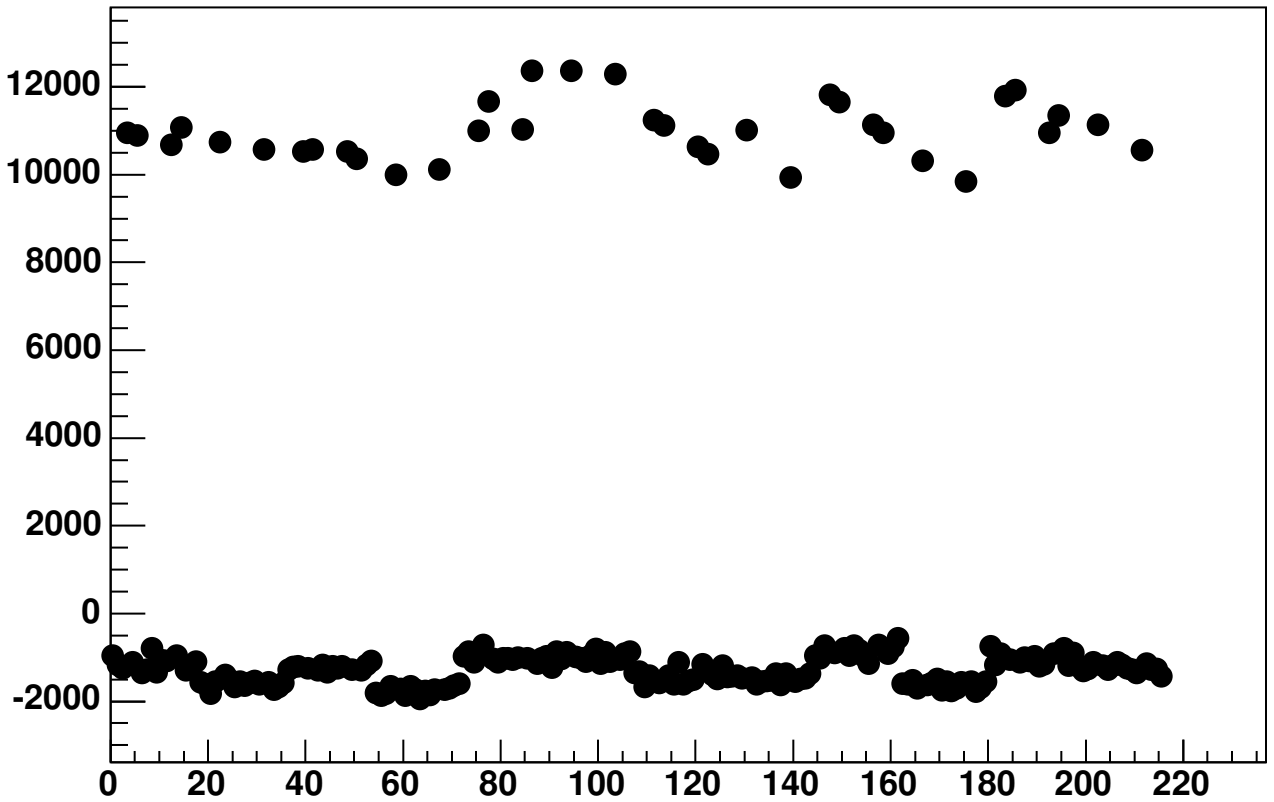
Enable 1, Hold=30, DAC=350, ADC Mean vs 18*Chip+Chan



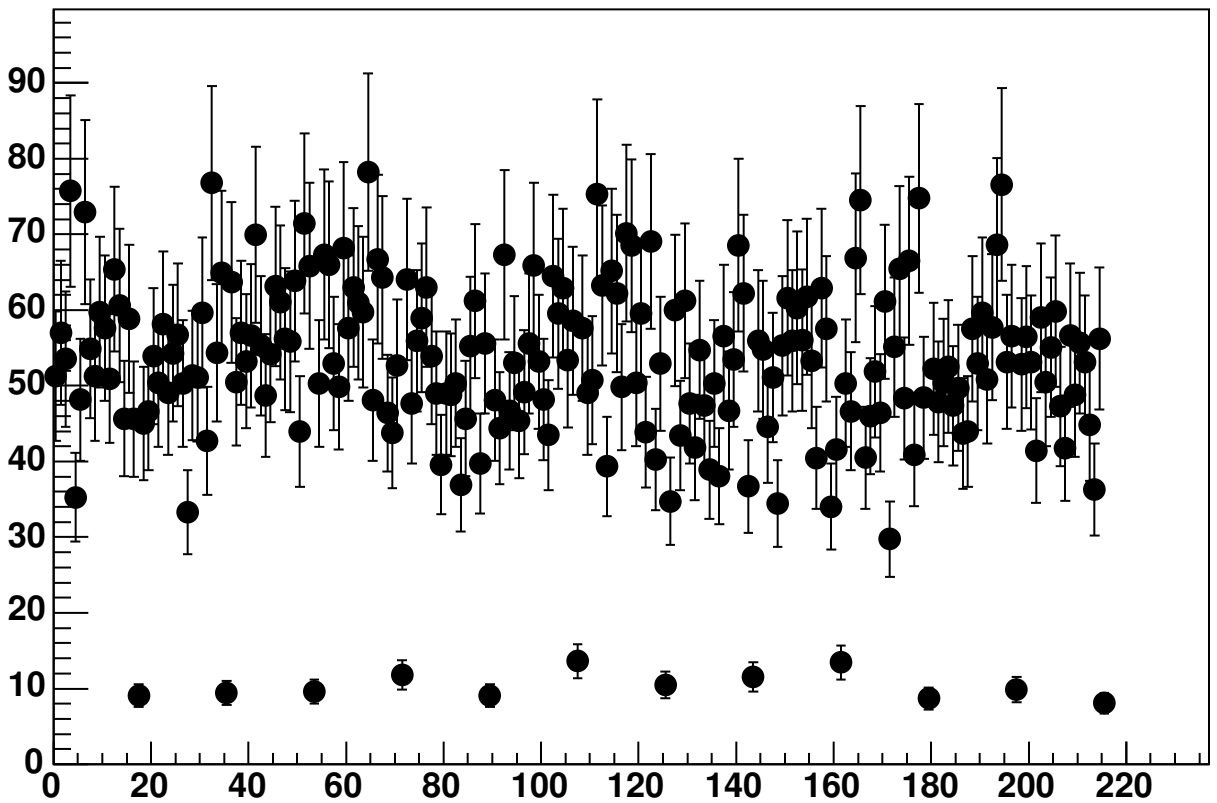
Enable 1, Hold=30, DAC=350, ADC Noise vs 18*Chip+Chan



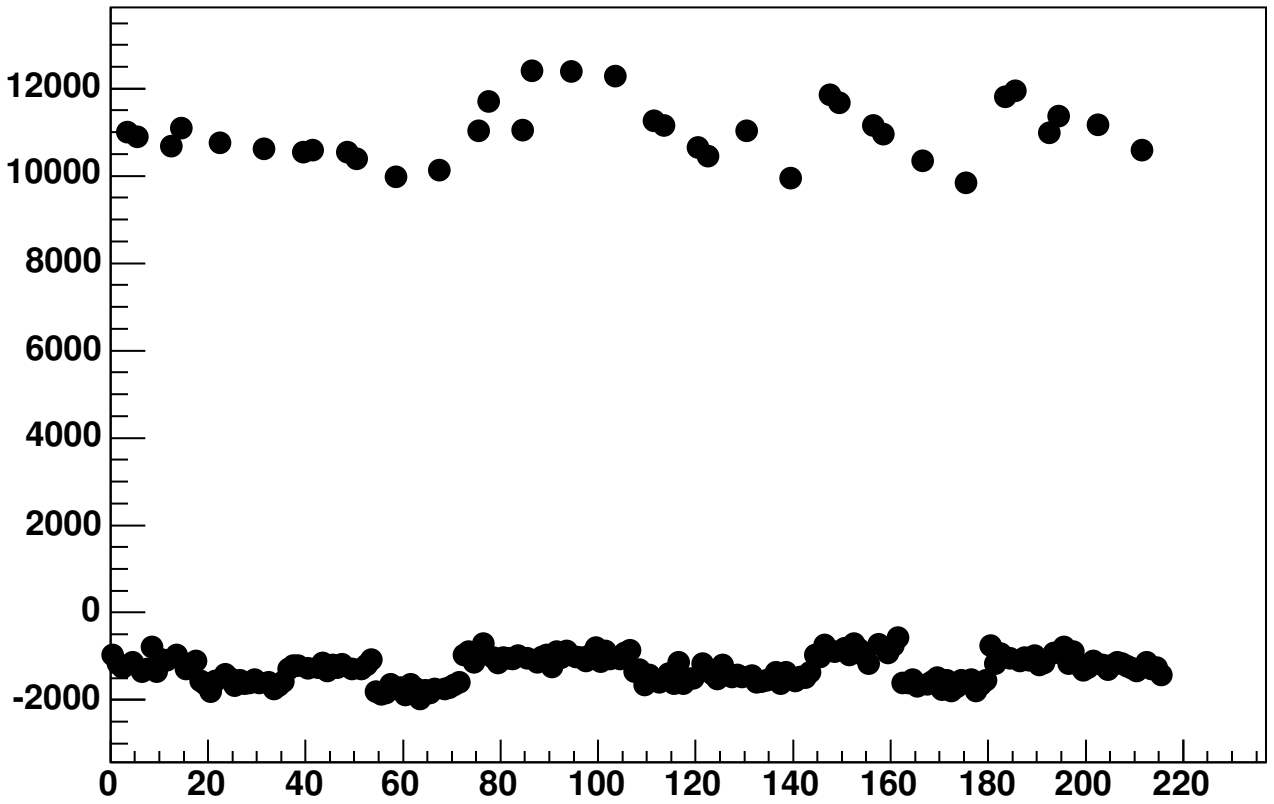
Enable 1, Hold=30, DAC=400, ADC Mean vs 18*Chip+Chan



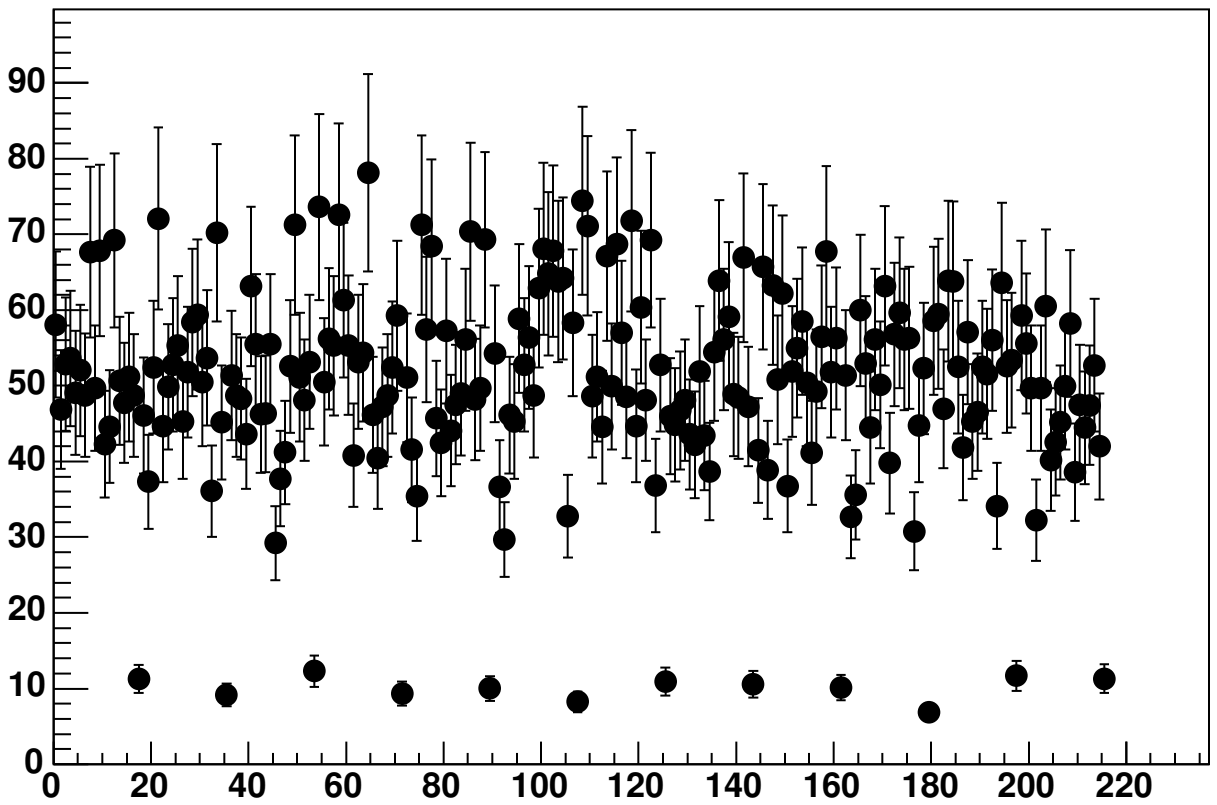
Enable 1, Hold=30, DAC=400, ADC Noise vs 18*Chip+Chan



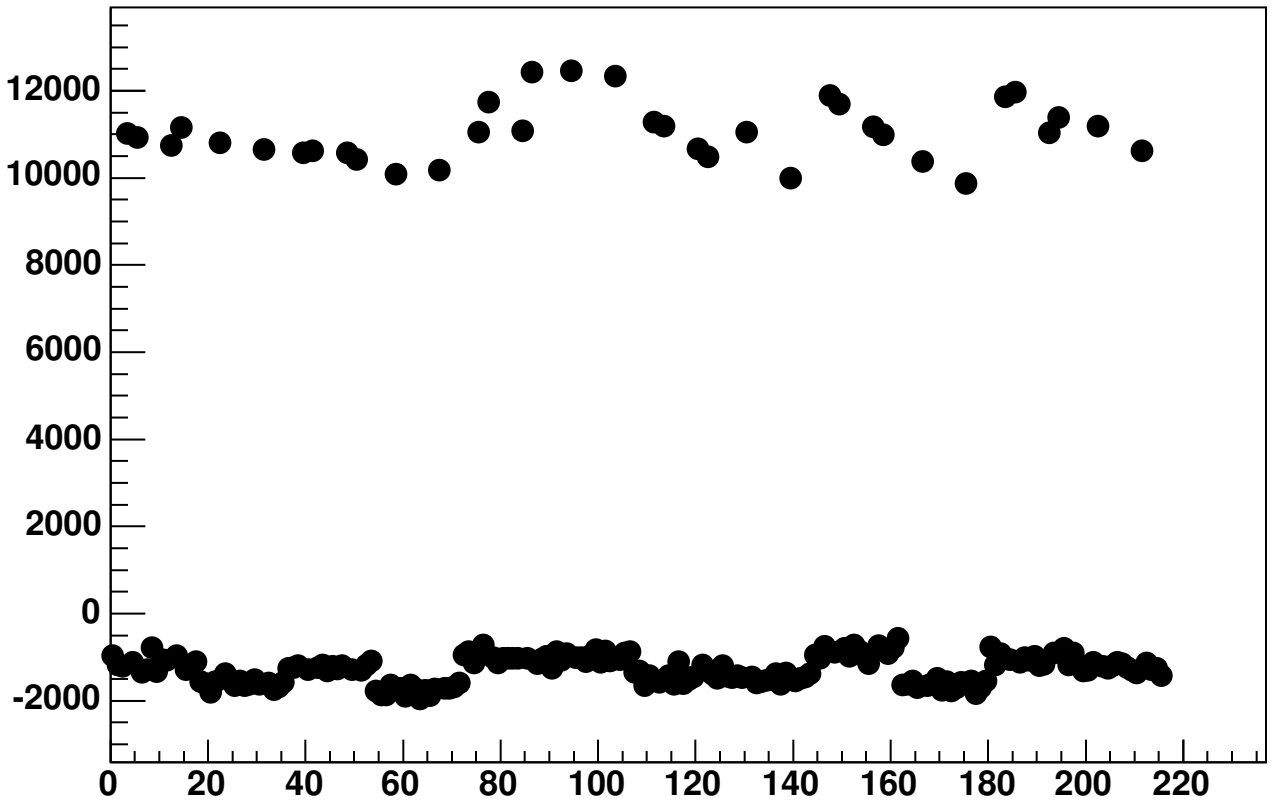
Enable 1, Hold=30, DAC=450, ADC Mean vs 18*Chip+Chan



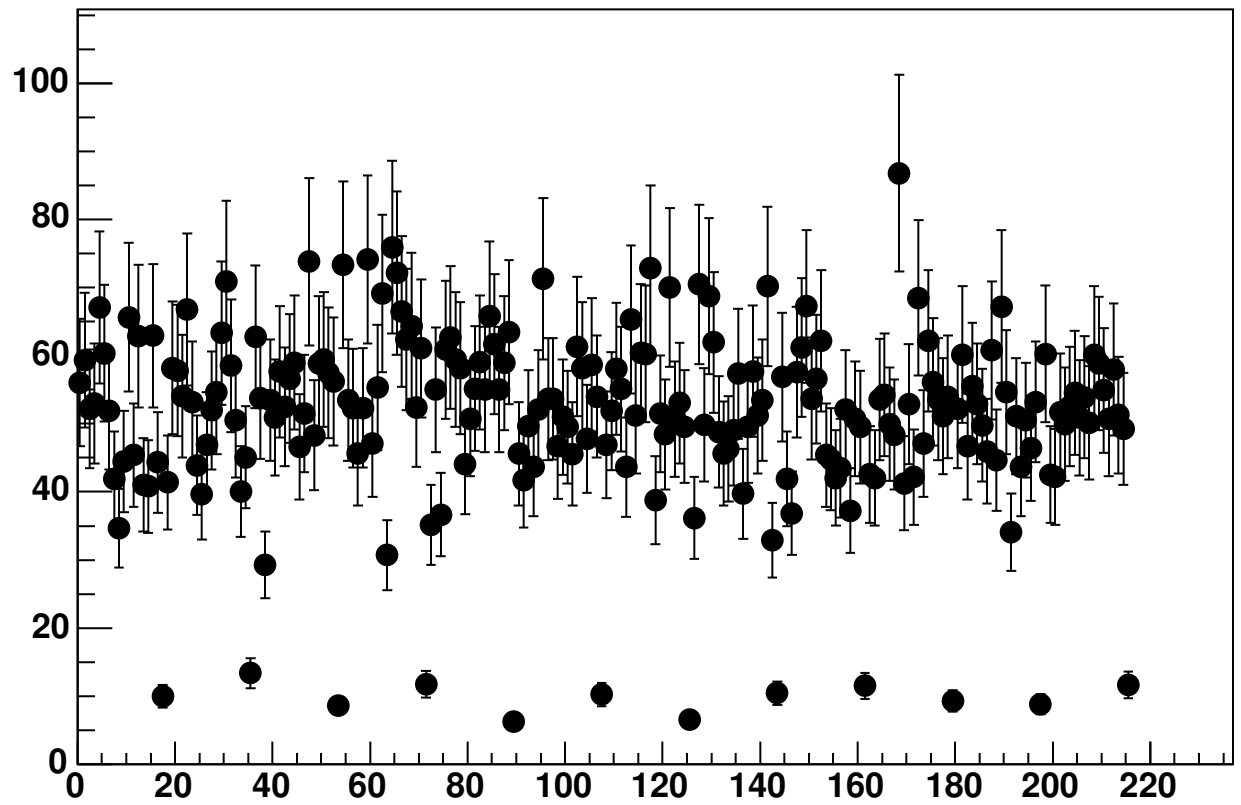
Enable 1, Hold=30, DAC=450, ADC Noise vs 18*Chip+Chan



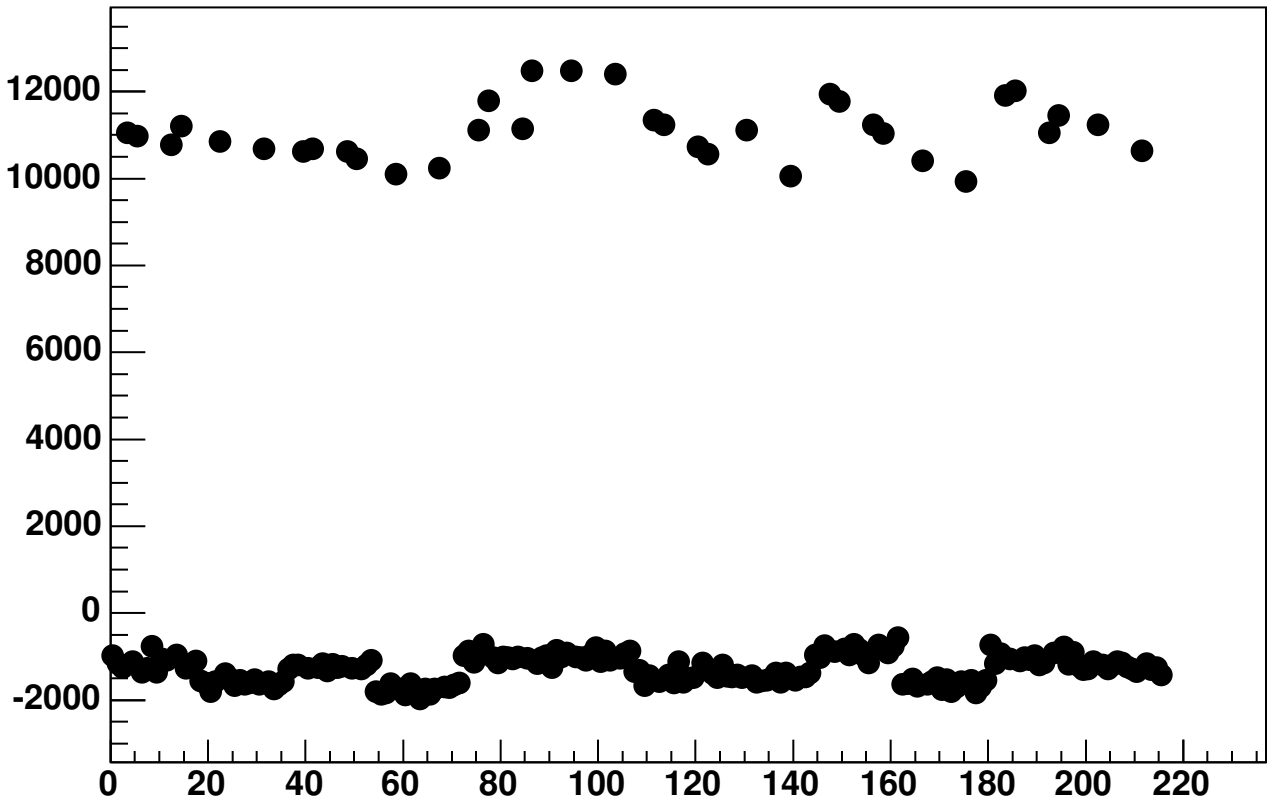
Enable 1, Hold=30, DAC=500, ADC Mean vs 18*Chip+Chan



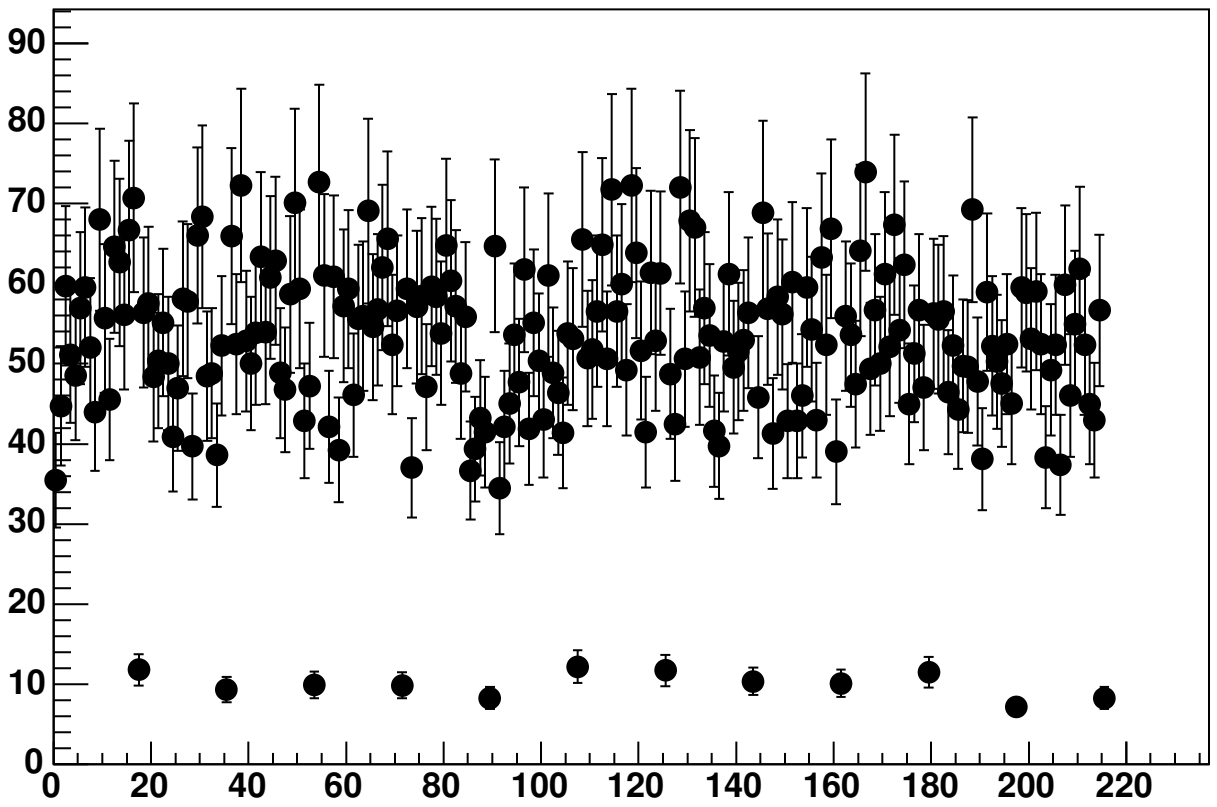
Enable 1, Hold=30, DAC=500, ADC Noise vs 18*Chip+Chan



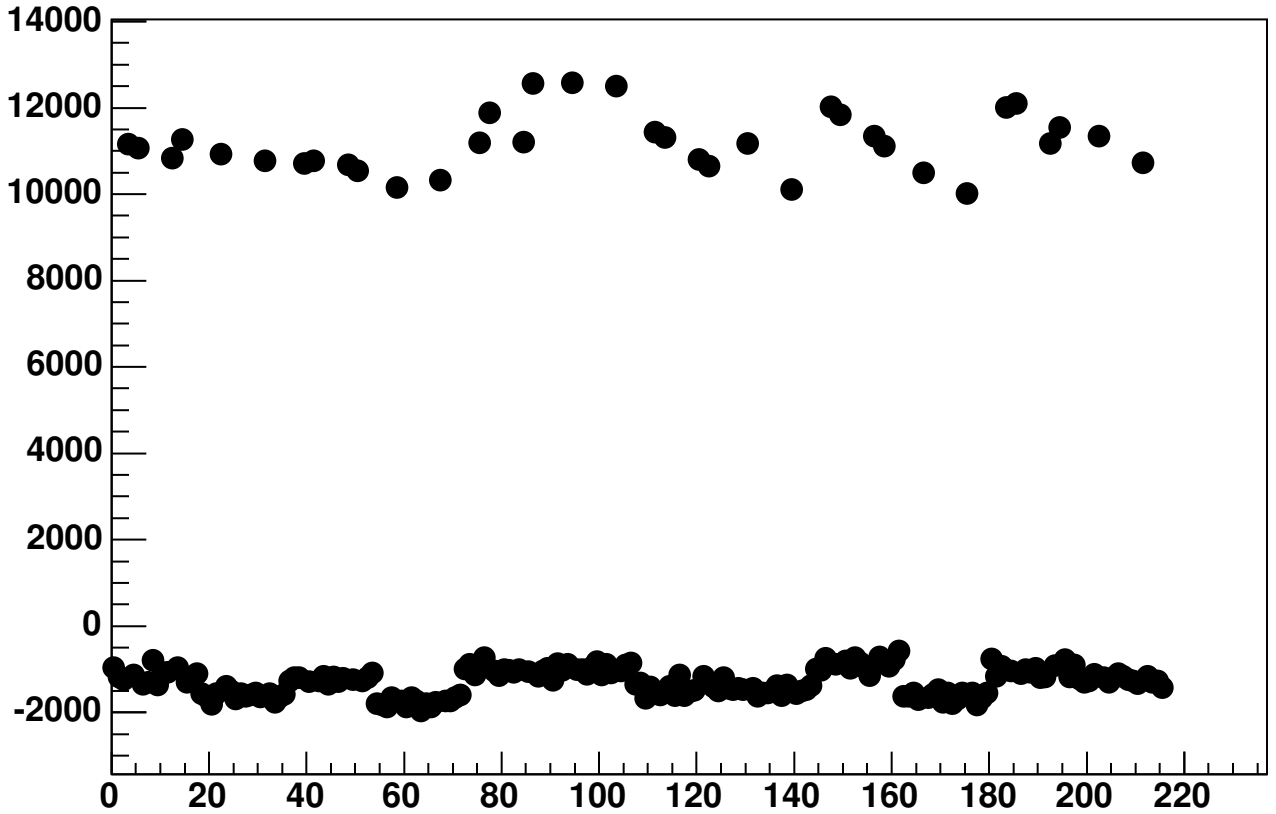
Enable 1, Hold=30, DAC=550, ADC Mean vs 18*Chip+Chan



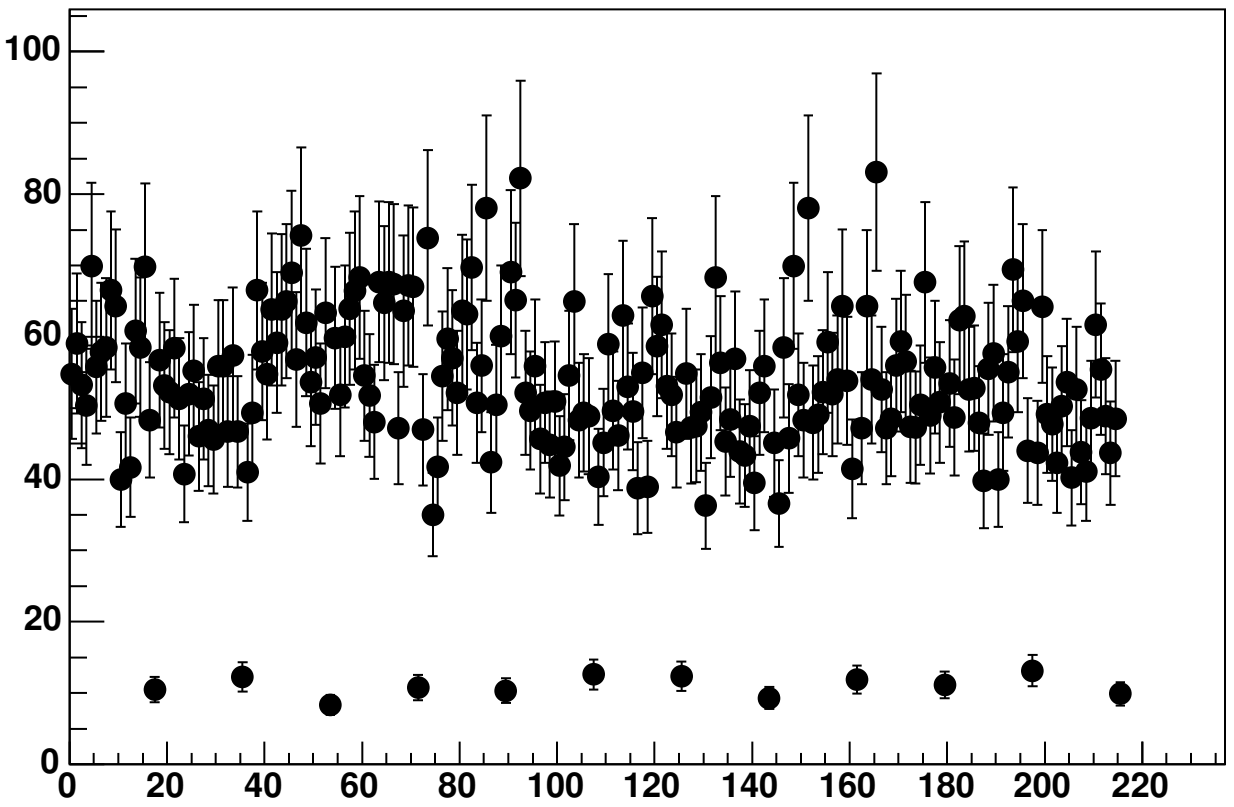
Enable 1, Hold=30, DAC=550, ADC Noise vs 18*Chip+Chan



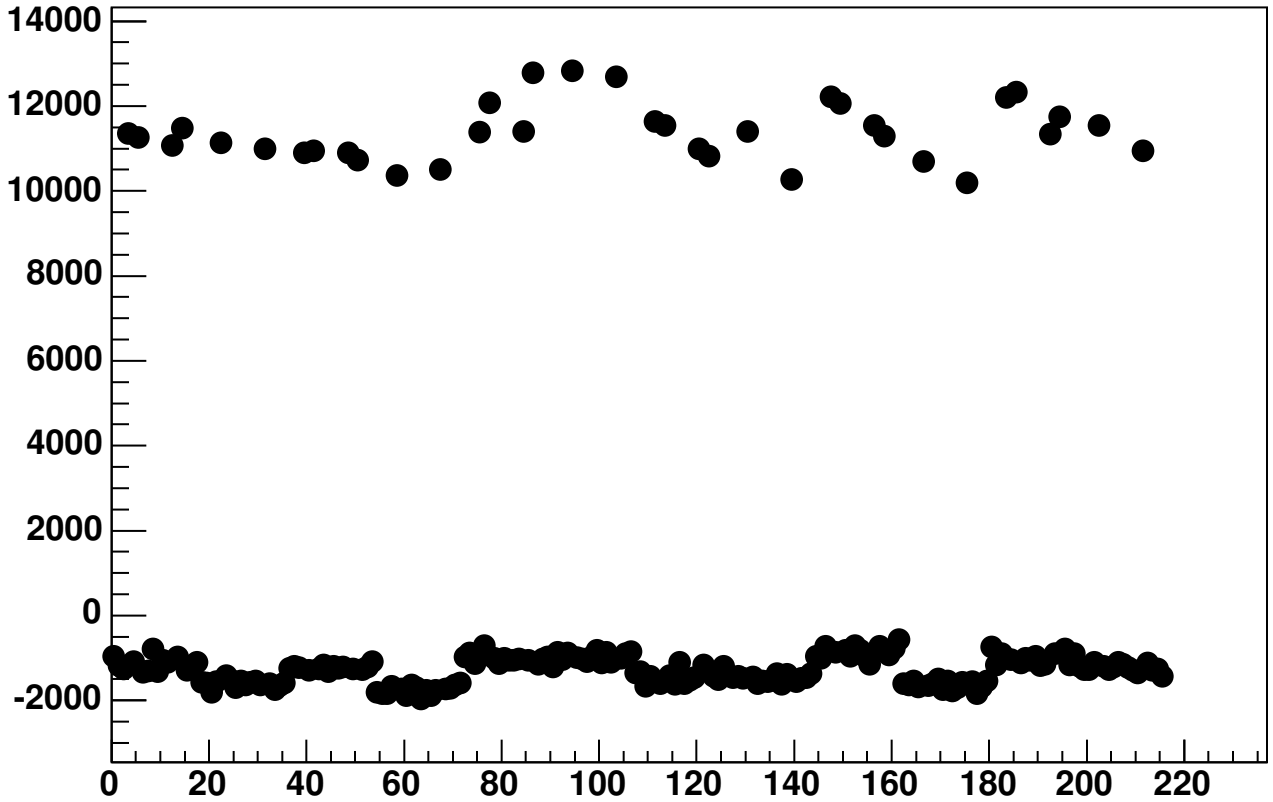
Enable 1, Hold=30, DAC=600, ADC Mean vs 18*Chip+Chan



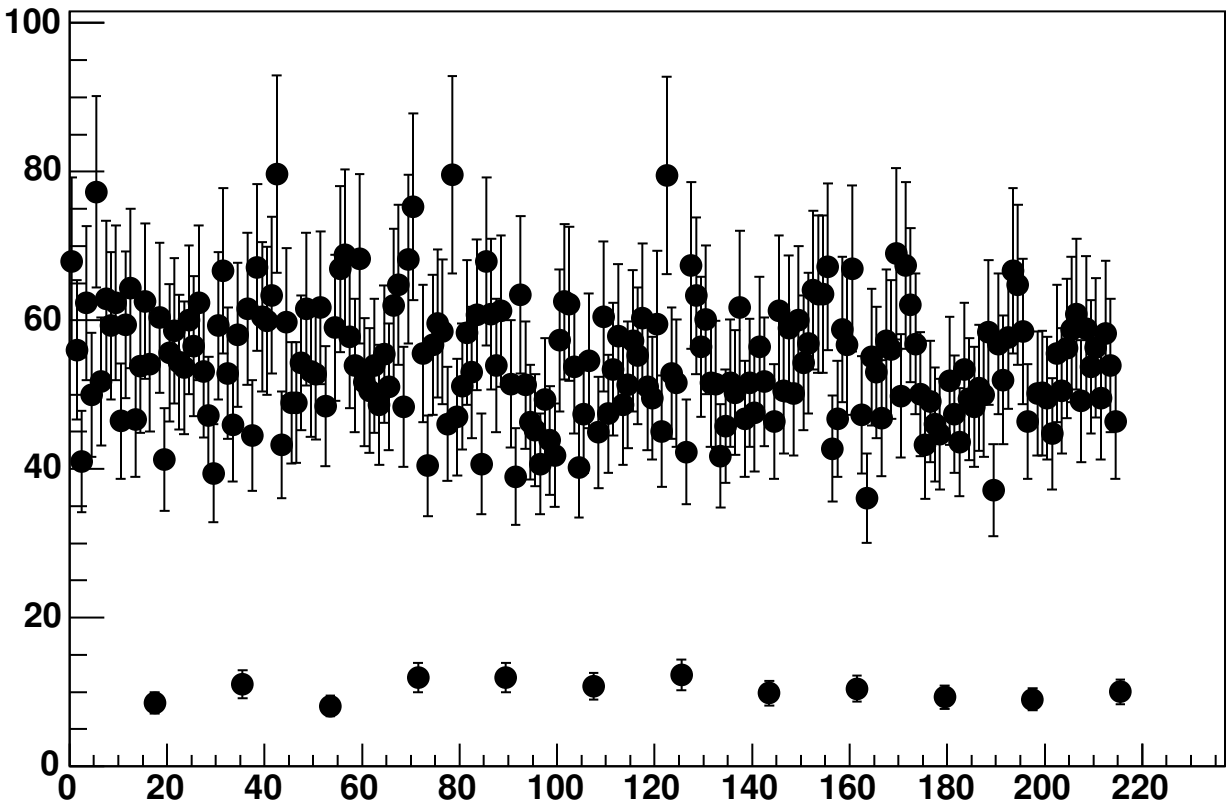
Enable 1, Hold=30, DAC=600, ADC Noise vs 18*Chip+Chan



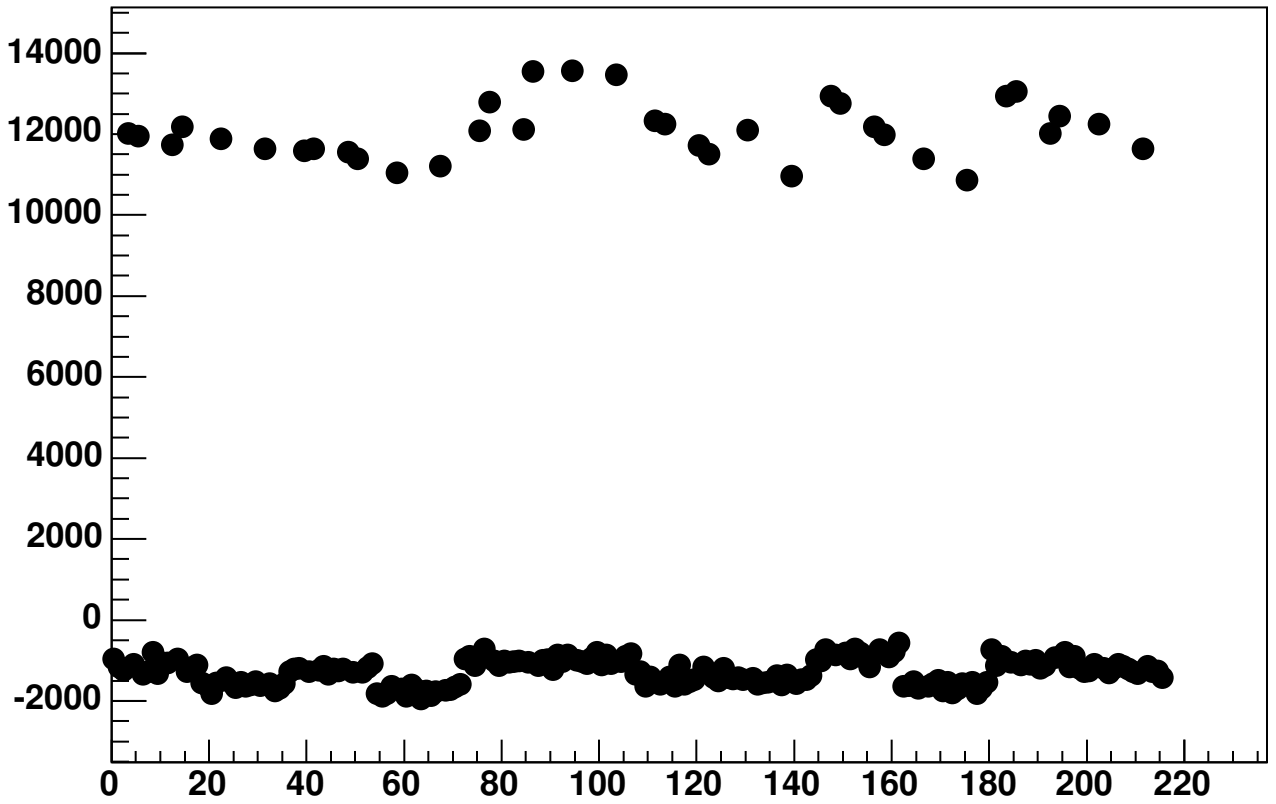
Enable 1, Hold=30, DAC=650, ADC Mean vs 18*Chip+Chan



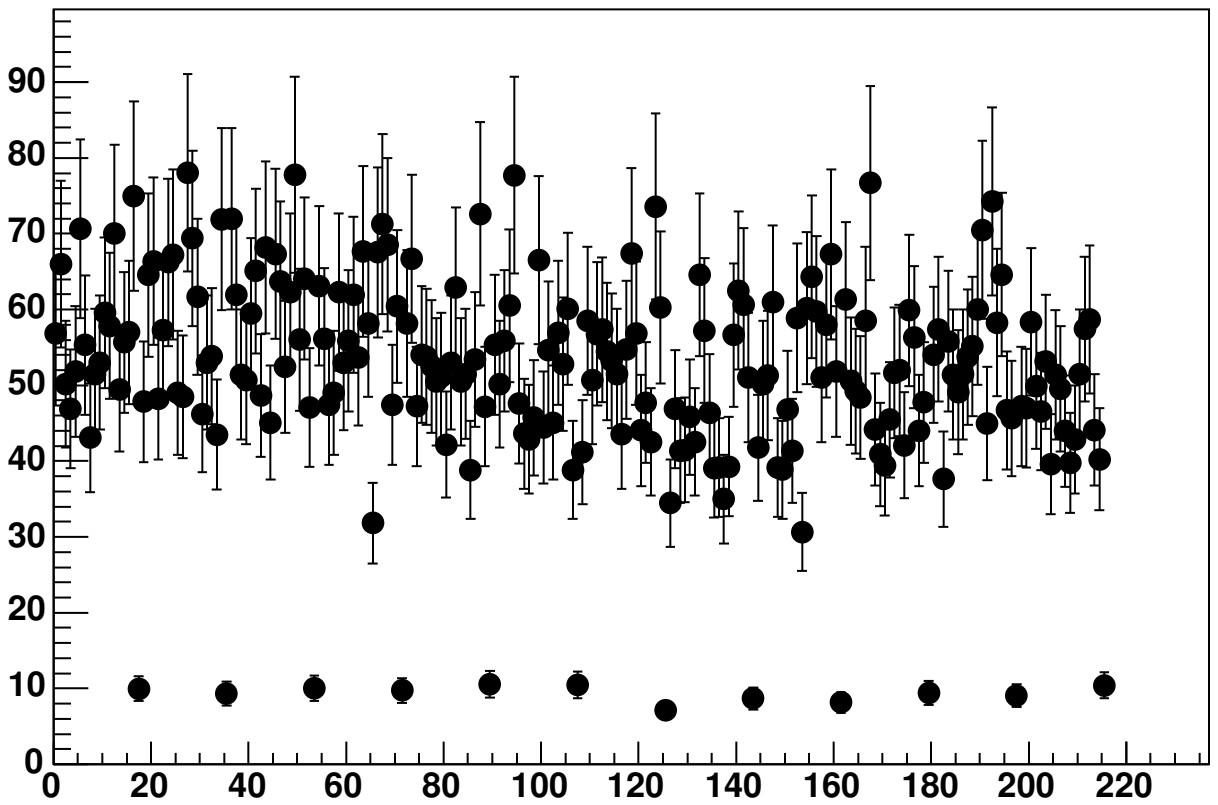
Enable 1, Hold=30, DAC=650, ADC Noise vs 18*Chip+Chan



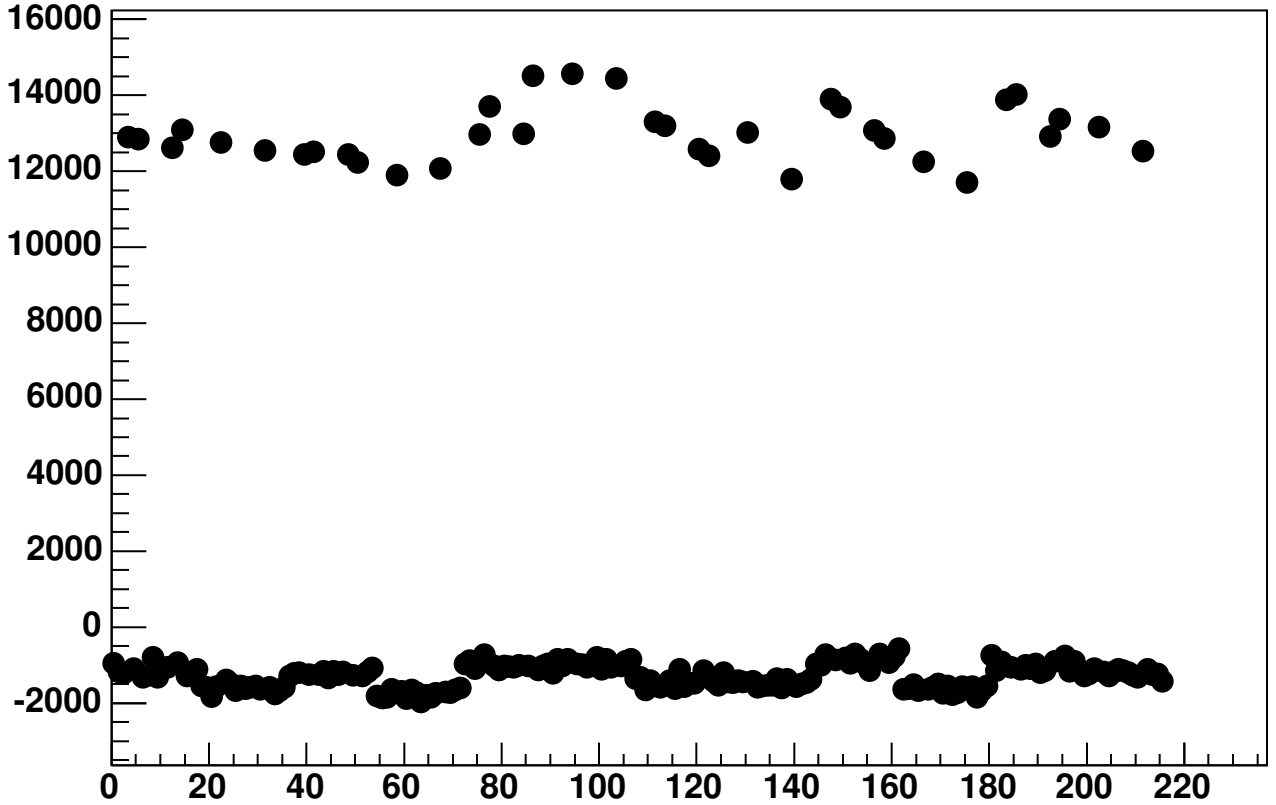
Enable 1, Hold=30, DAC=700, ADC Mean vs 18*Chip+Chan



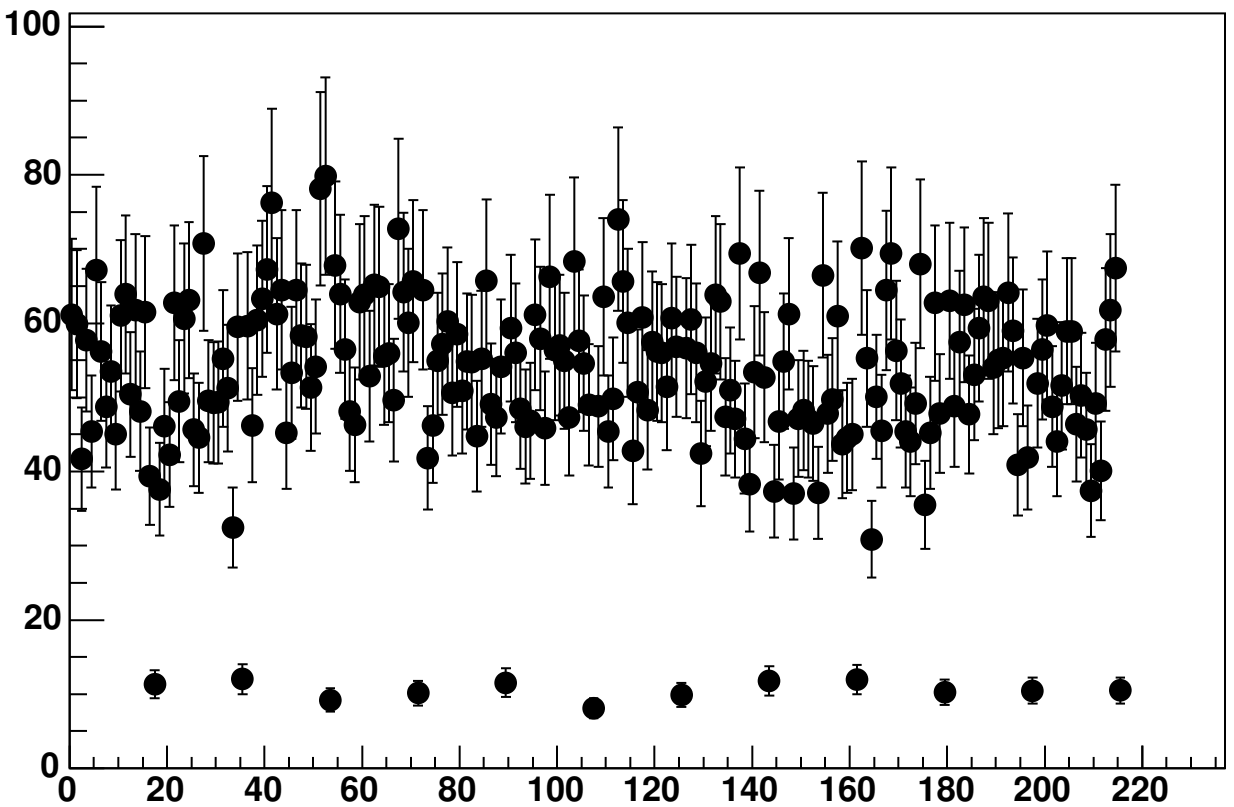
Enable 1, Hold=30, DAC=700, ADC Noise vs 18*Chip+Chan



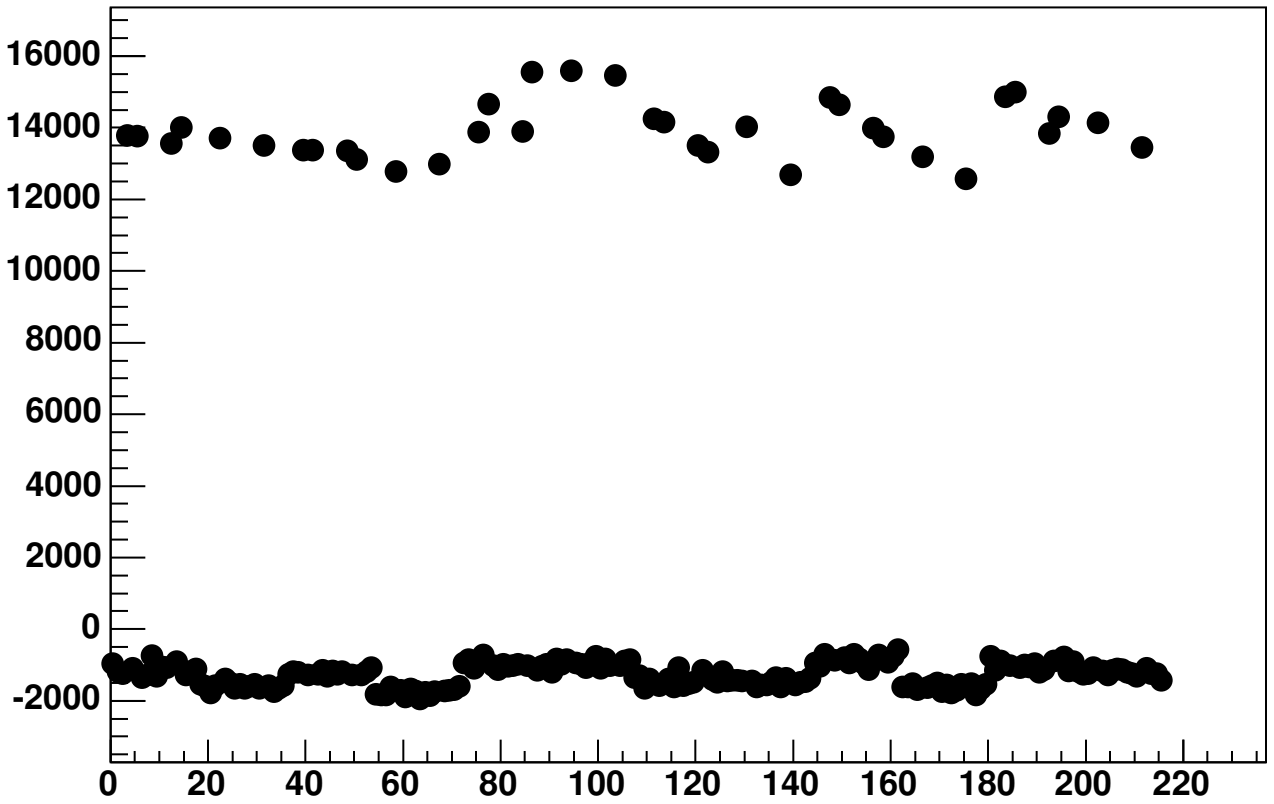
Enable 1, Hold=30, DAC=750, ADC Mean vs 18*Chip+Chan



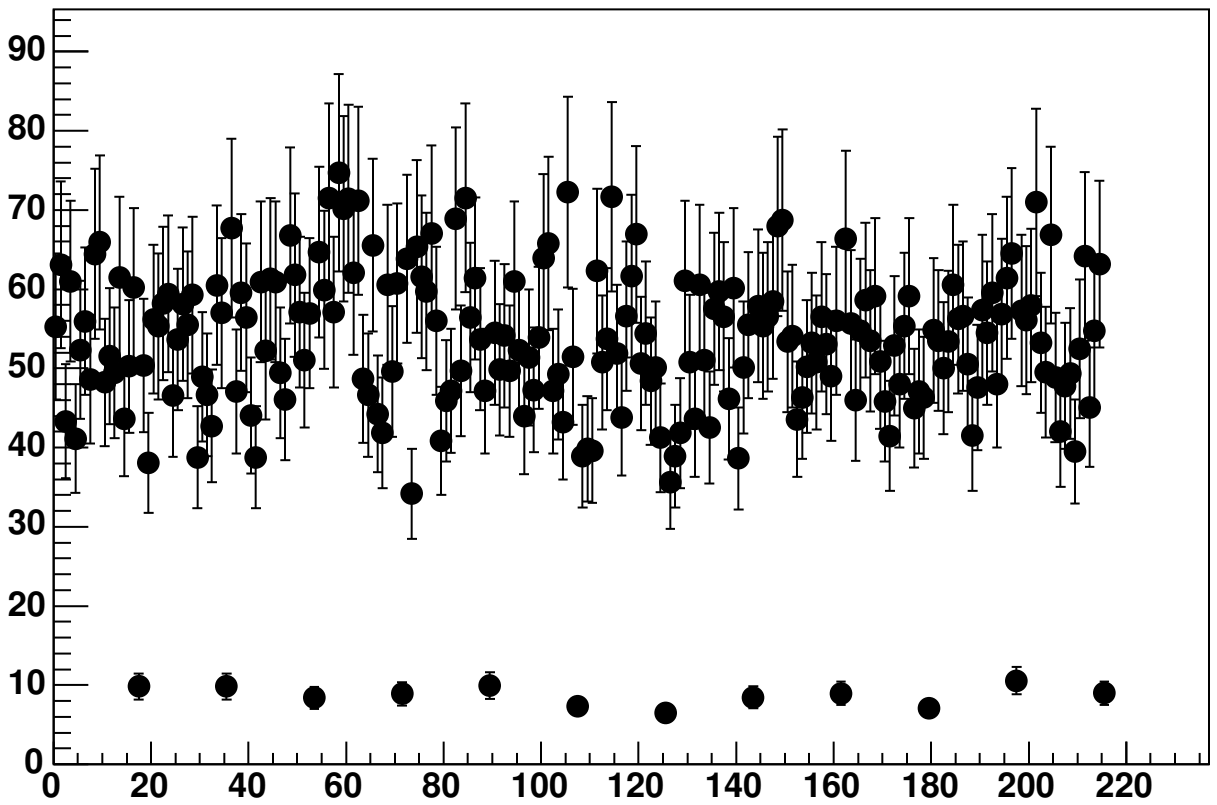
Enable 1, Hold=30, DAC=750, ADC Noise vs 18*Chip+Chan



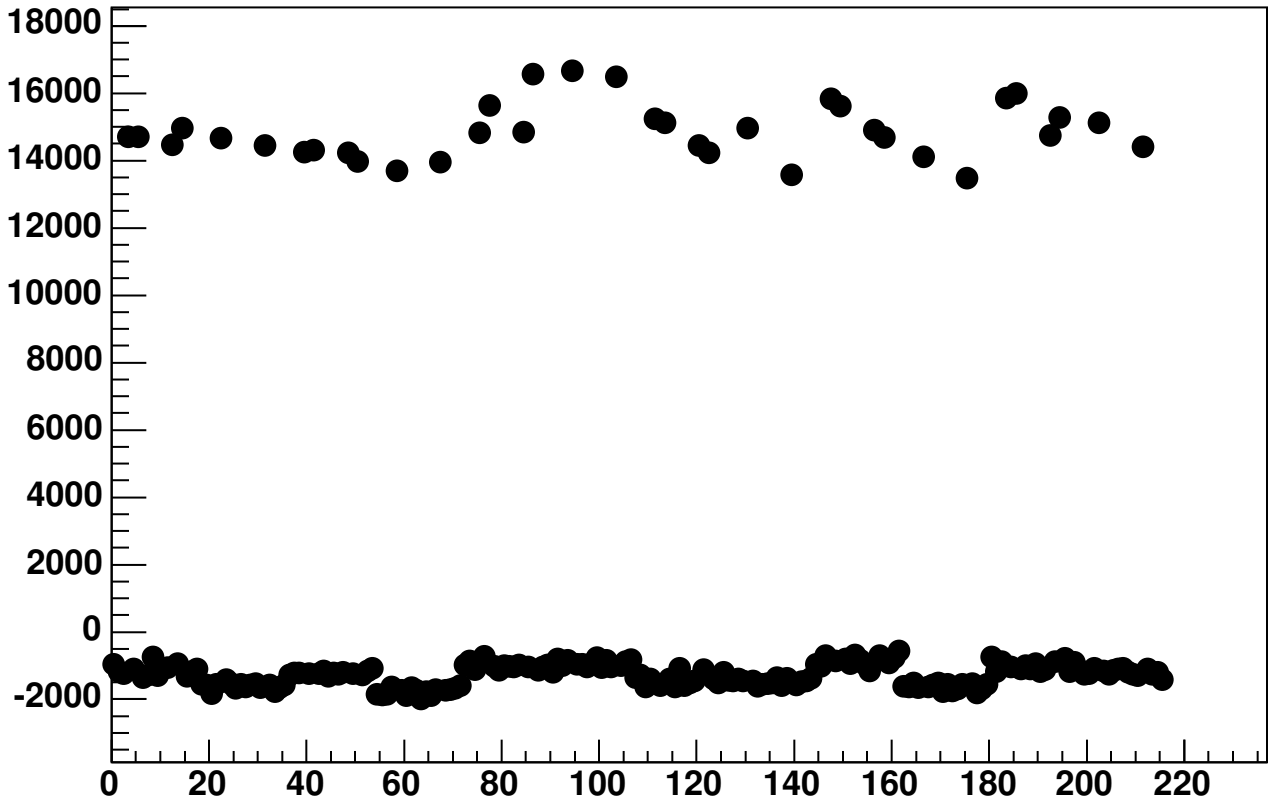
Enable 1, Hold=30, DAC=800, ADC Mean vs 18*Chip+Chan



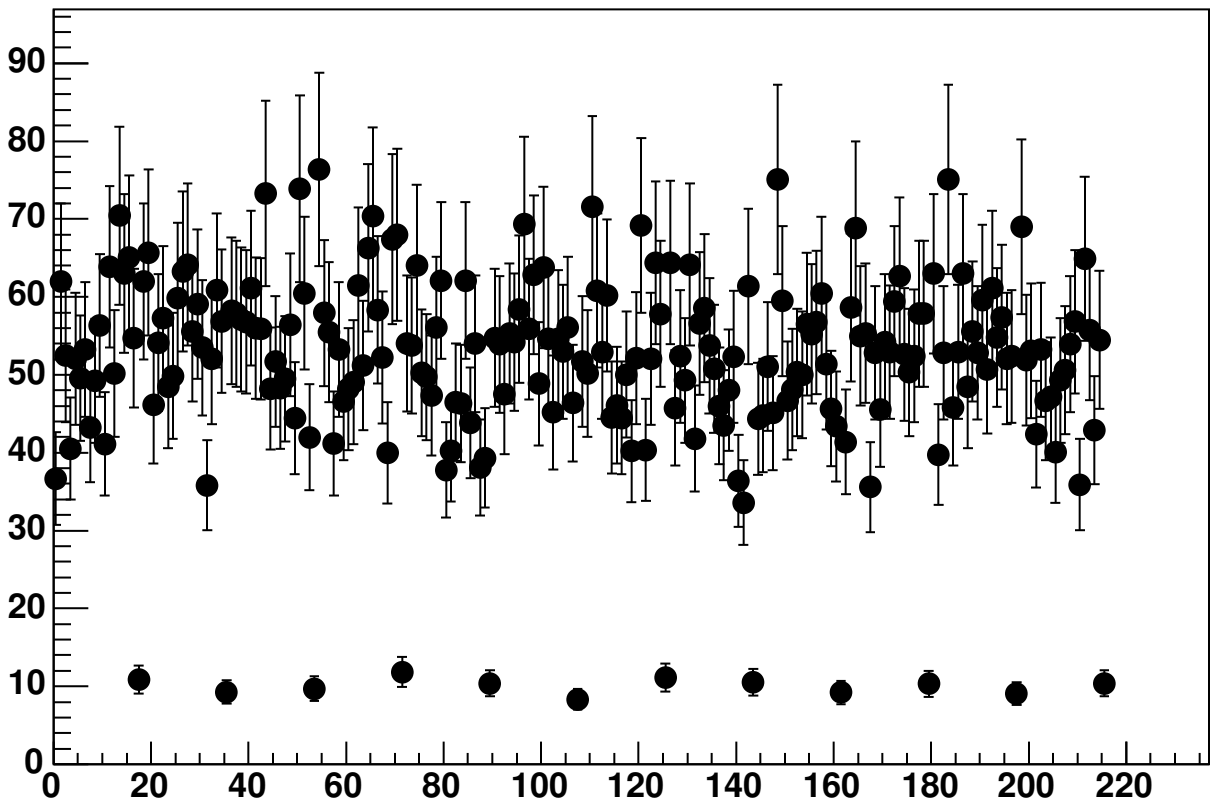
Enable 1, Hold=30, DAC=800, ADC Noise vs 18*Chip+Chan



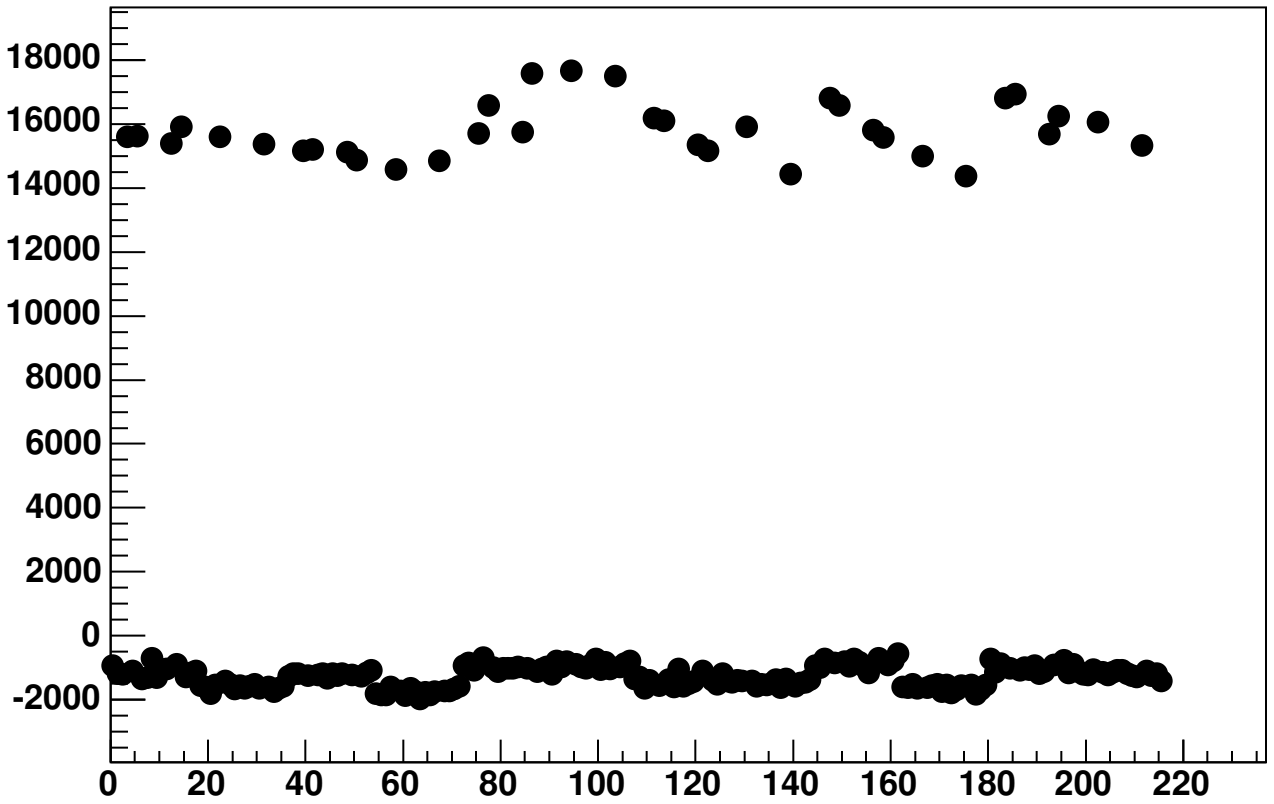
Enable 1, Hold=30, DAC=850, ADC Mean vs 18*Chip+Chan



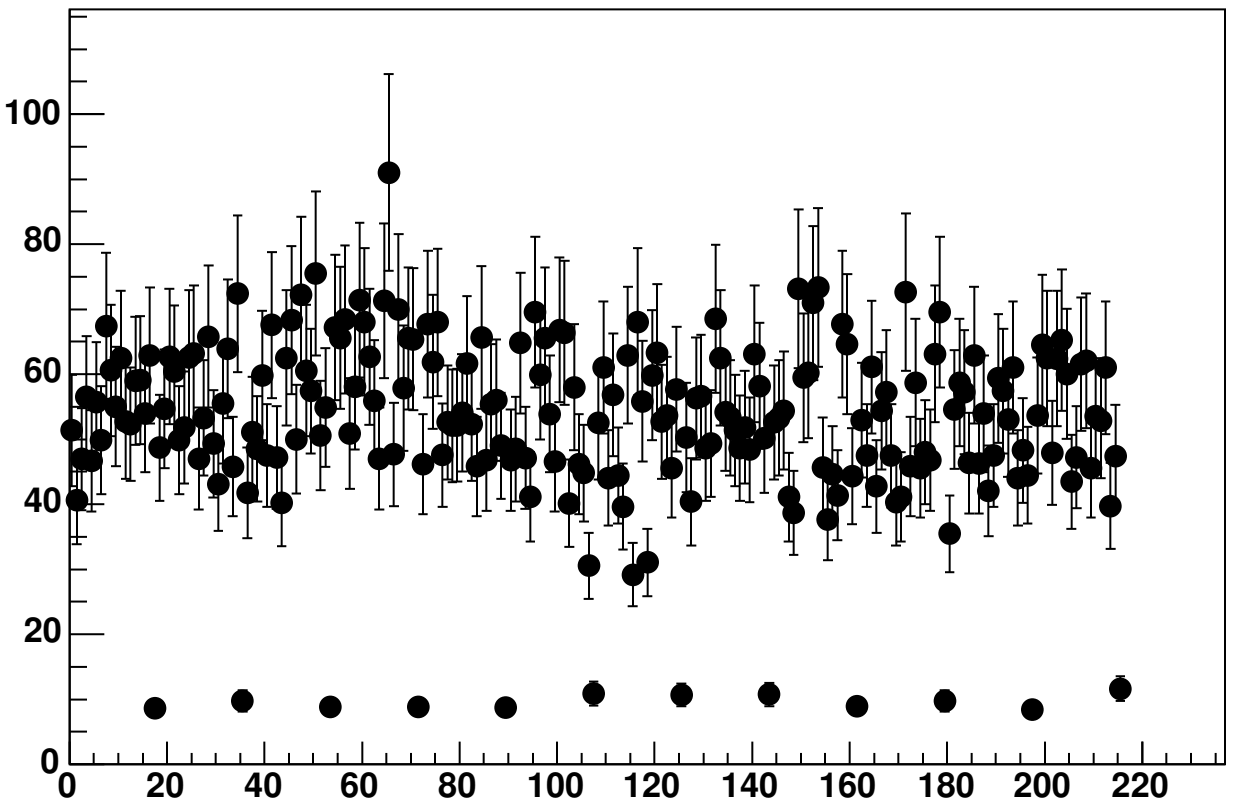
Enable 1, Hold=30, DAC=850, ADC Noise vs 18*Chip+Chan



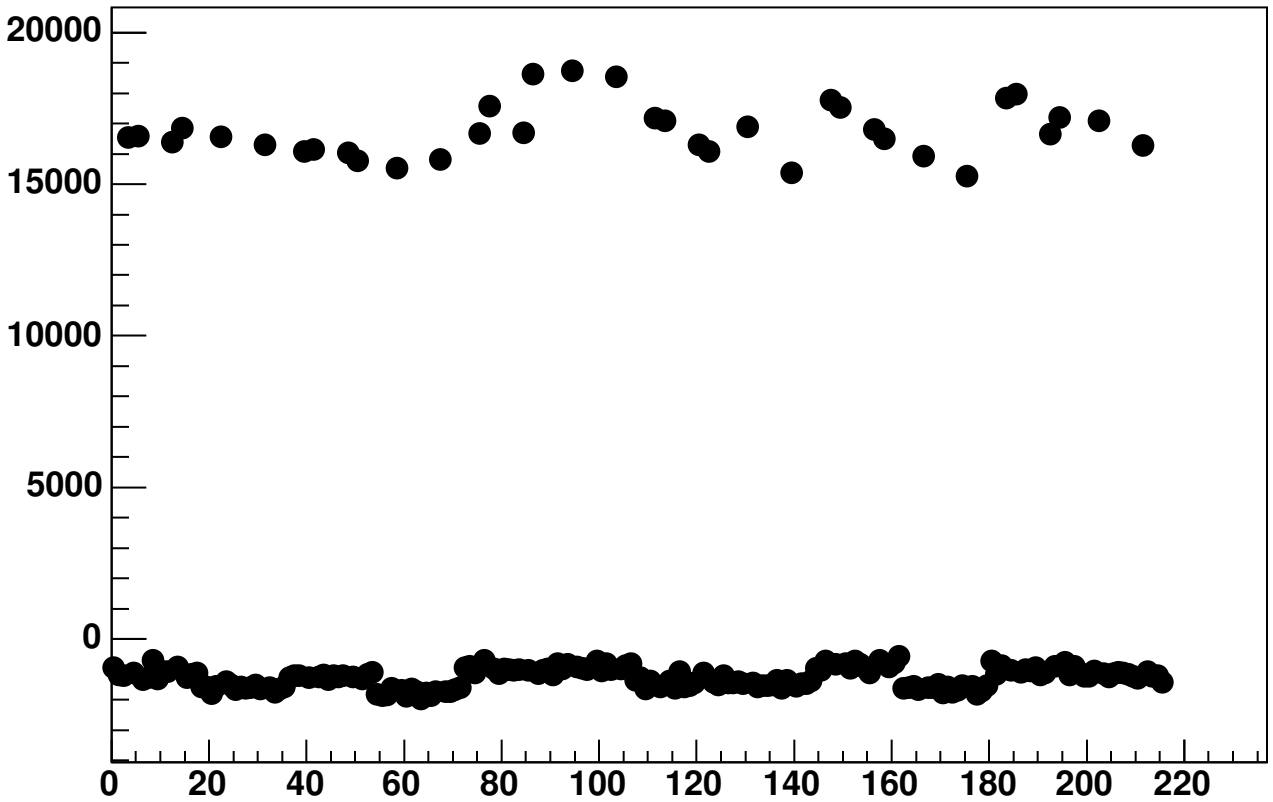
Enable 1, Hold=30, DAC=900, ADC Mean vs 18*Chip+Chan



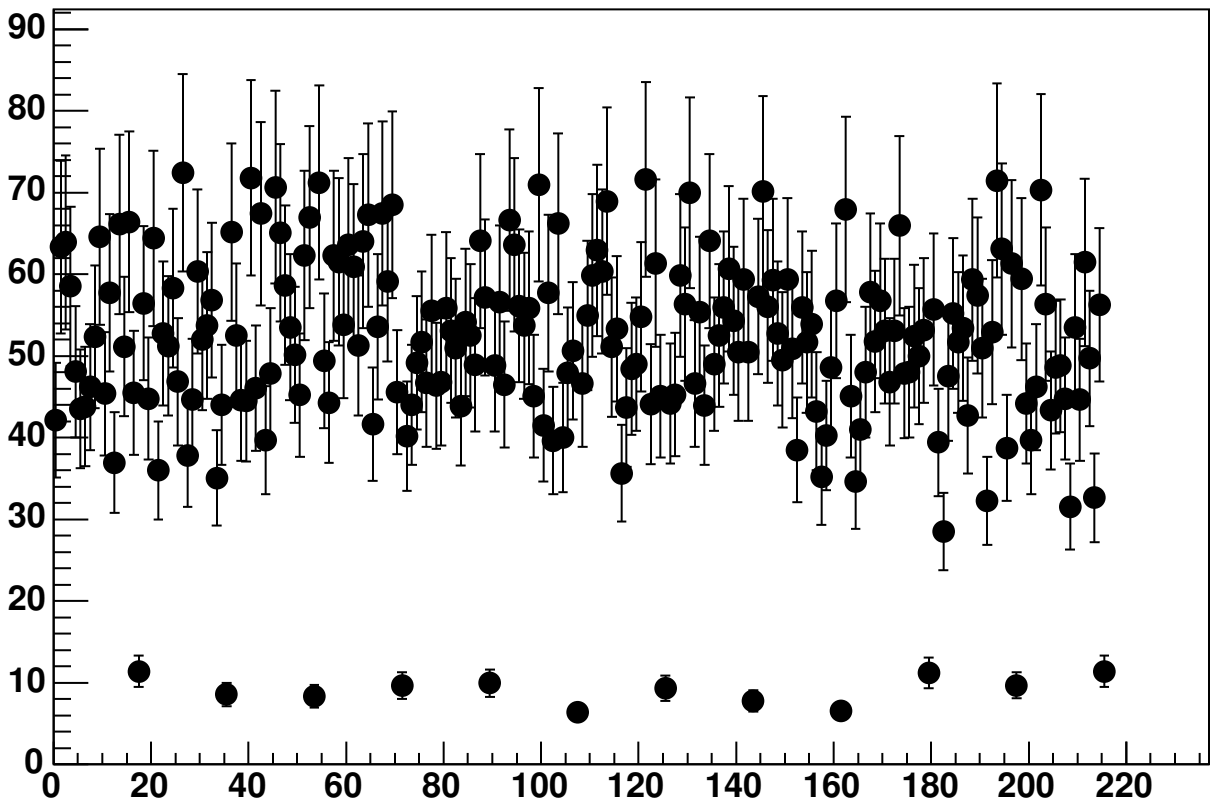
Enable 1, Hold=30, DAC=900, ADC Noise vs 18*Chip+Chan



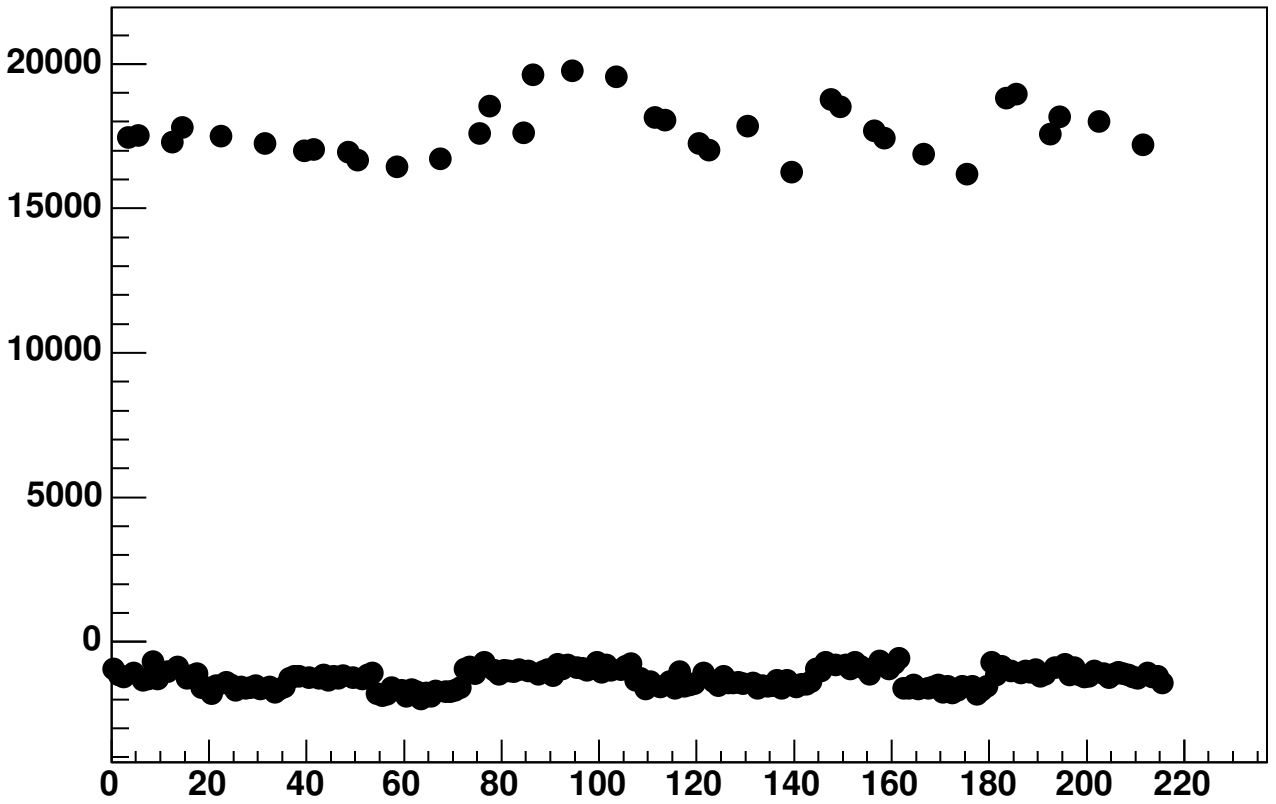
Enable 1, Hold=30, DAC=950, ADC Mean vs 18*Chip+Chan



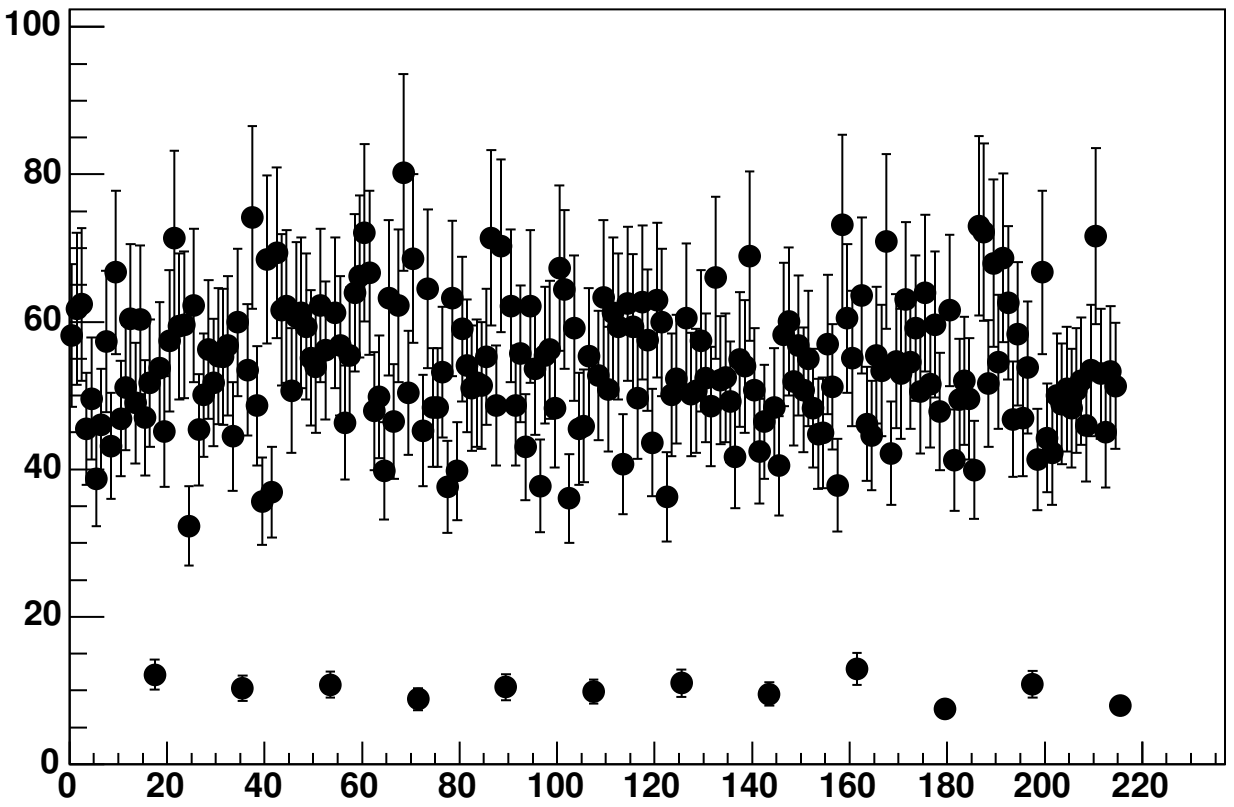
Enable 1, Hold=30, DAC=950, ADC Noise vs 18*Chip+Chan



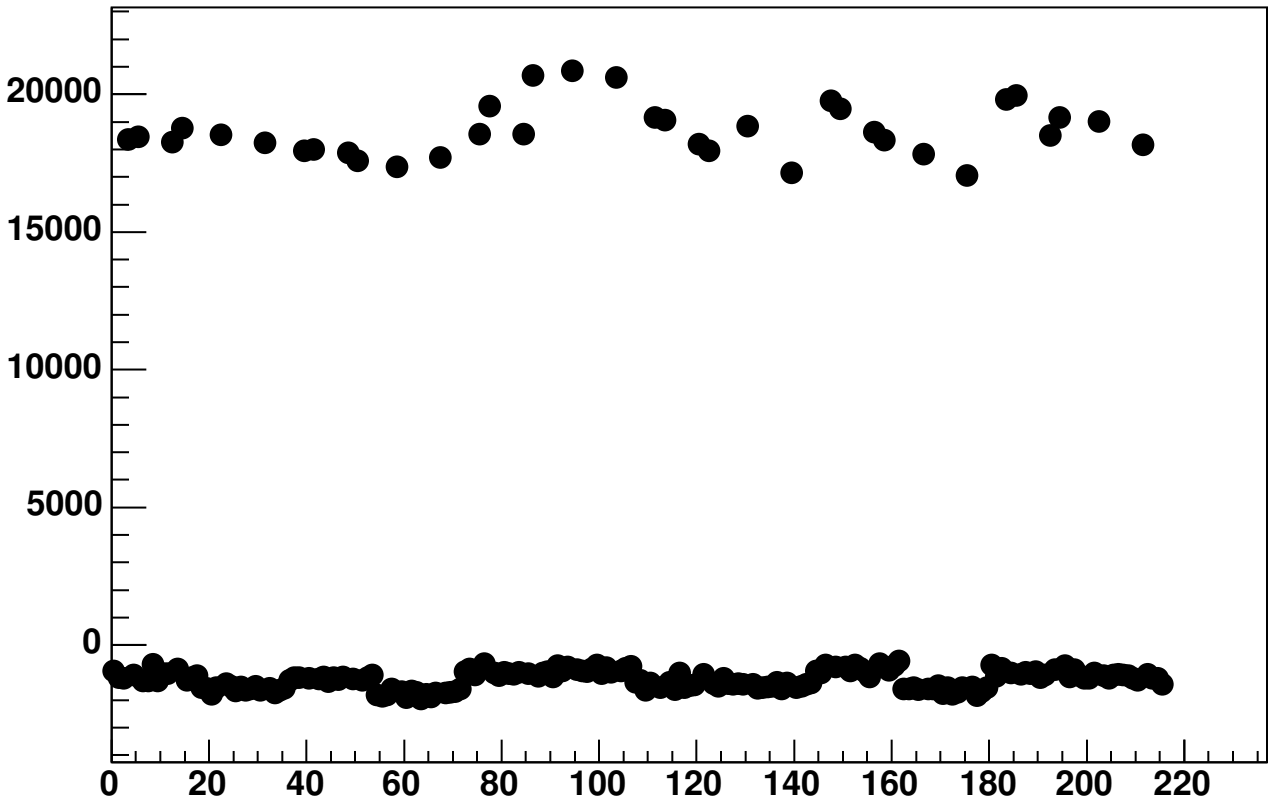
Enable 1, Hold=30, DAC=1000, ADC Mean vs 18*Chip+Chan



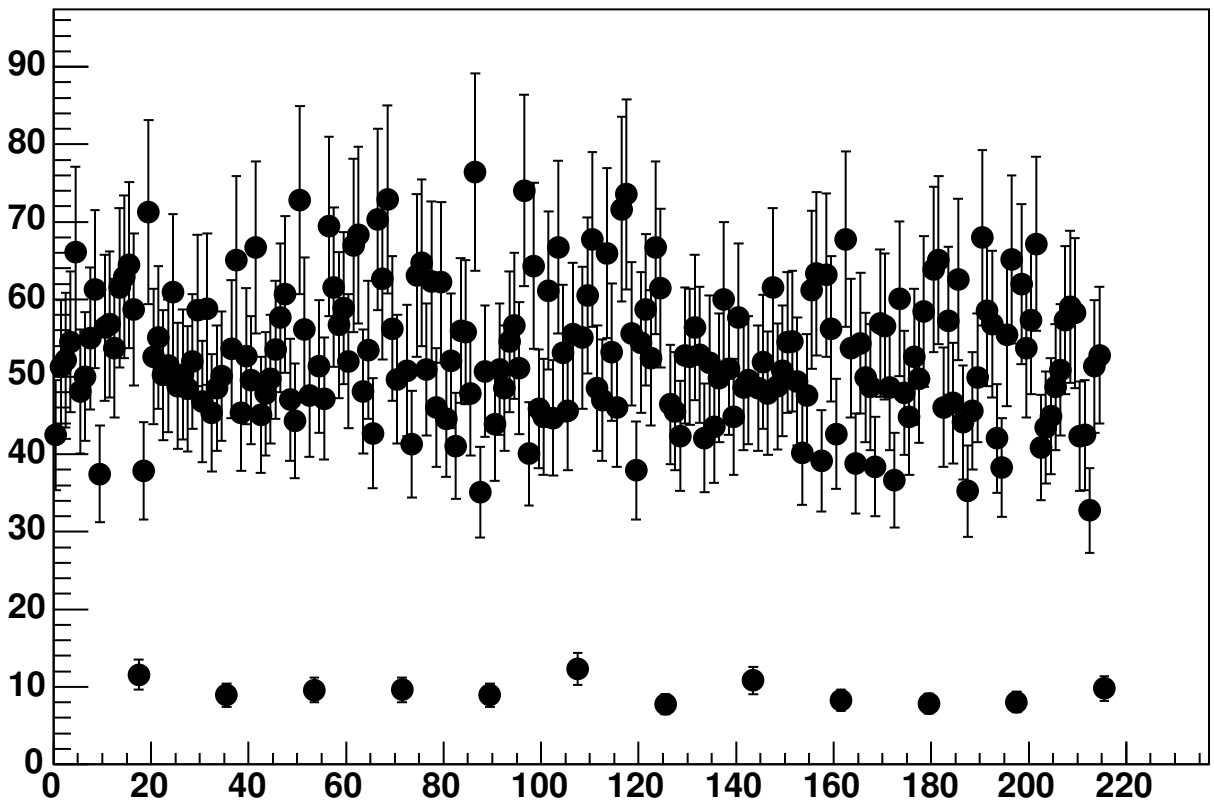
Enable 1, Hold=30, DAC=1000, ADC Noise vs 18*Chip+Chan



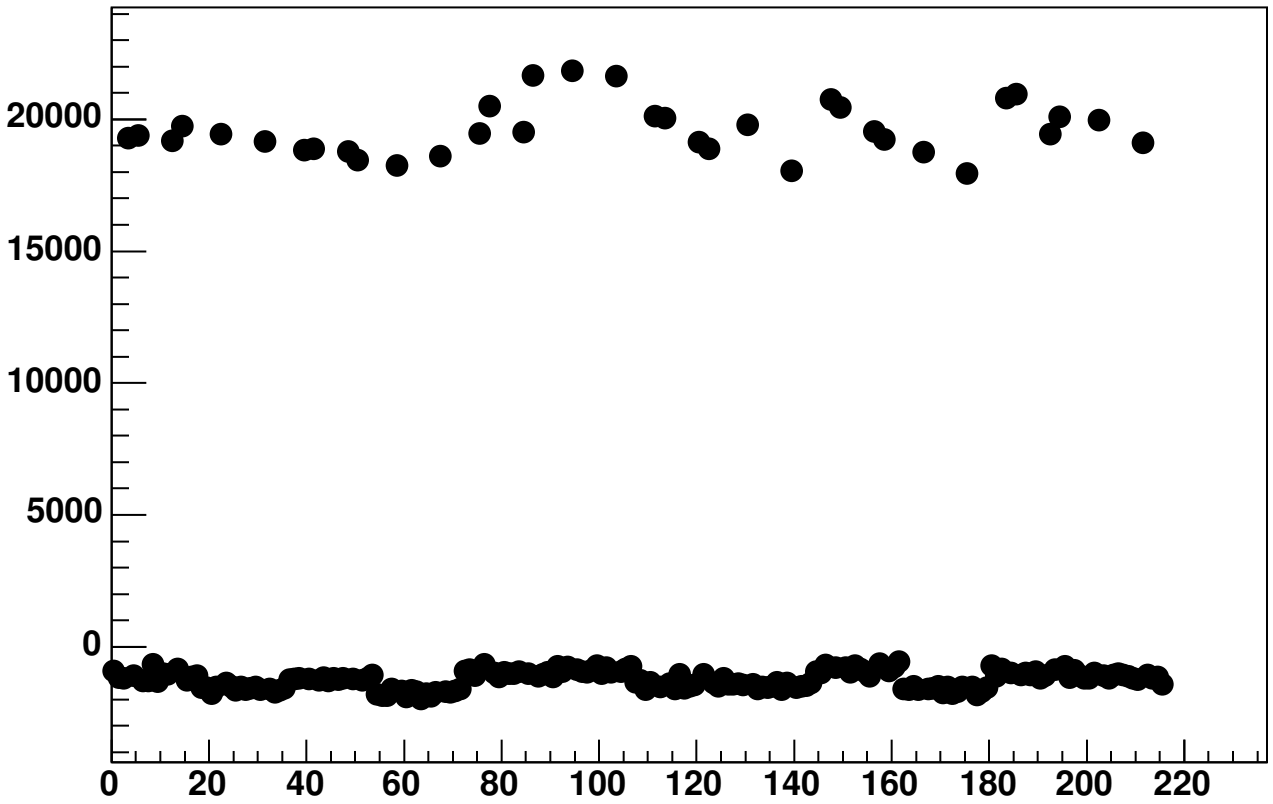
Enable 1, Hold=30, DAC=1050, ADC Mean vs 18*Chip+Chan



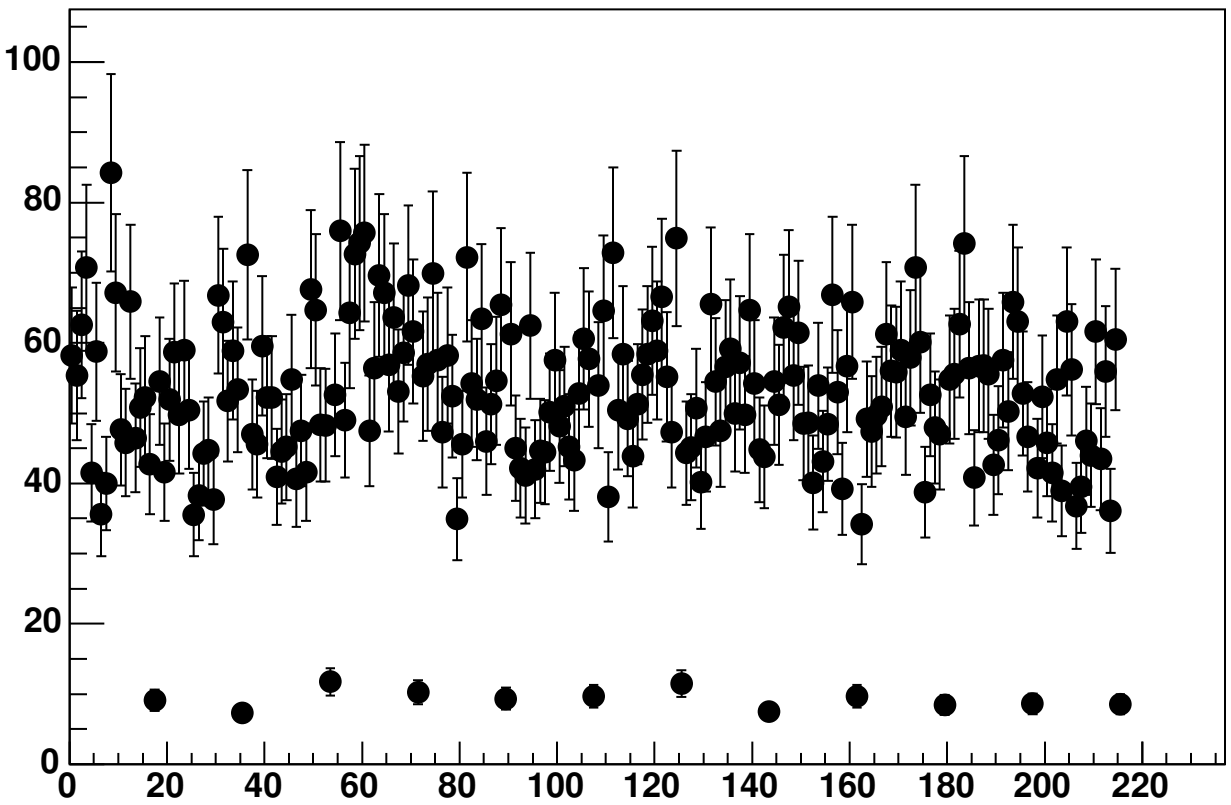
Enable 1, Hold=30, DAC=1050, ADC Noise vs 18*Chip+Chan



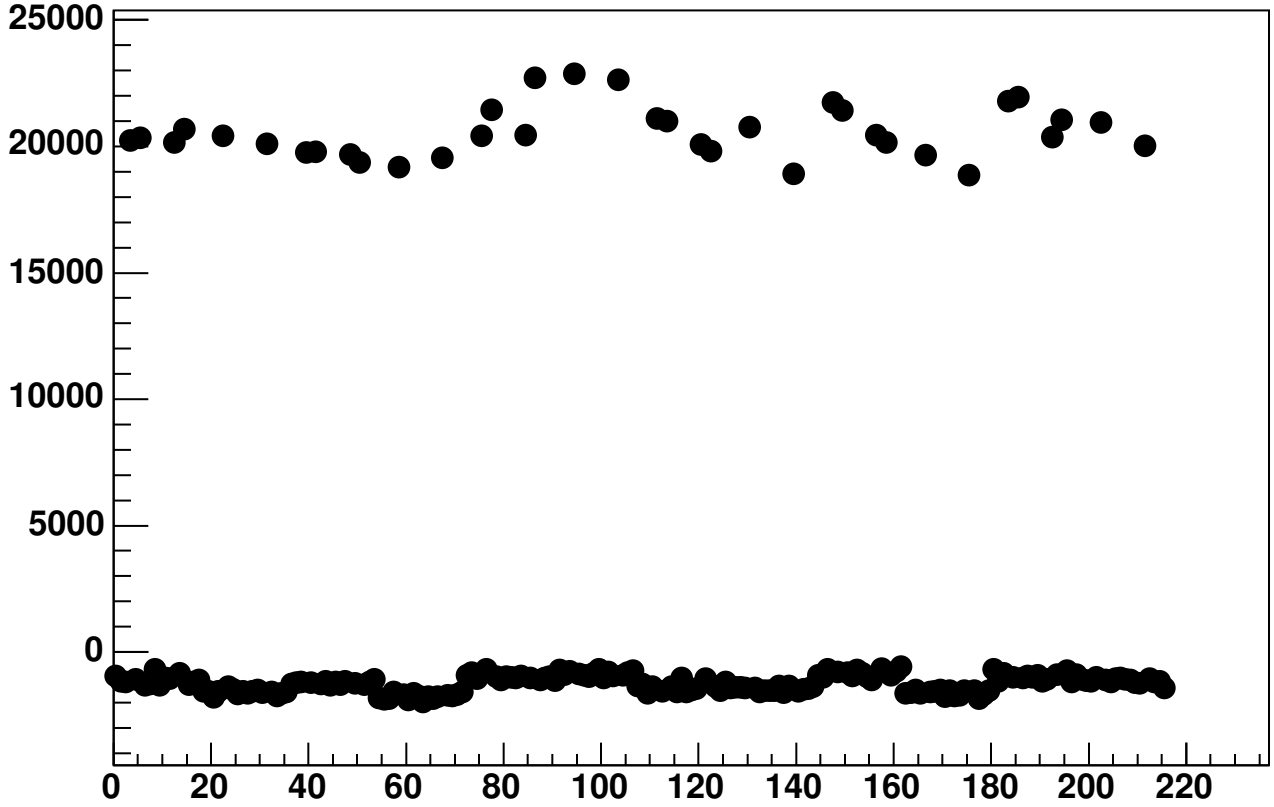
Enable 1, Hold=30, DAC=1100, ADC Mean vs 18*Chip+Chan



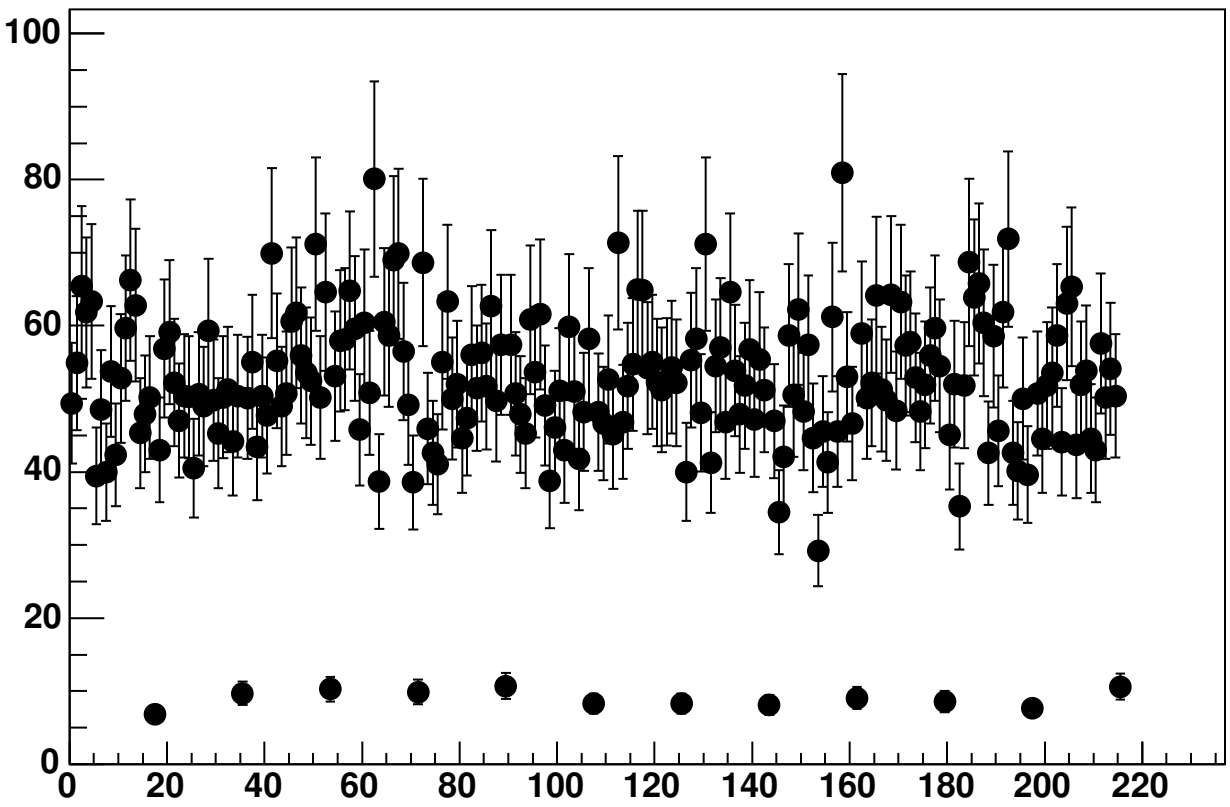
Enable 1, Hold=30, DAC=1100, ADC Noise vs 18*Chip+Chan



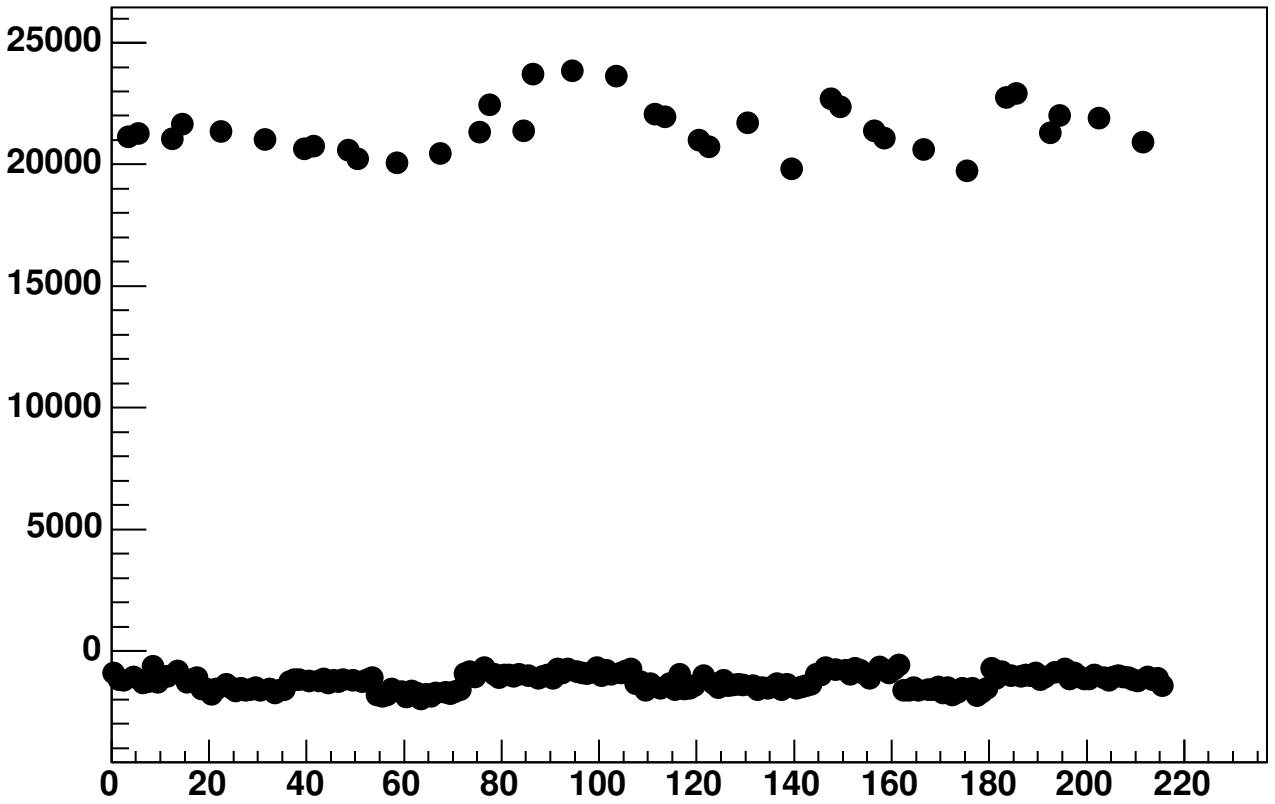
Enable 1, Hold=30, DAC=1150, ADC Mean vs 18*Chip+Chan



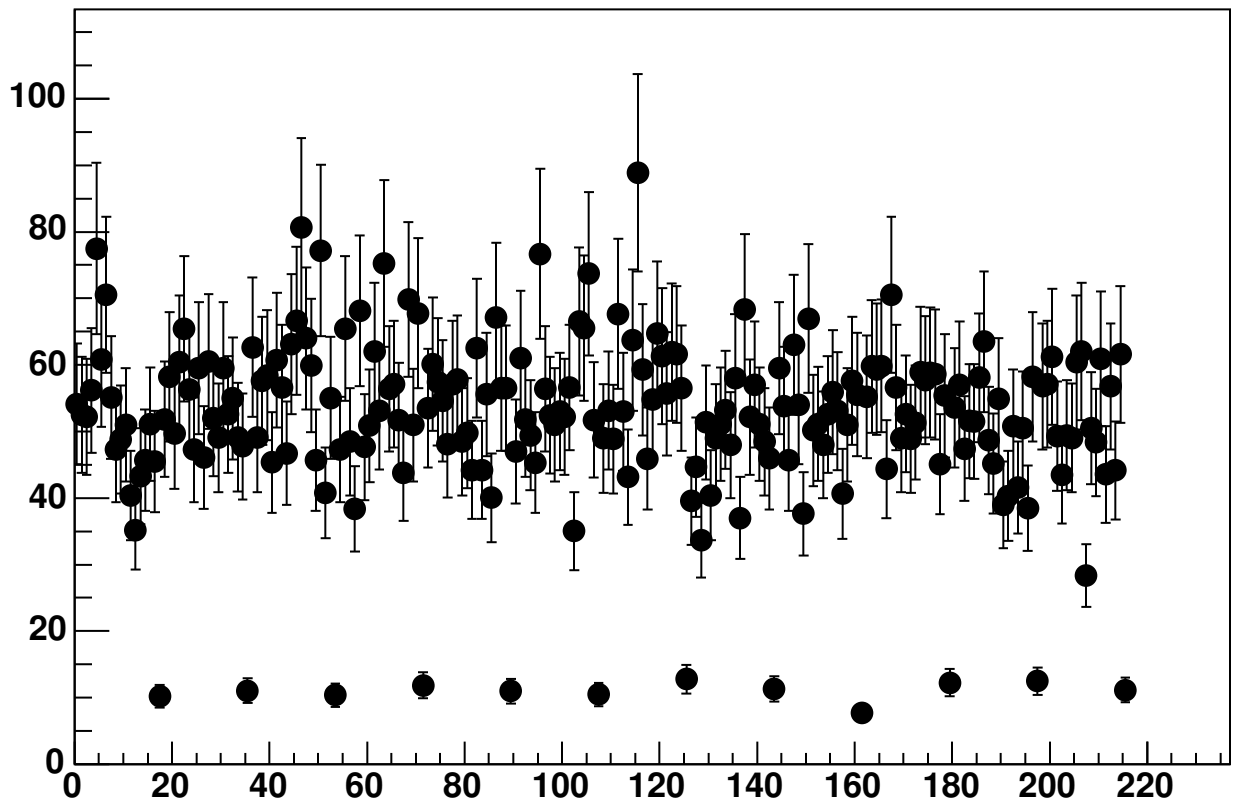
Enable 1, Hold=30, DAC=1150, ADC Noise vs 18*Chip+Chan



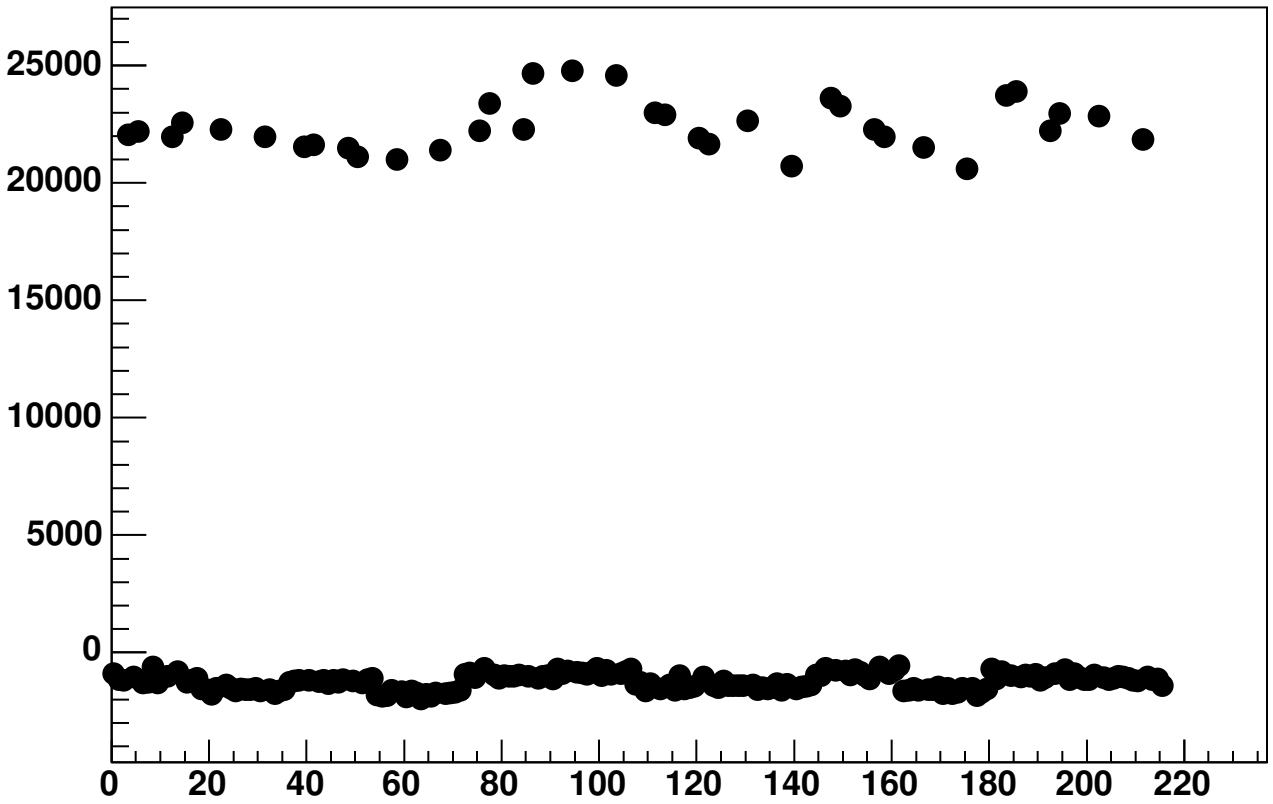
Enable 1, Hold=30, DAC=1200, ADC Mean vs 18*Chip+Chan



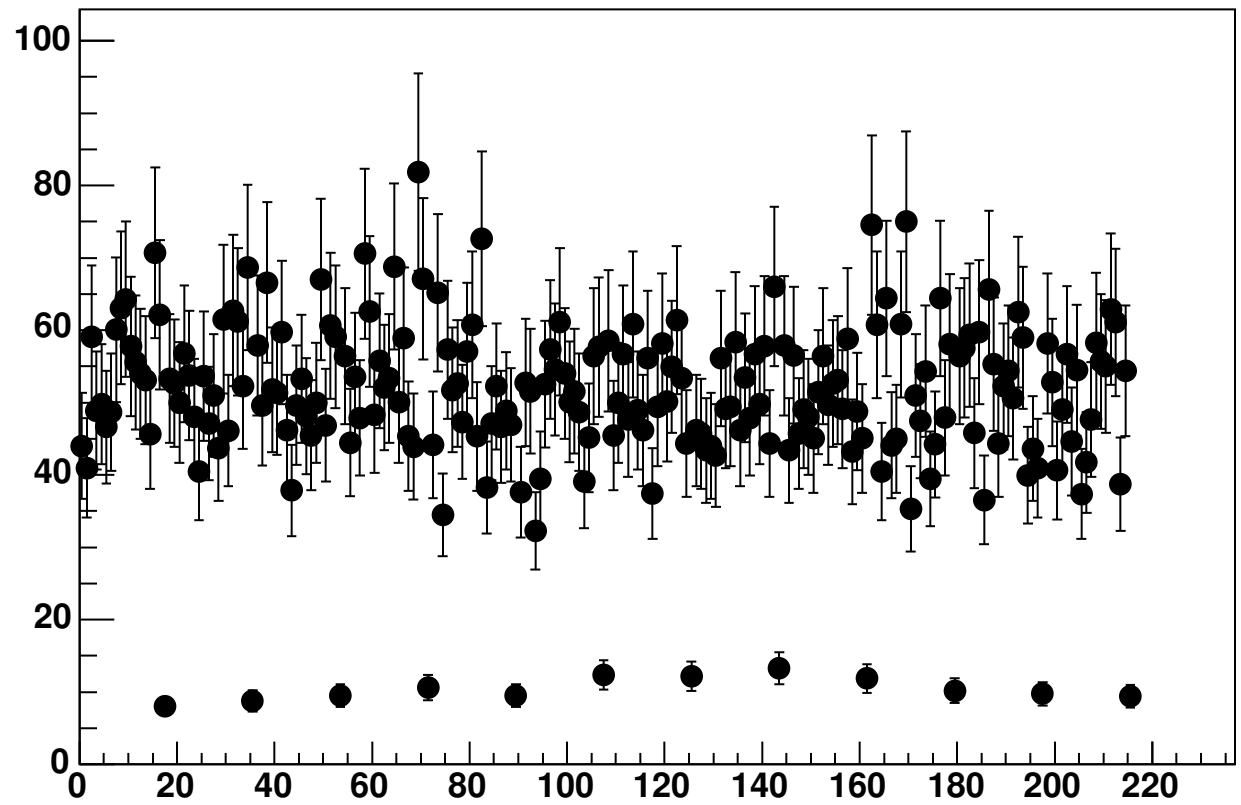
Enable 1, Hold=30, DAC=1200, ADC Noise vs 18*Chip+Chan



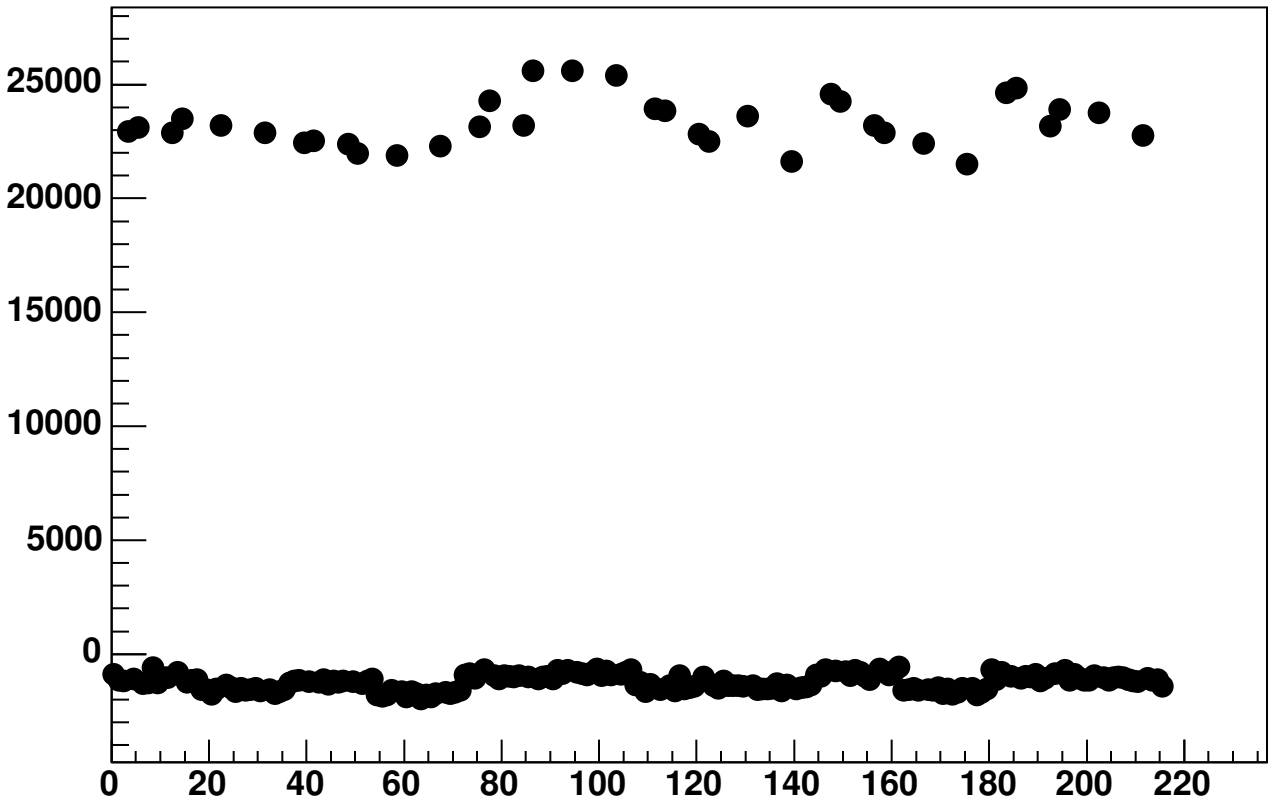
Enable 1, Hold=30, DAC=1250, ADC Mean vs 18*Chip+Chan



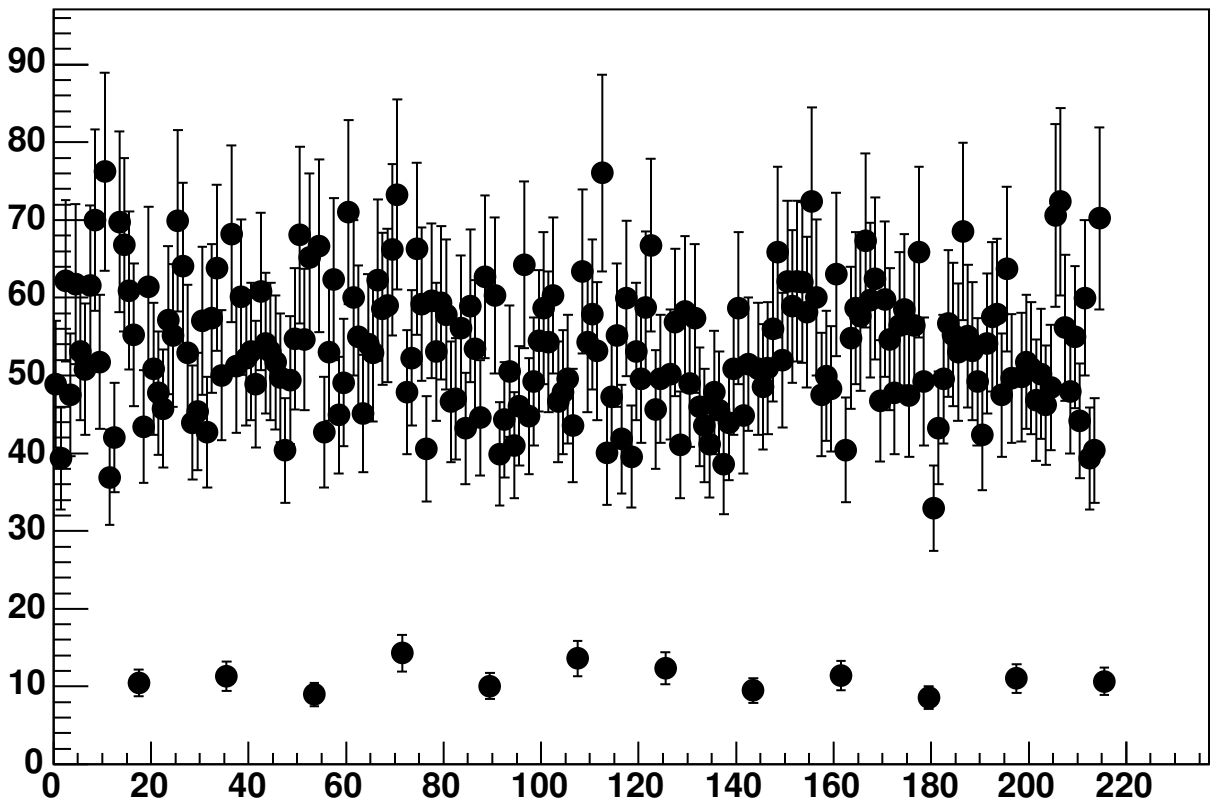
Enable 1, Hold=30, DAC=1250, ADC Noise vs 18*Chip+Chan



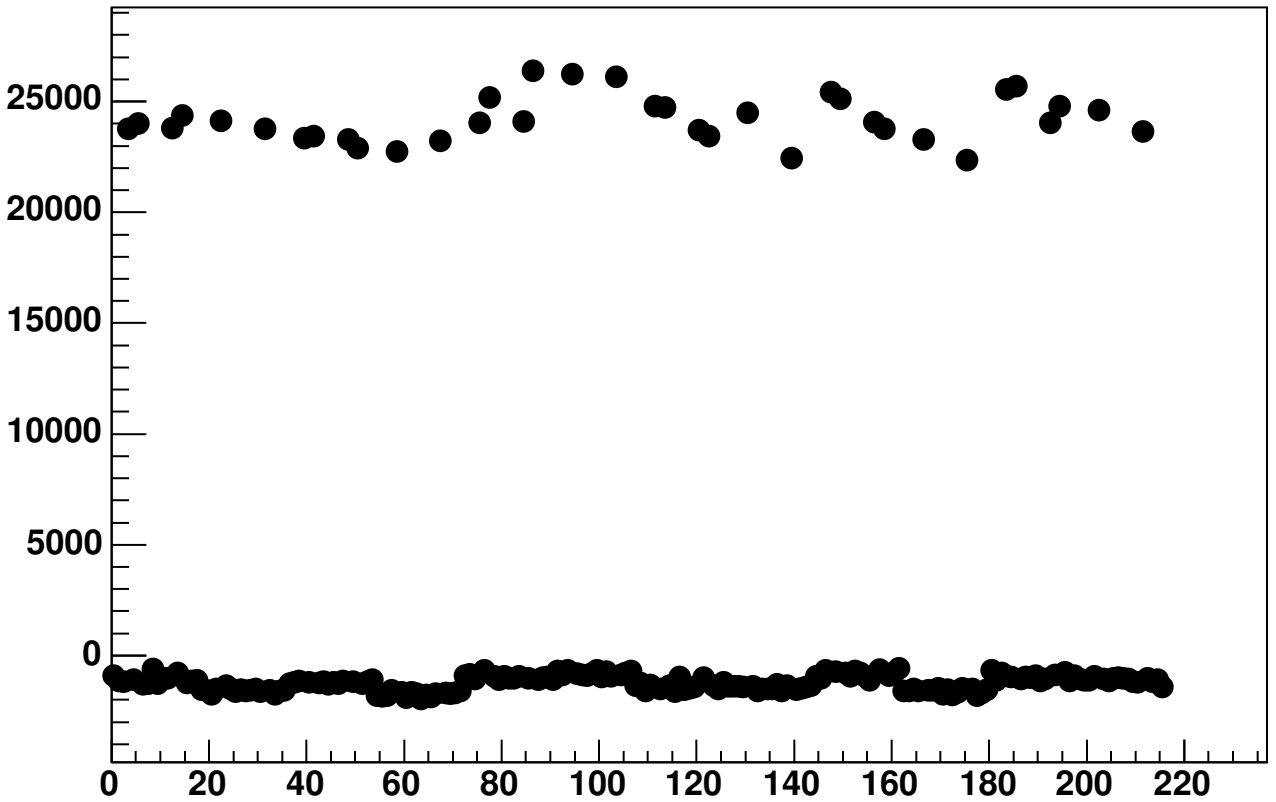
Enable 1, Hold=30, DAC=1300, ADC Mean vs 18*Chip+Chan



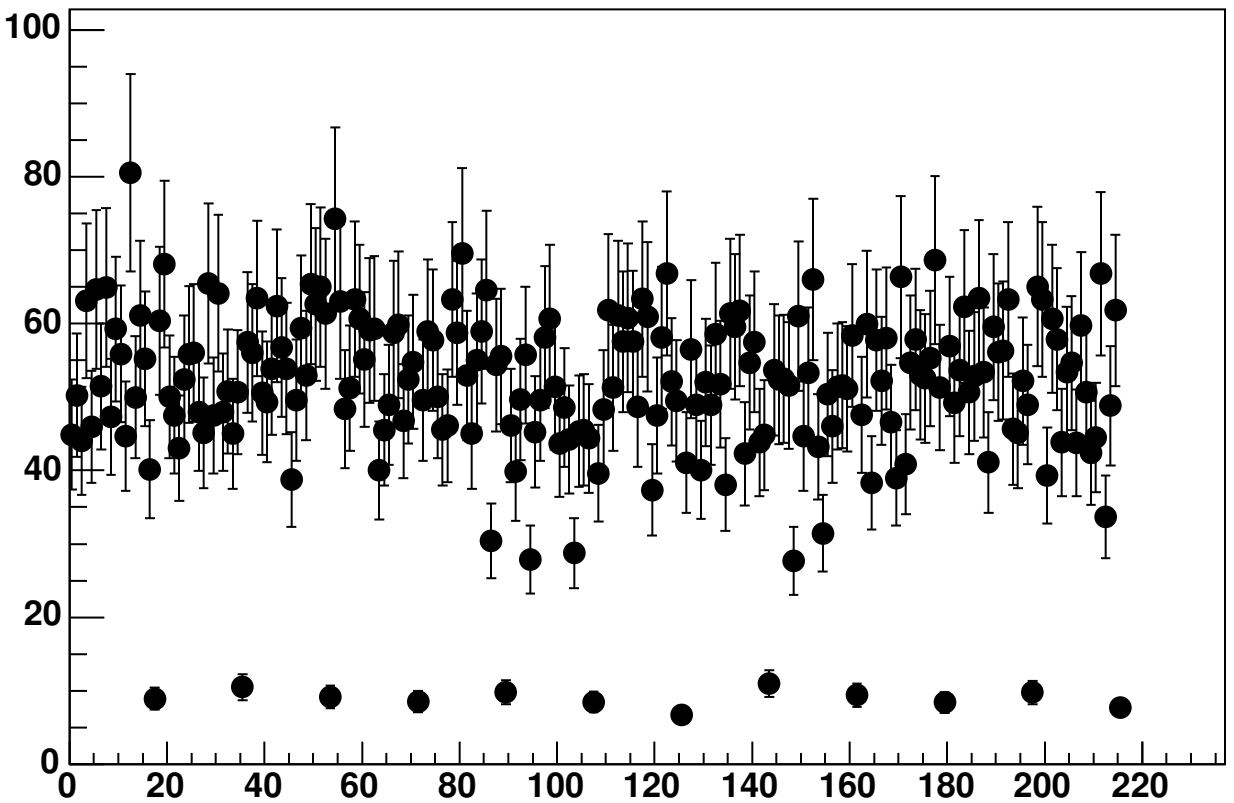
Enable 1, Hold=30, DAC=1300, ADC Noise vs 18*Chip+Chan



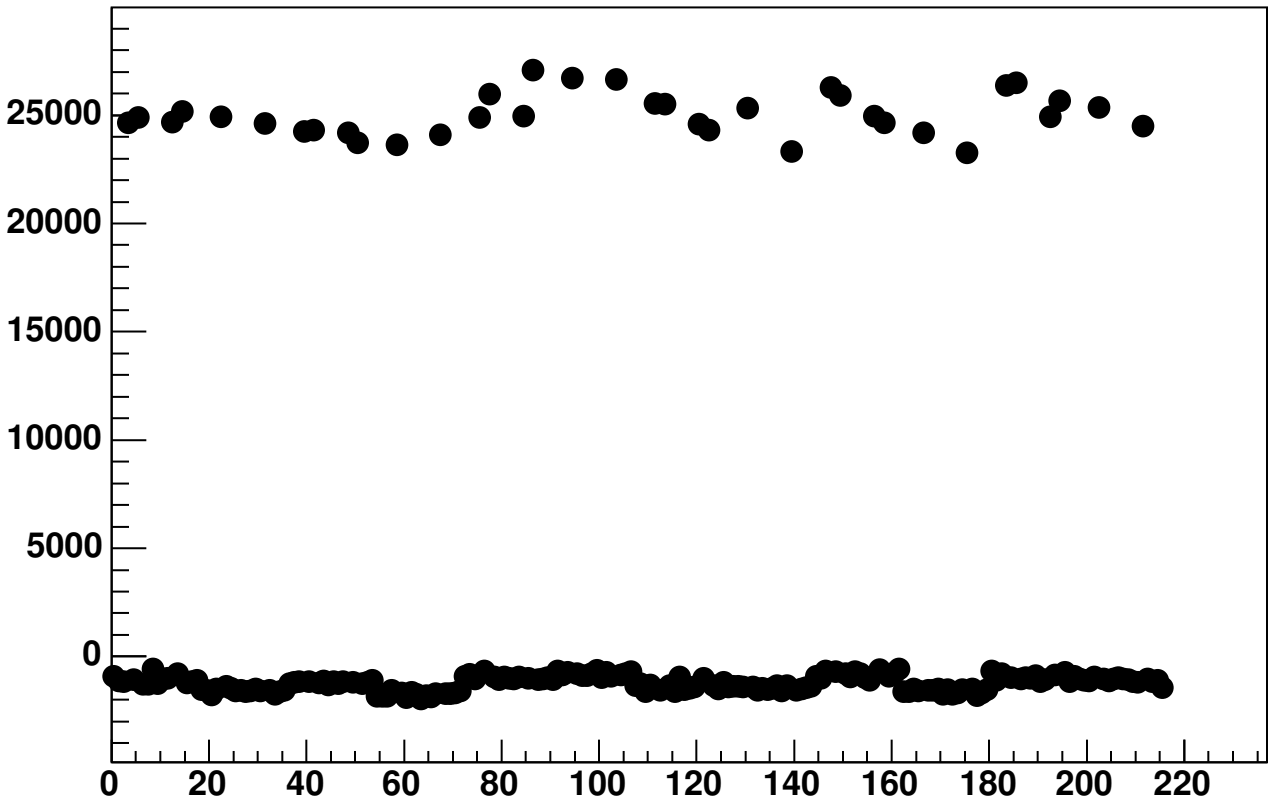
Enable 1, Hold=30, DAC=1350, ADC Mean vs 18*Chip+Chan



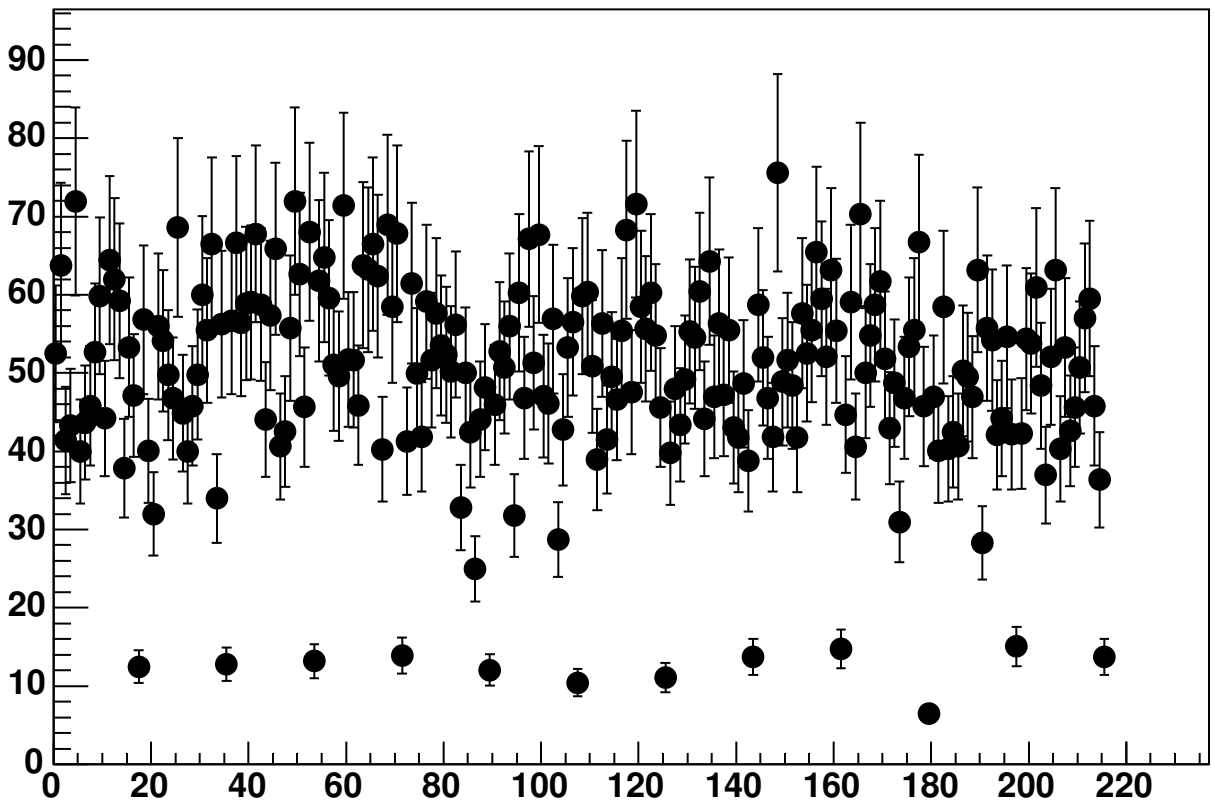
Enable 1, Hold=30, DAC=1350, ADC Noise vs 18*Chip+Chan



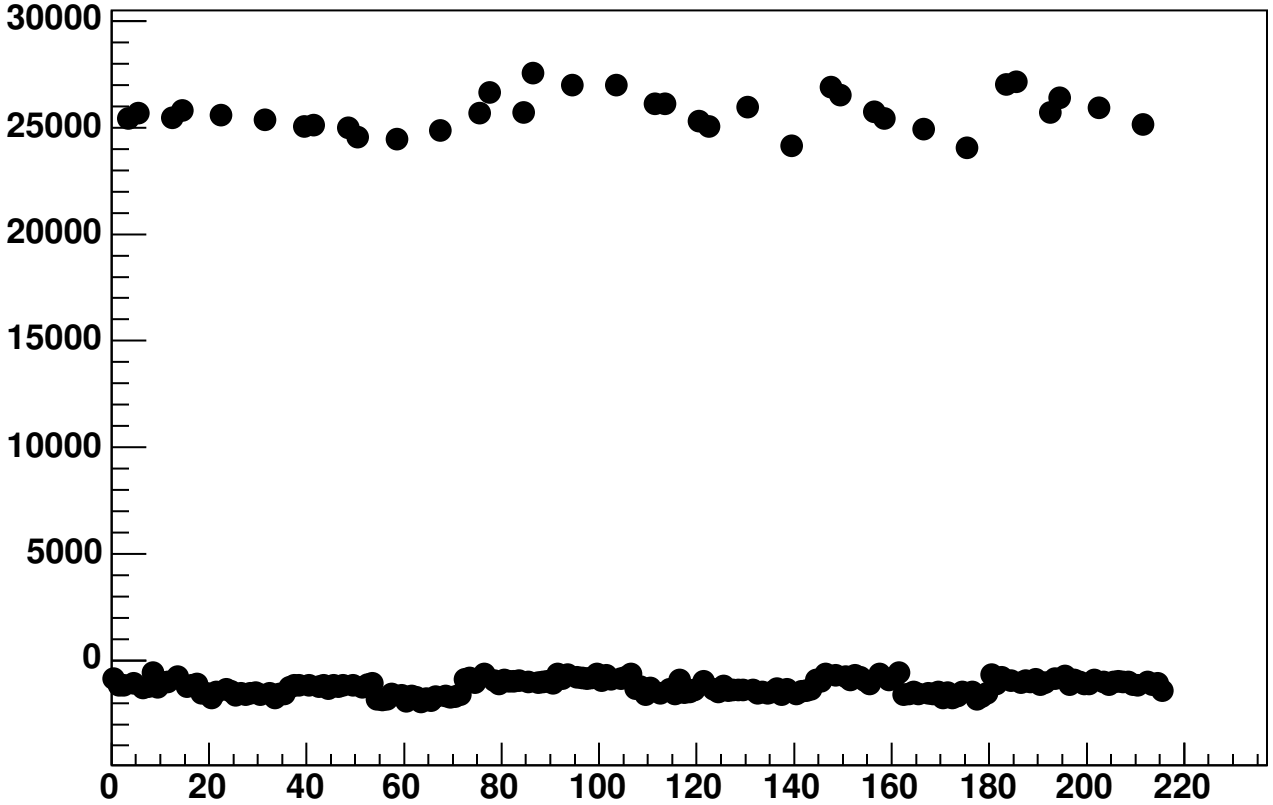
Enable 1, Hold=30, DAC=1400, ADC Mean vs 18*Chip+Chan



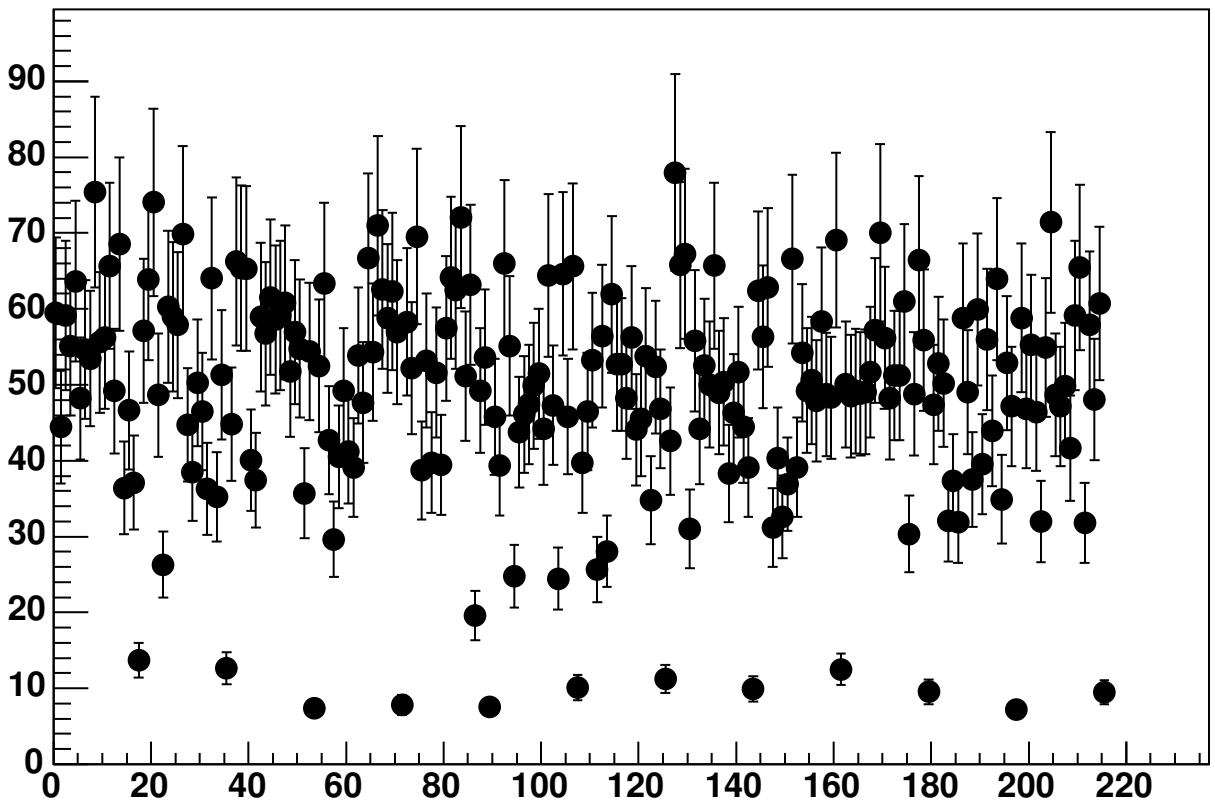
Enable 1, Hold=30, DAC=1400, ADC Noise vs 18*Chip+Chan



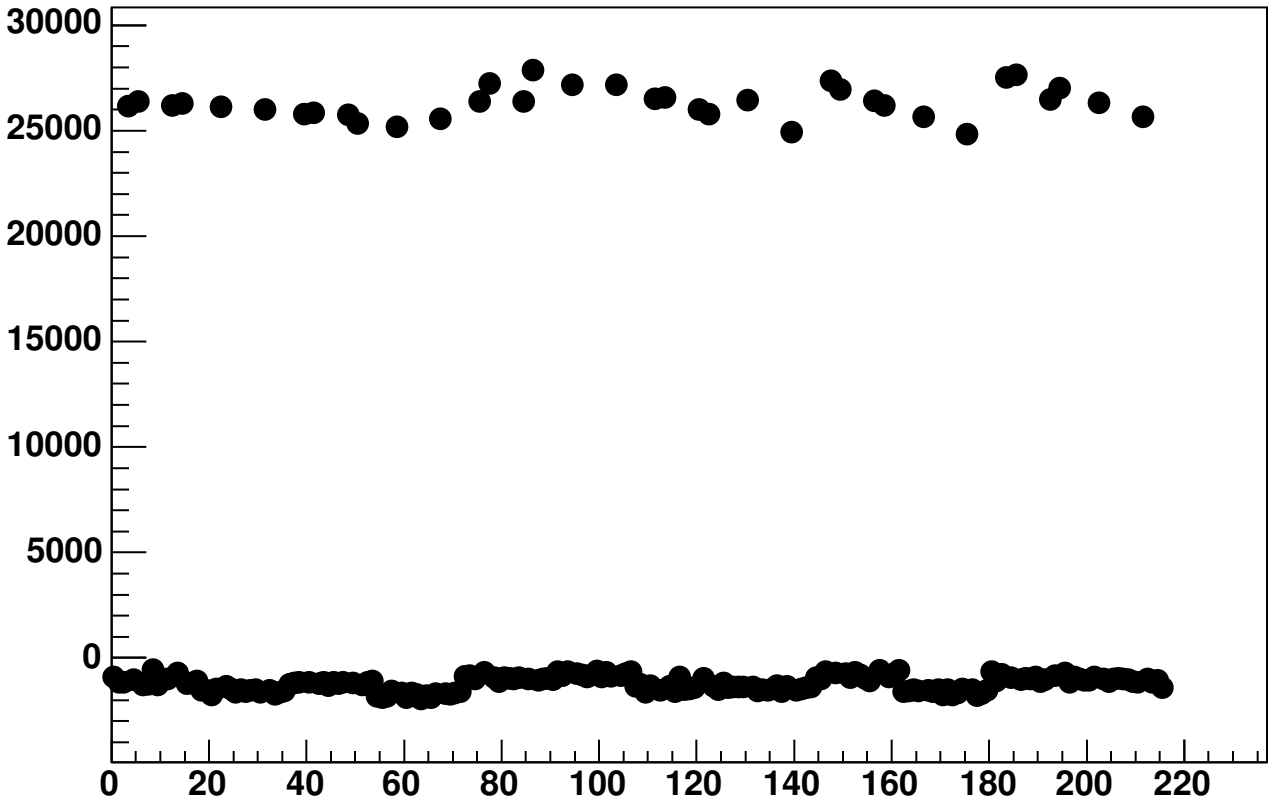
Enable 1, Hold=30, DAC=1450, ADC Mean vs 18*Chip+Chan



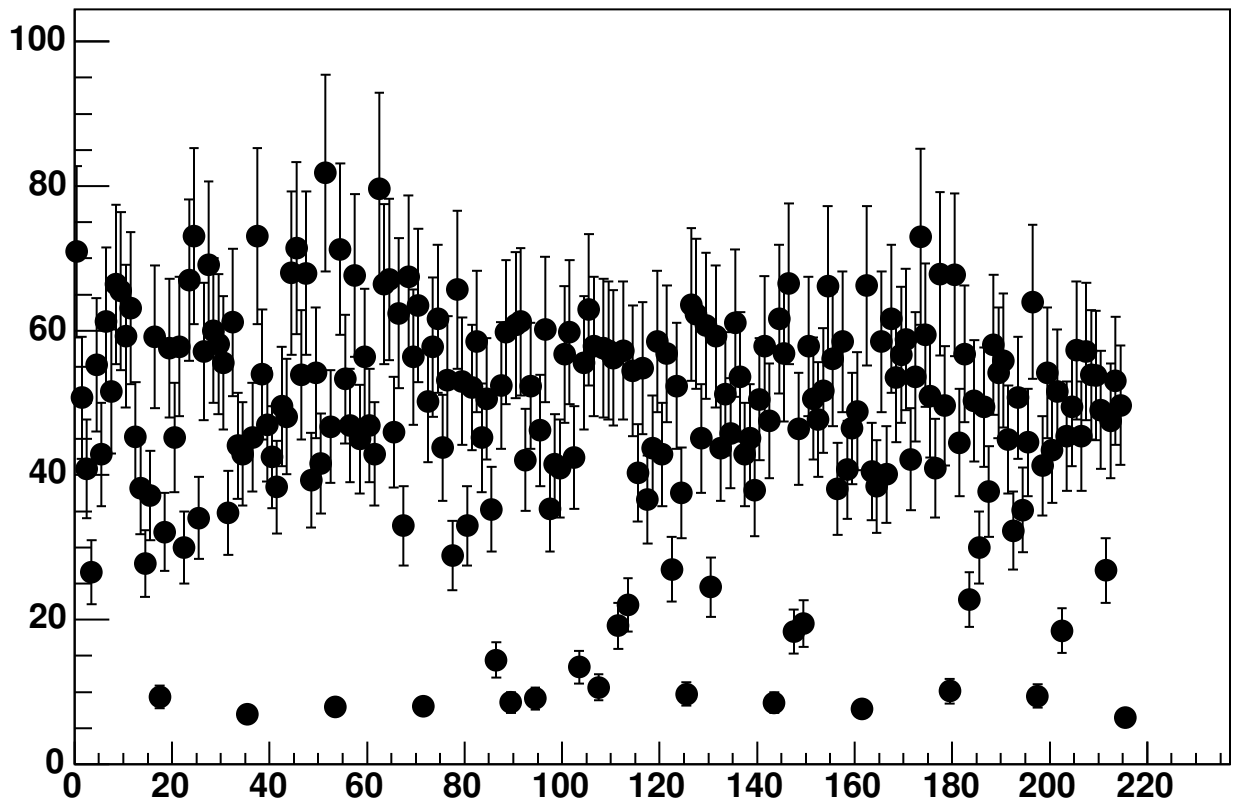
Enable 1, Hold=30, DAC=1450, ADC Noise vs 18*Chip+Chan



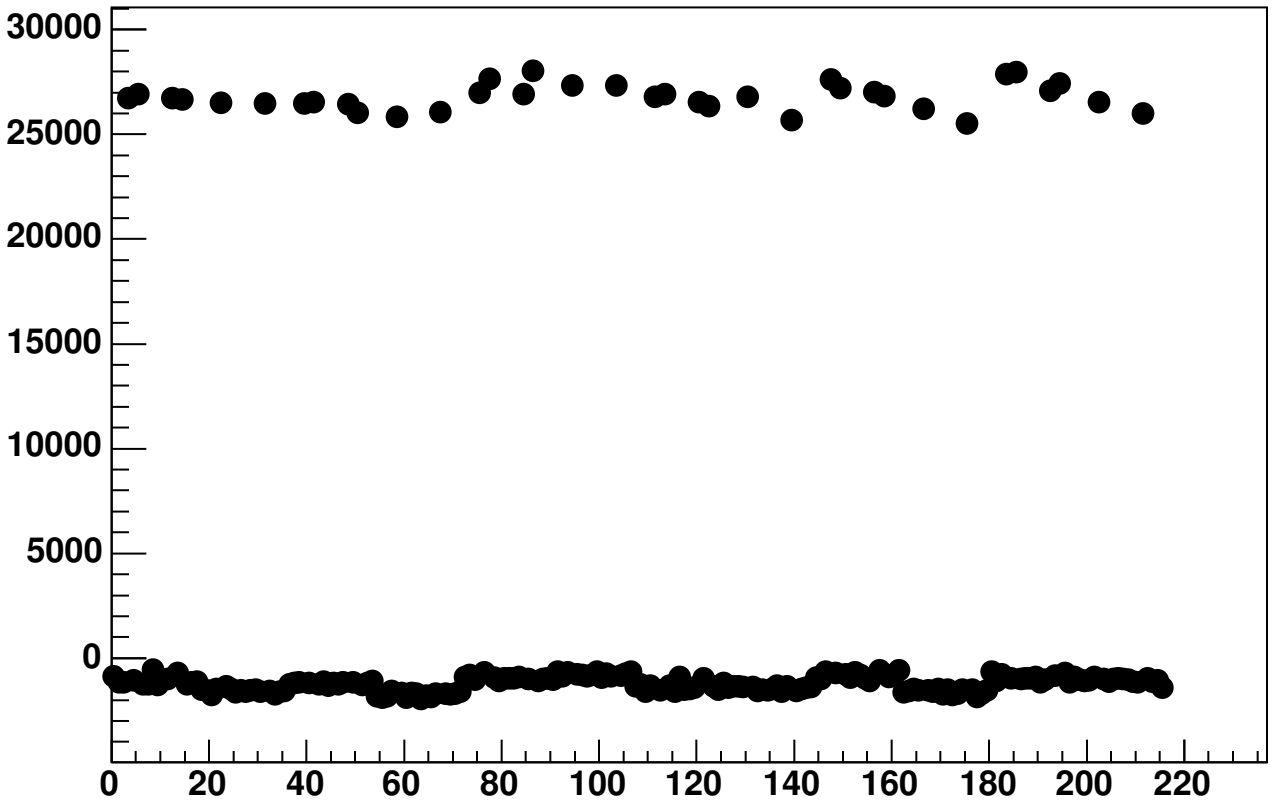
Enable 1, Hold=30, DAC=1500, ADC Mean vs 18*Chip+Chan



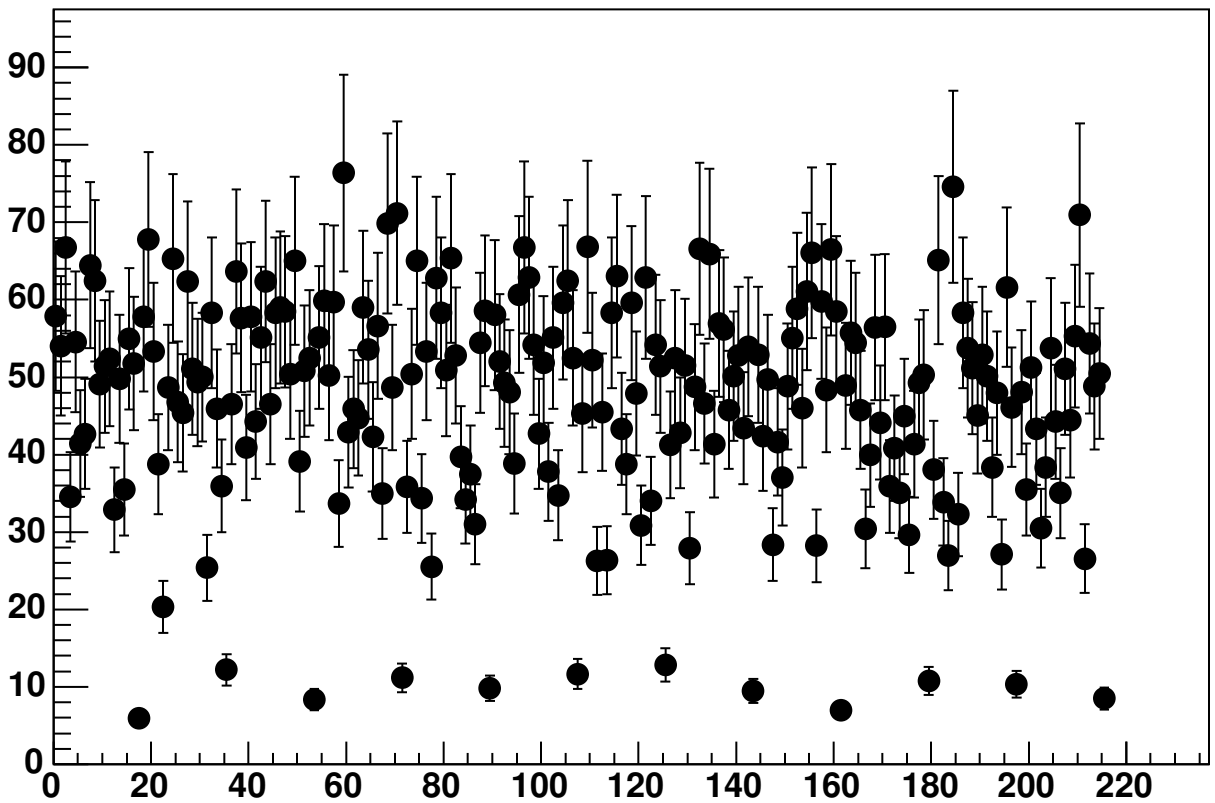
Enable 1, Hold=30, DAC=1500, ADC Noise vs 18*Chip+Chan



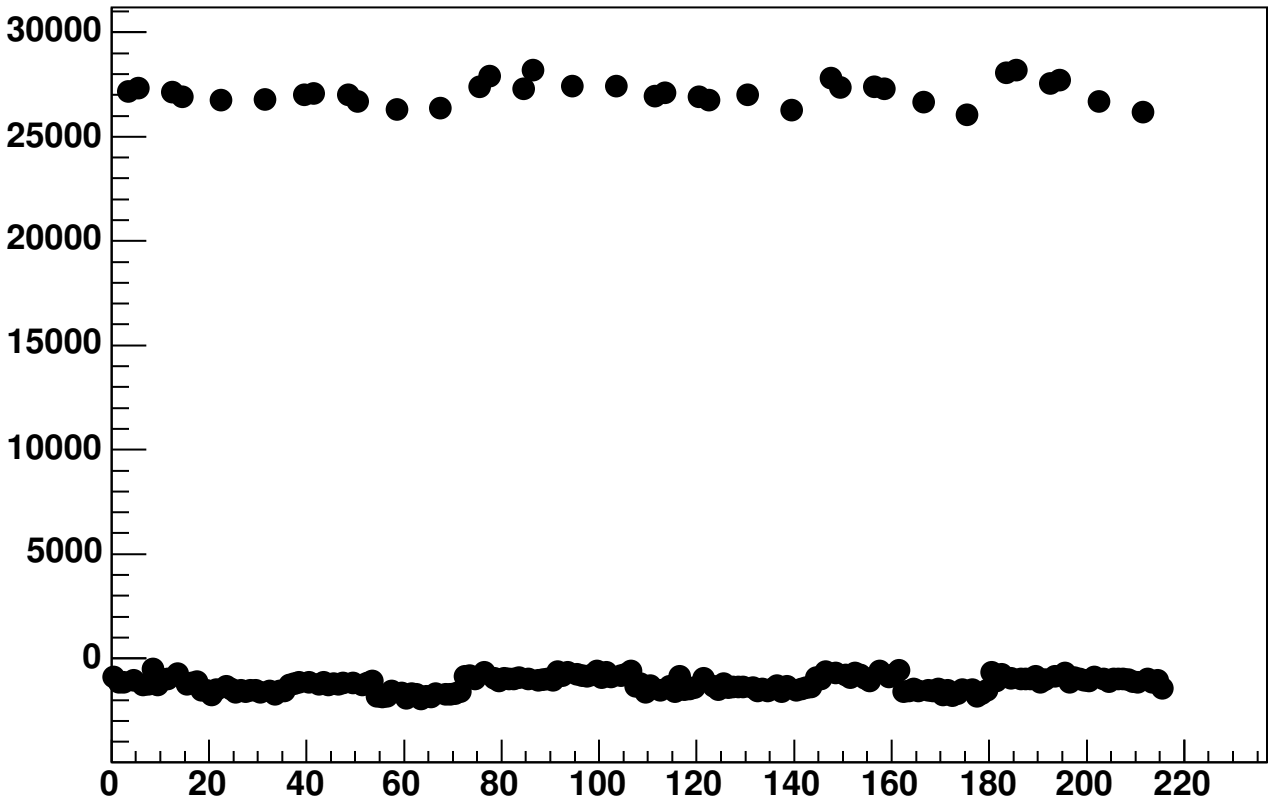
Enable 1, Hold=30, DAC=1550, ADC Mean vs 18*Chip+Chan



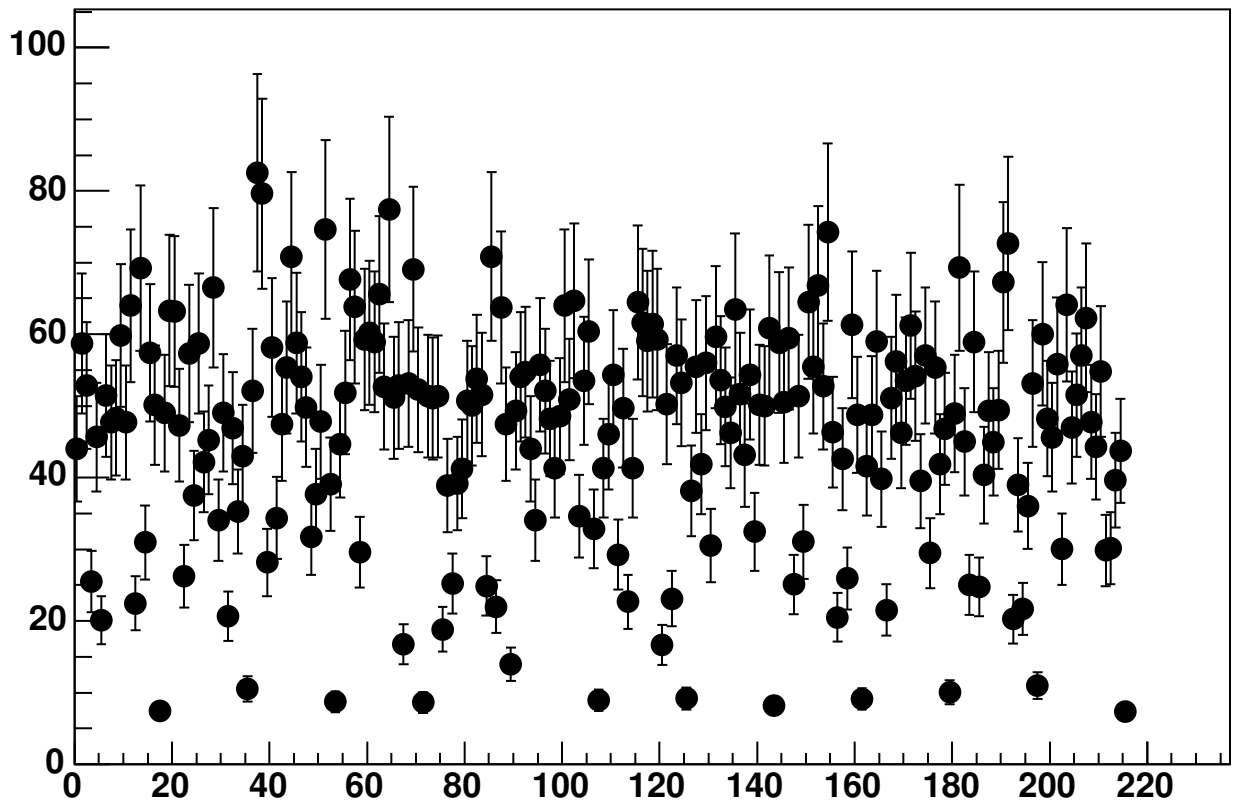
Enable 1, Hold=30, DAC=1550, ADC Noise vs 18*Chip+Chan



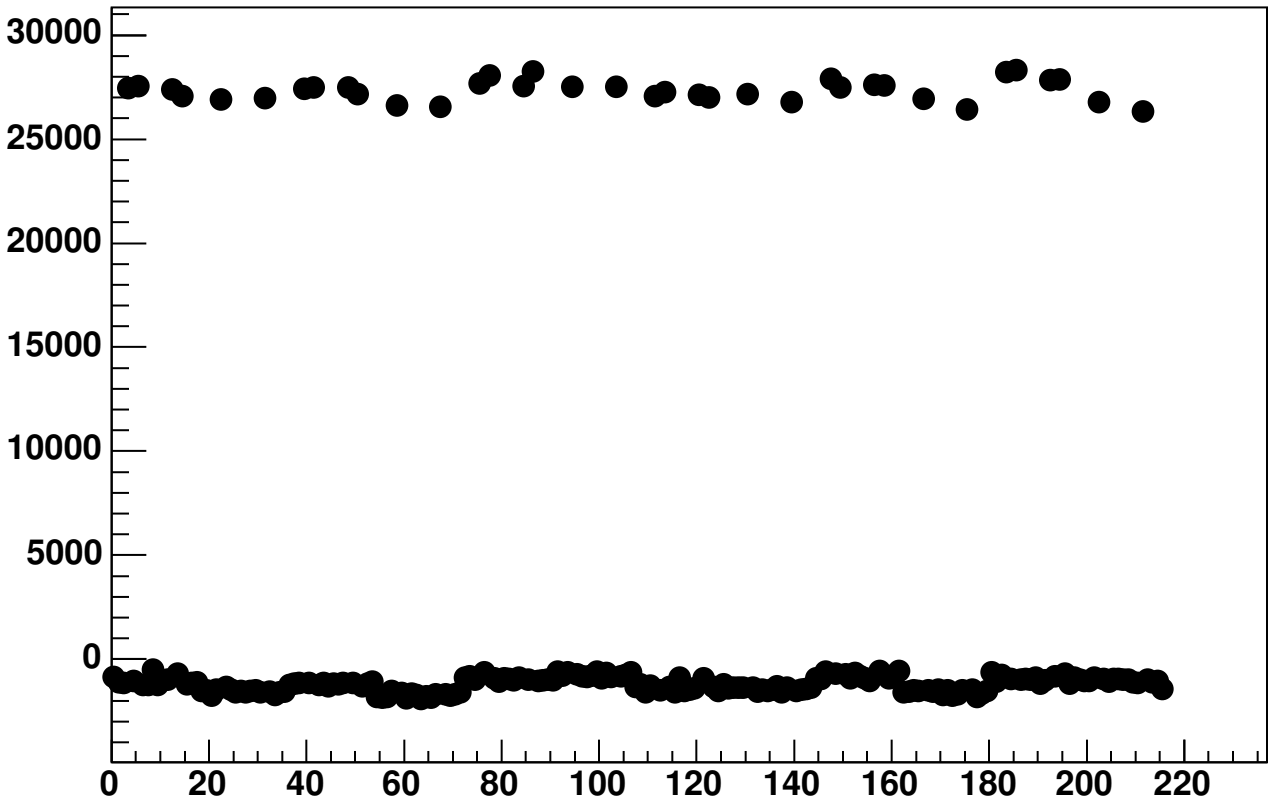
Enable 1, Hold=30, DAC=1600, ADC Mean vs 18*Chip+Chan



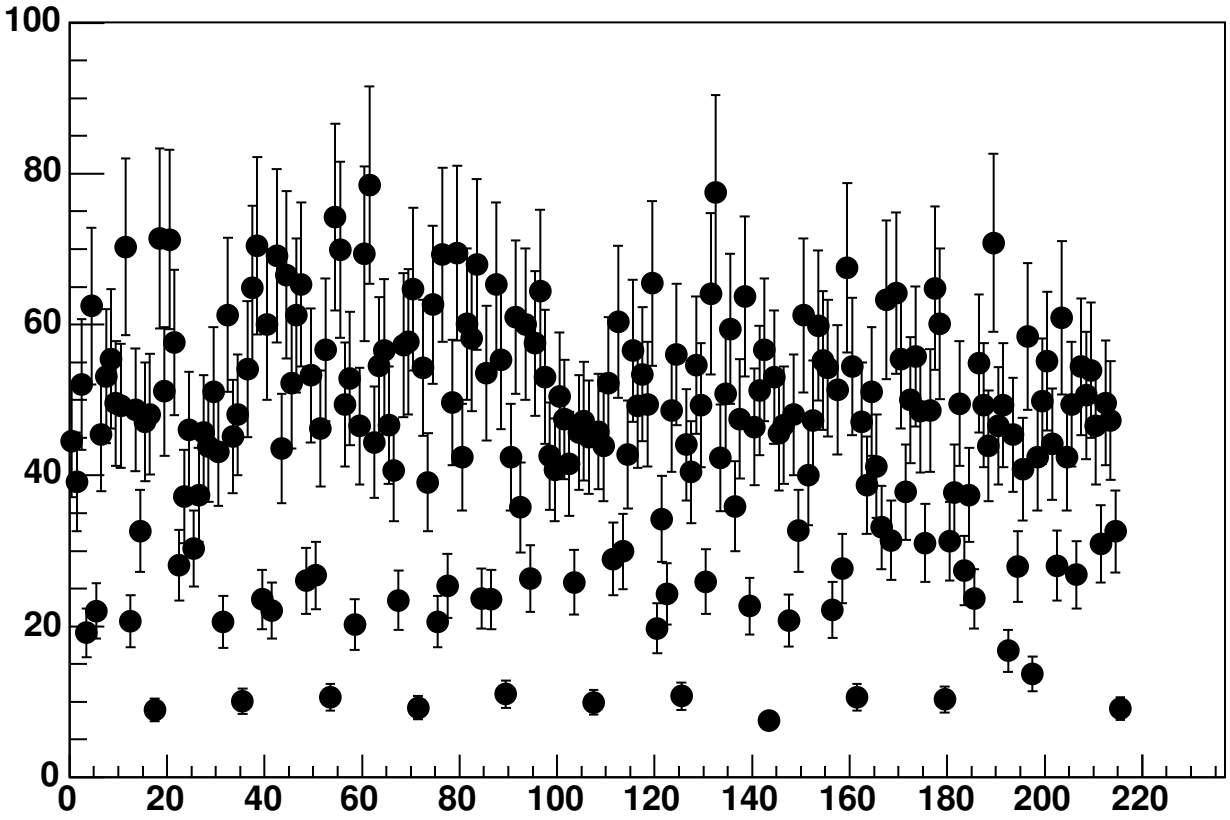
Enable 1, Hold=30, DAC=1600, ADC Noise vs 18*Chip+Chan



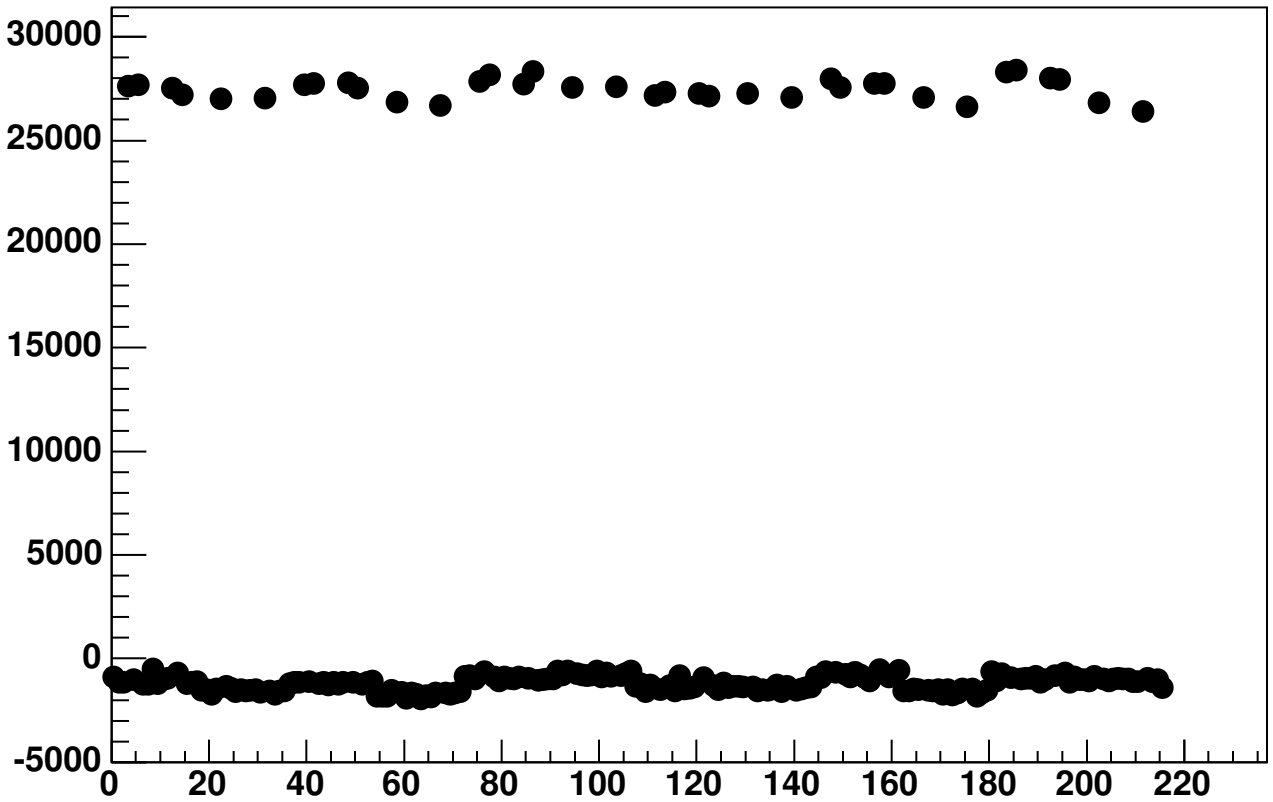
Enable 1, Hold=30, DAC=1650, ADC Mean vs 18*Chip+Chan



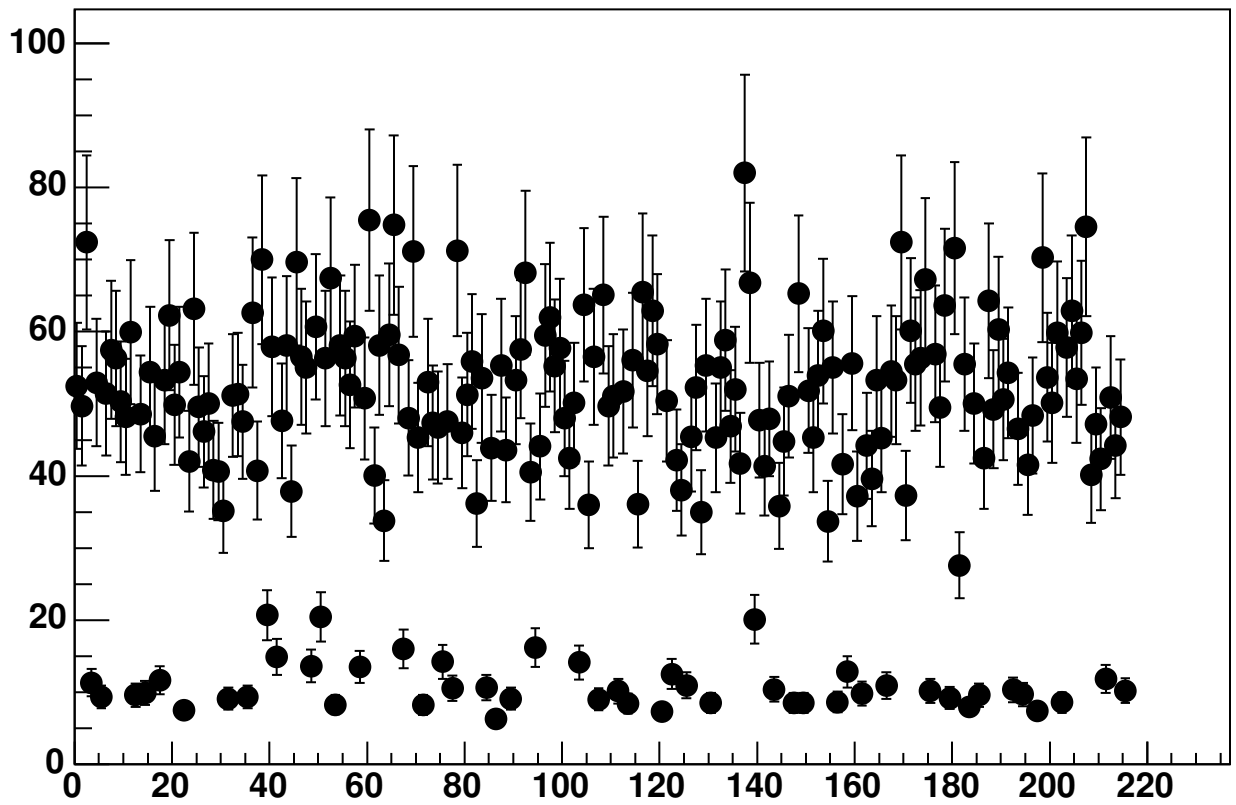
Enable 1, Hold=30, DAC=1650, ADC Noise vs 18*Chip+Chan



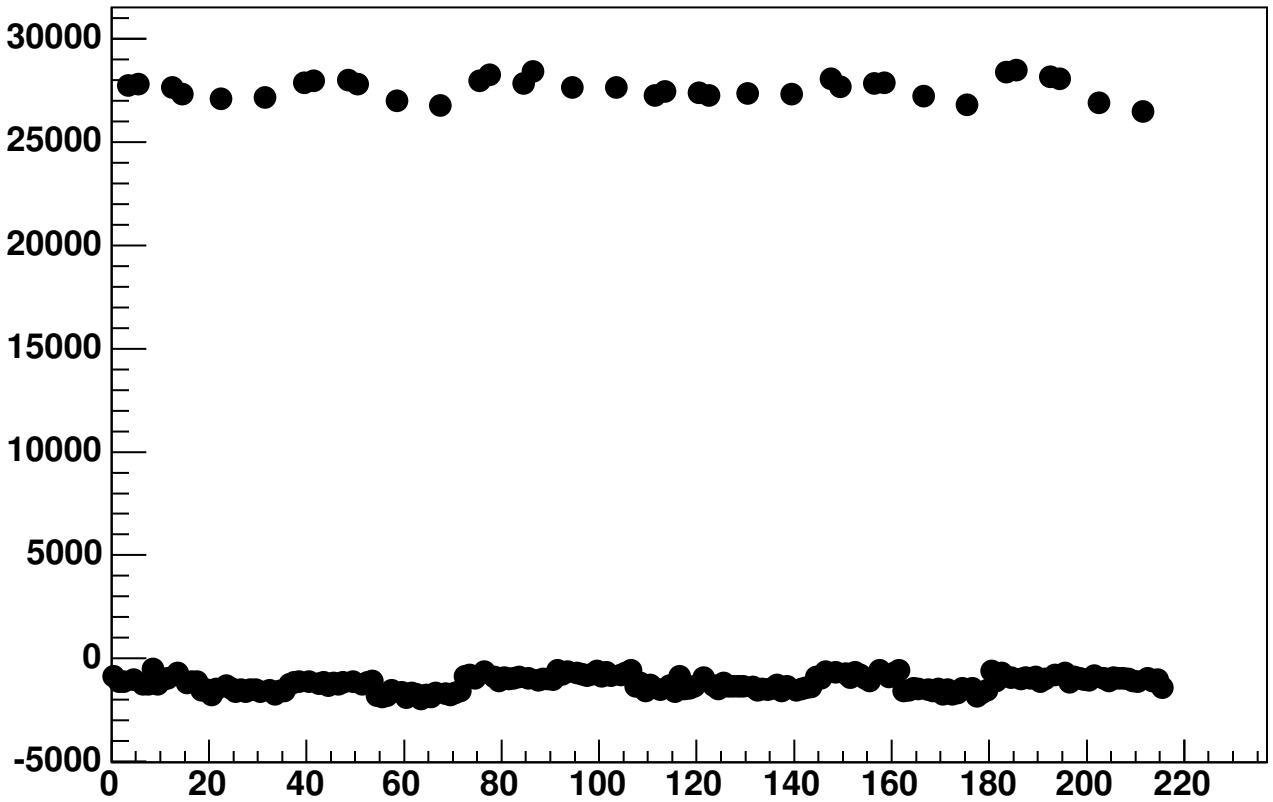
Enable 1, Hold=30, DAC=1700, ADC Mean vs 18*Chip+Chan



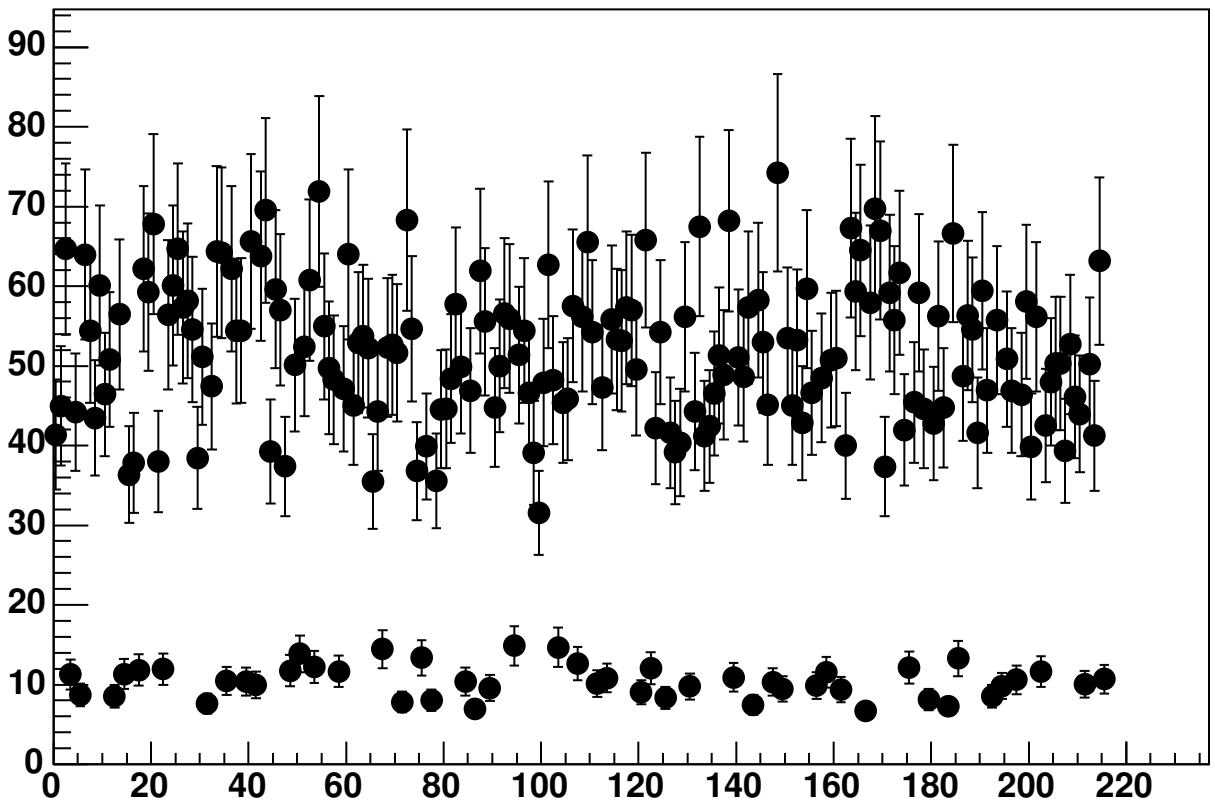
Enable 1, Hold=30, DAC=1700, ADC Noise vs 18*Chip+Chan



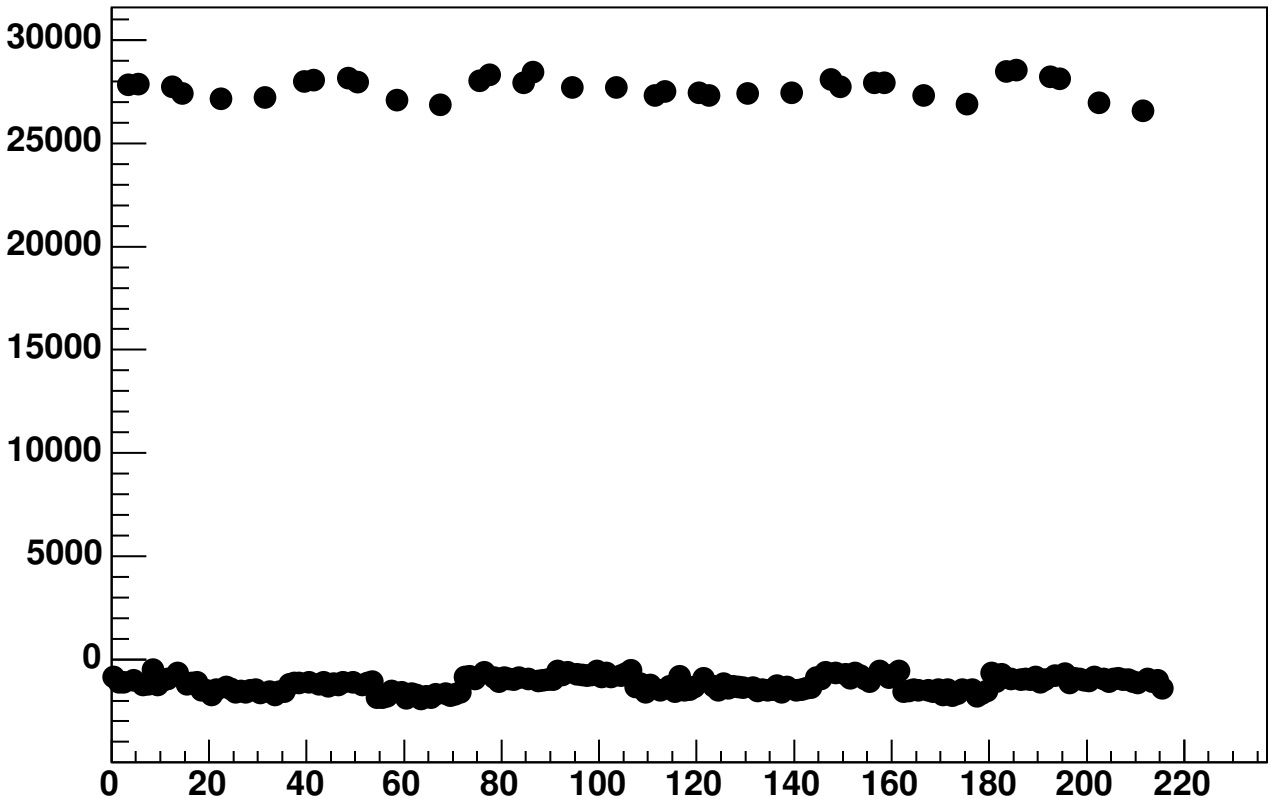
Enable 1, Hold=30, DAC=1750, ADC Mean vs 18*Chip+Chan



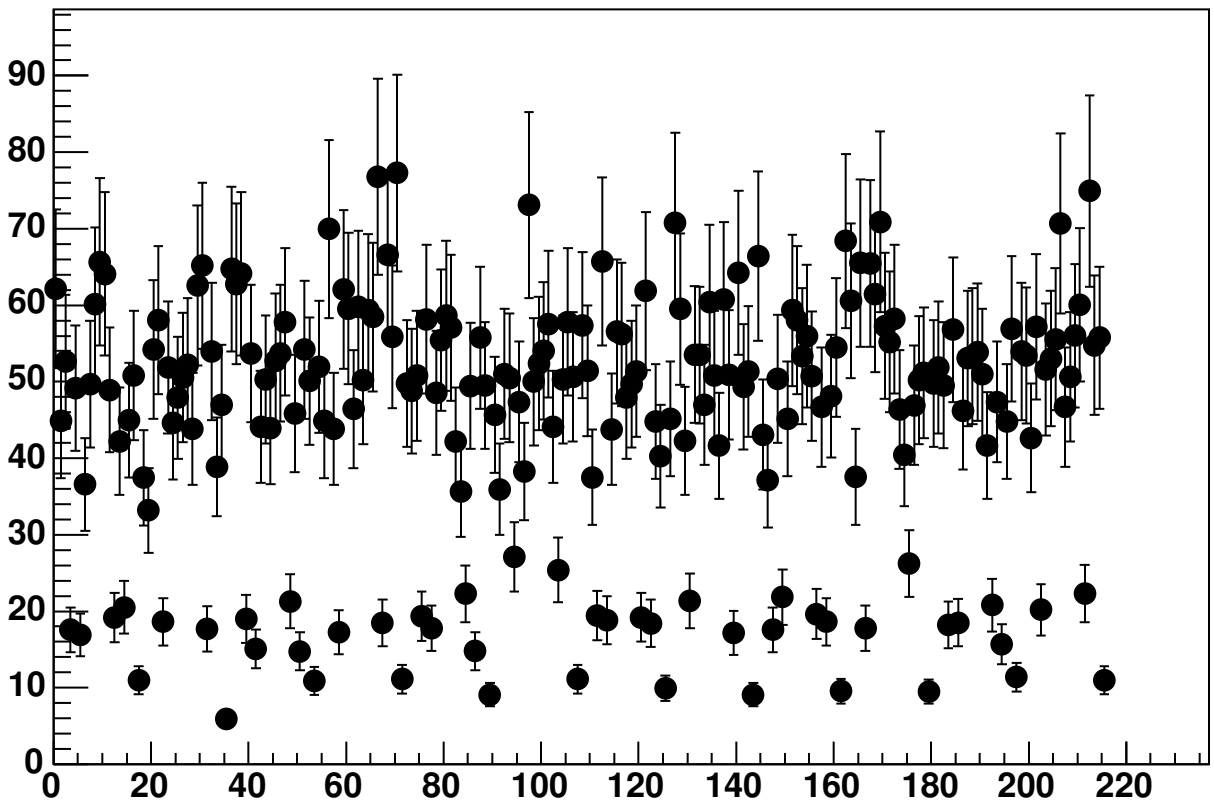
Enable 1, Hold=30, DAC=1750, ADC Noise vs 18*Chip+Chan



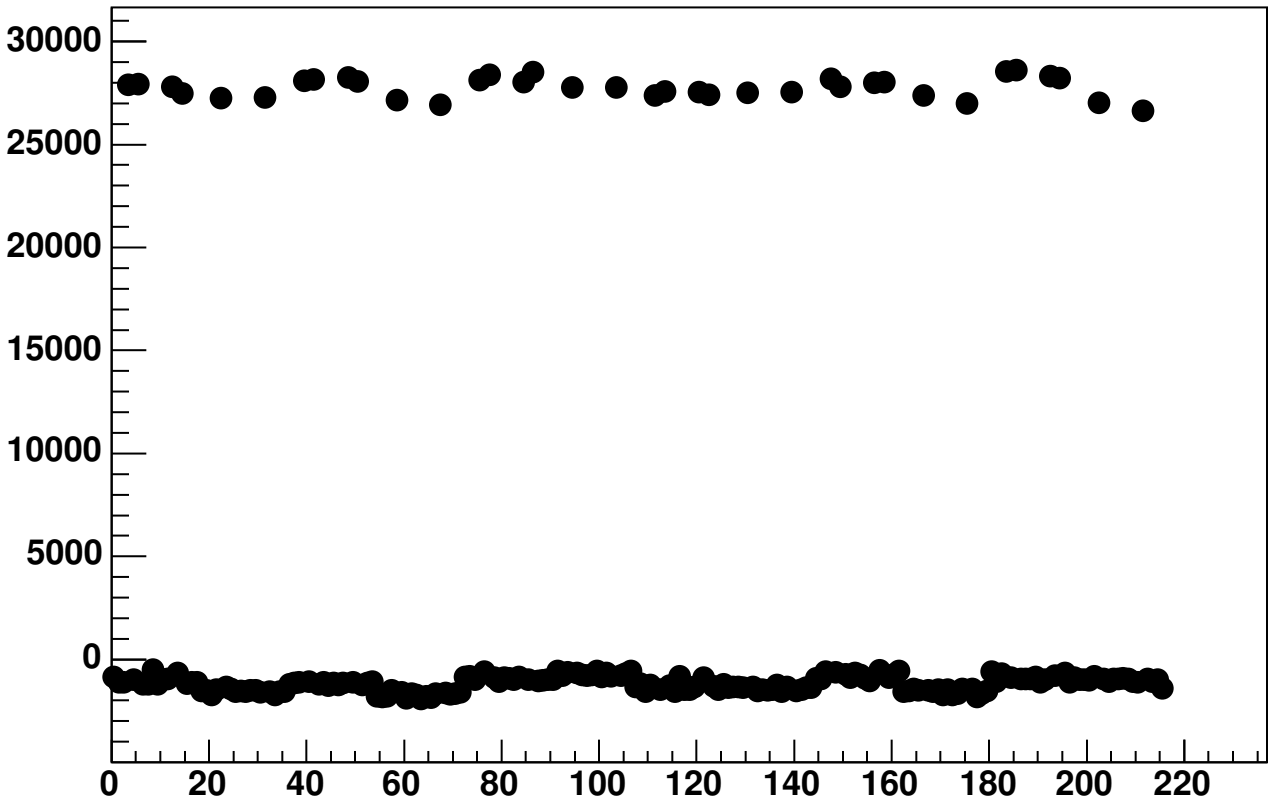
Enable 1, Hold=30, DAC=1800, ADC Mean vs 18*Chip+Chan



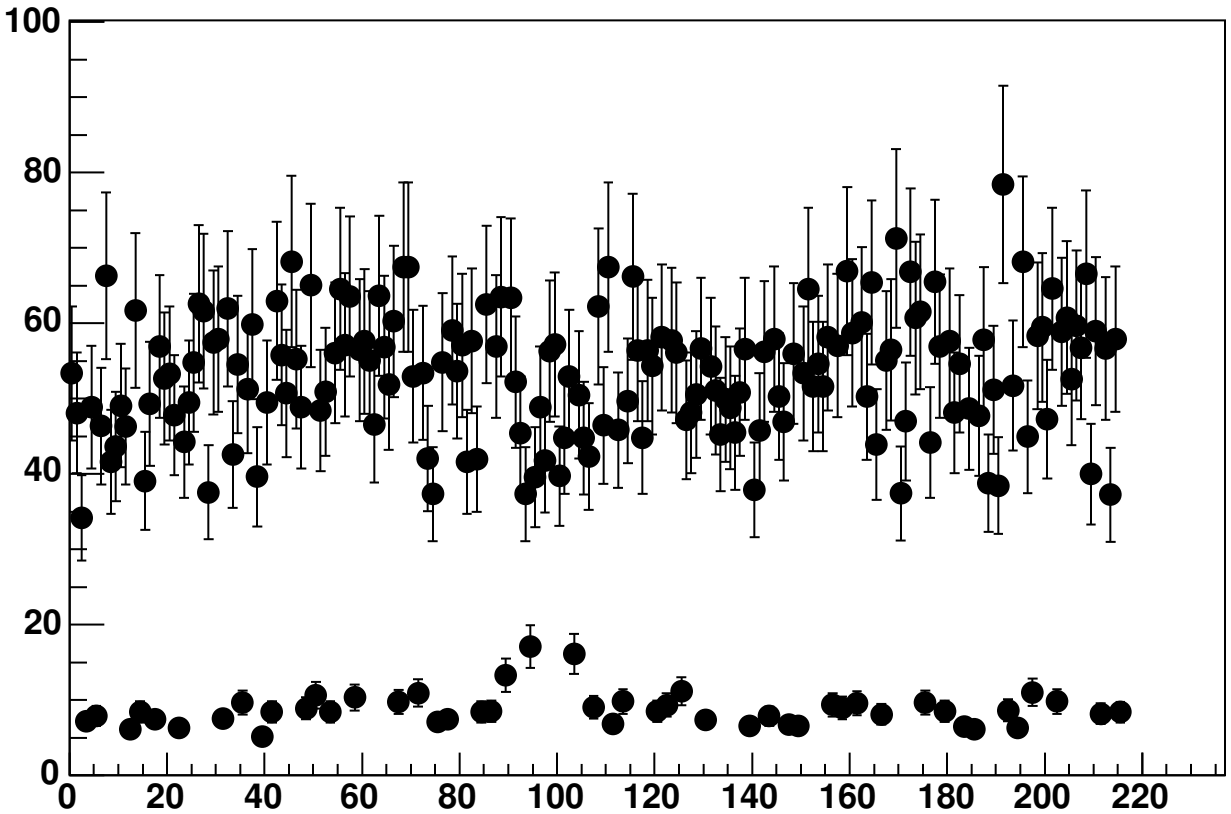
Enable 1, Hold=30, DAC=1800, ADC Noise vs 18*Chip+Chan



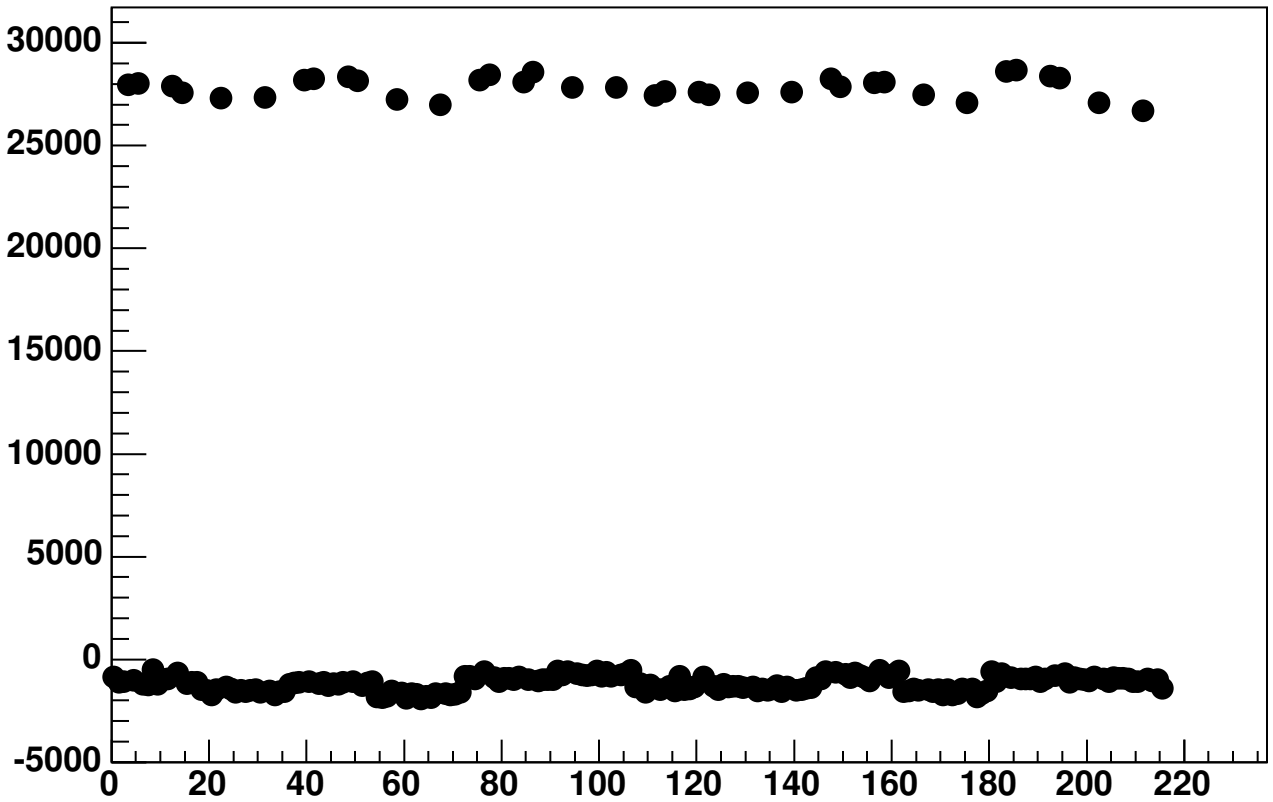
Enable 1, Hold=30, DAC=1850, ADC Mean vs 18*Chip+Chan



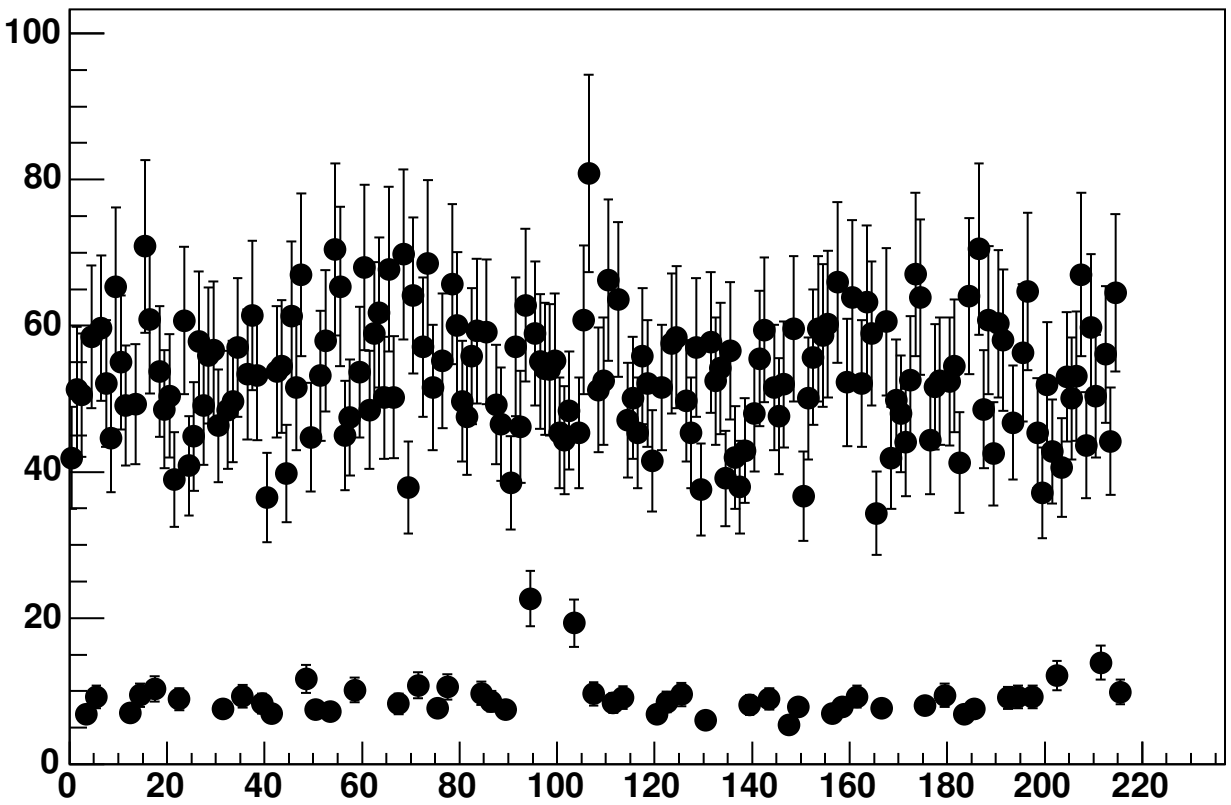
Enable 1, Hold=30, DAC=1850, ADC Noise vs 18*Chip+Chan



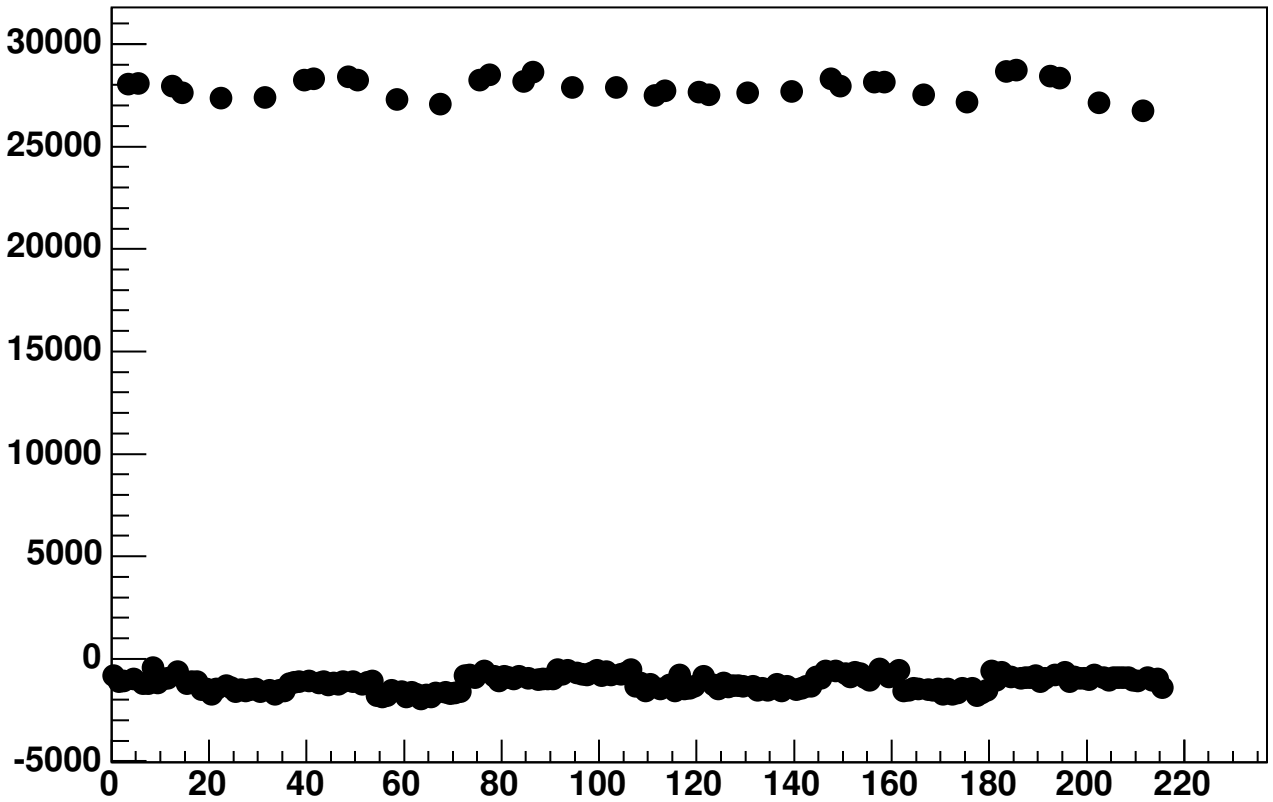
Enable 1, Hold=30, DAC=1900, ADC Mean vs 18*Chip+Chan



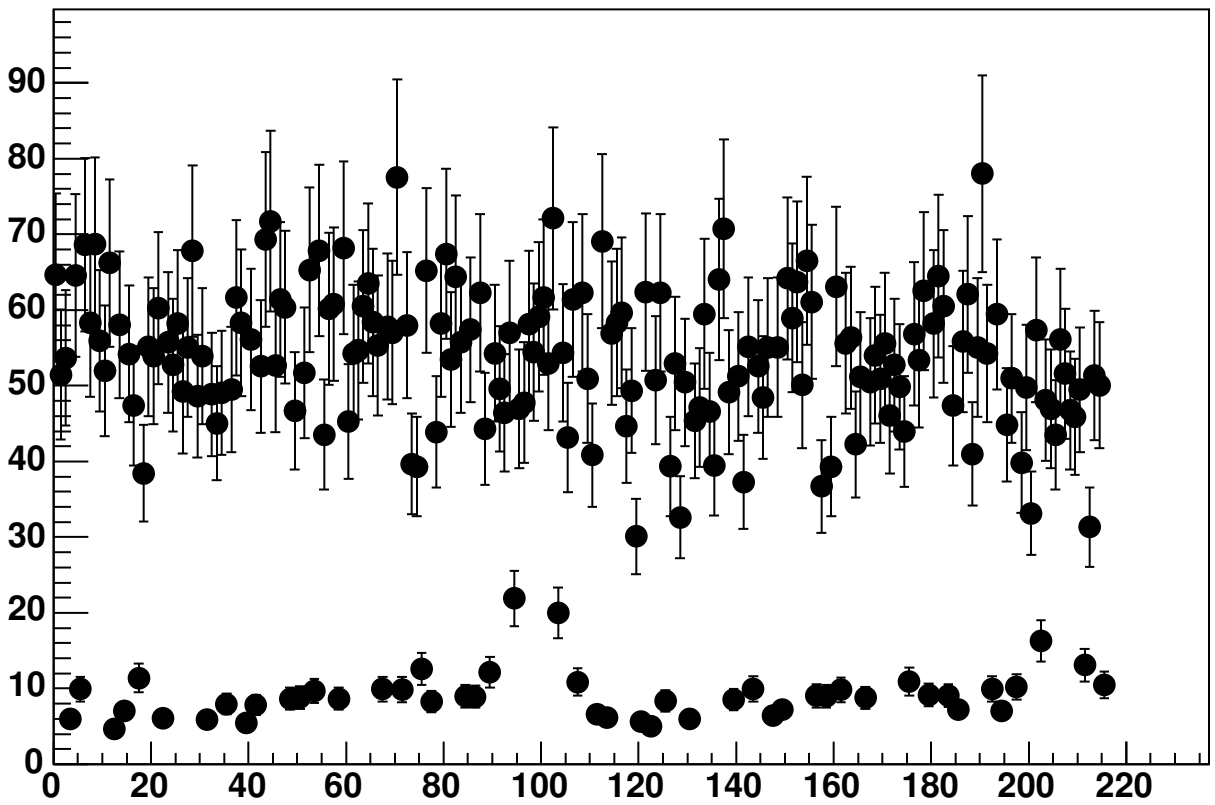
Enable 1, Hold=30, DAC=1900, ADC Noise vs 18*Chip+Chan



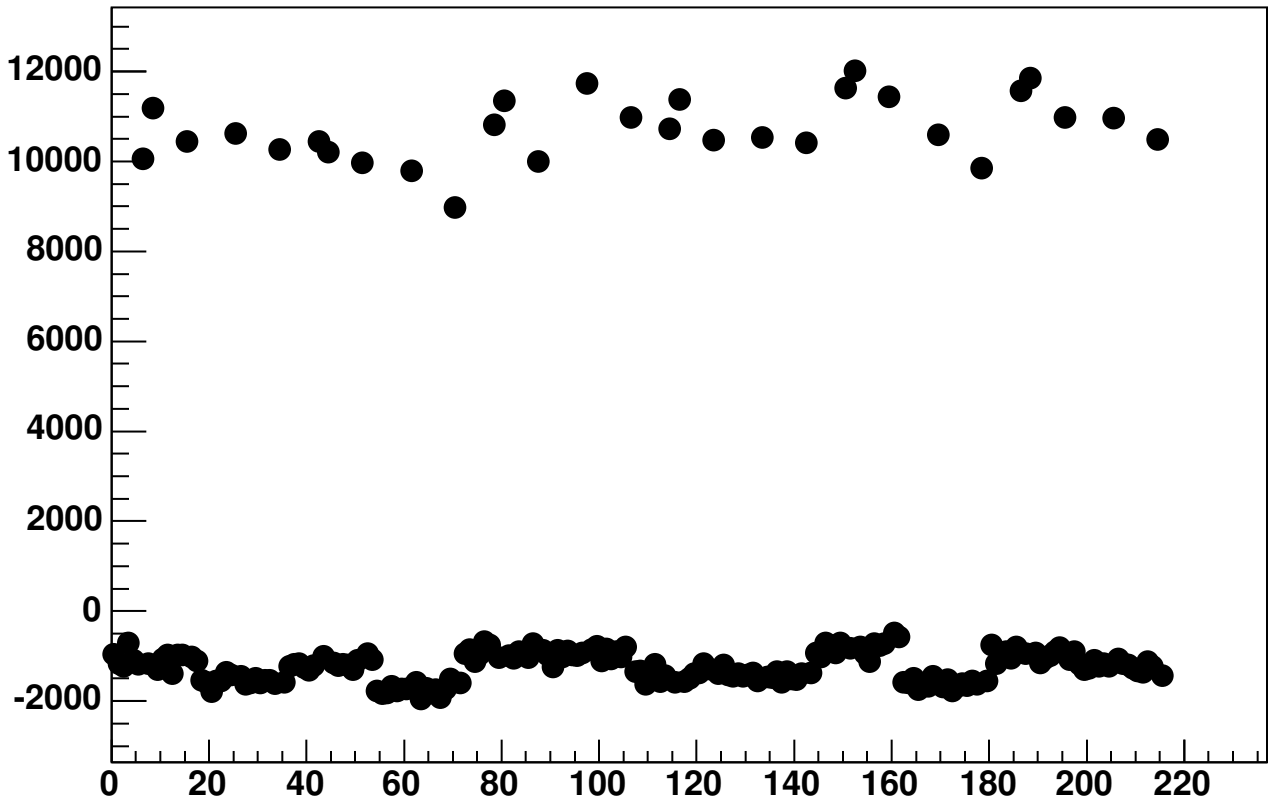
Enable 1, Hold=30, DAC=1950, ADC Mean vs 18*Chip+Chan



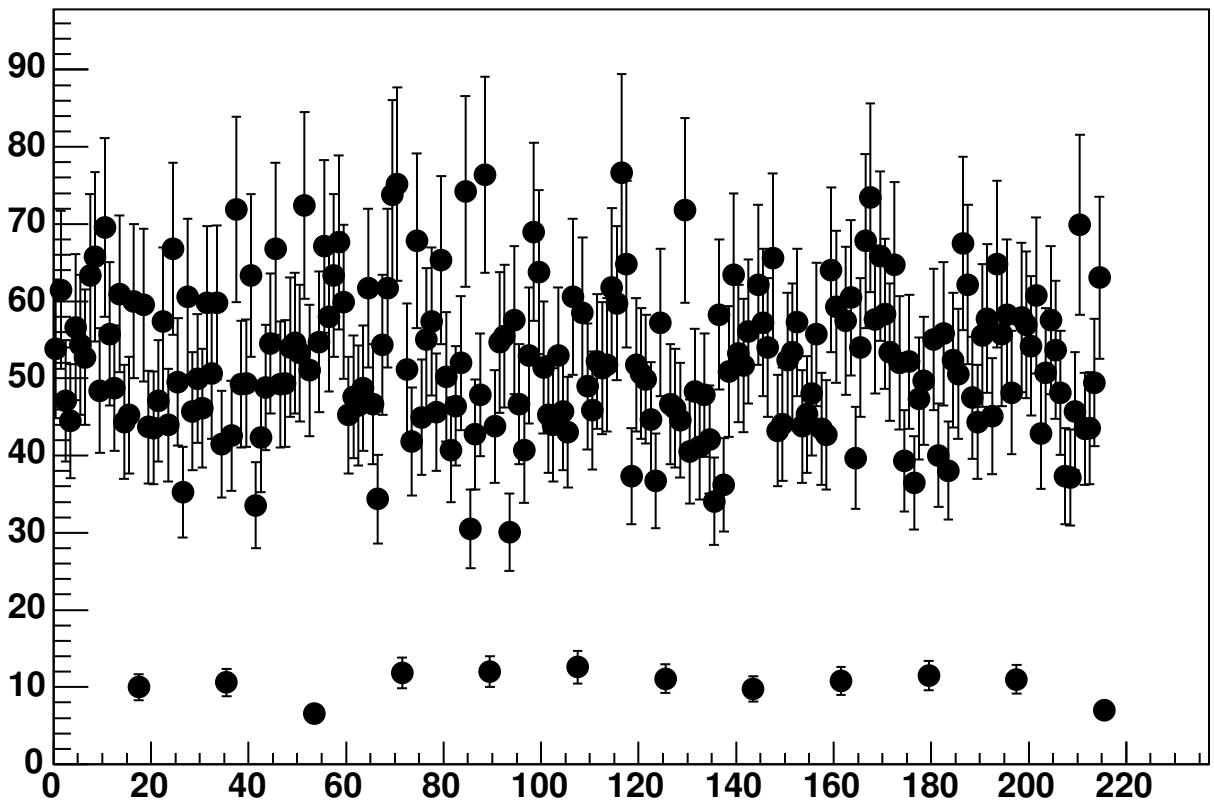
Enable 1, Hold=30, DAC=1950, ADC Noise vs 18*Chip+Chan



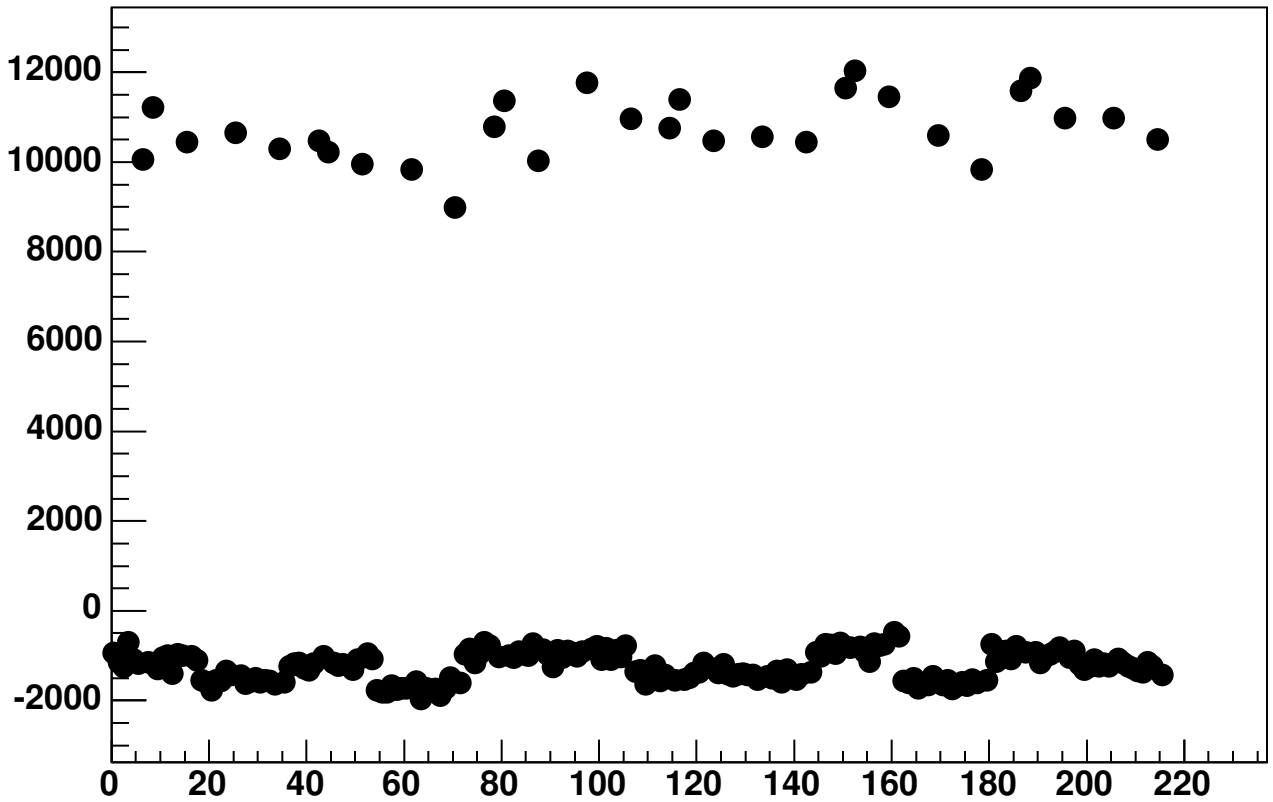
Enable 2, Hold=30, DAC=0, ADC Mean vs 18*Chip+Chan



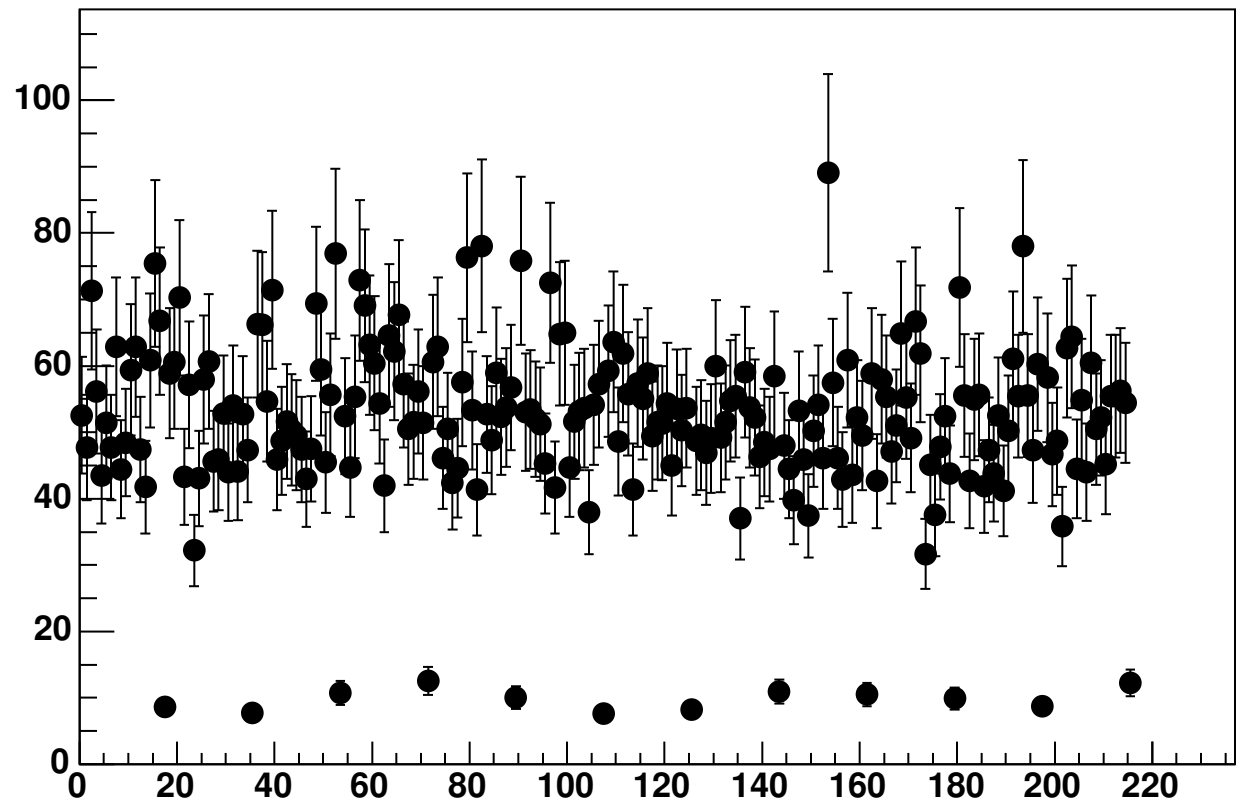
Enable 2, Hold=30, DAC=0, ADC Noise vs 18*Chip+Chan



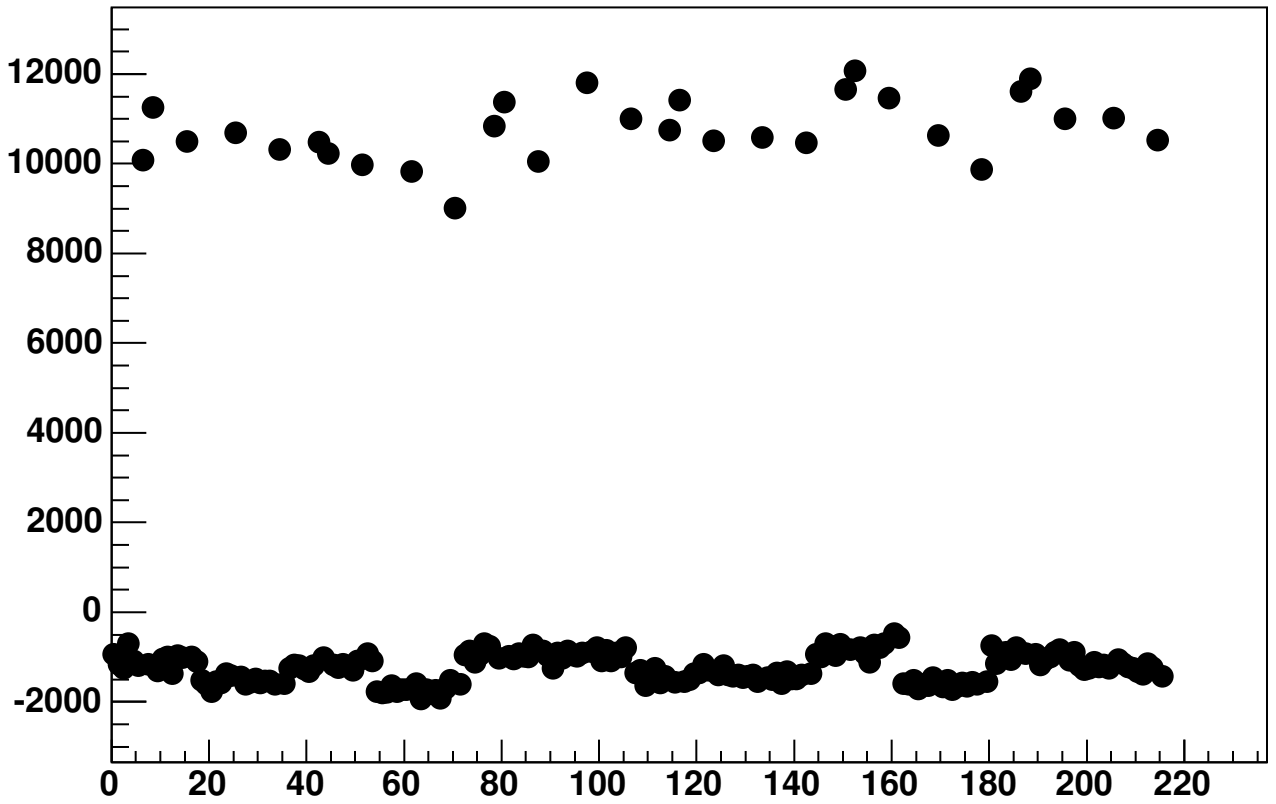
Enable 2, Hold=30, DAC=50, ADC Mean vs 18*Chip+Chan



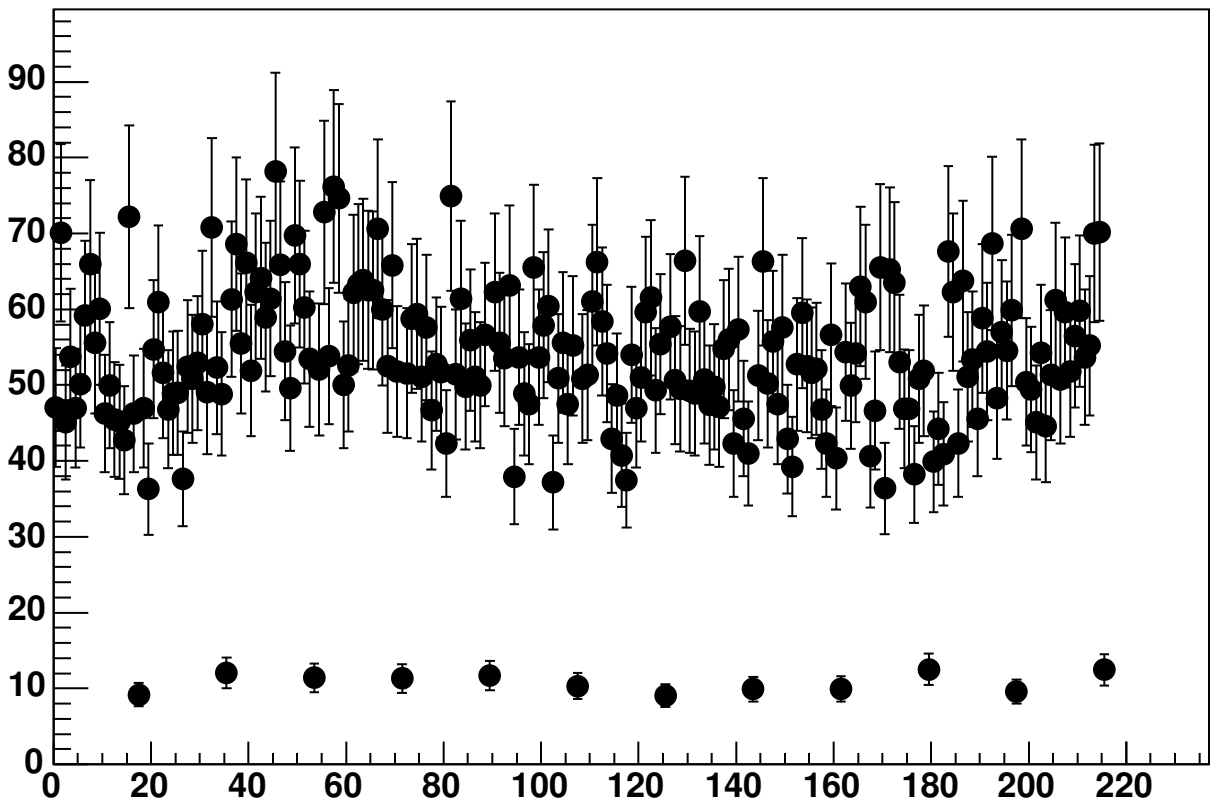
Enable 2, Hold=30, DAC=50, ADC Noise vs 18*Chip+Chan



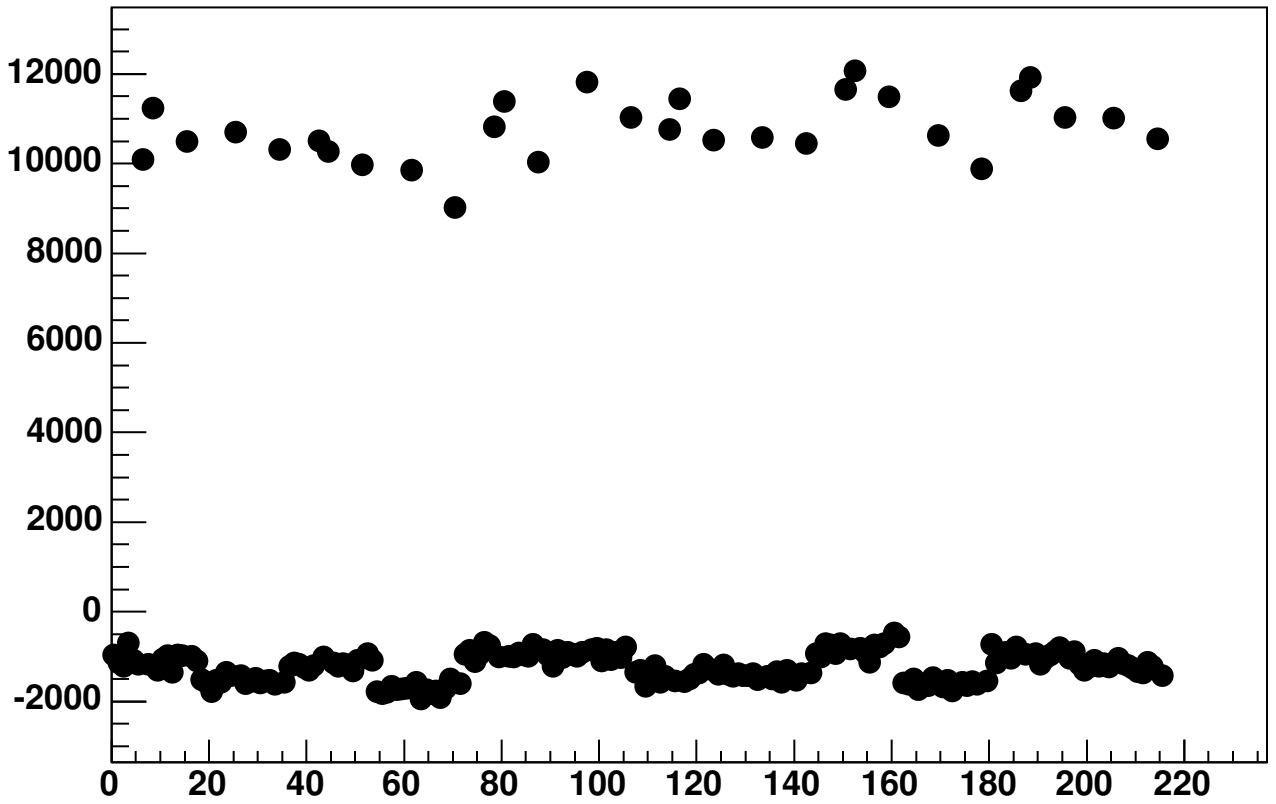
Enable 2, Hold=30, DAC=100, ADC Mean vs 18*Chip+Chan



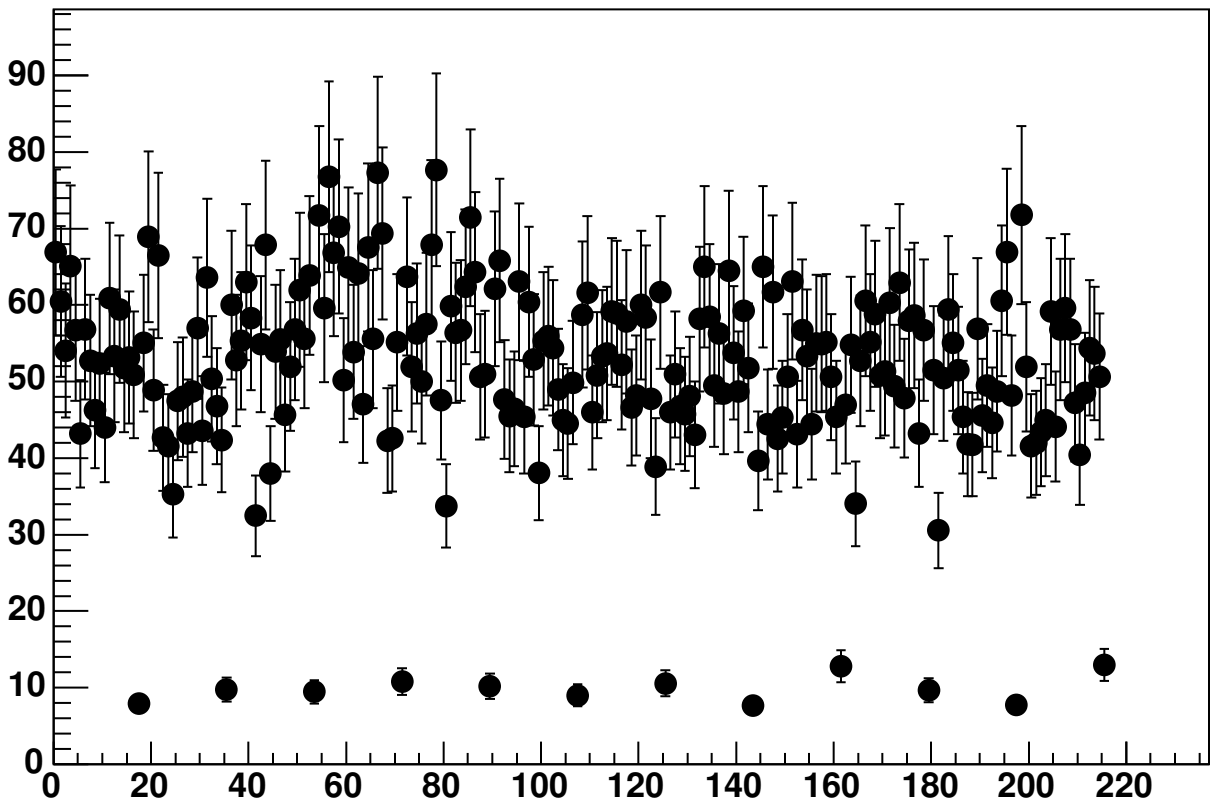
Enable 2, Hold=30, DAC=100, ADC Noise vs 18*Chip+Chan



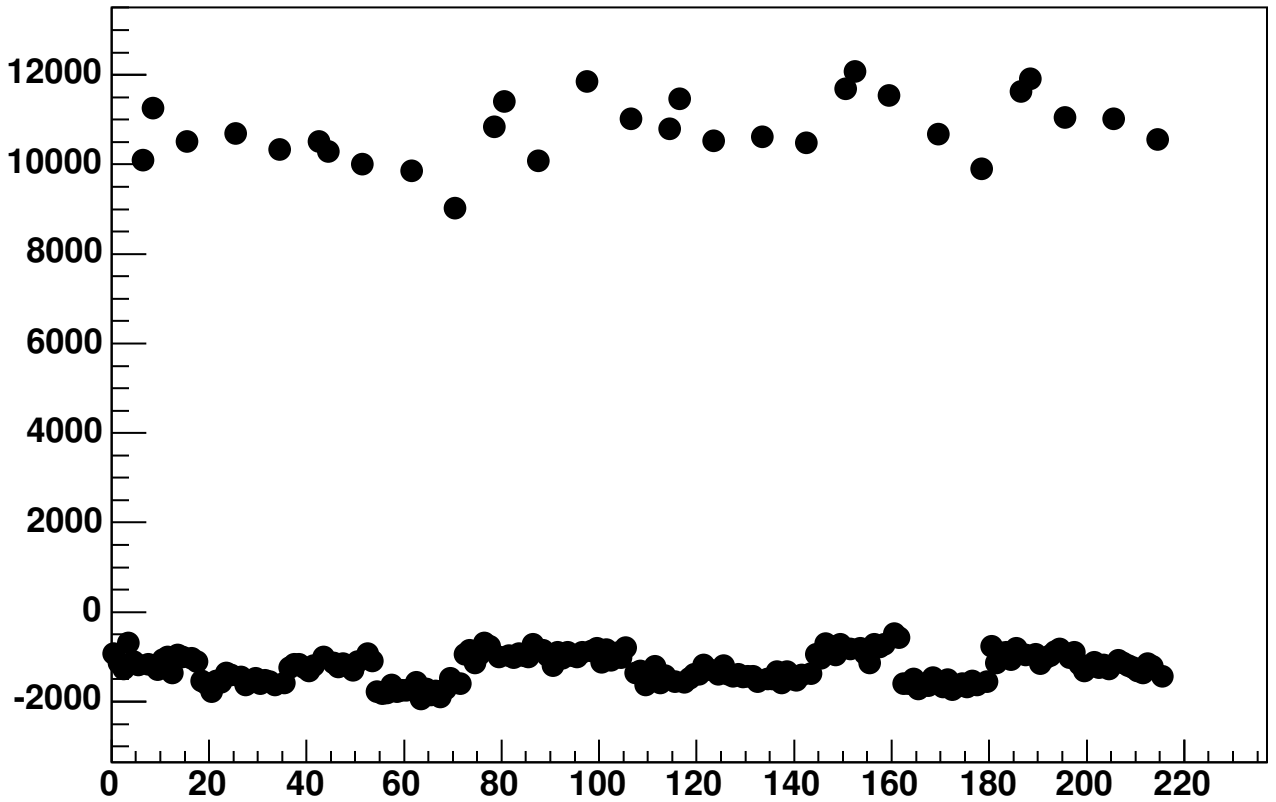
Enable 2, Hold=30, DAC=150, ADC Mean vs 18*Chip+Chan



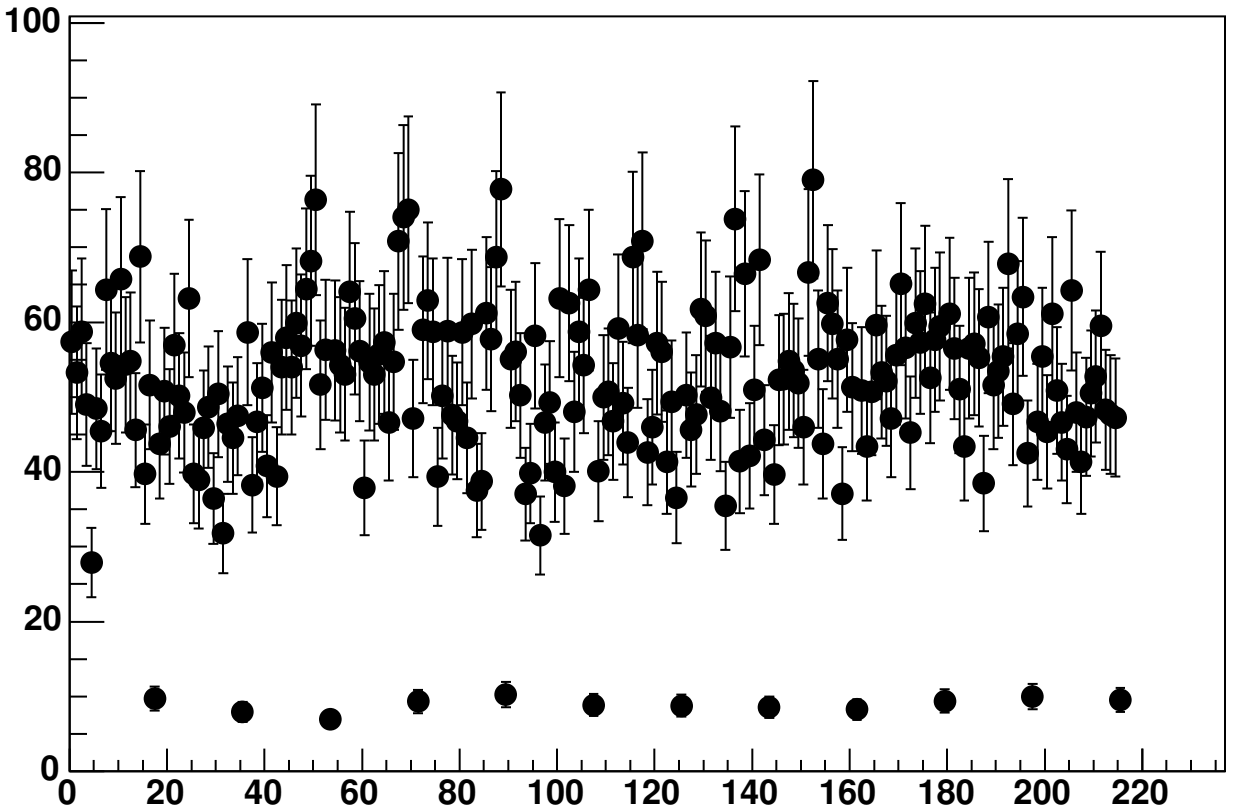
Enable 2, Hold=30, DAC=150, ADC Noise vs 18*Chip+Chan



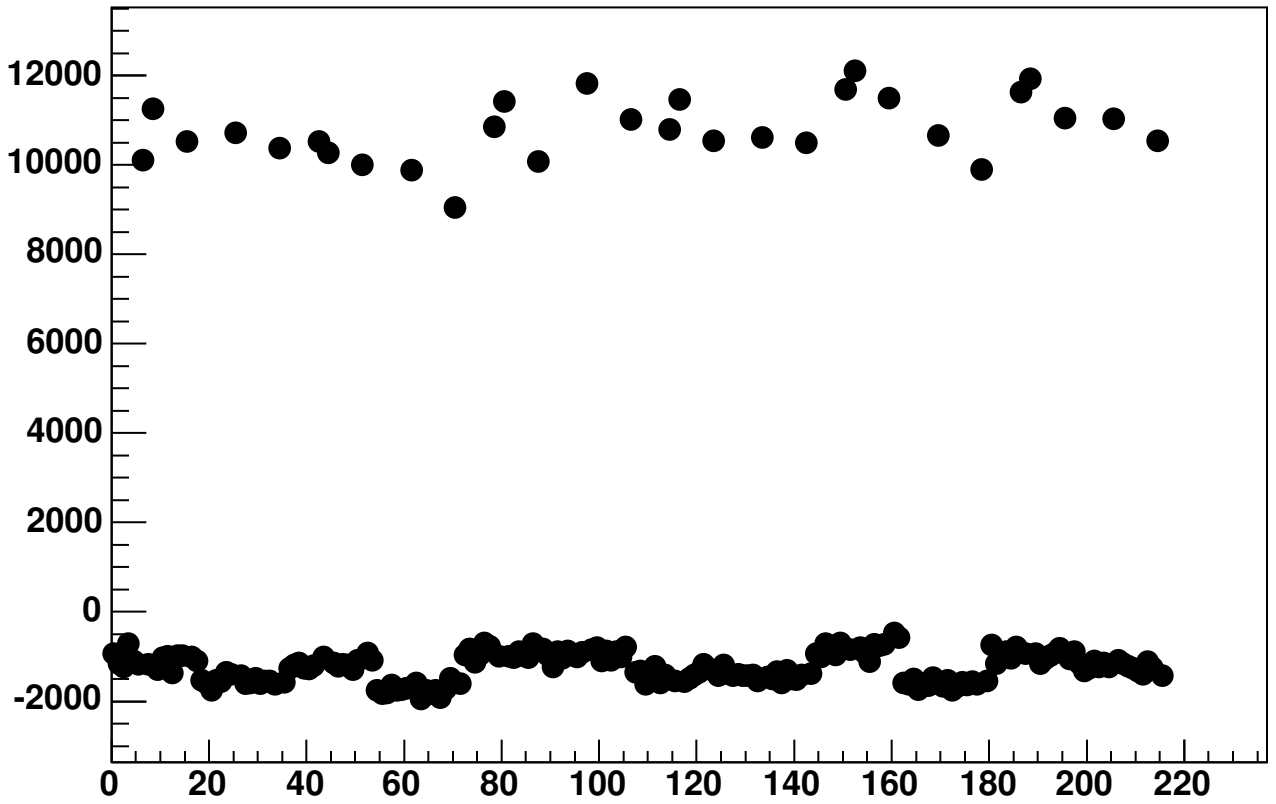
Enable 2, Hold=30, DAC=200, ADC Mean vs 18*Chip+Chan



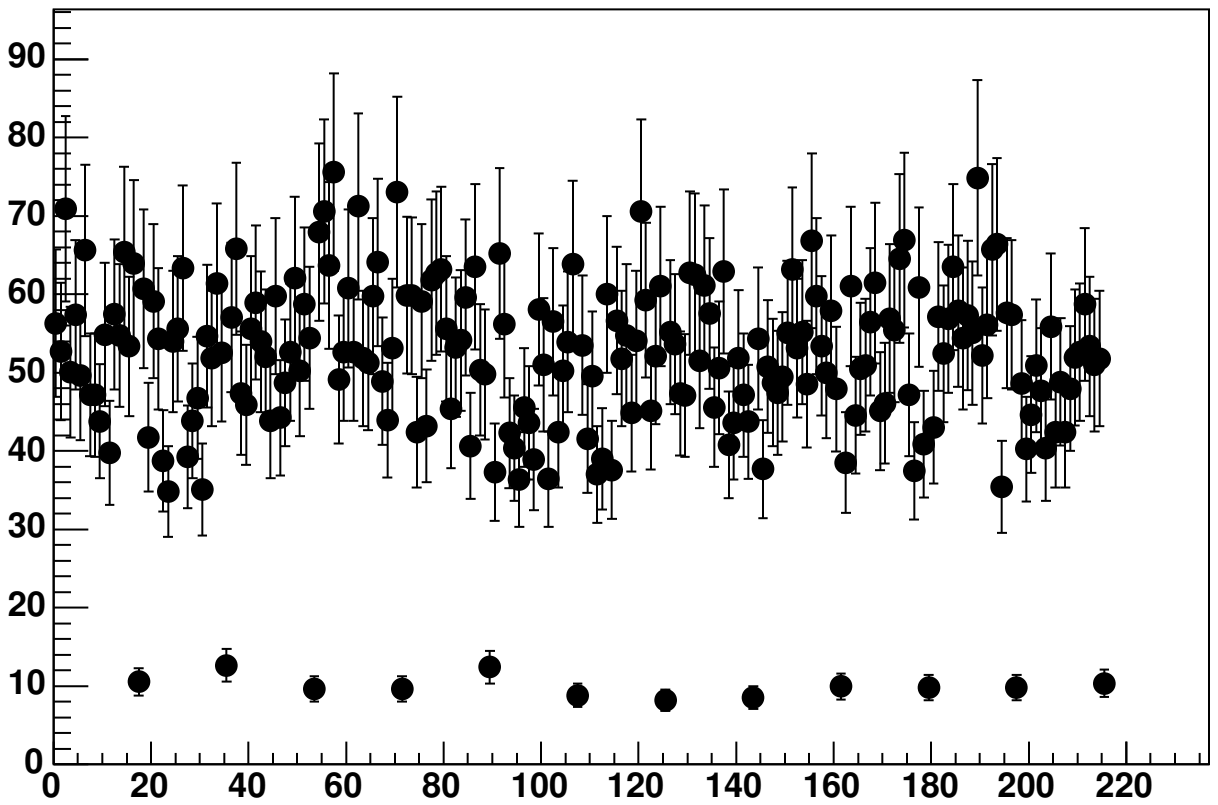
Enable 2, Hold=30, DAC=200, ADC Noise vs 18*Chip+Chan



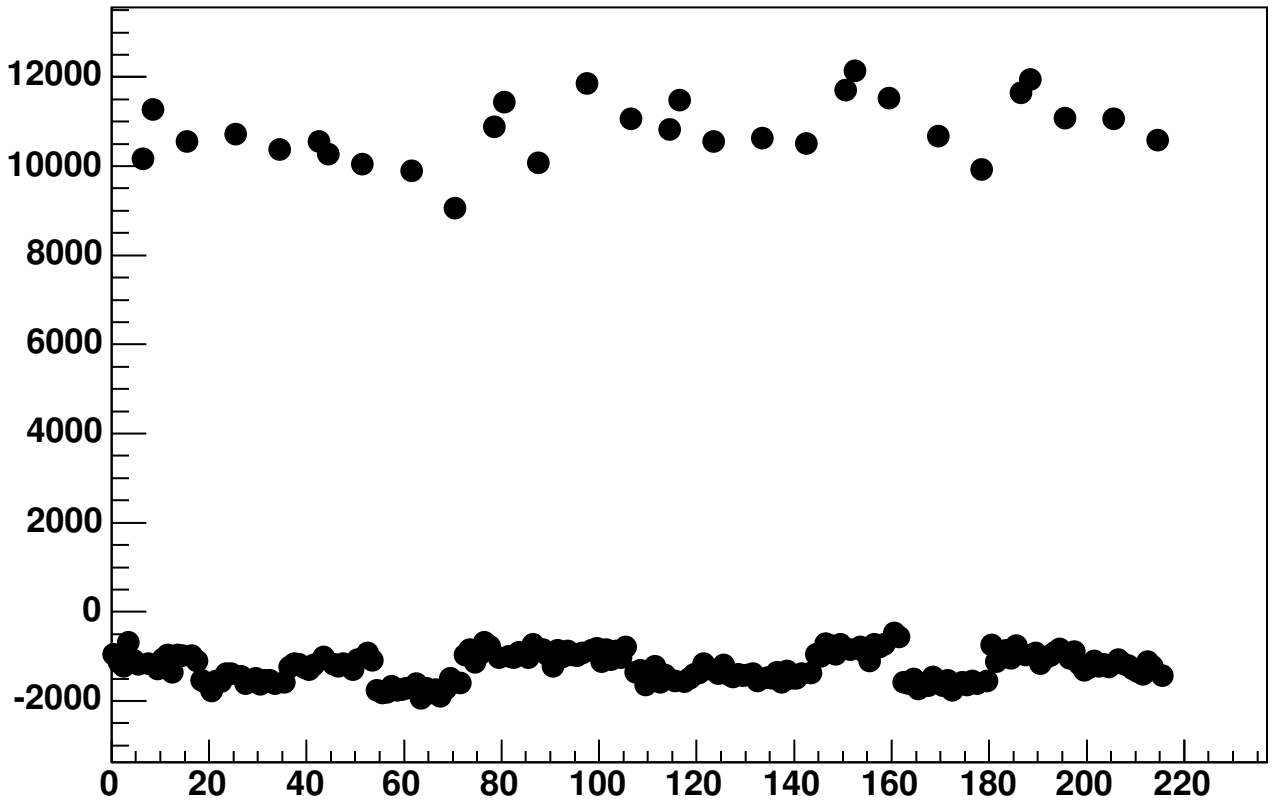
Enable 2, Hold=30, DAC=250, ADC Mean vs 18*Chip+Chan



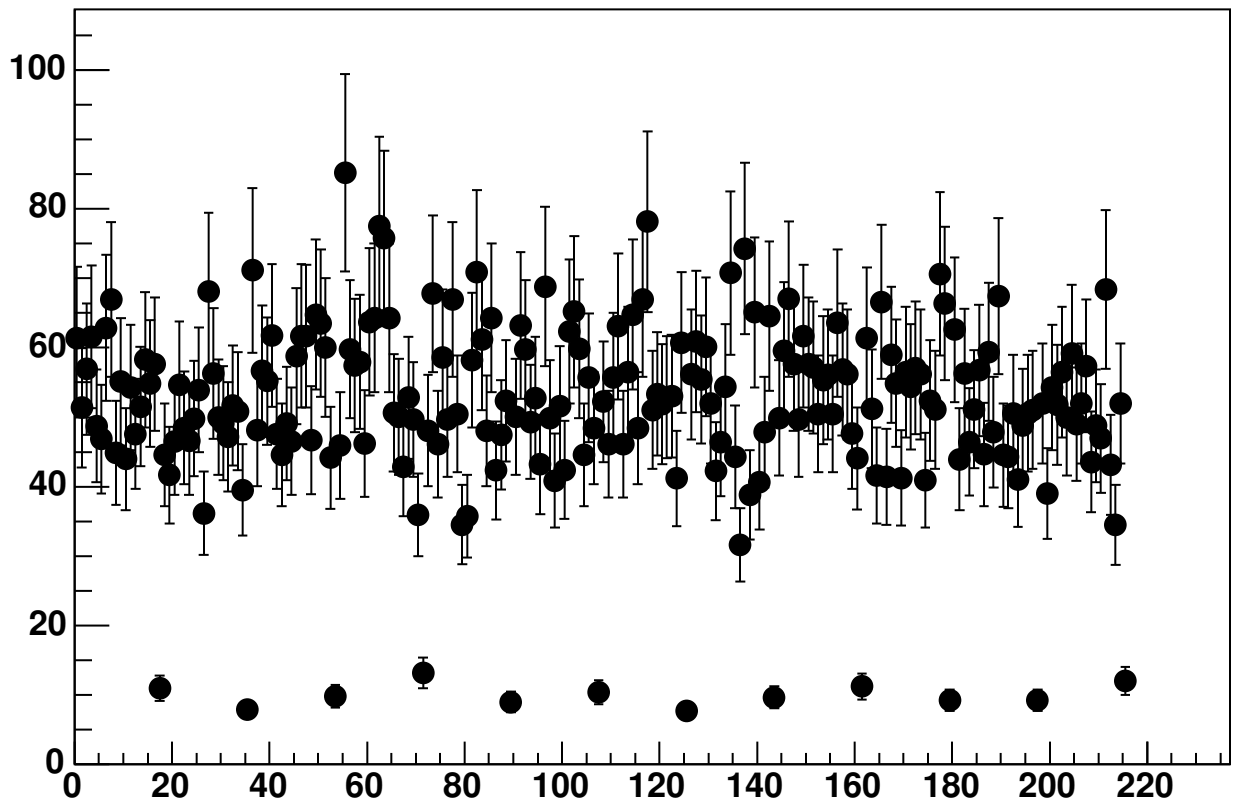
Enable 2, Hold=30, DAC=250, ADC Noise vs 18*Chip+Chan



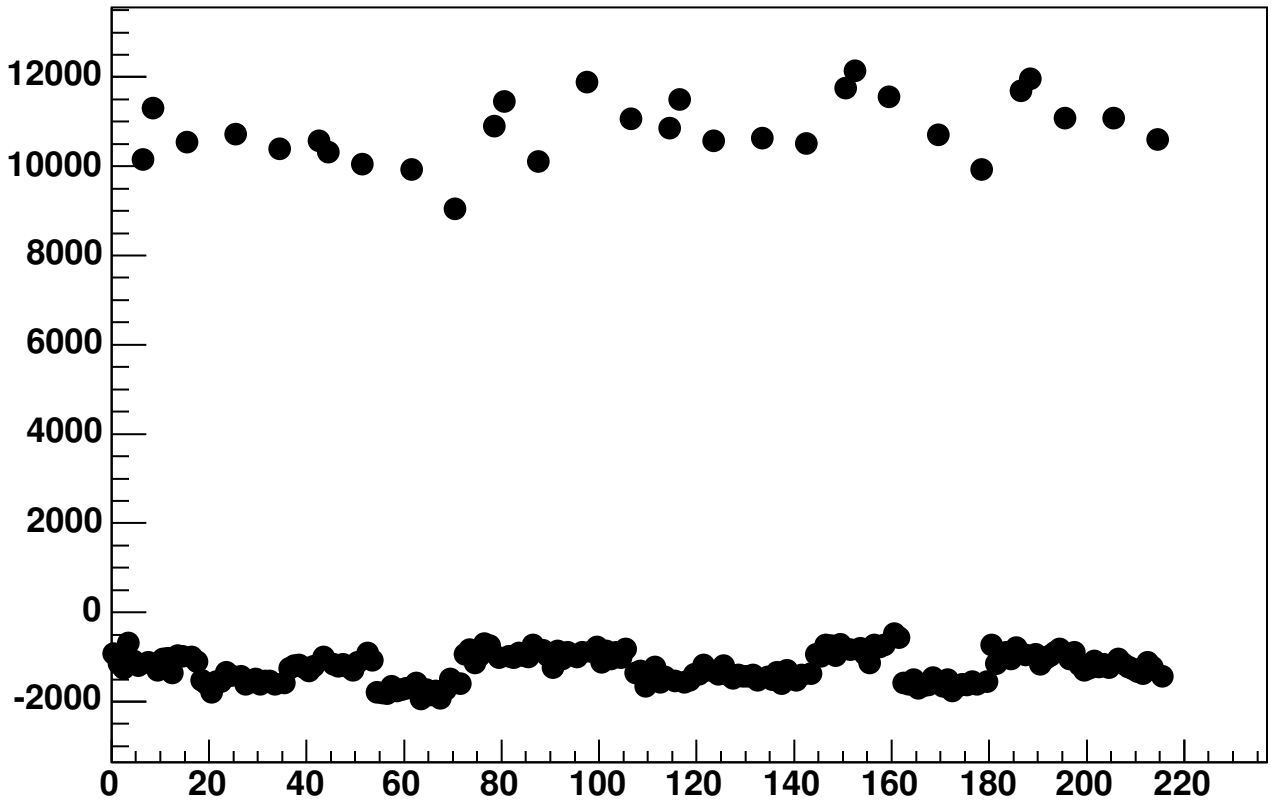
Enable 2, Hold=30, DAC=300, ADC Mean vs 18*Chip+Chan



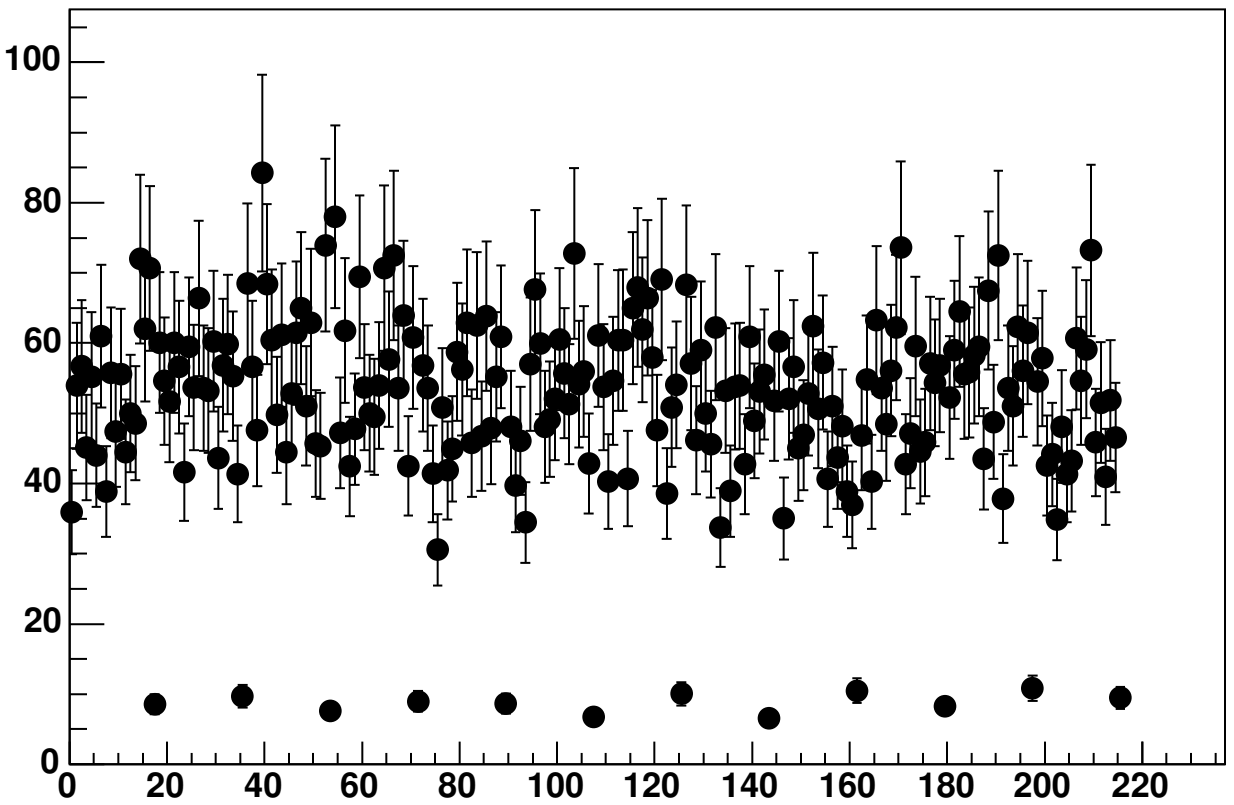
Enable 2, Hold=30, DAC=300, ADC Noise vs 18*Chip+Chan



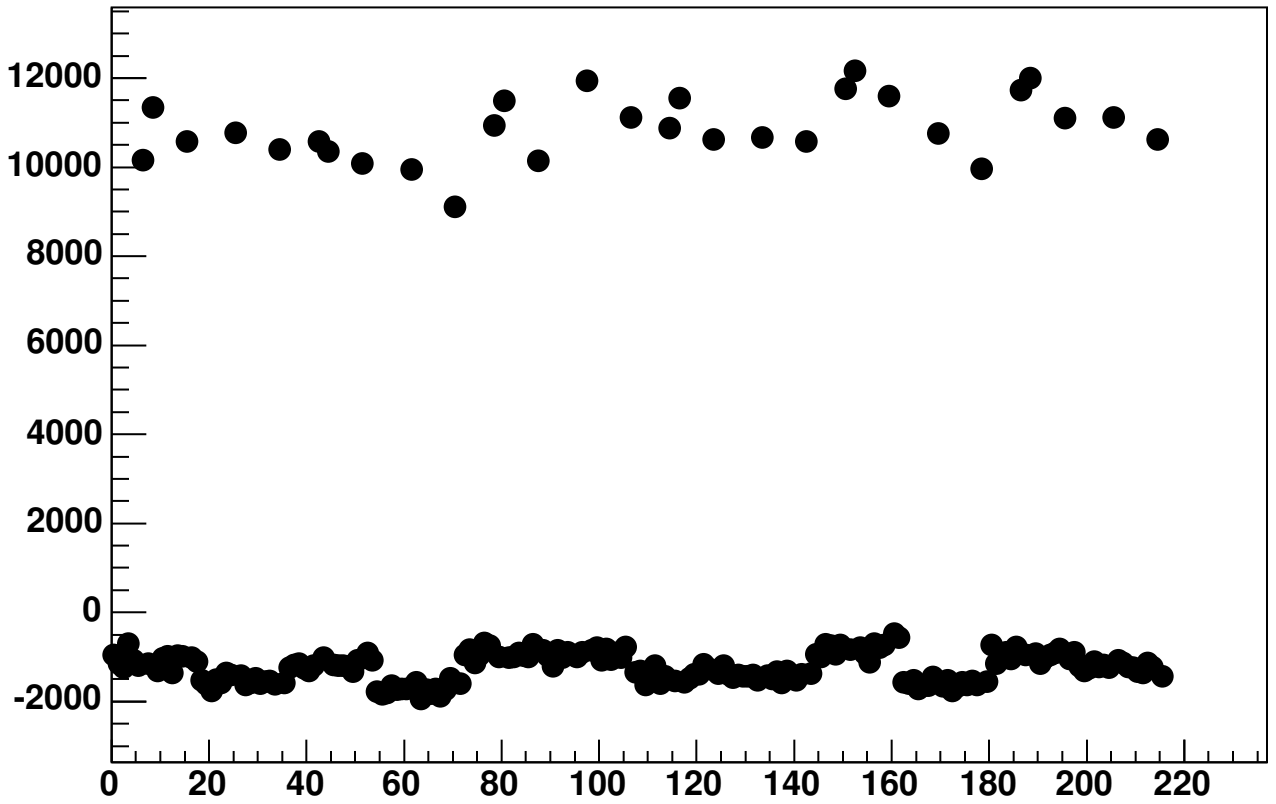
Enable 2, Hold=30, DAC=350, ADC Mean vs 18*Chip+Chan



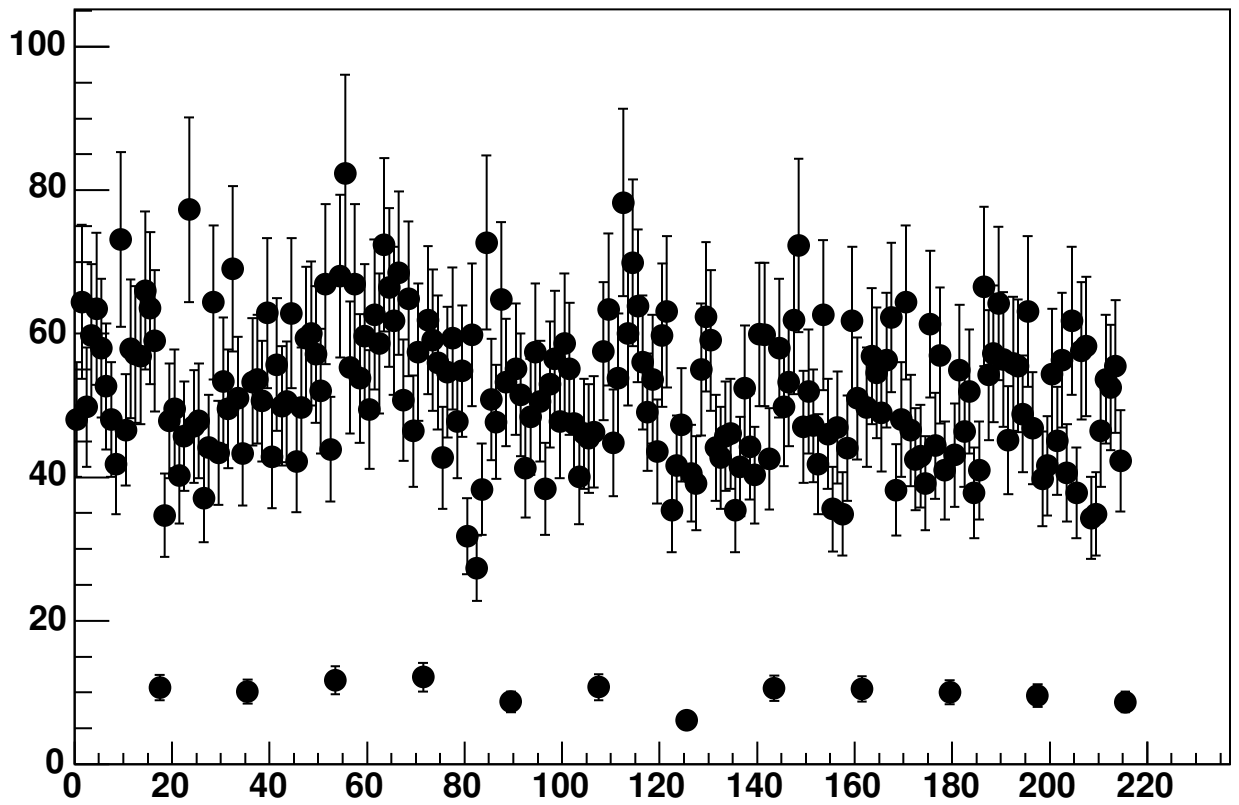
Enable 2, Hold=30, DAC=350, ADC Noise vs 18*Chip+Chan



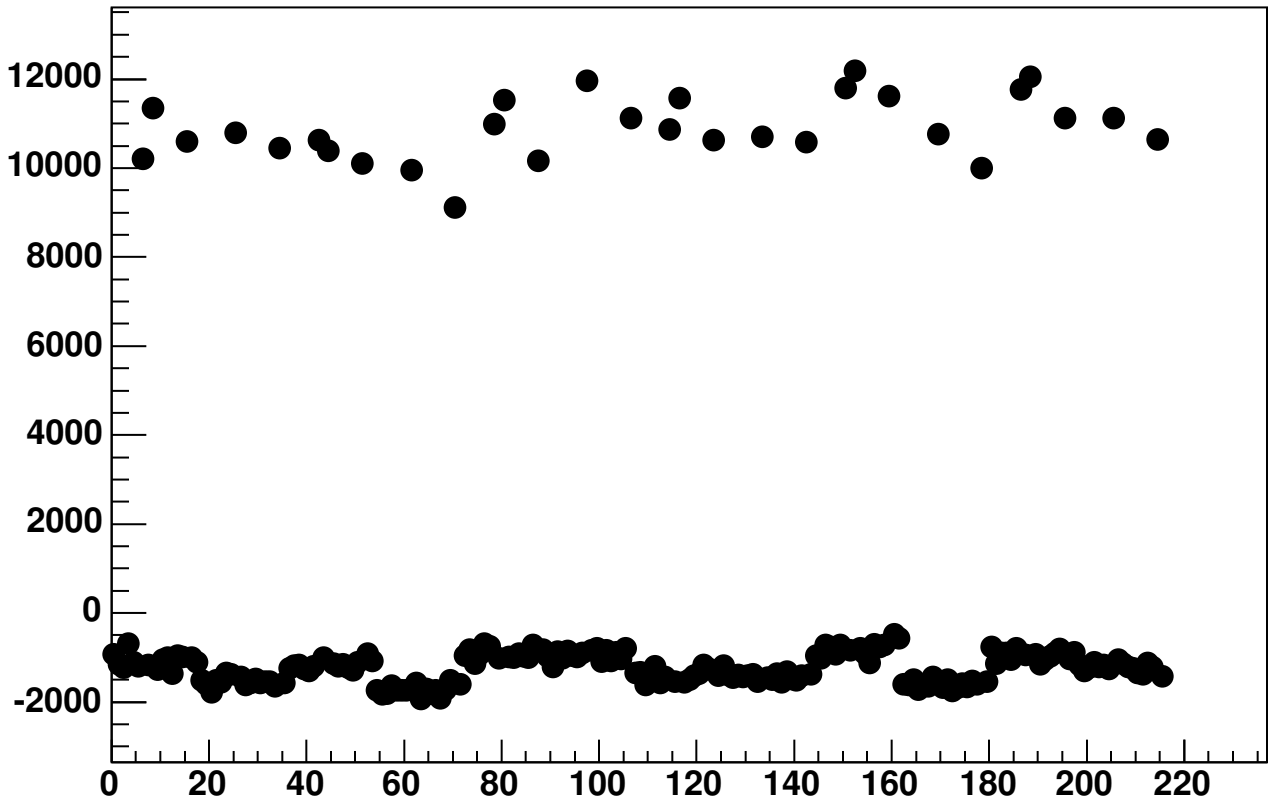
Enable 2, Hold=30, DAC=400, ADC Mean vs 18*Chip+Chan



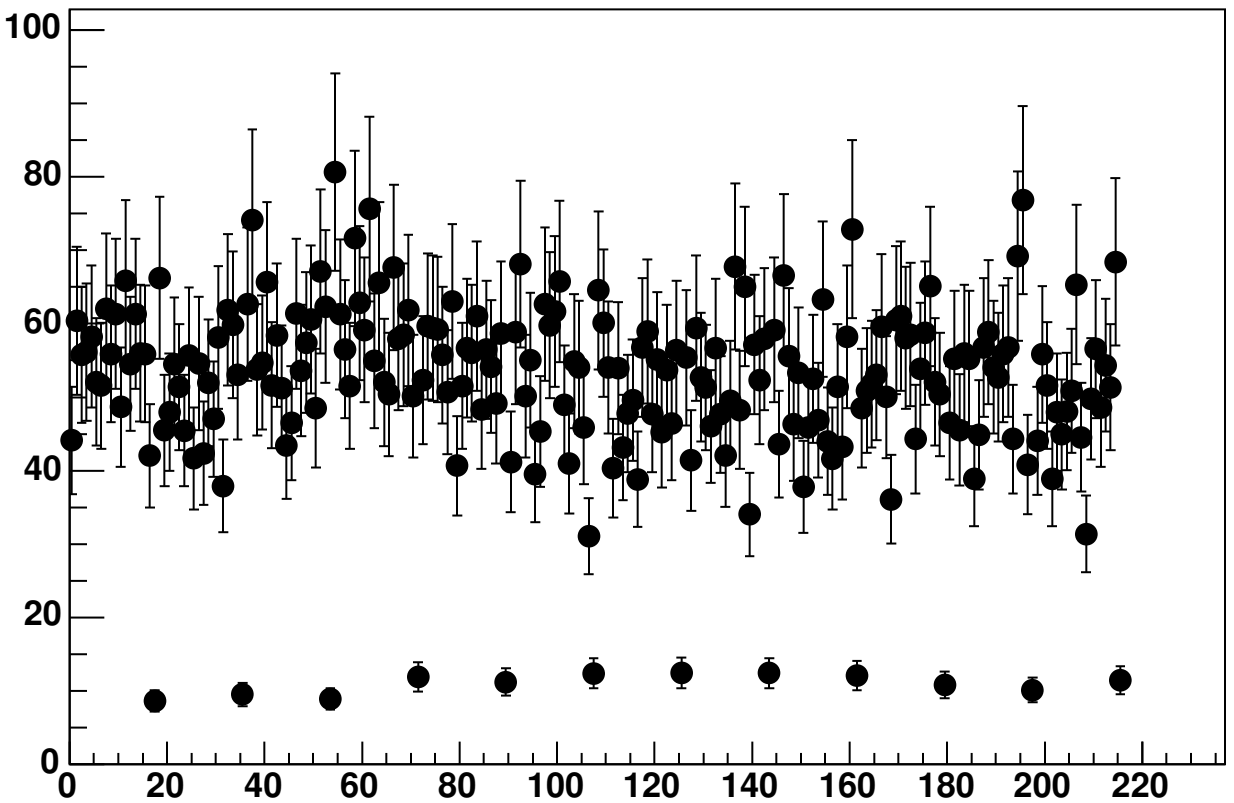
Enable 2, Hold=30, DAC=400, ADC Noise vs 18*Chip+Chan



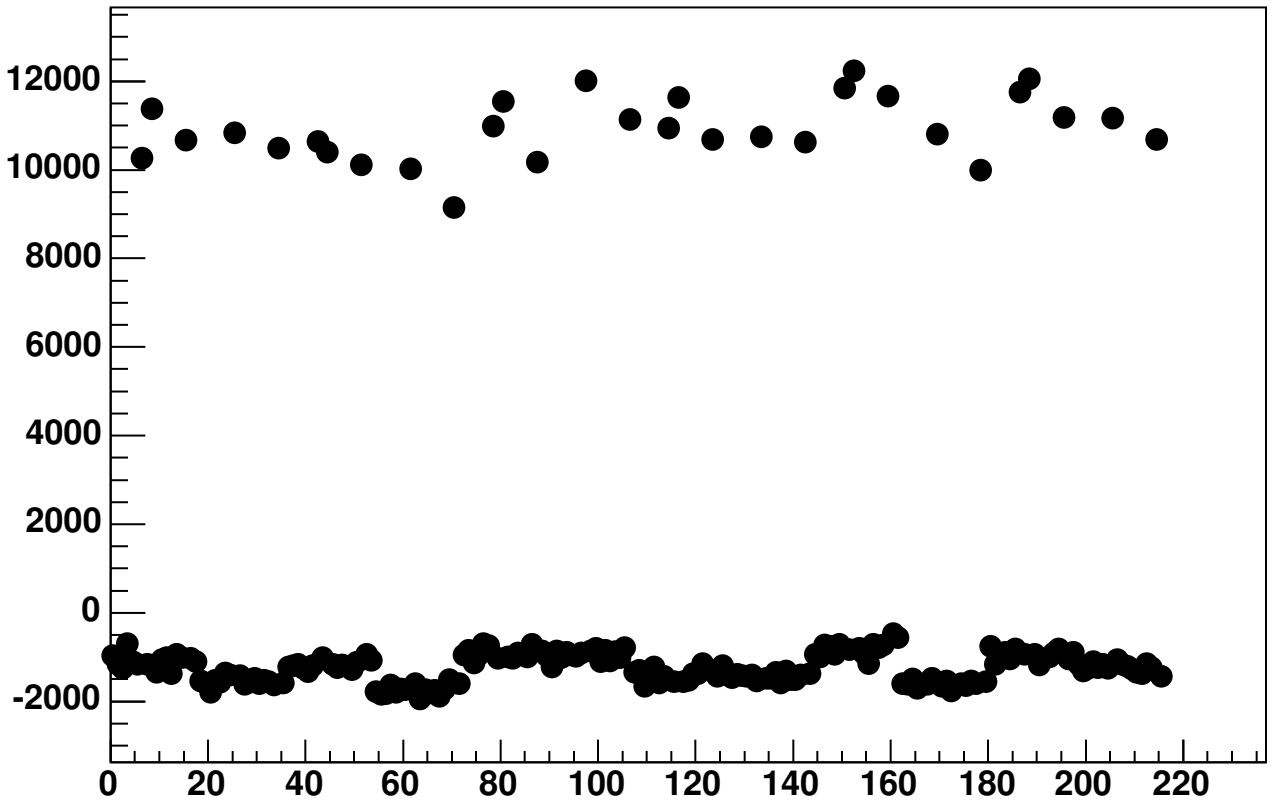
Enable 2, Hold=30, DAC=450, ADC Mean vs 18*Chip+Chan



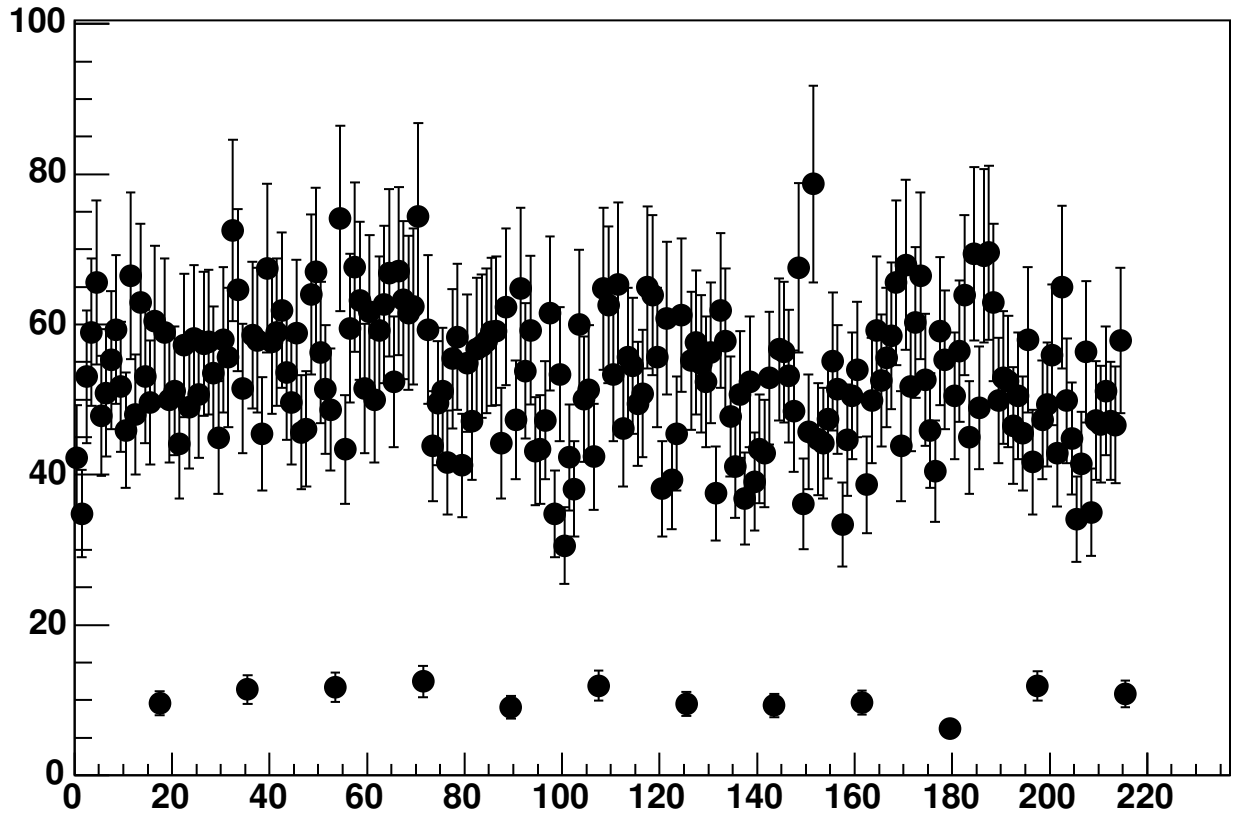
Enable 2, Hold=30, DAC=450, ADC Noise vs 18*Chip+Chan



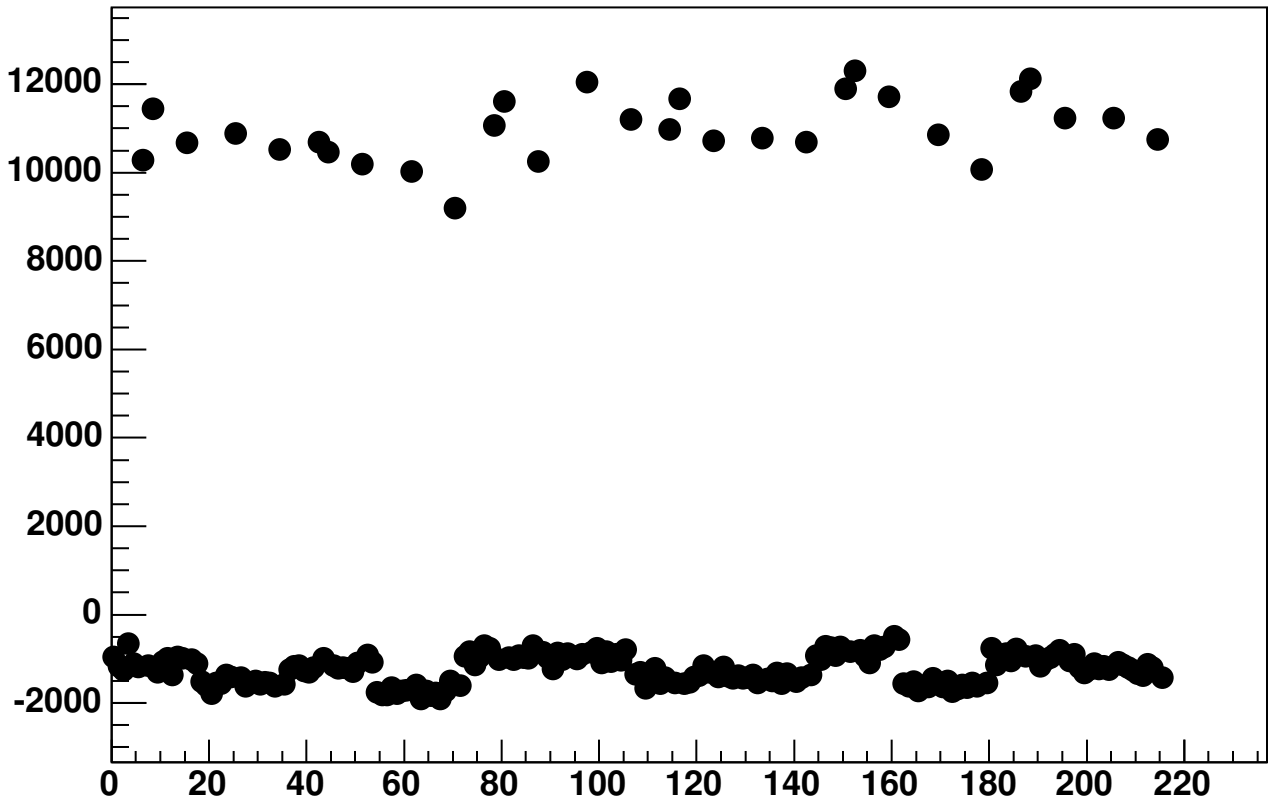
Enable 2, Hold=30, DAC=500, ADC Mean vs 18*Chip+Chan



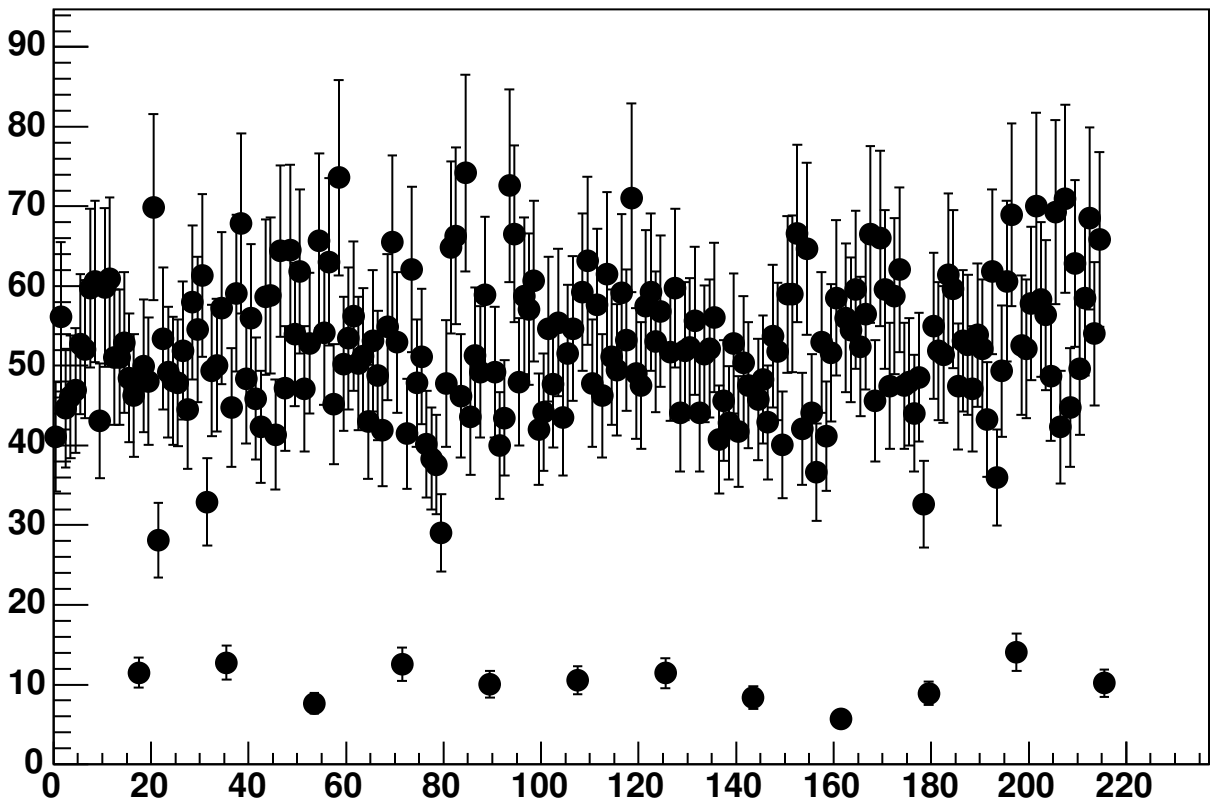
Enable 2, Hold=30, DAC=500, ADC Noise vs 18*Chip+Chan



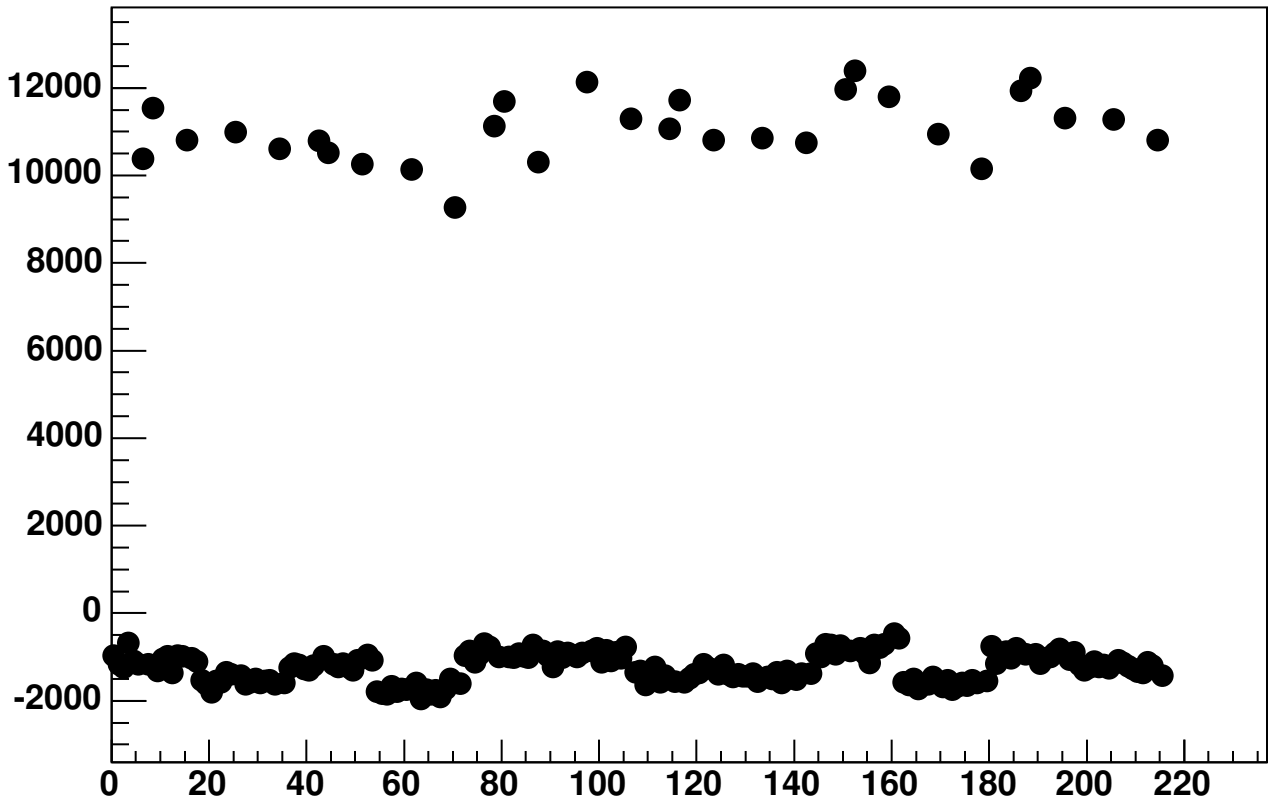
Enable 2, Hold=30, DAC=550, ADC Mean vs 18*Chip+Chan



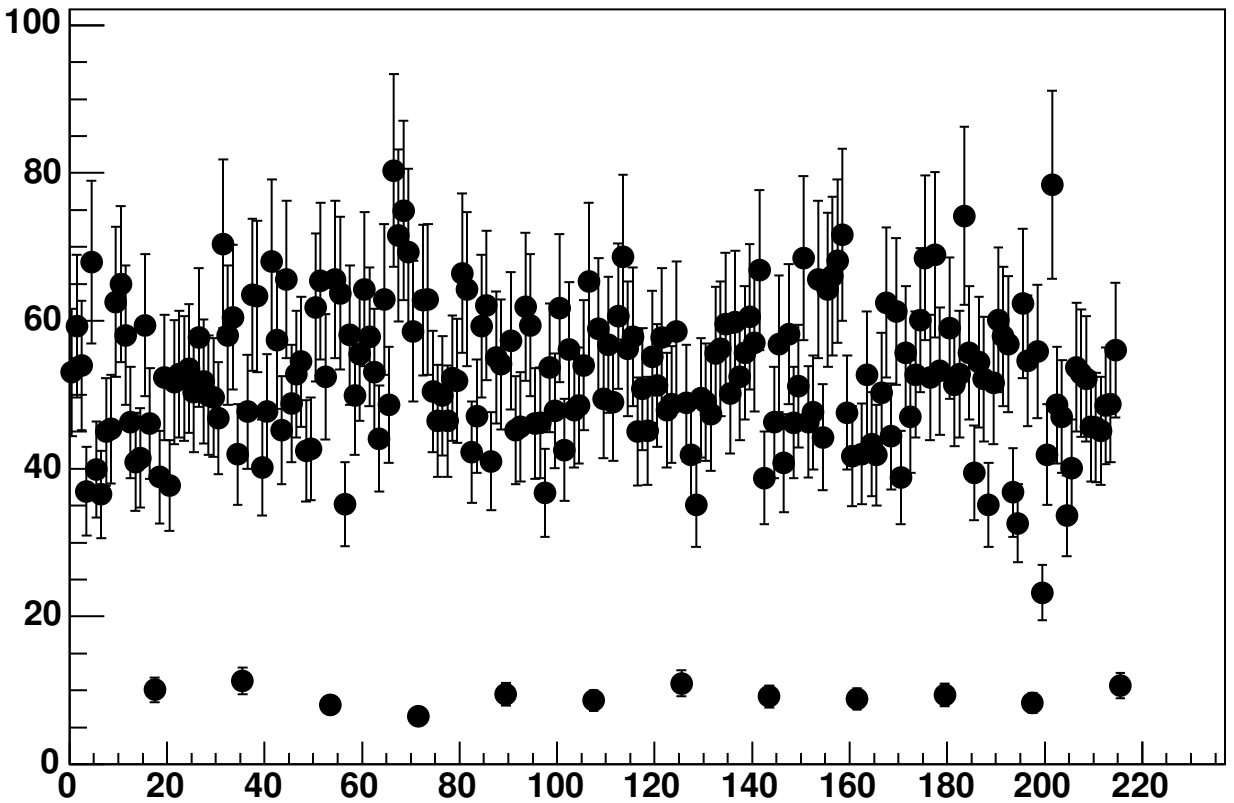
Enable 2, Hold=30, DAC=550, ADC Noise vs 18*Chip+Chan



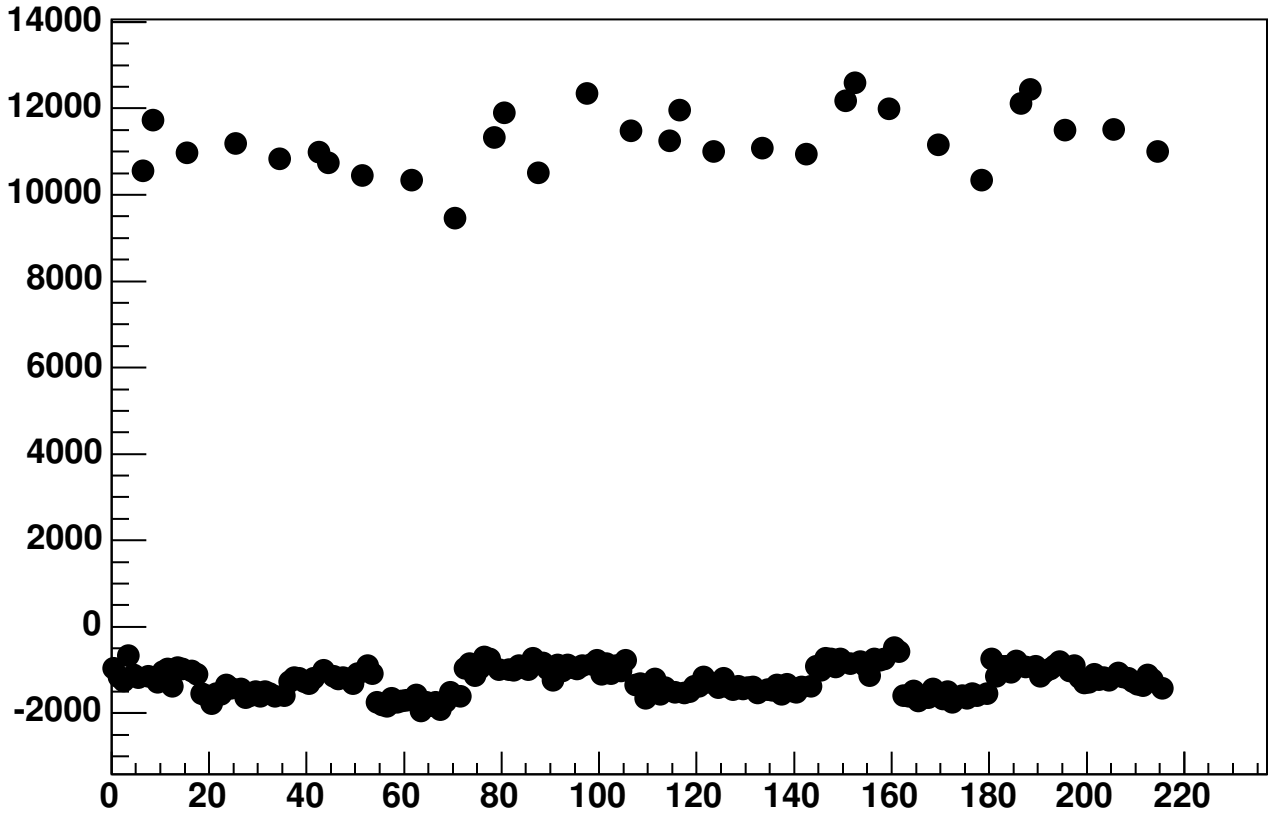
Enable 2, Hold=30, DAC=600, ADC Mean vs 18*Chip+Chan



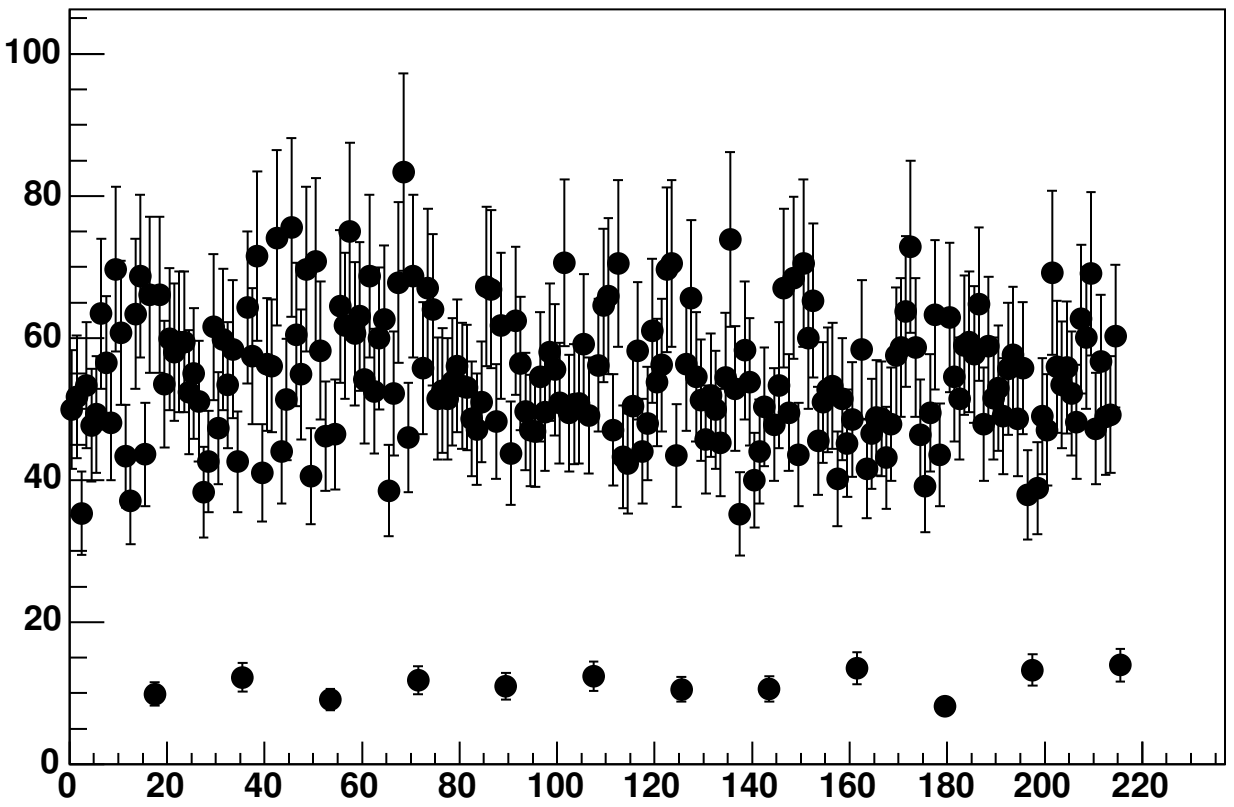
Enable 2, Hold=30, DAC=600, ADC Noise vs 18*Chip+Chan



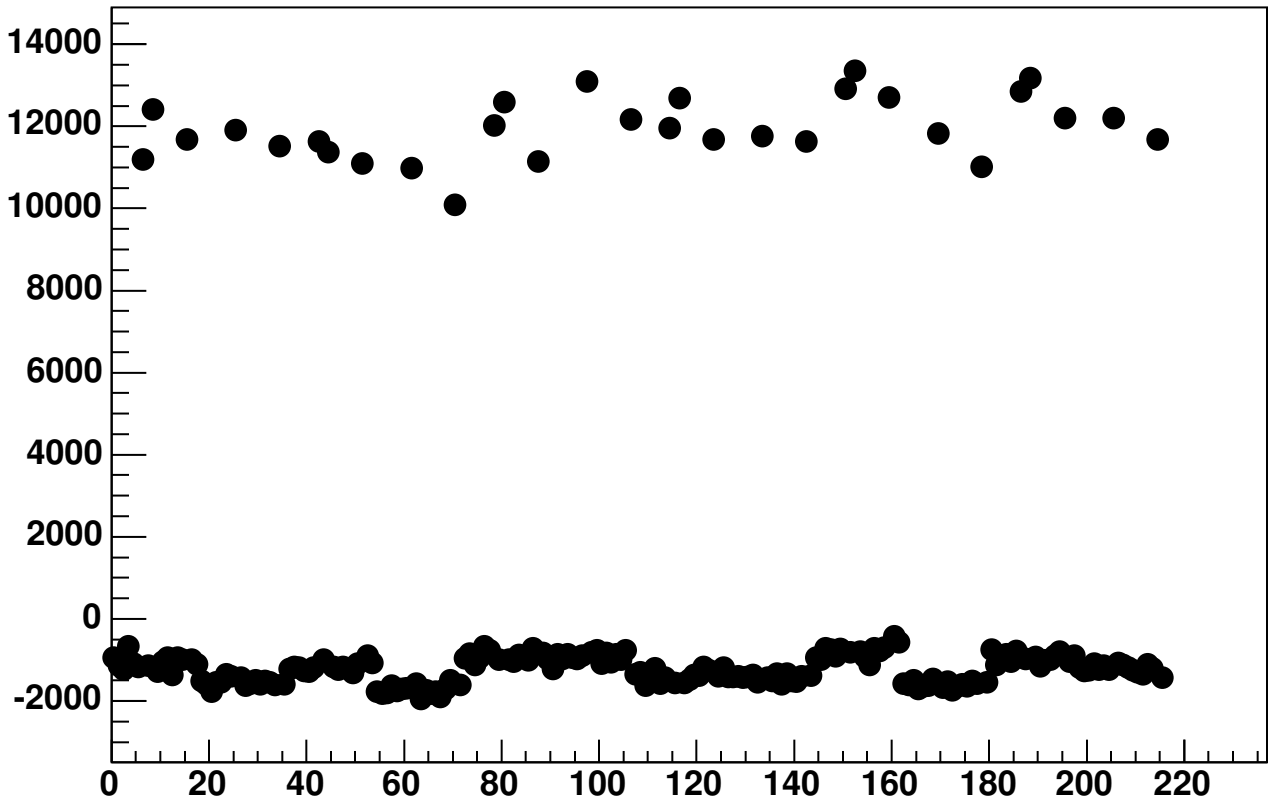
Enable 2, Hold=30, DAC=650, ADC Mean vs 18*Chip+Chan



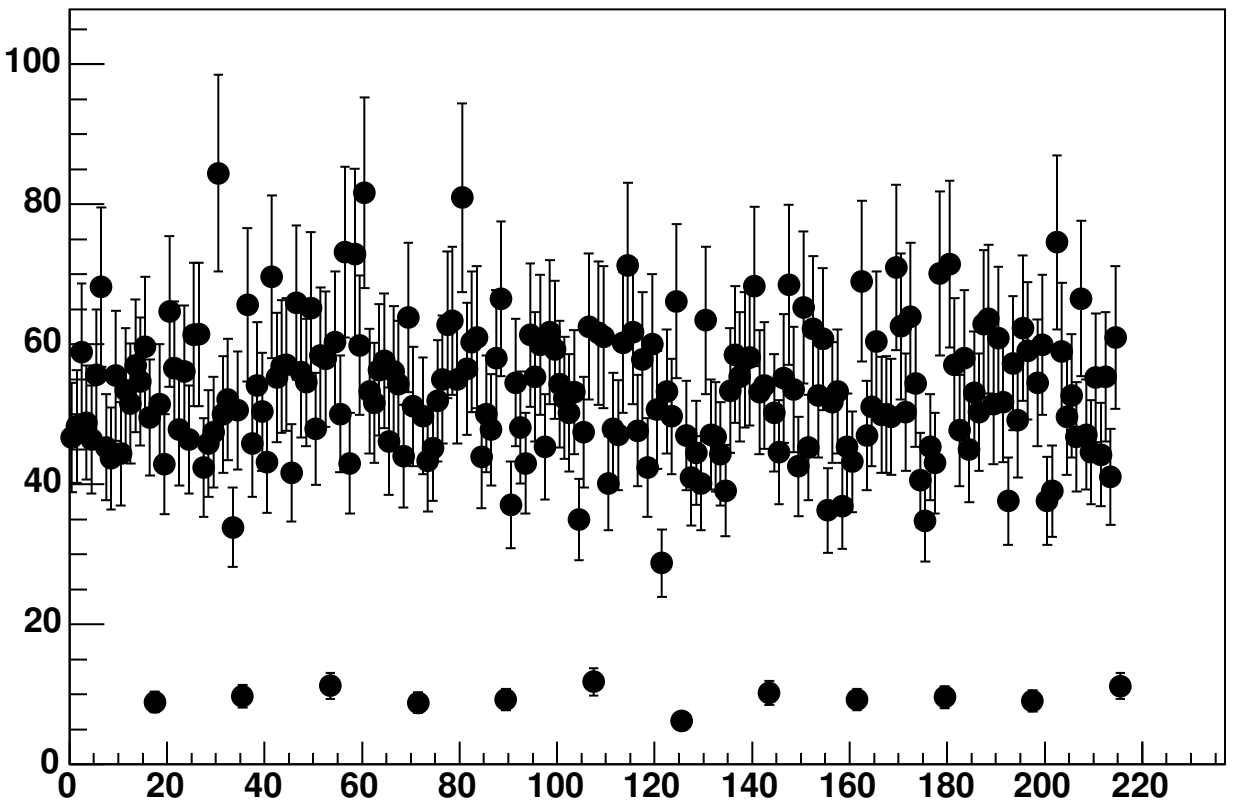
Enable 2, Hold=30, DAC=650, ADC Noise vs 18*Chip+Chan



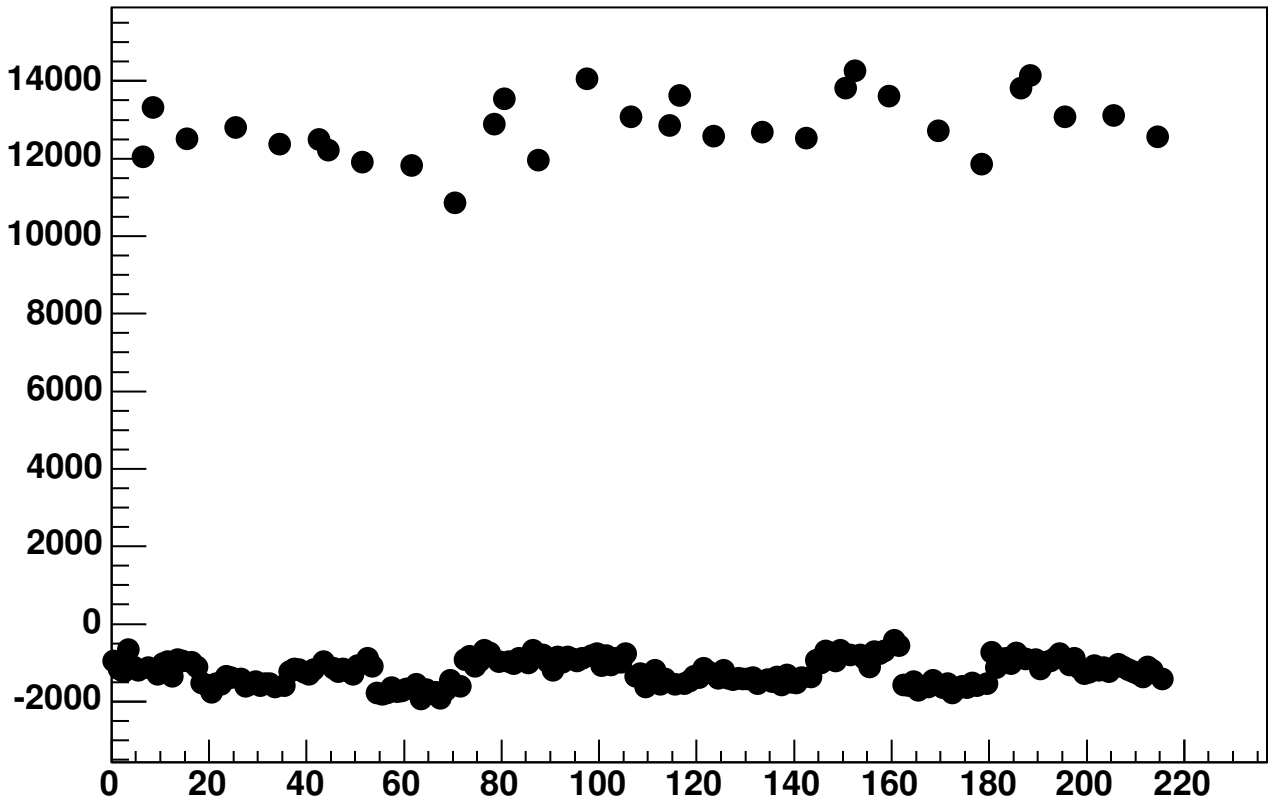
Enable 2, Hold=30, DAC=700, ADC Mean vs 18*Chip+Chan



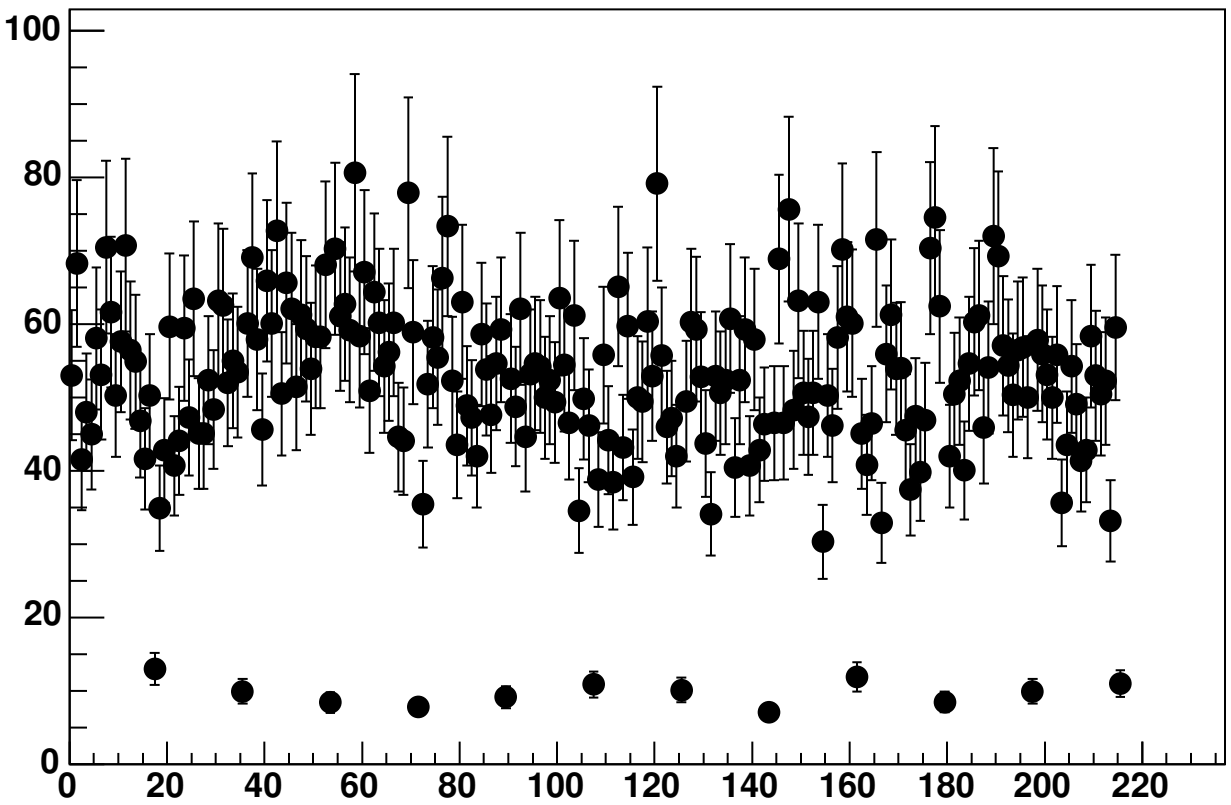
Enable 2, Hold=30, DAC=700, ADC Noise vs 18*Chip+Chan



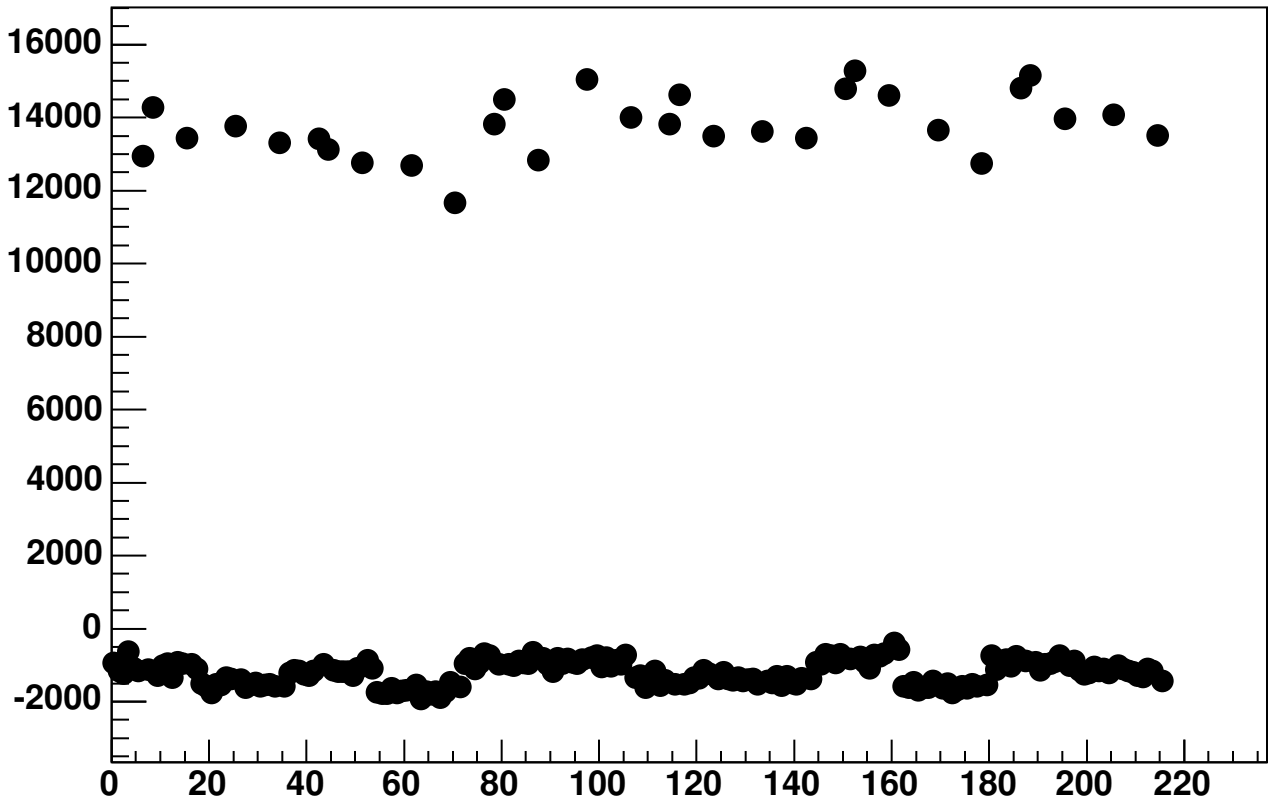
Enable 2, Hold=30, DAC=750, ADC Mean vs 18*Chip+Chan



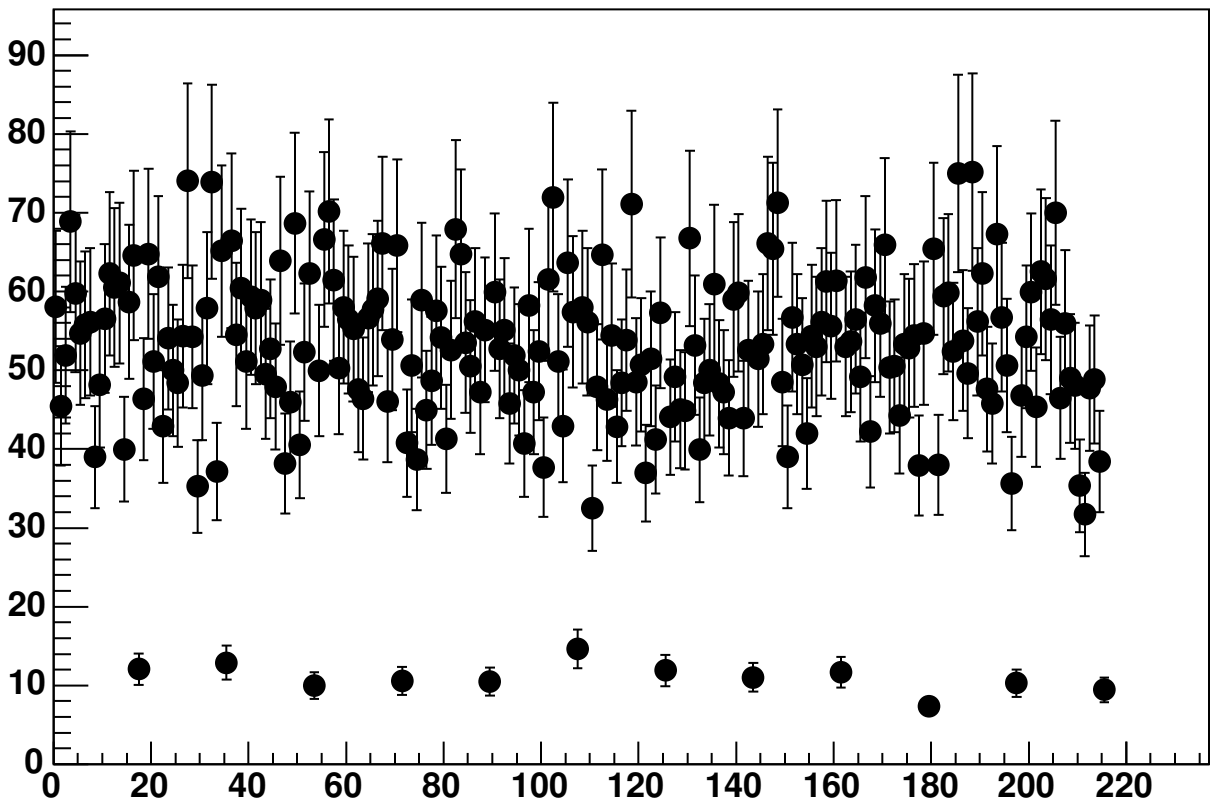
Enable 2, Hold=30, DAC=750, ADC Noise vs 18*Chip+Chan



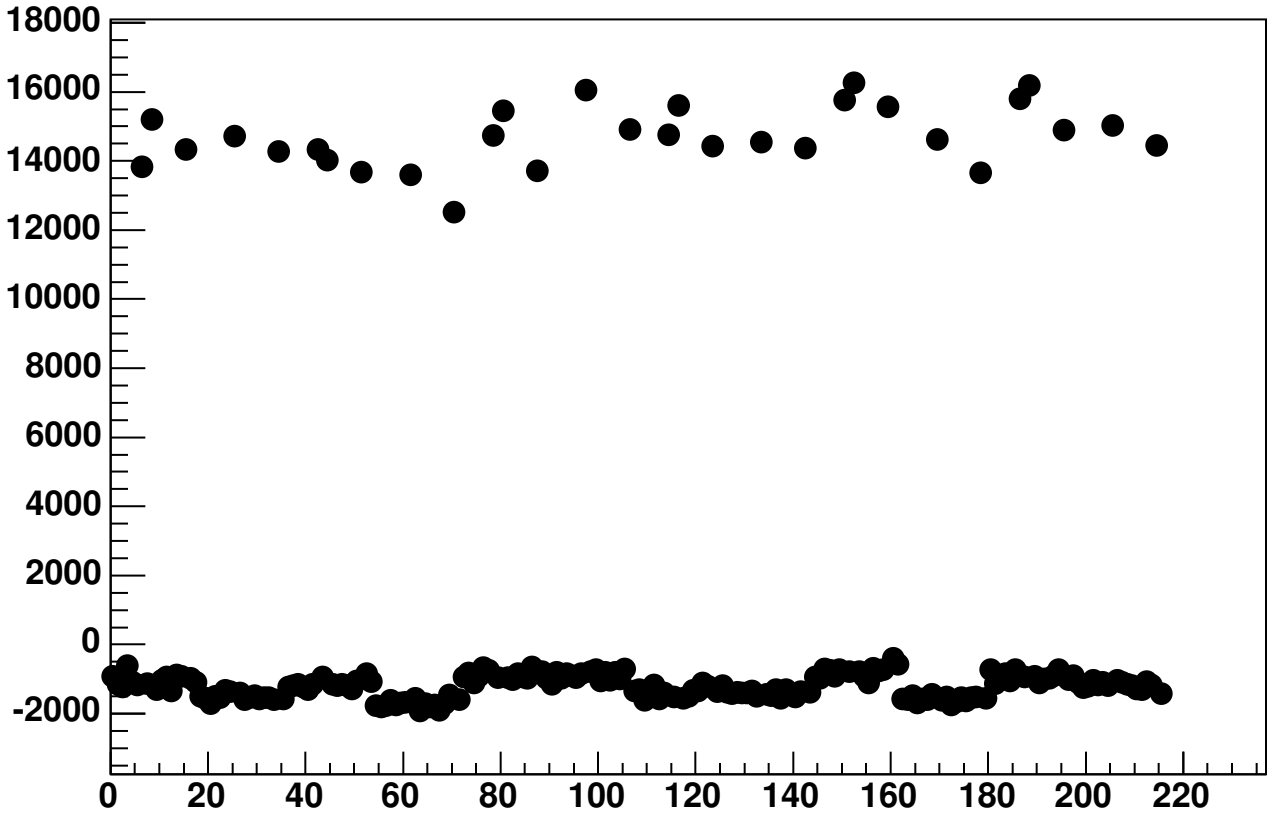
Enable 2, Hold=30, DAC=800, ADC Mean vs 18*Chip+Chan



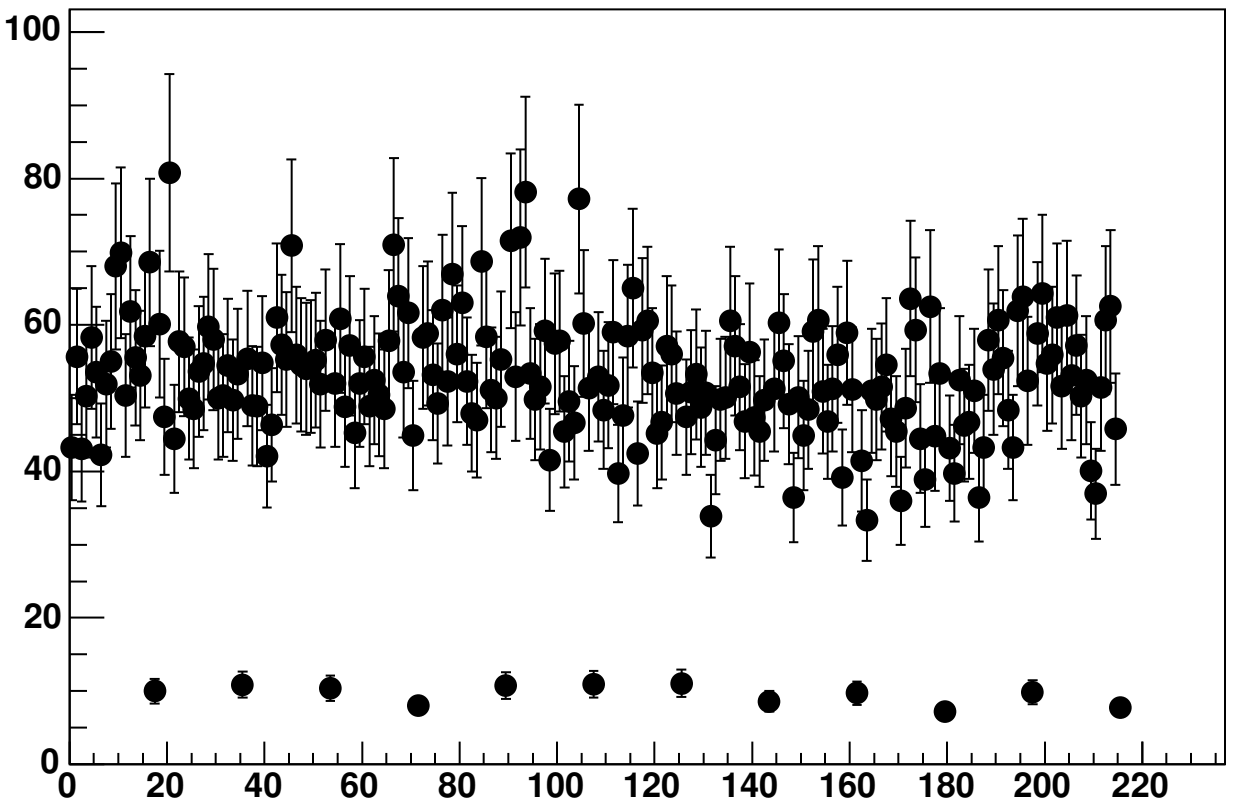
Enable 2, Hold=30, DAC=800, ADC Noise vs 18*Chip+Chan



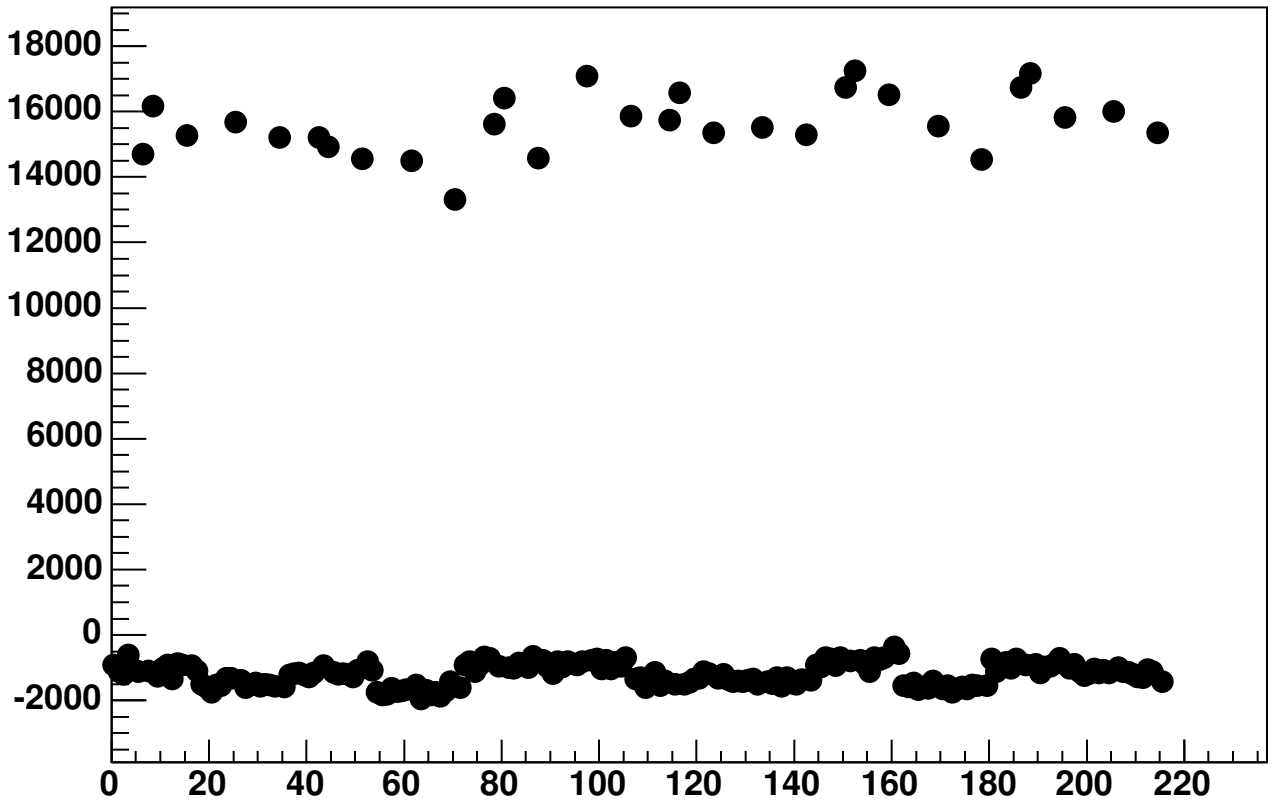
Enable 2, Hold=30, DAC=850, ADC Mean vs 18*Chip+Chan



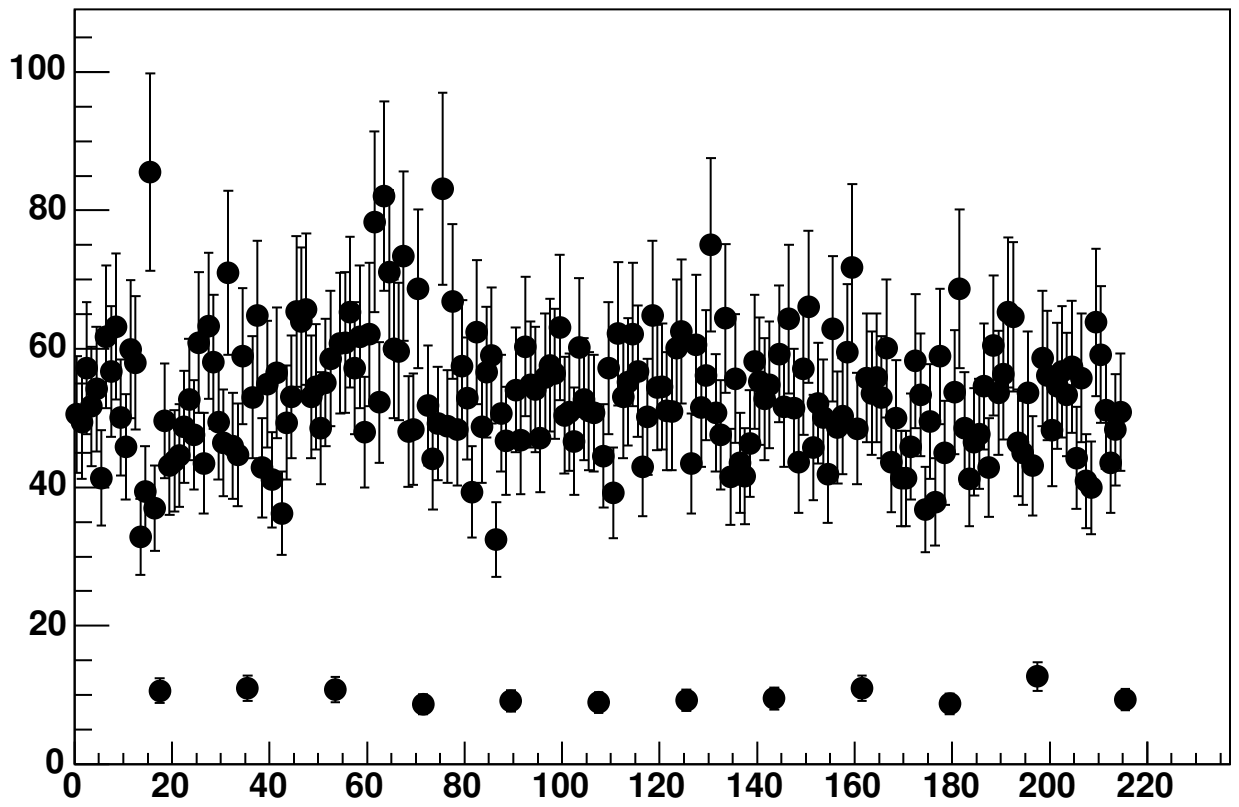
Enable 2, Hold=30, DAC=850, ADC Noise vs 18*Chip+Chan



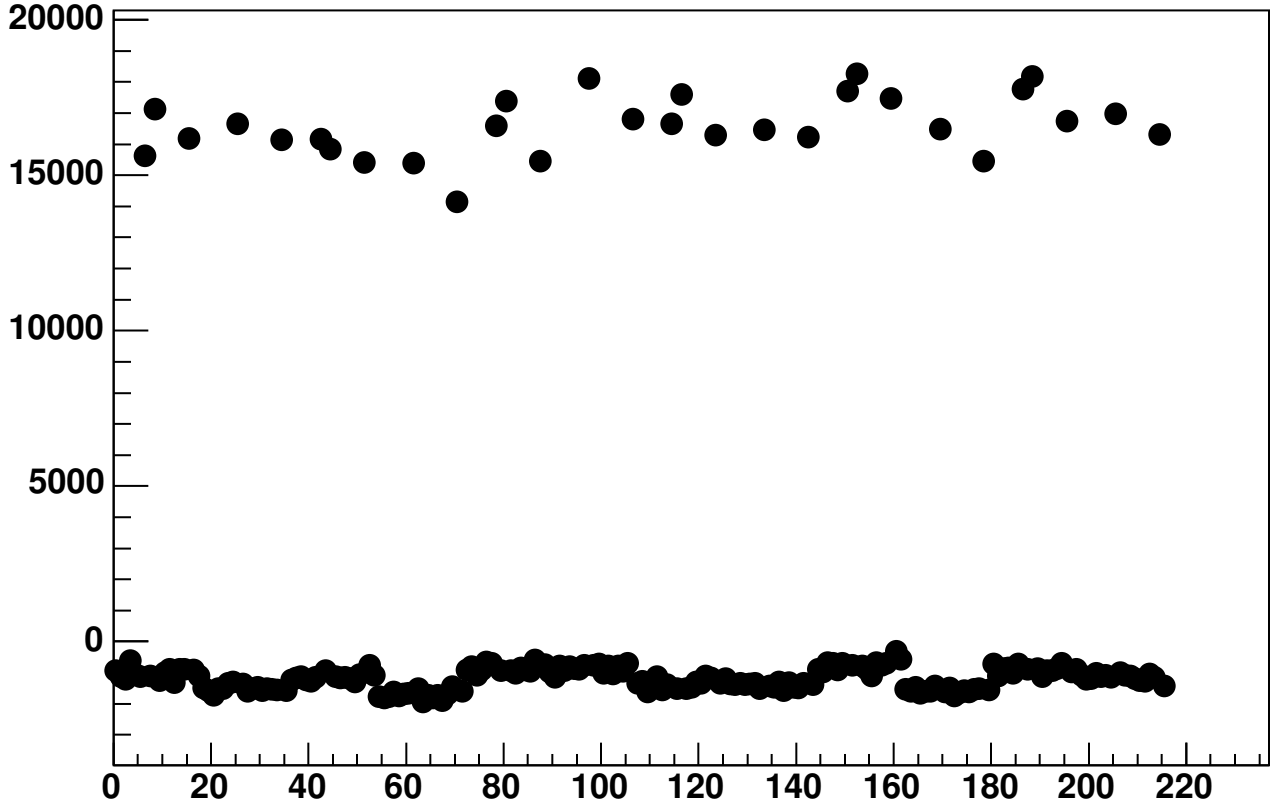
Enable 2, Hold=30, DAC=900, ADC Mean vs 18*Chip+Chan



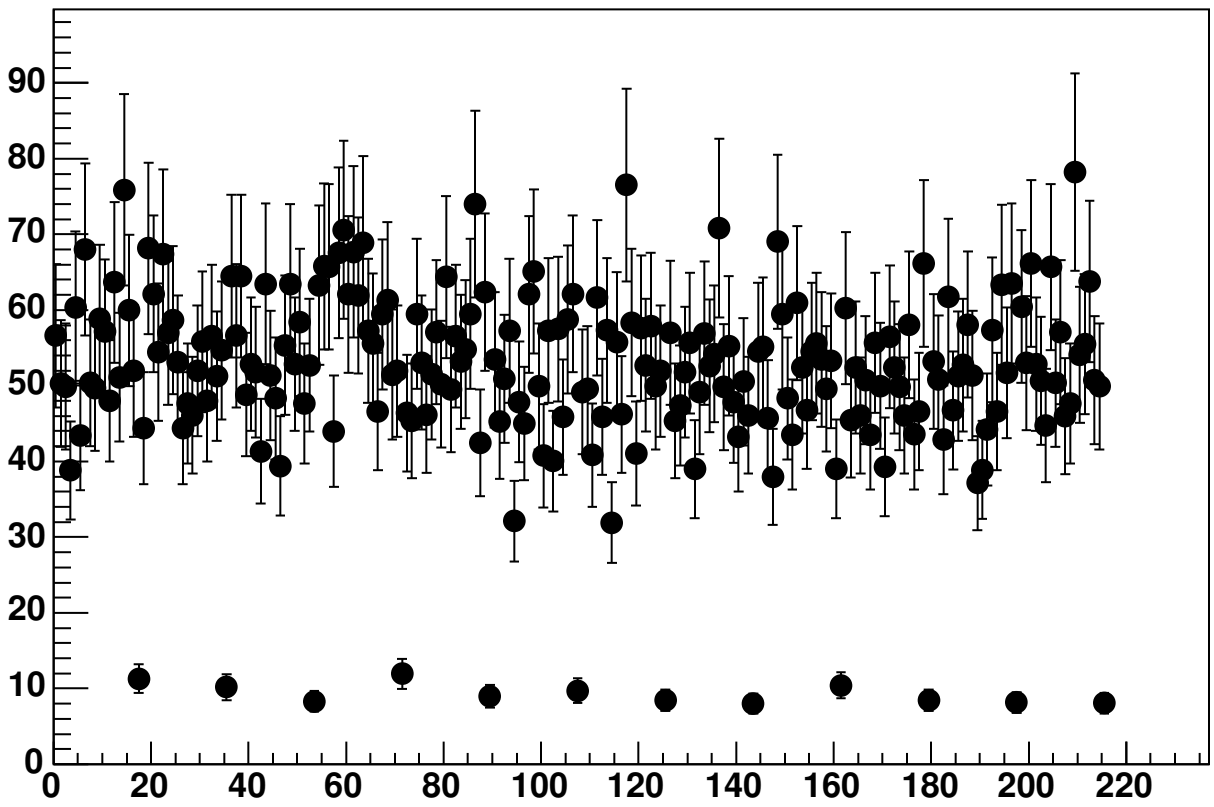
Enable 2, Hold=30, DAC=900, ADC Noise vs 18*Chip+Chan



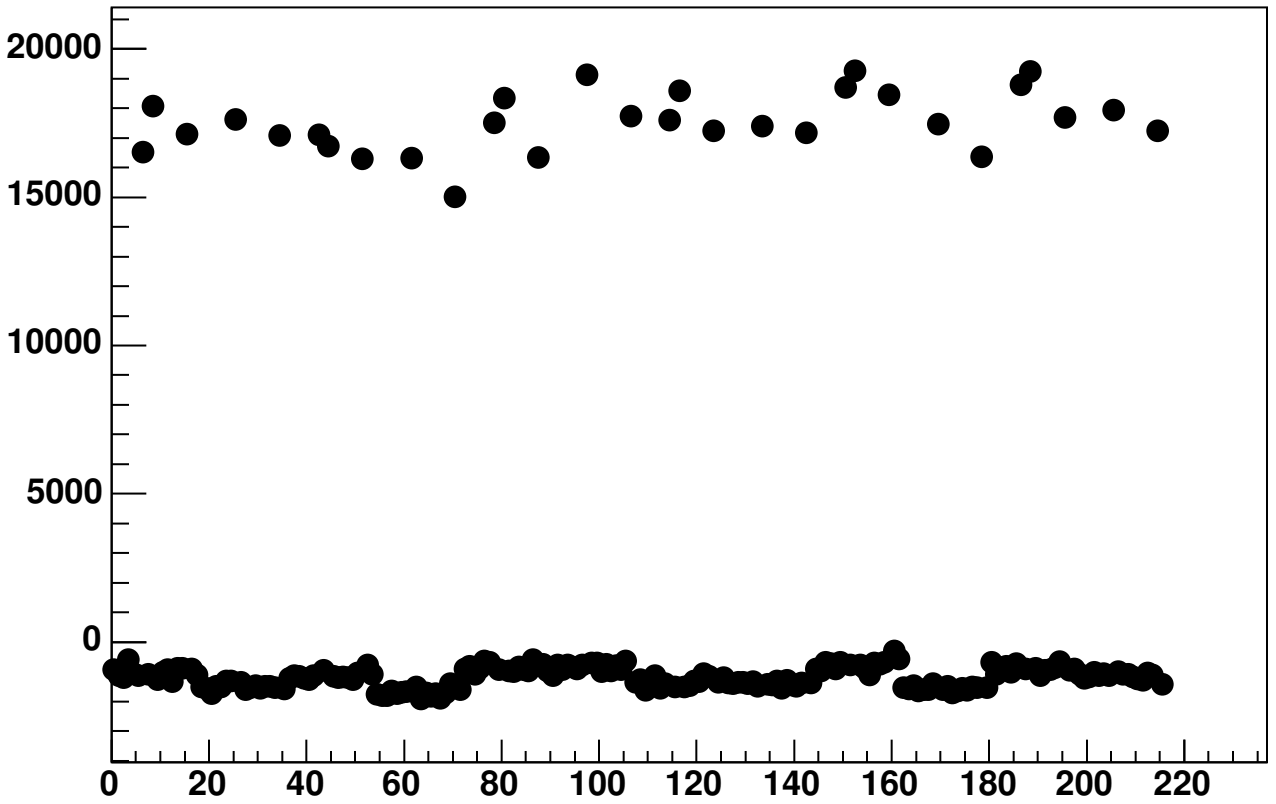
Enable 2, Hold=30, DAC=950, ADC Mean vs 18*Chip+Chan



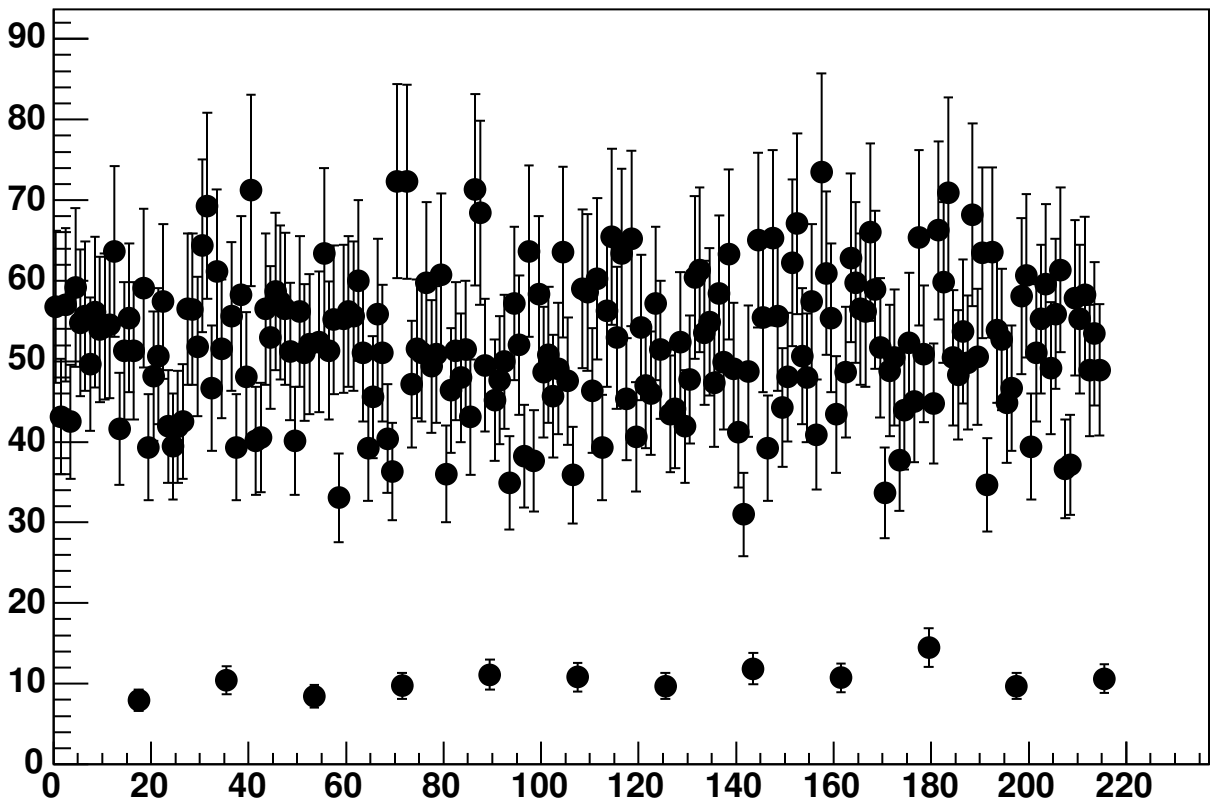
Enable 2, Hold=30, DAC=950, ADC Noise vs 18*Chip+Chan



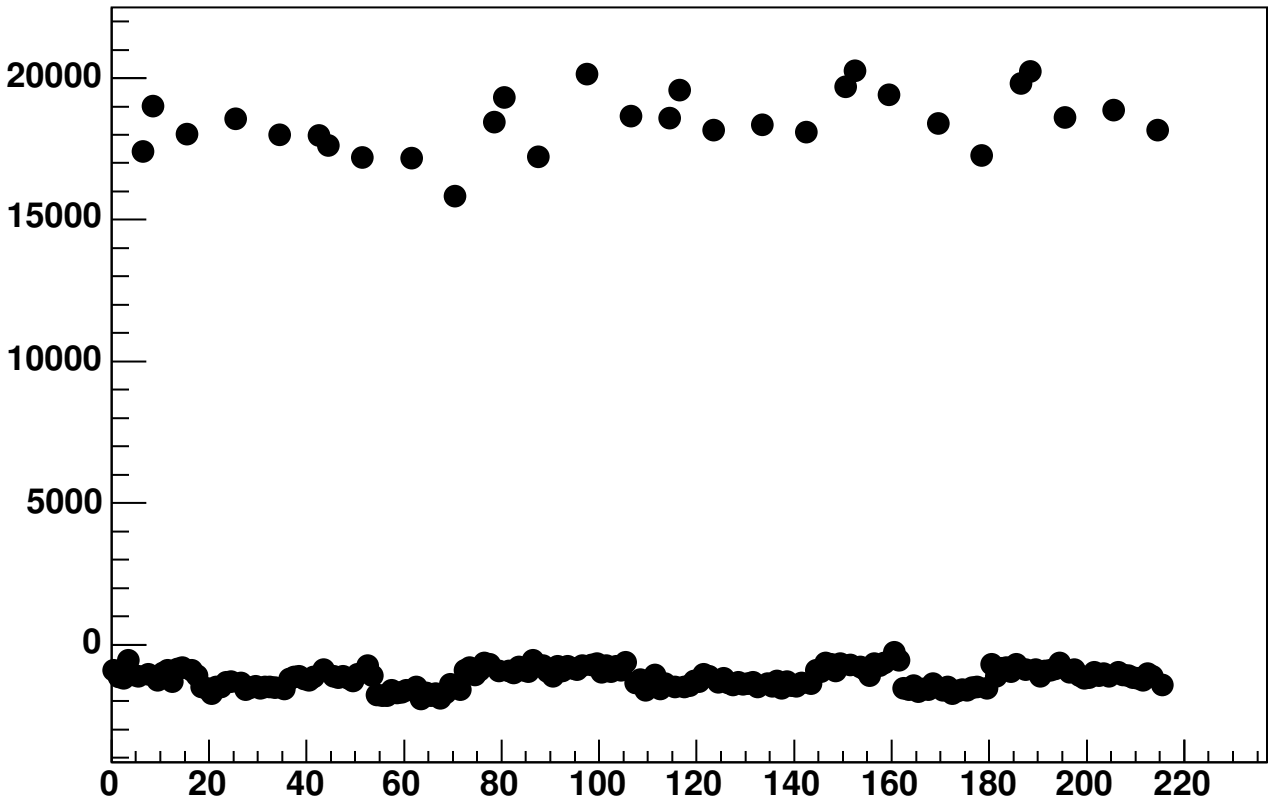
Enable 2, Hold=30, DAC=1000, ADC Mean vs 18*Chip+Chan



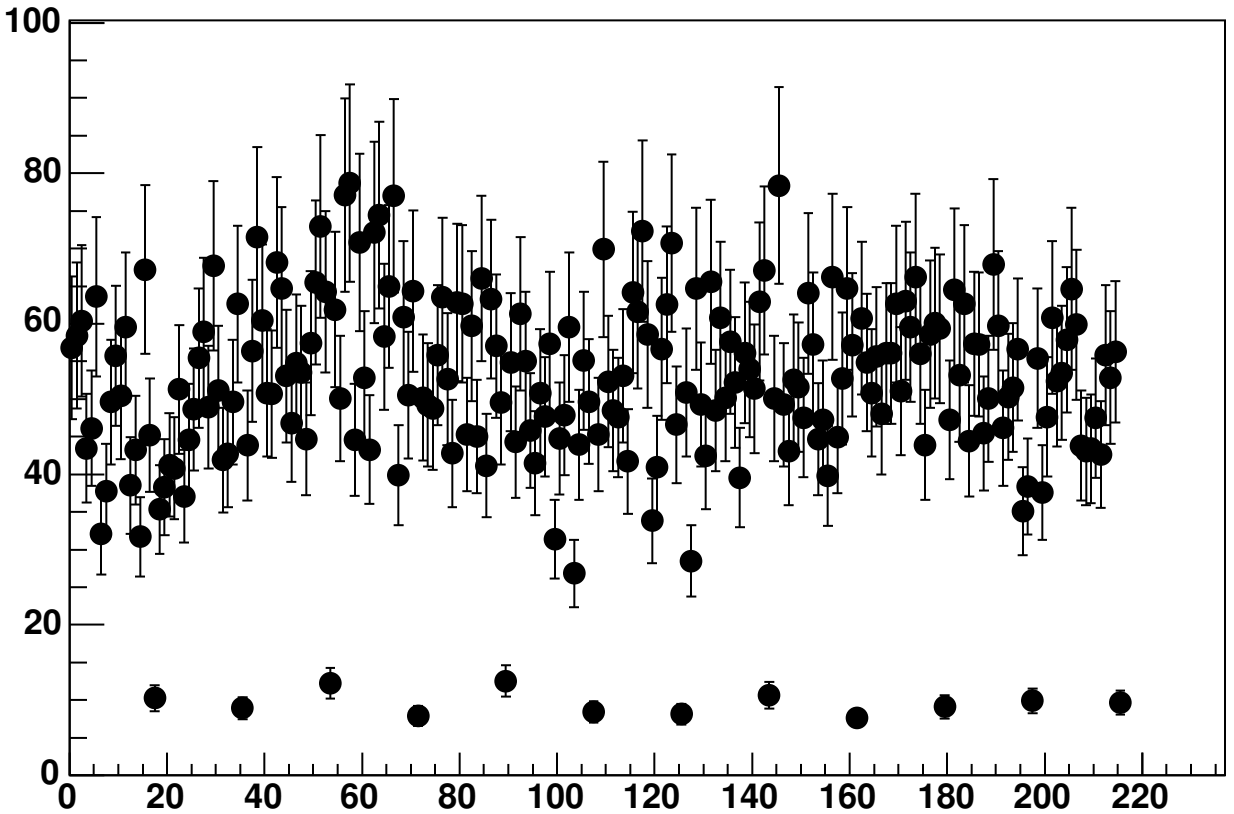
Enable 2, Hold=30, DAC=1000, ADC Noise vs 18*Chip+Chan



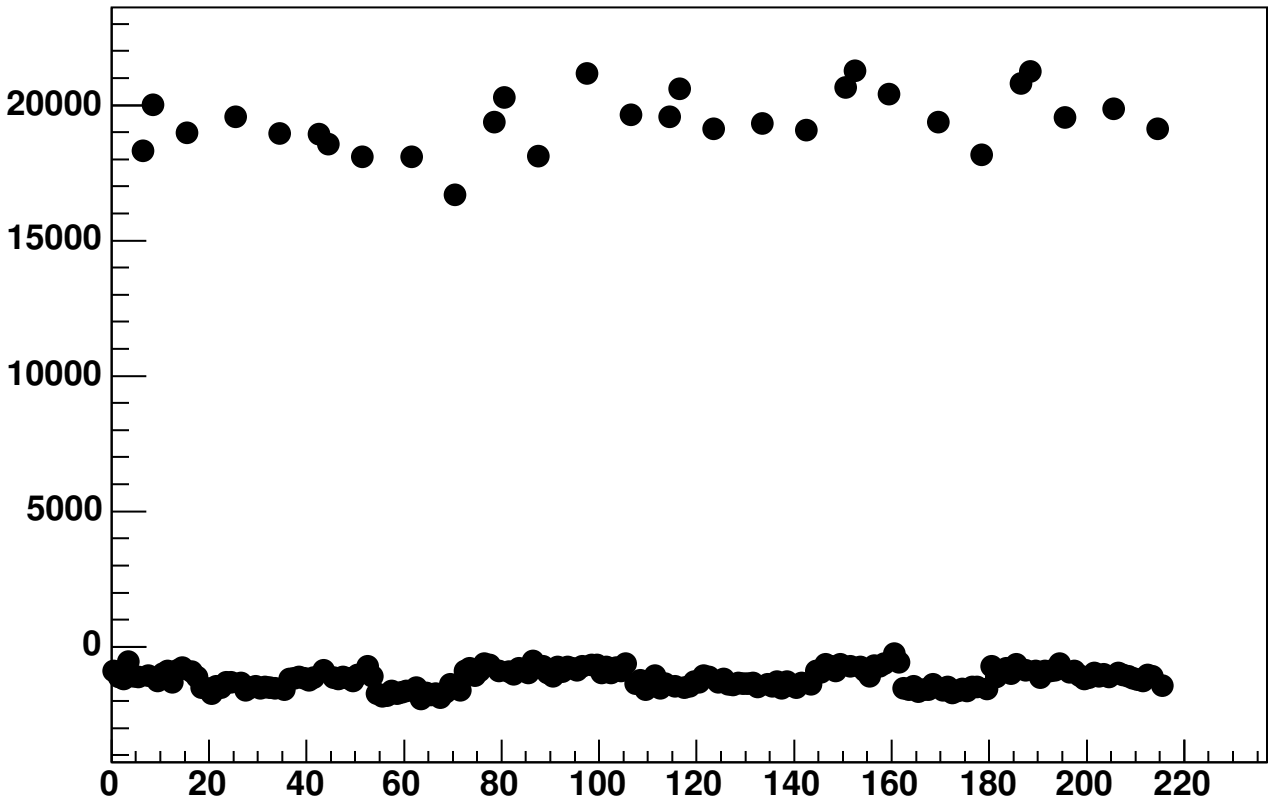
Enable 2, Hold=30, DAC=1050, ADC Mean vs 18*Chip+Chan



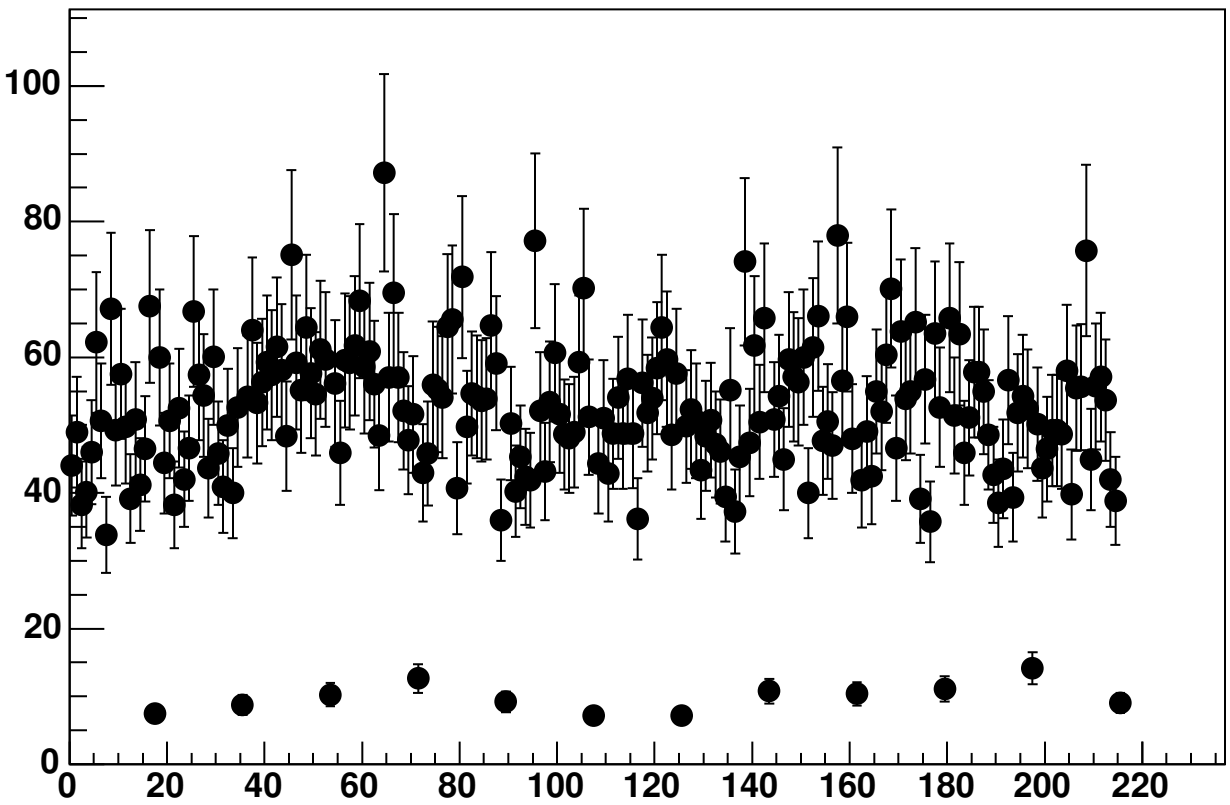
Enable 2, Hold=30, DAC=1050, ADC Noise vs 18*Chip+Chan



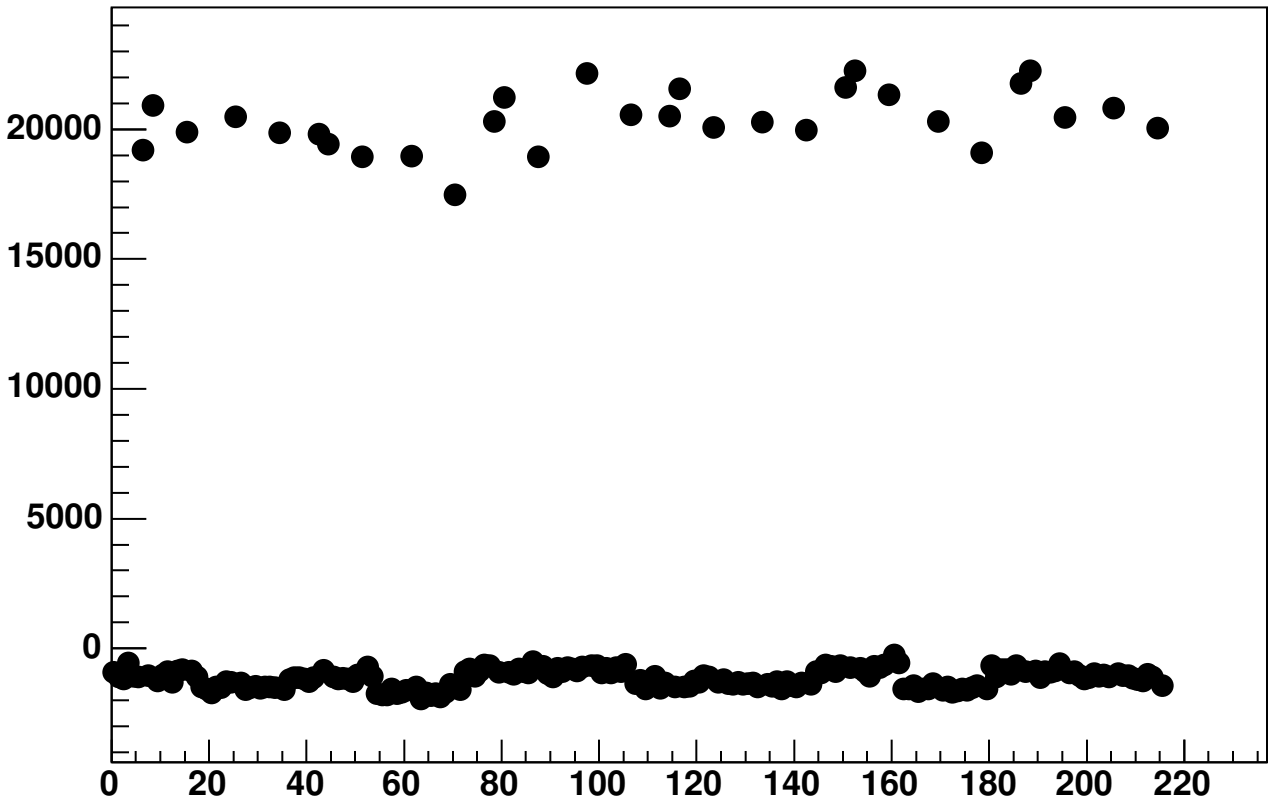
Enable 2, Hold=30, DAC=1100, ADC Mean vs 18*Chip+Chan



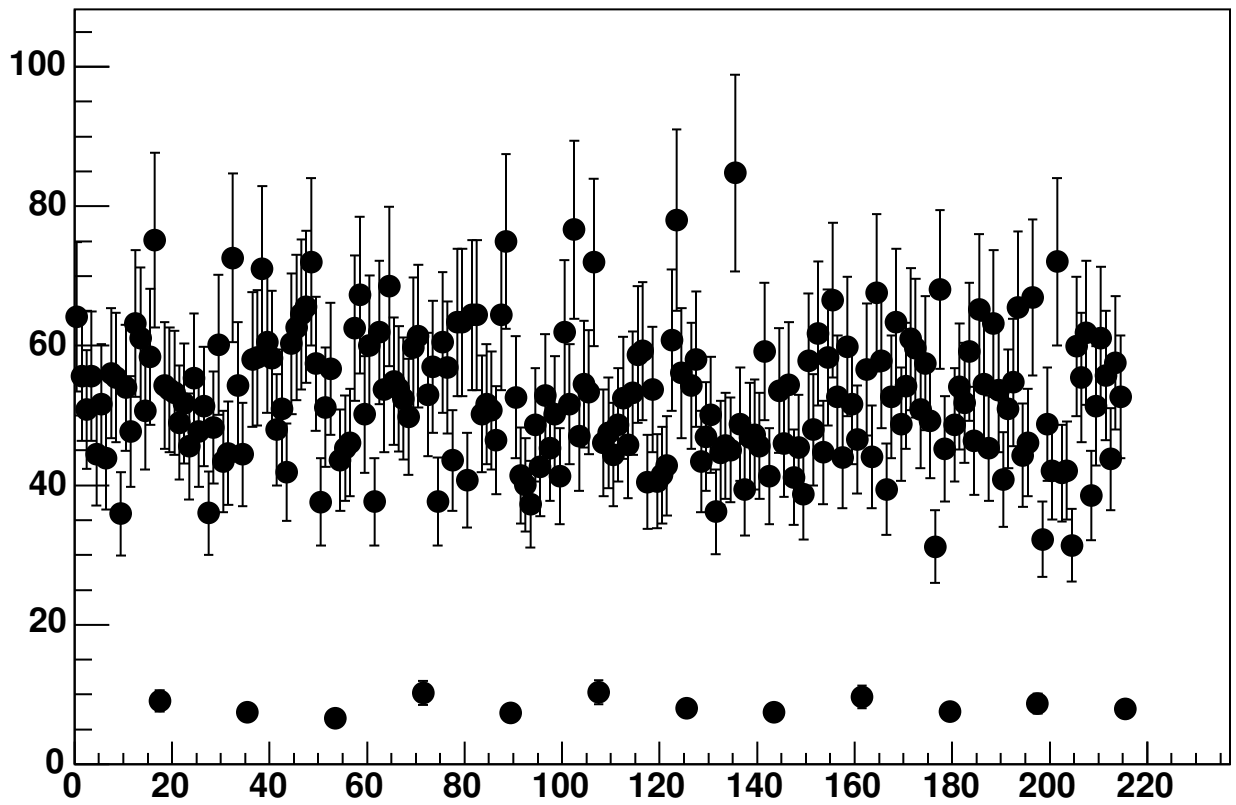
Enable 2, Hold=30, DAC=1100, ADC Noise vs 18*Chip+Chan



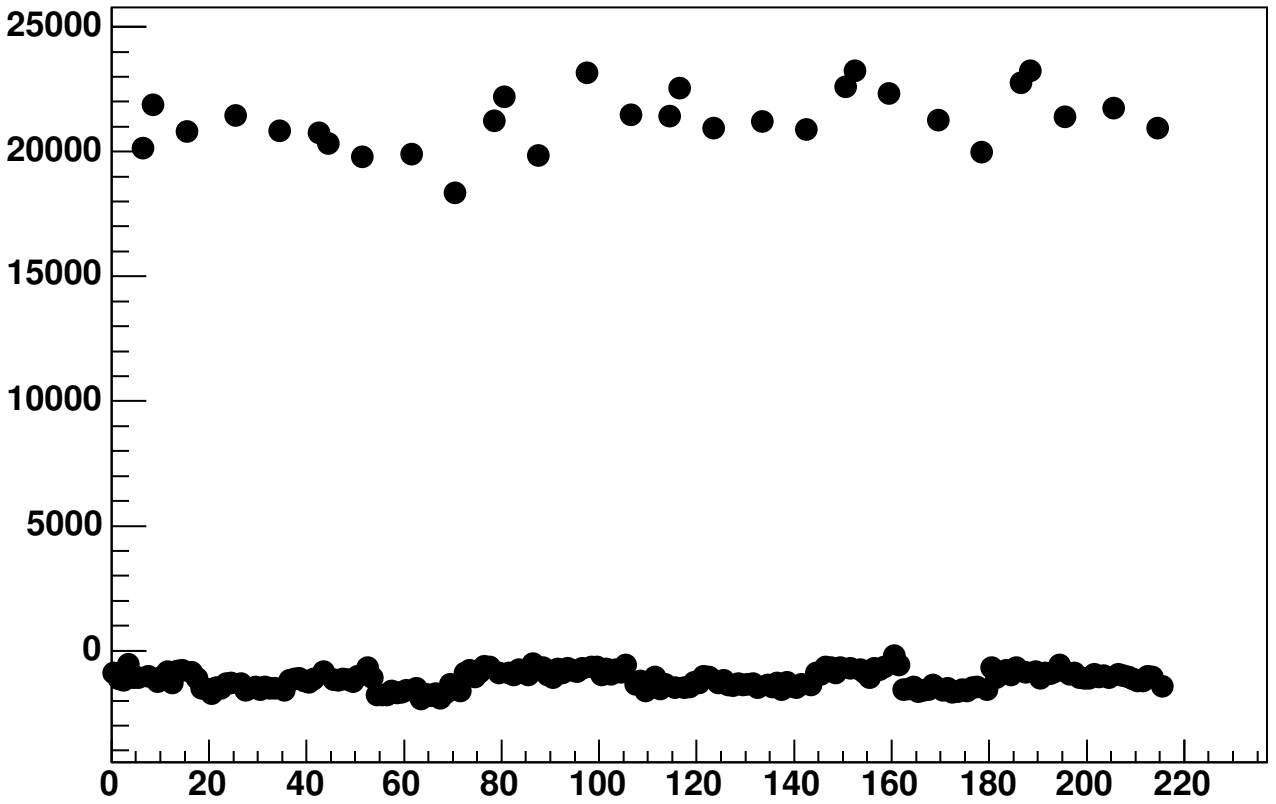
Enable 2, Hold=30, DAC=1150, ADC Mean vs 18*Chip+Chan



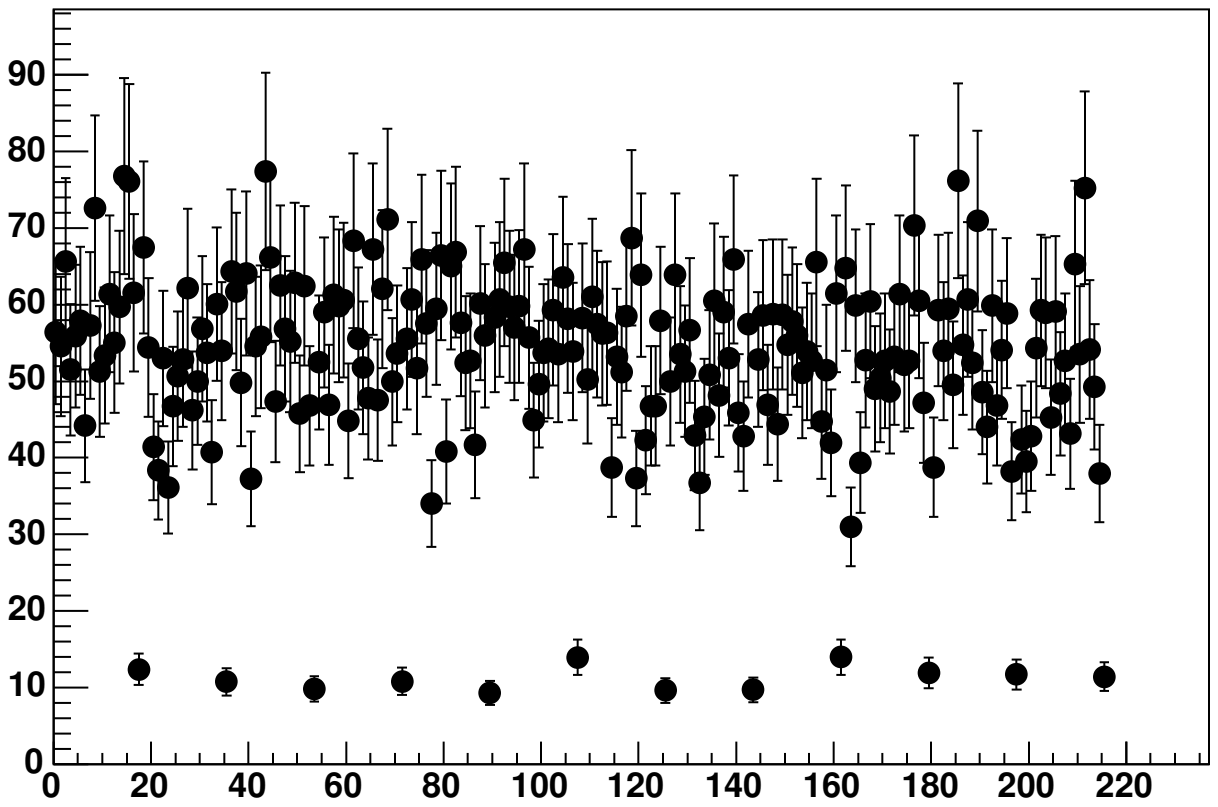
Enable 2, Hold=30, DAC=1150, ADC Noise vs 18*Chip+Chan



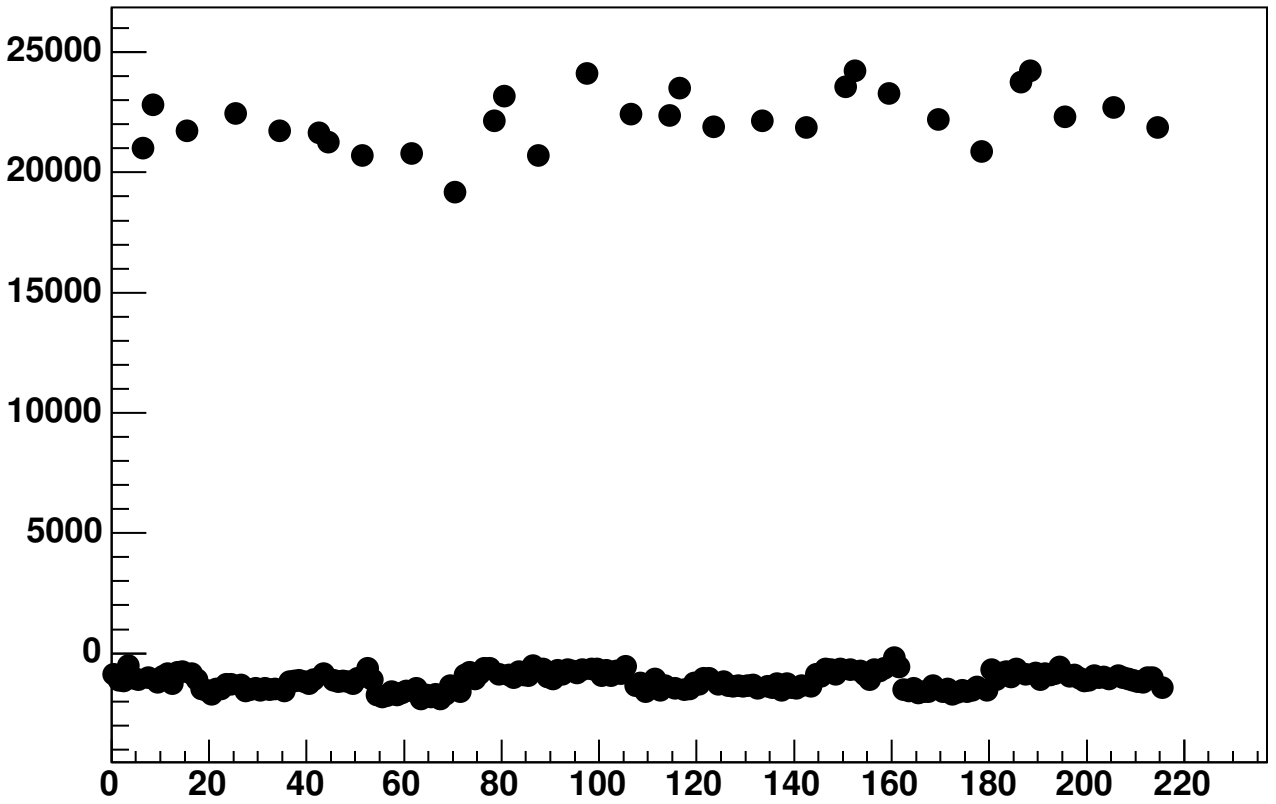
Enable 2, Hold=30, DAC=1200, ADC Mean vs 18*Chip+Chan



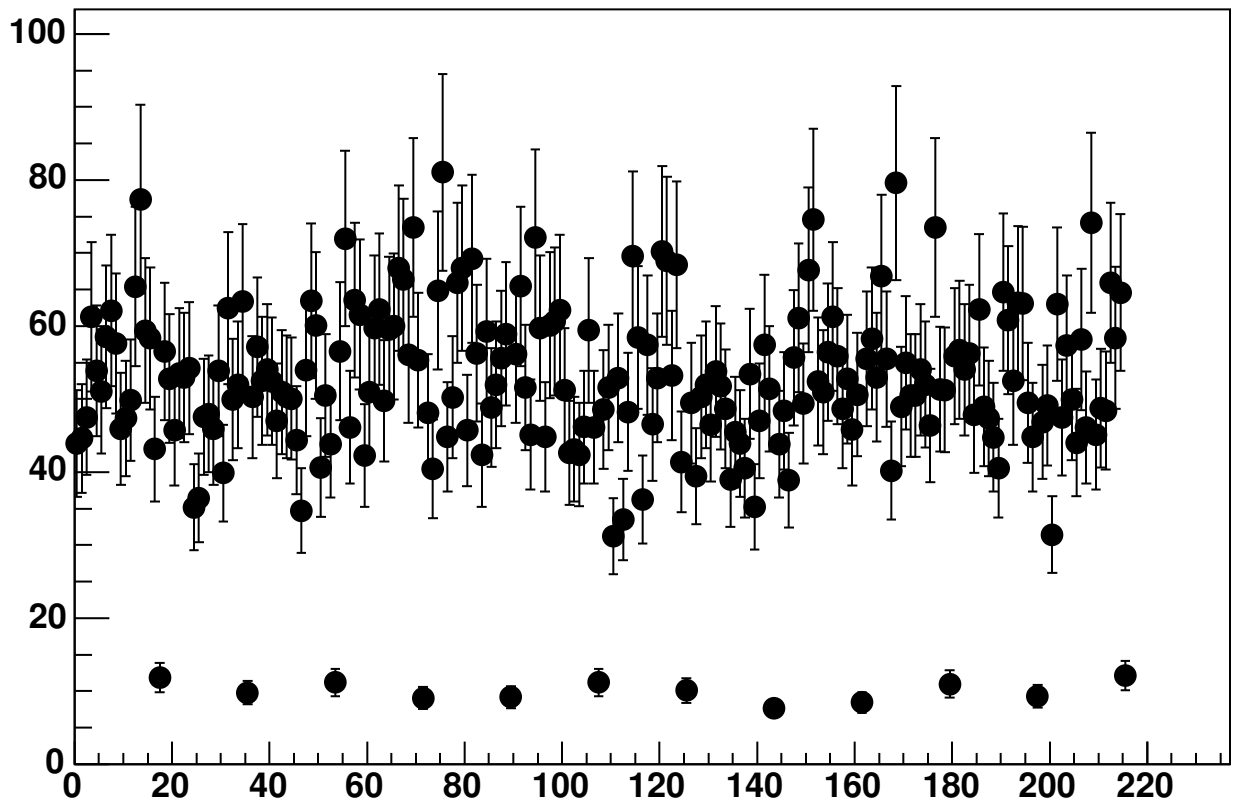
Enable 2, Hold=30, DAC=1200, ADC Noise vs 18*Chip+Chan



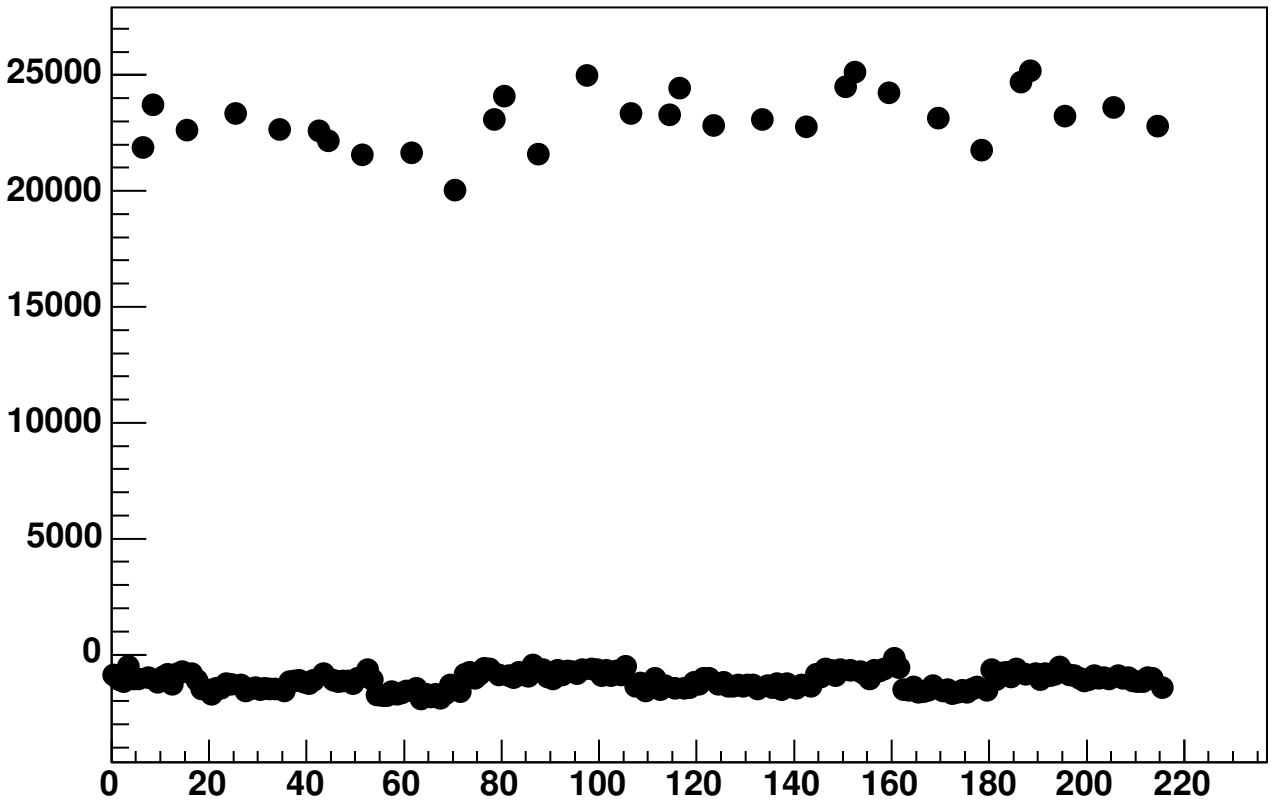
Enable 2, Hold=30, DAC=1250, ADC Mean vs 18*Chip+Chan



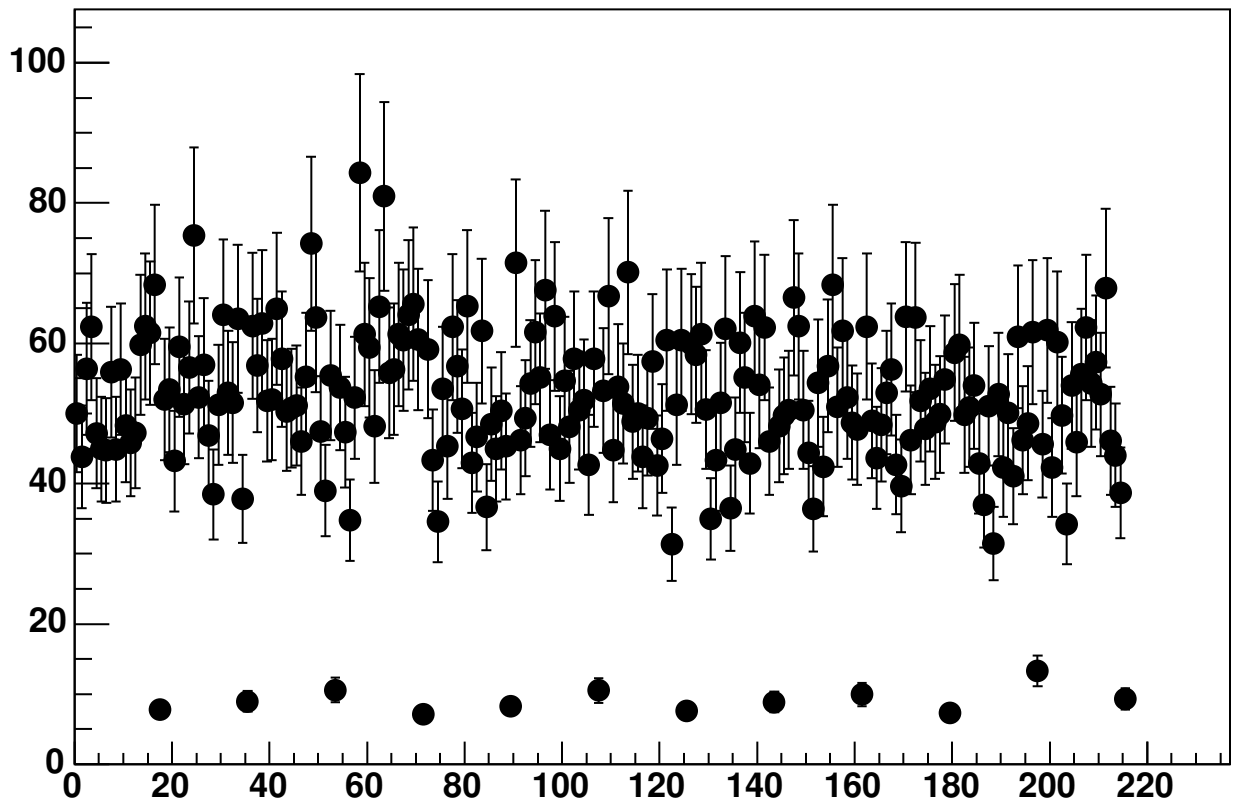
Enable 2, Hold=30, DAC=1250, ADC Noise vs 18*Chip+Chan



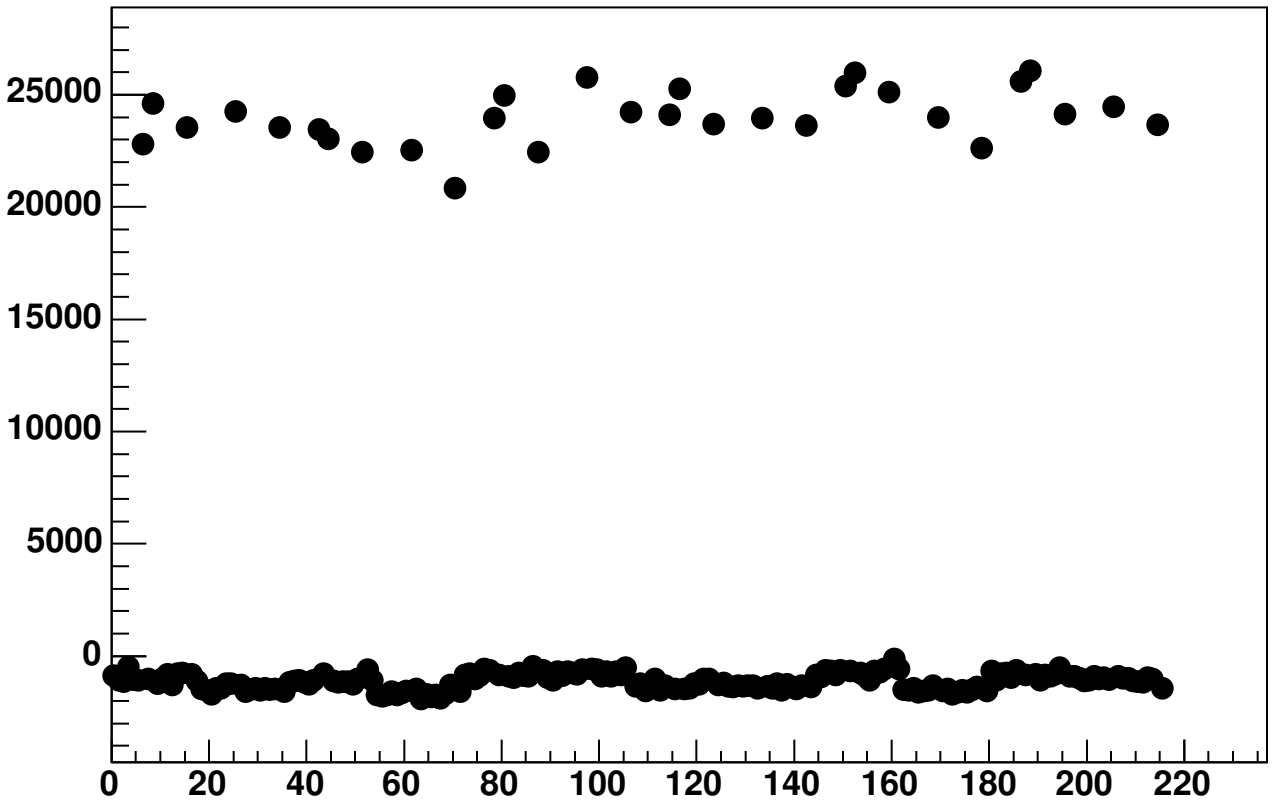
Enable 2, Hold=30, DAC=1300, ADC Mean vs 18*Chip+Chan



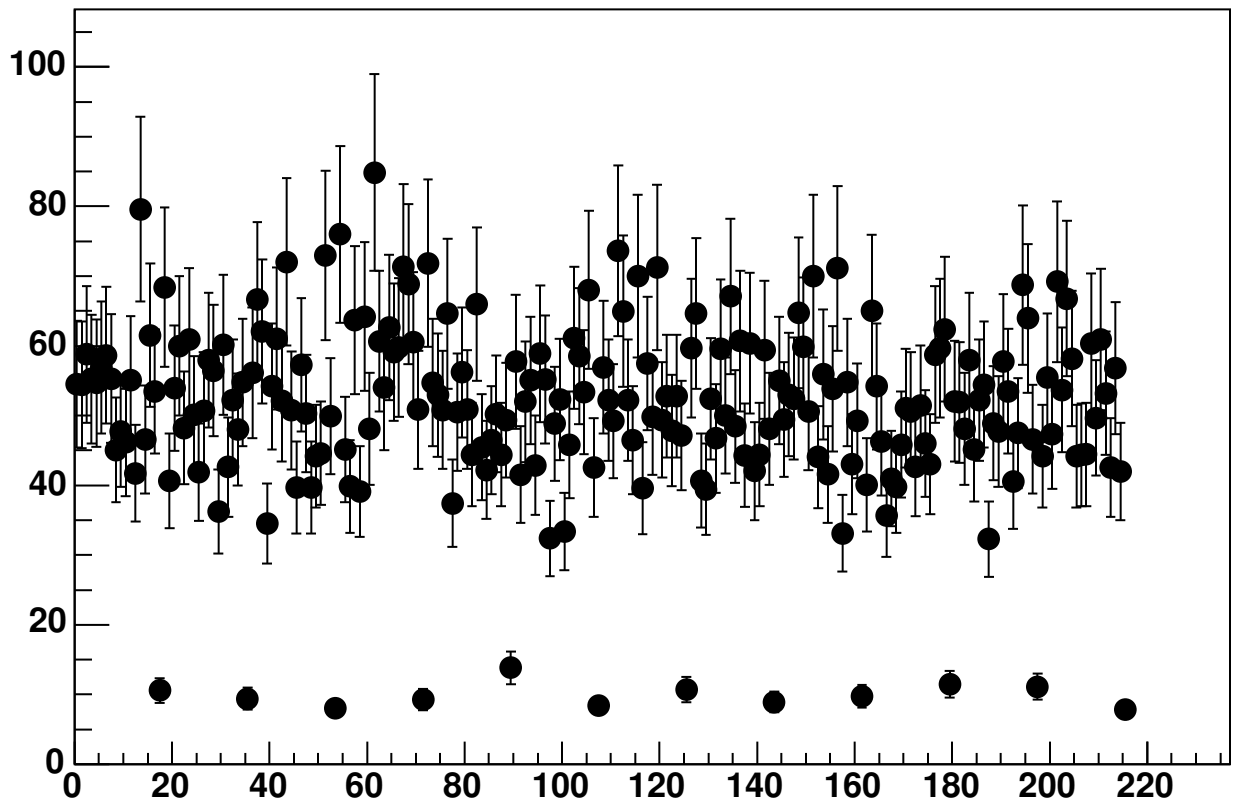
Enable 2, Hold=30, DAC=1300, ADC Noise vs 18*Chip+Chan



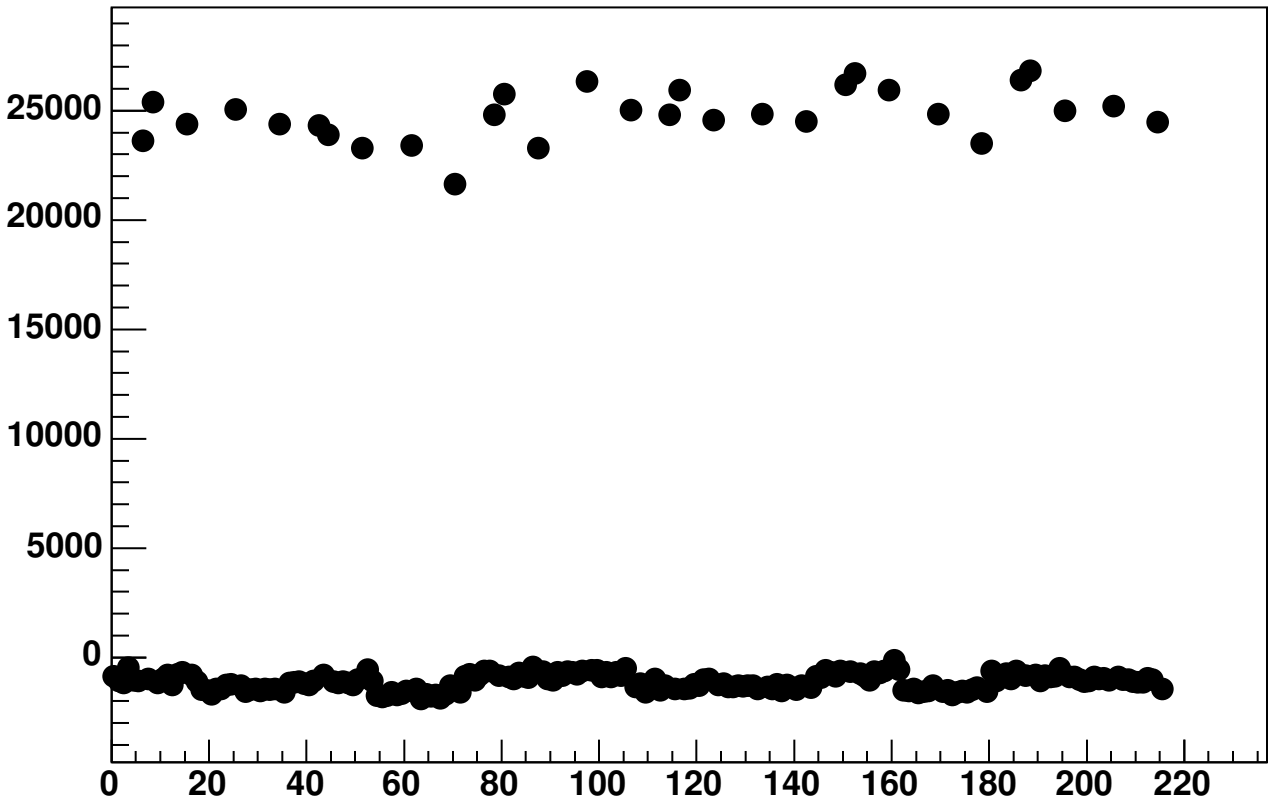
Enable 2, Hold=30, DAC=1350, ADC Mean vs 18*Chip+Chan



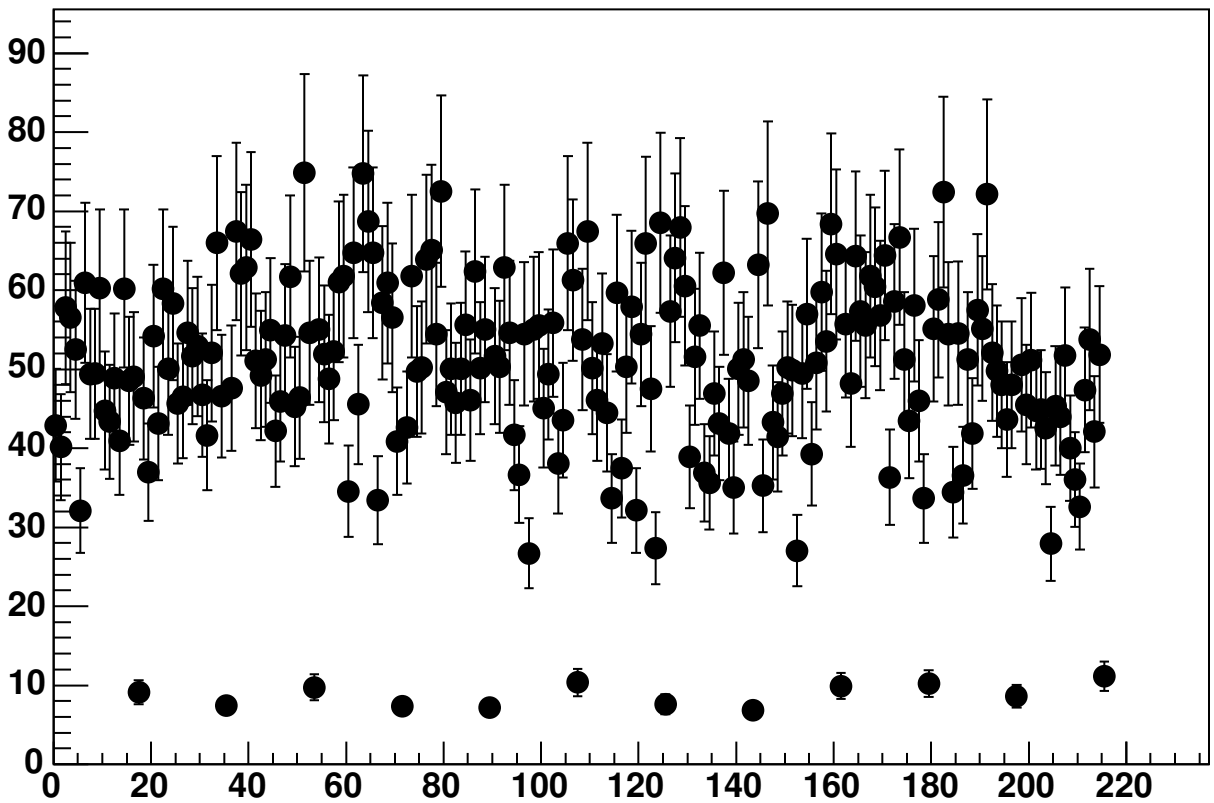
Enable 2, Hold=30, DAC=1350, ADC Noise vs 18*Chip+Chan



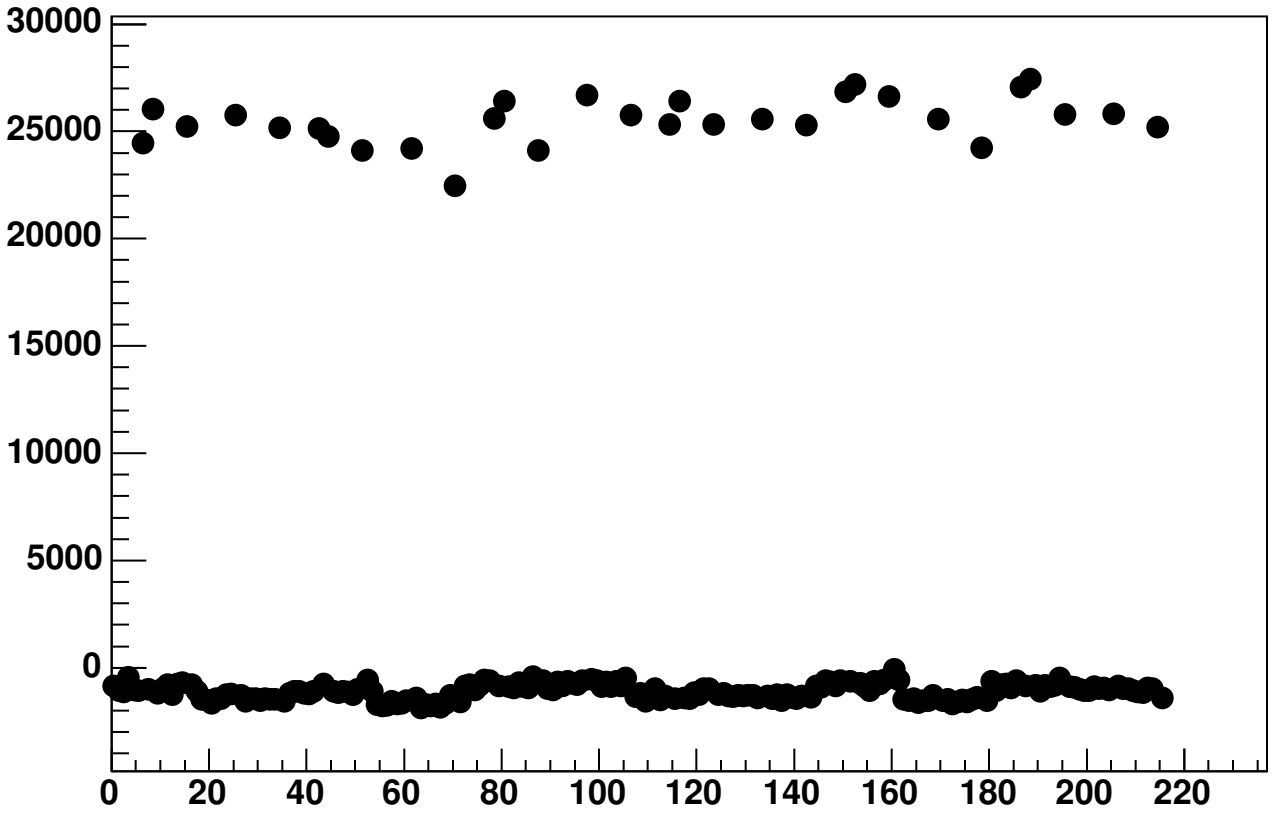
Enable 2, Hold=30, DAC=1400, ADC Mean vs 18*Chip+Chan



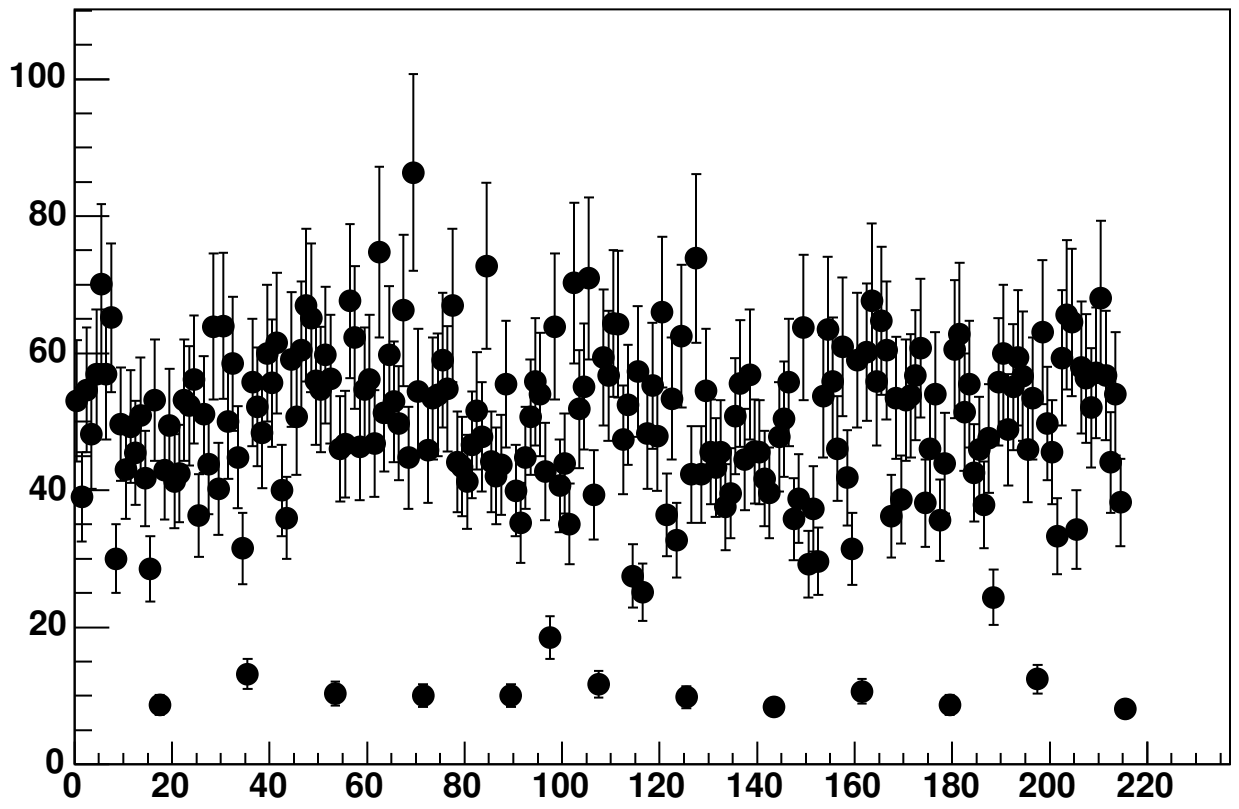
Enable 2, Hold=30, DAC=1400, ADC Noise vs 18*Chip+Chan



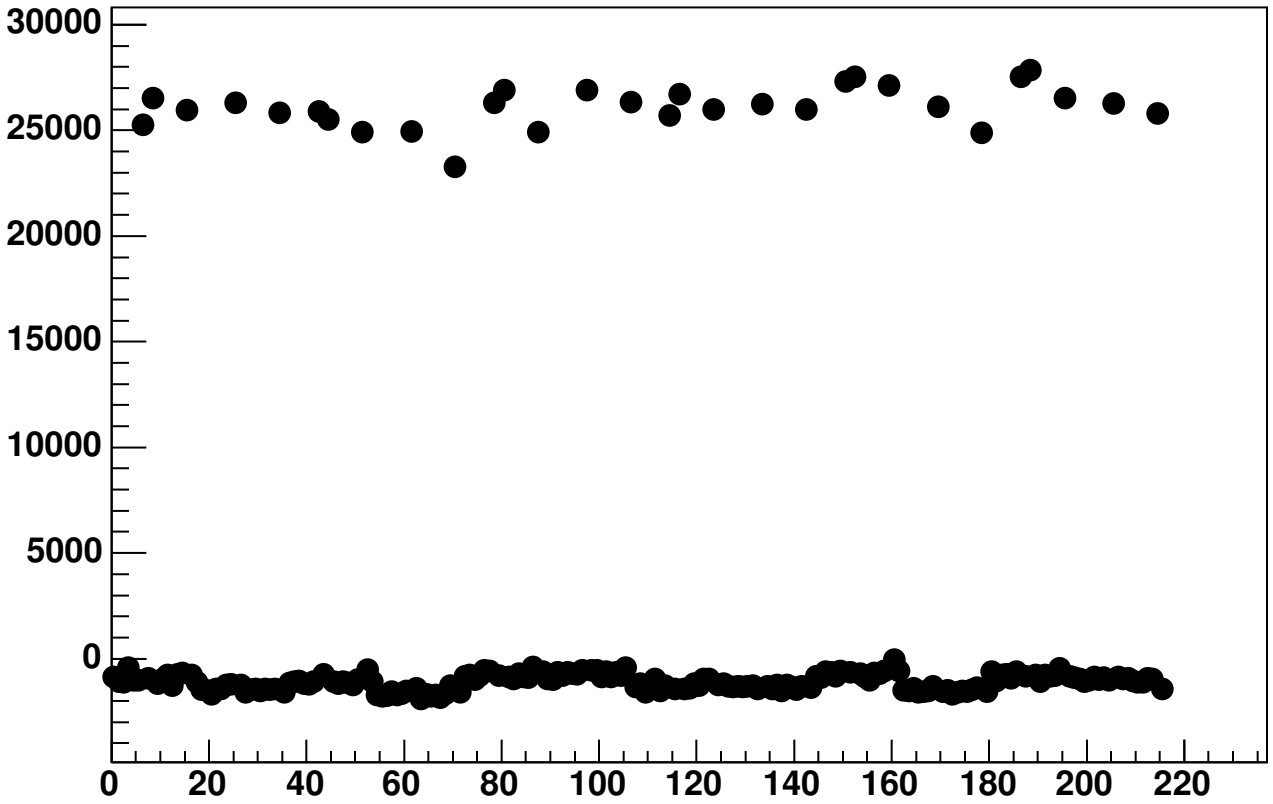
Enable 2, Hold=30, DAC=1450, ADC Mean vs 18*Chip+Chan



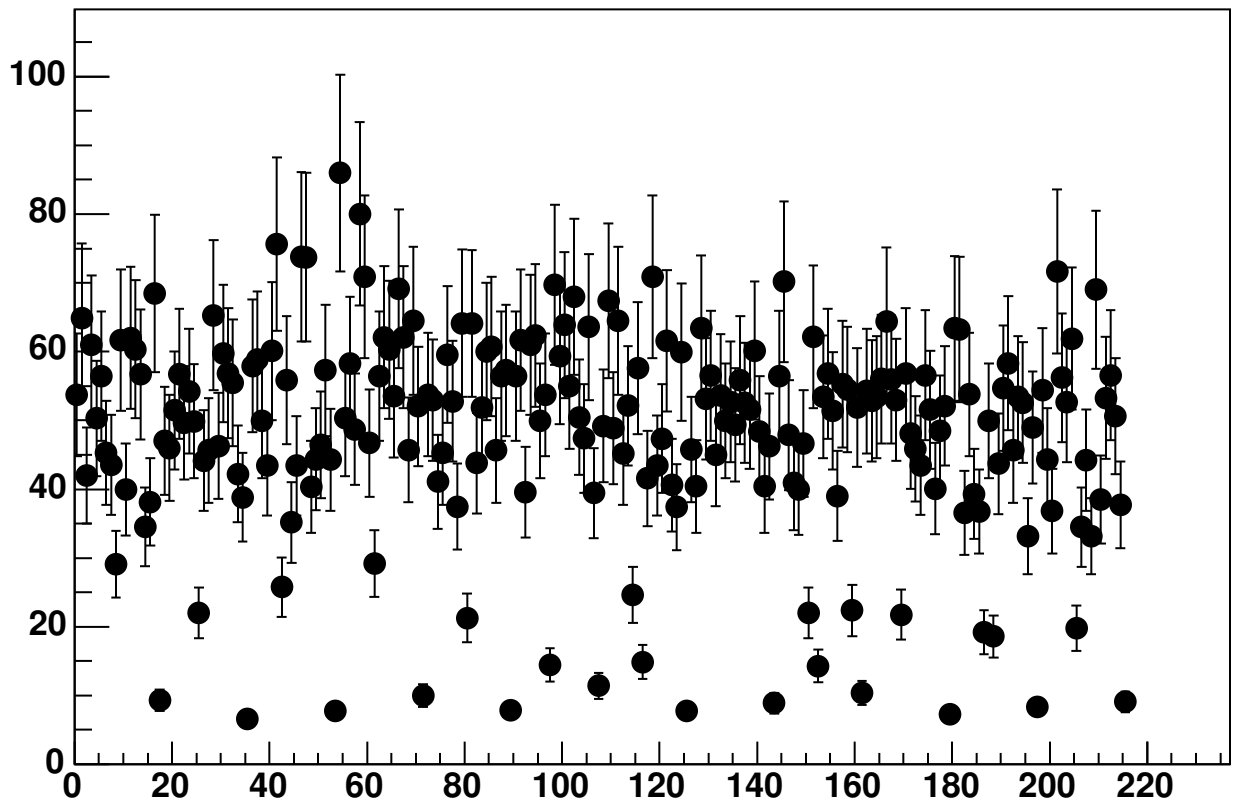
Enable 2, Hold=30, DAC=1450, ADC Noise vs 18*Chip+Chan



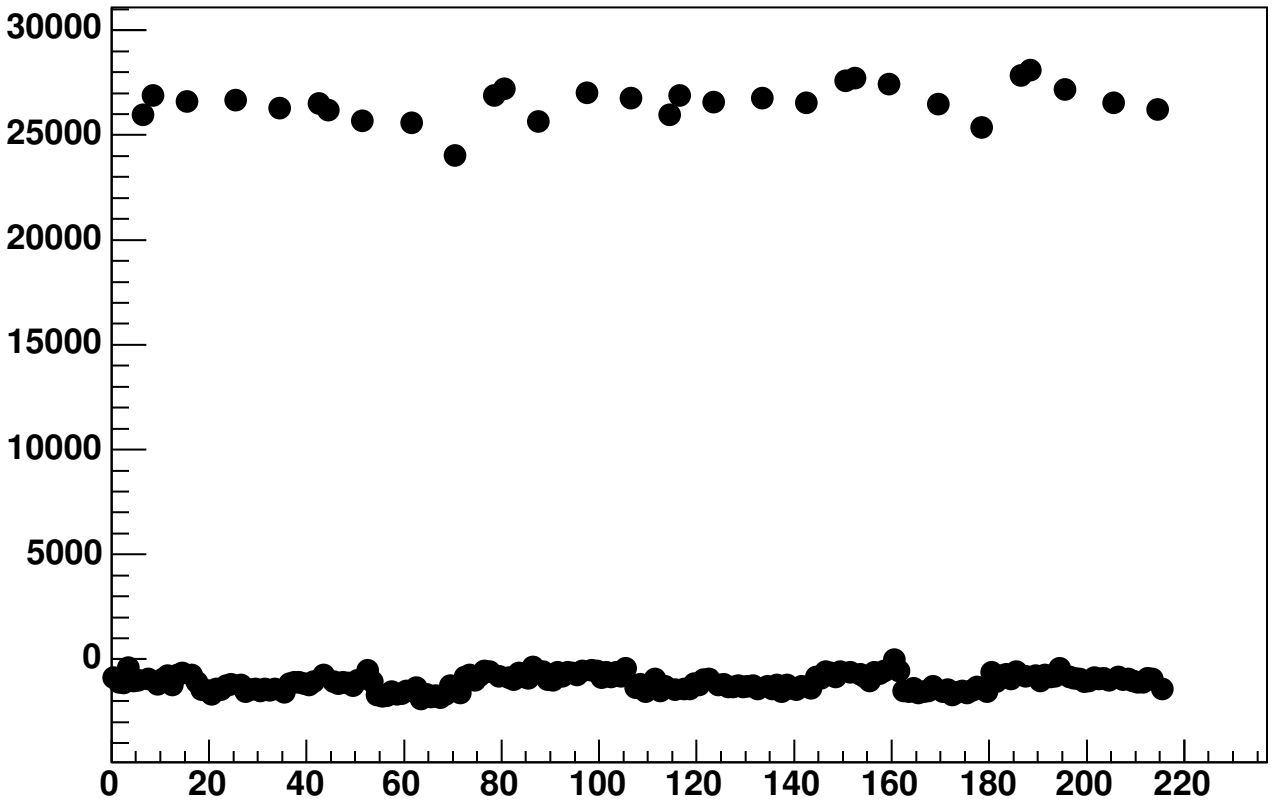
Enable 2, Hold=30, DAC=1500, ADC Mean vs 18*Chip+Chan



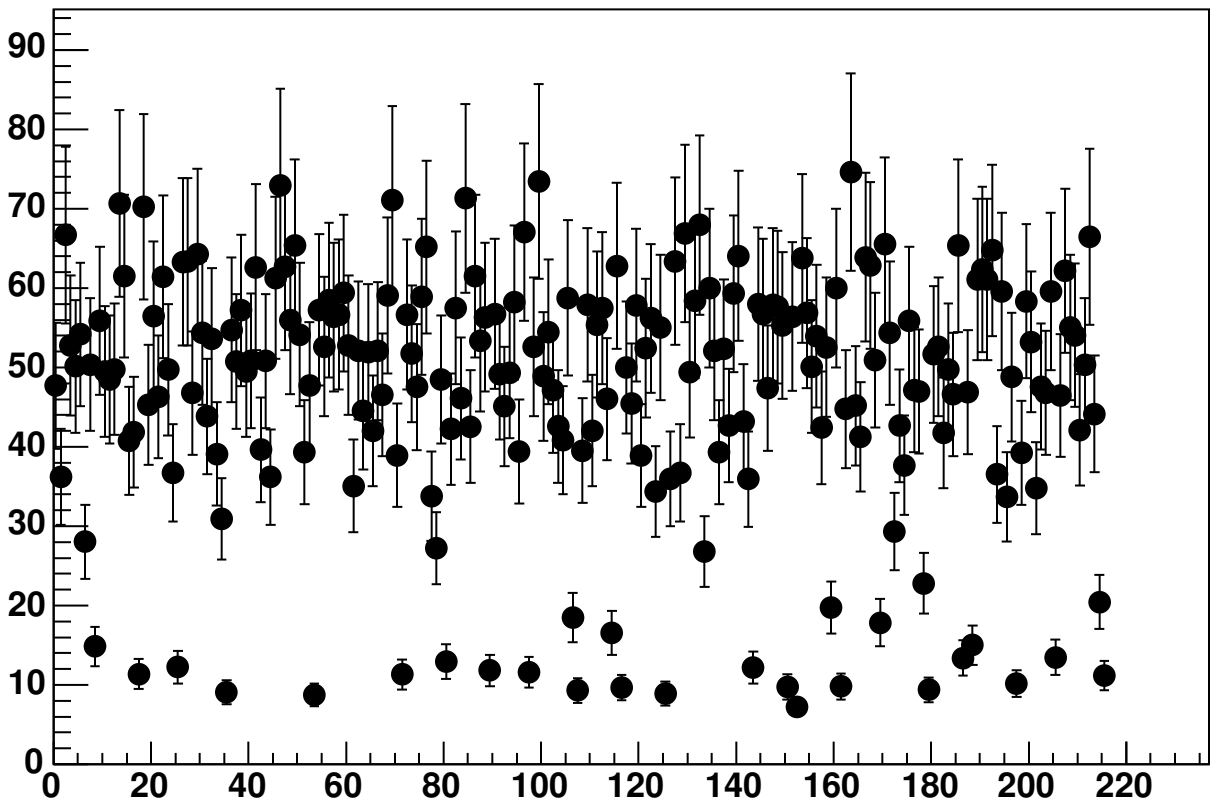
Enable 2, Hold=30, DAC=1500, ADC Noise vs 18*Chip+Chan



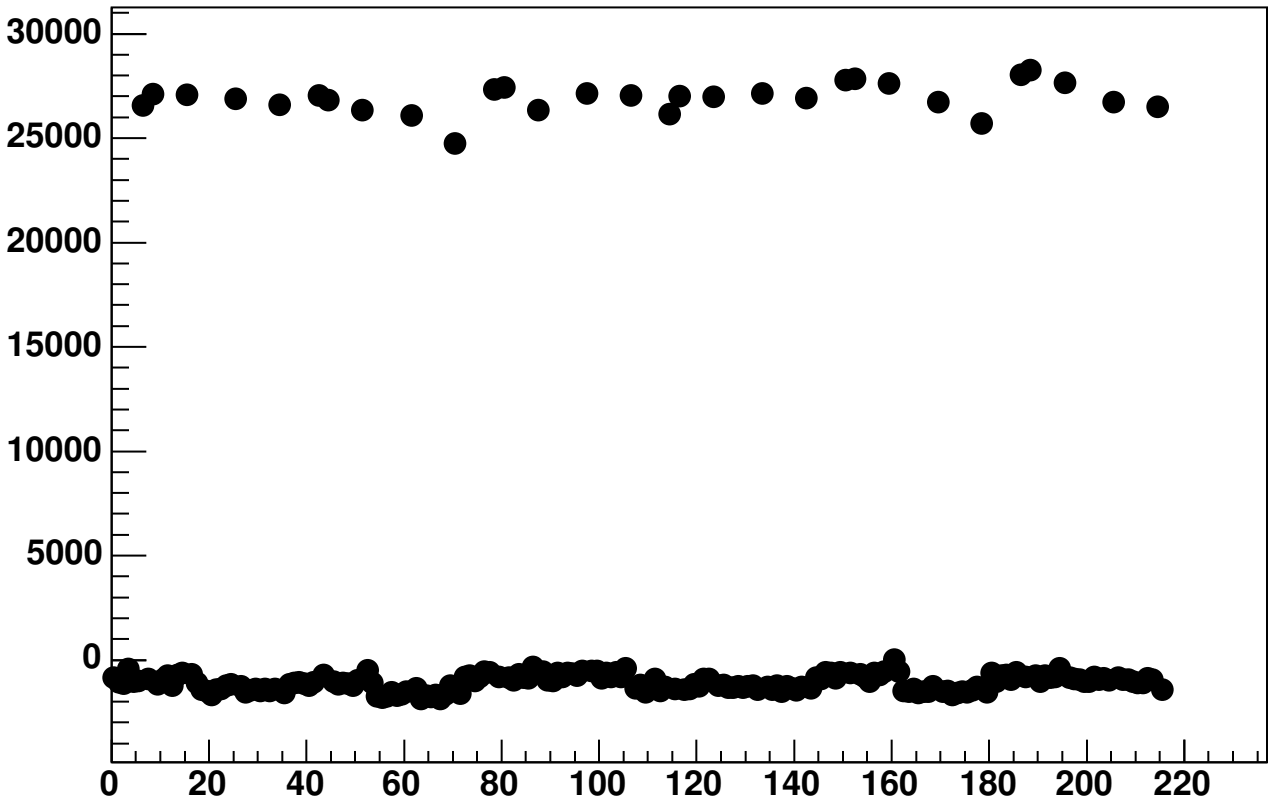
Enable 2, Hold=30, DAC=1550, ADC Mean vs 18*Chip+Chan



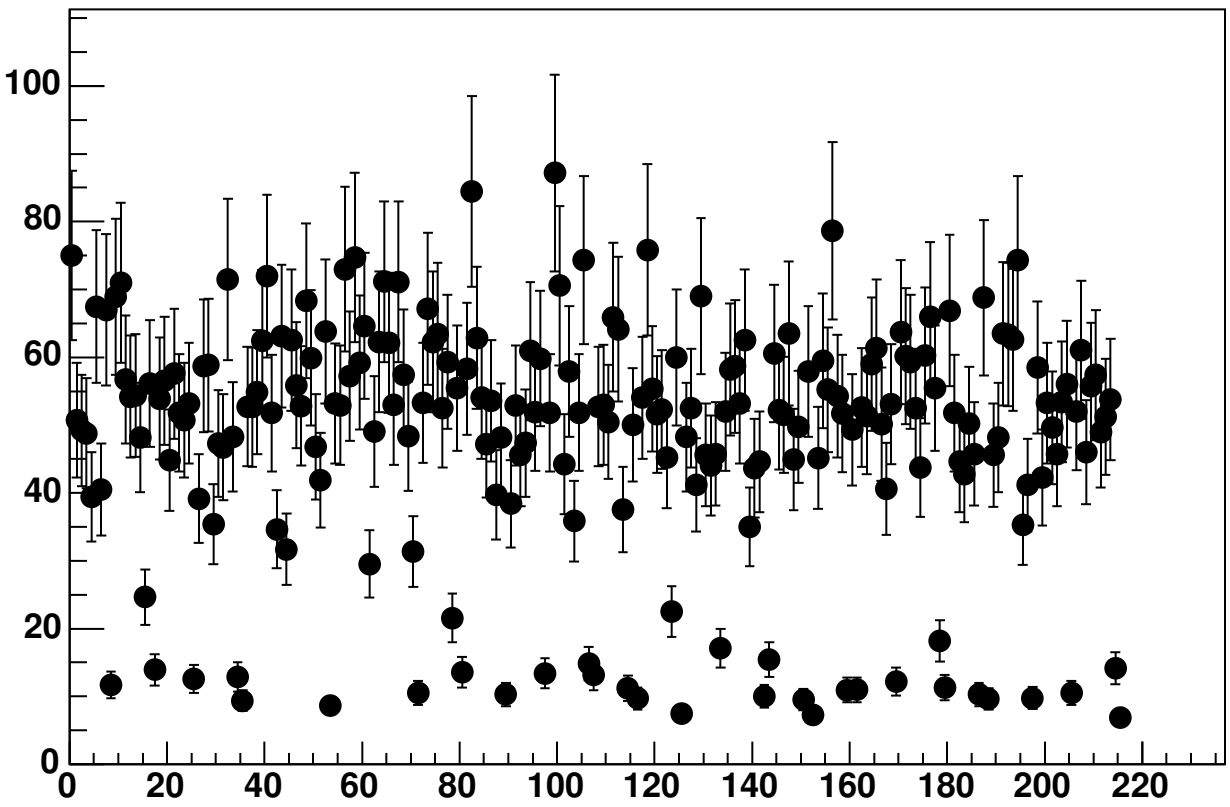
Enable 2, Hold=30, DAC=1550, ADC Noise vs 18*Chip+Chan



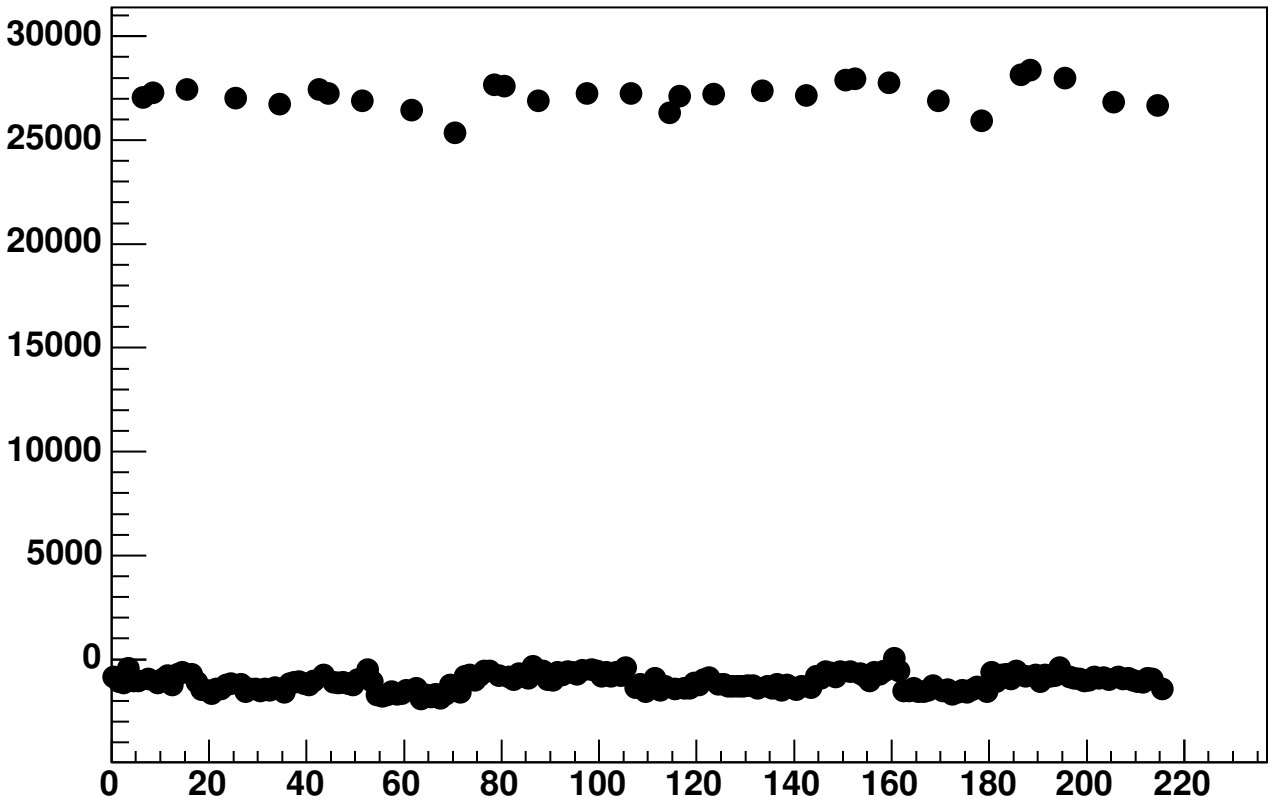
Enable 2, Hold=30, DAC=1600, ADC Mean vs 18*Chip+Chan



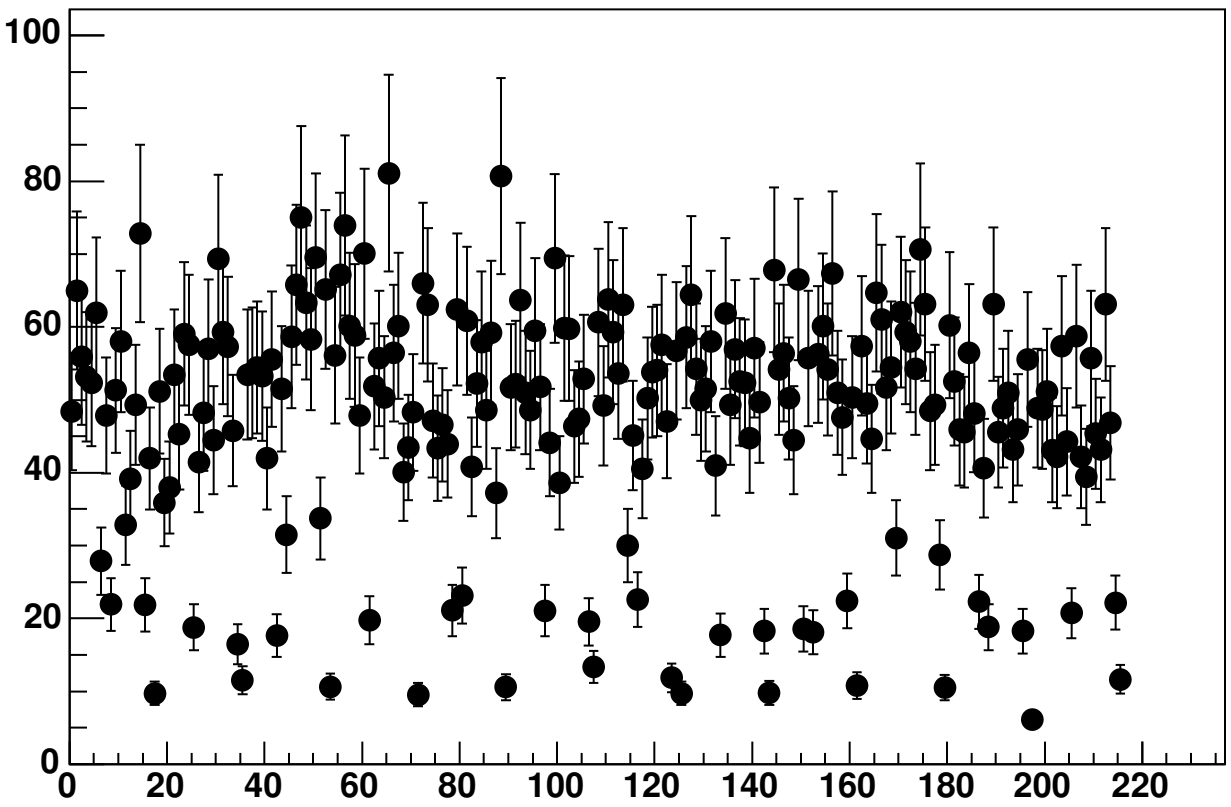
Enable 2, Hold=30, DAC=1600, ADC Noise vs 18*Chip+Chan



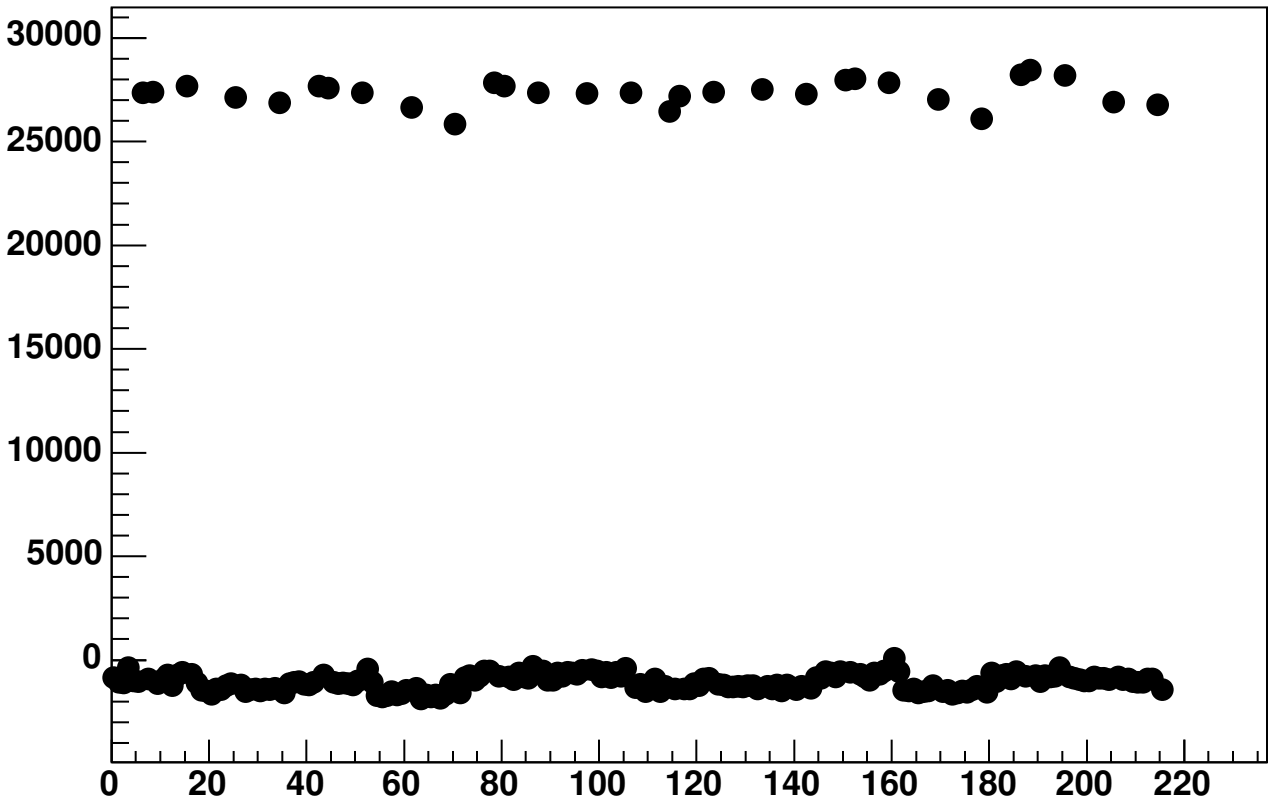
Enable 2, Hold=30, DAC=1650, ADC Mean vs 18*Chip+Chan



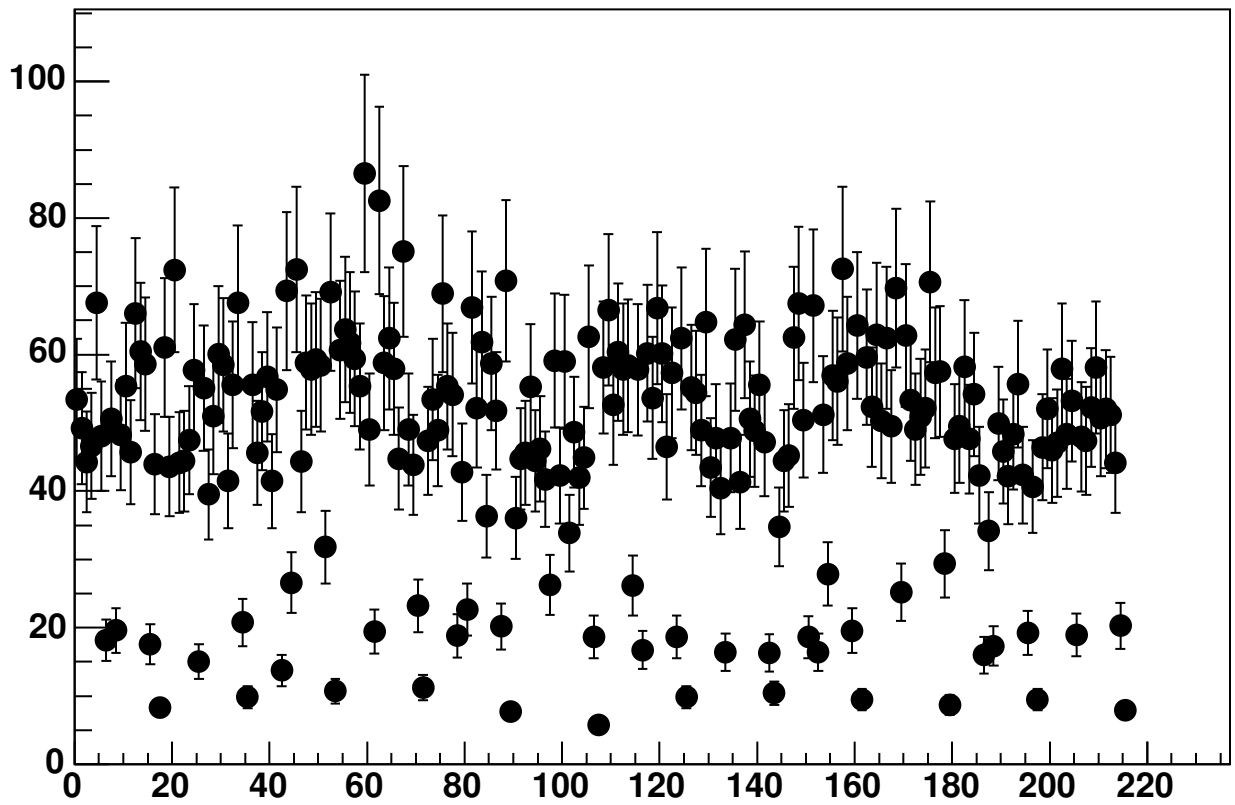
Enable 2, Hold=30, DAC=1650, ADC Noise vs 18*Chip+Chan



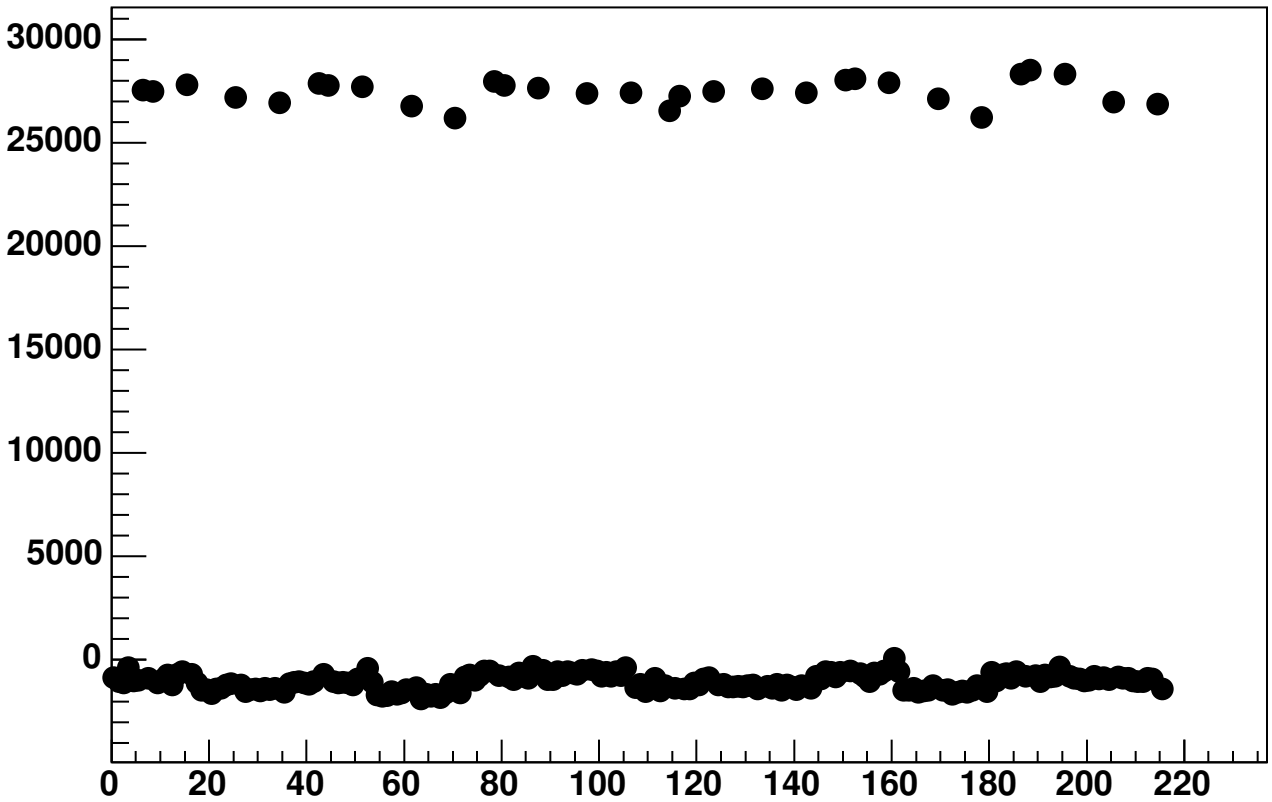
Enable 2, Hold=30, DAC=1700, ADC Mean vs 18*Chip+Chan



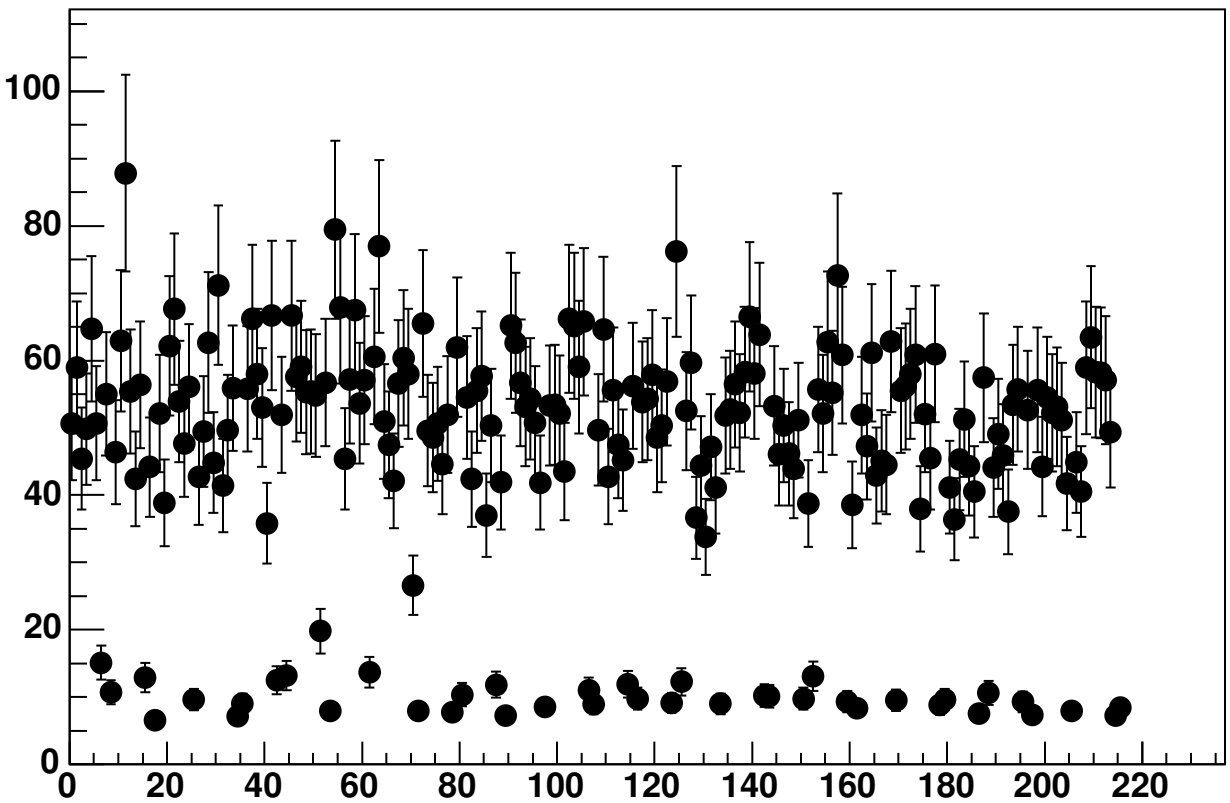
Enable 2, Hold=30, DAC=1700, ADC Noise vs 18*Chip+Chan



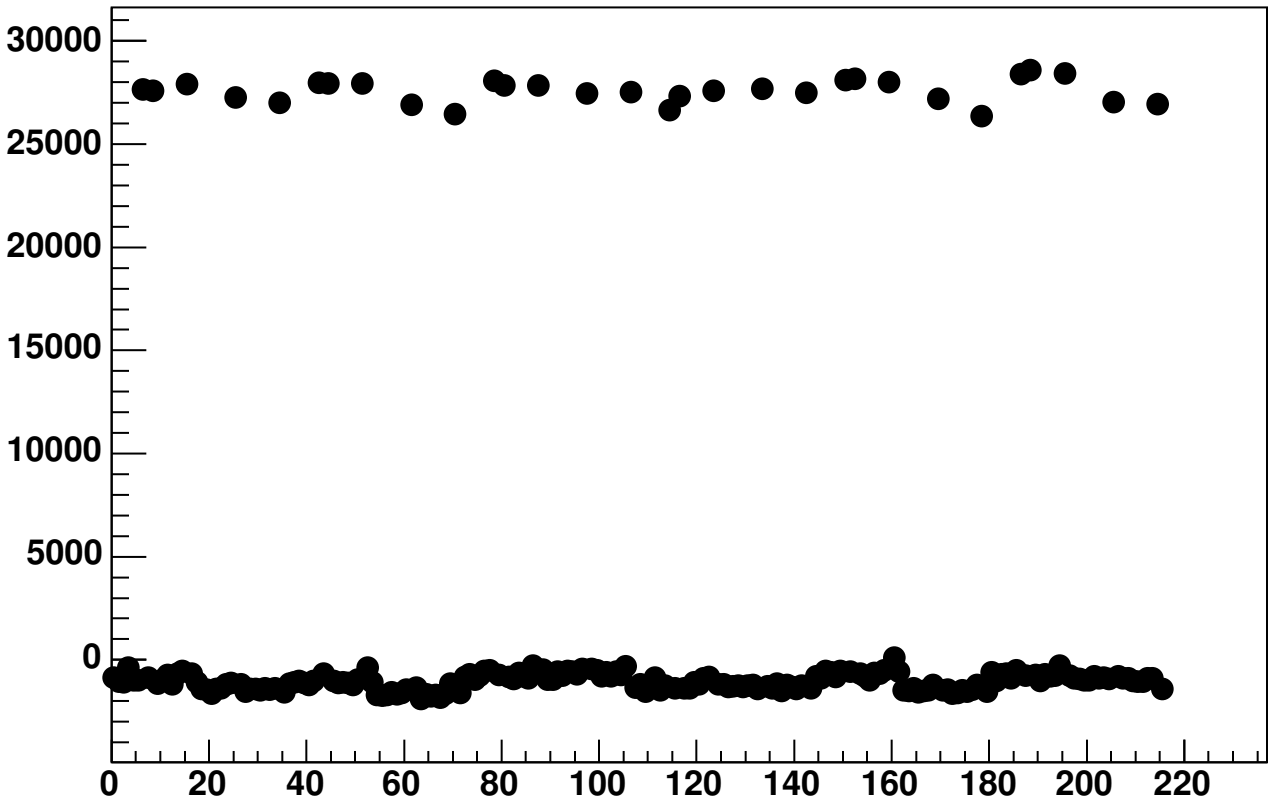
Enable 2, Hold=30, DAC=1750, ADC Mean vs 18*Chip+Chan



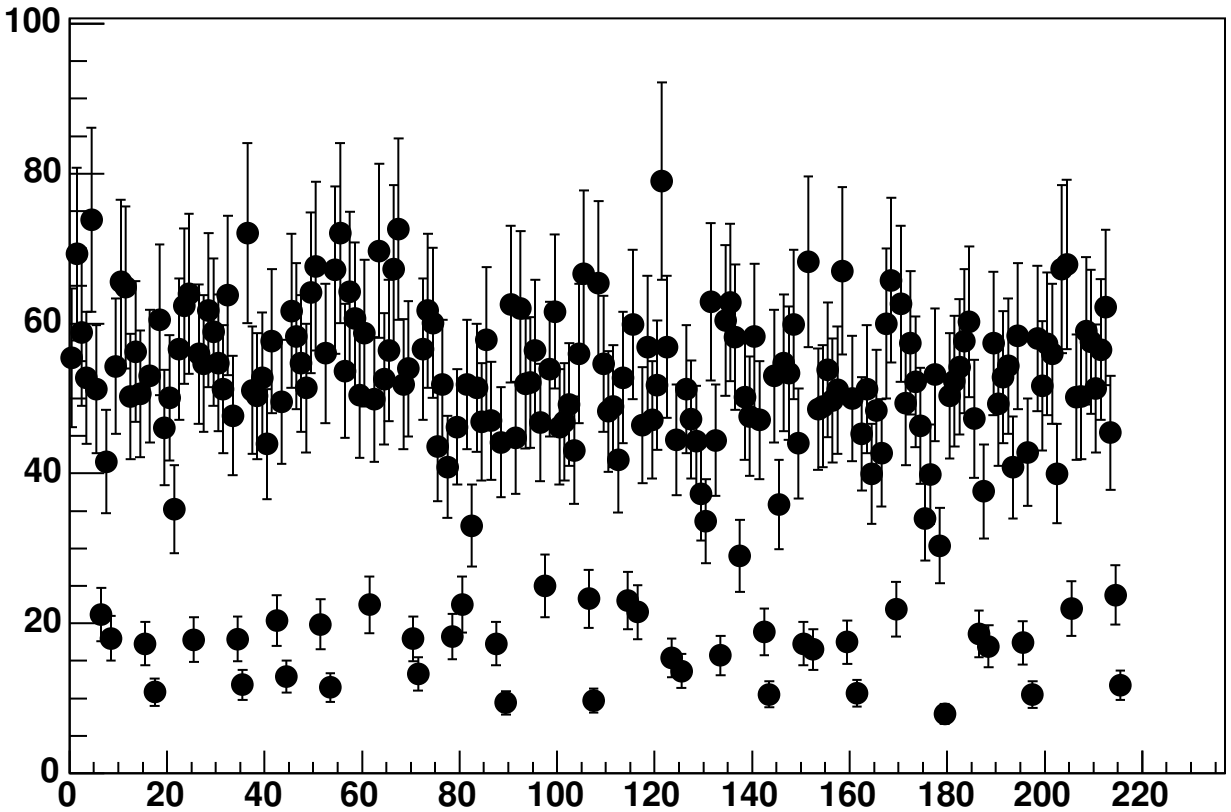
Enable 2, Hold=30, DAC=1750, ADC Noise vs 18*Chip+Chan



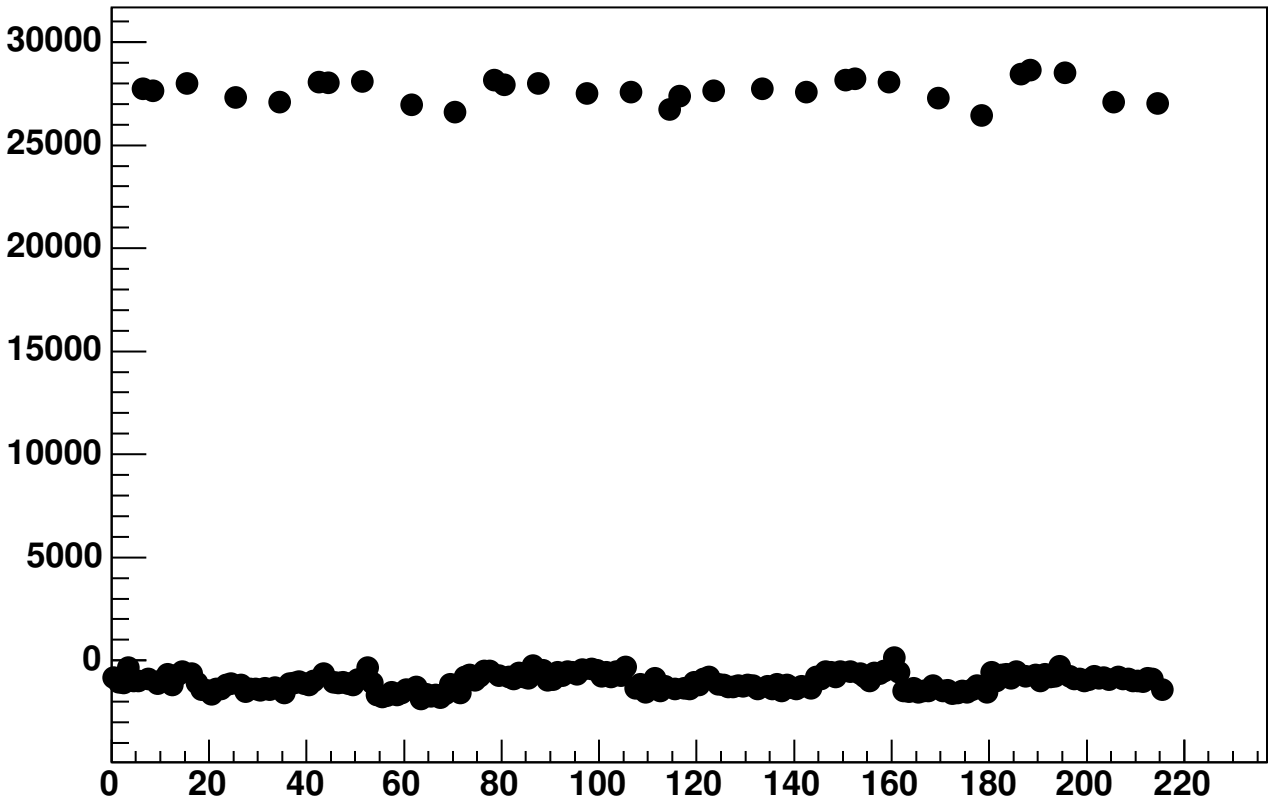
Enable 2, Hold=30, DAC=1800, ADC Mean vs 18*Chip+Chan



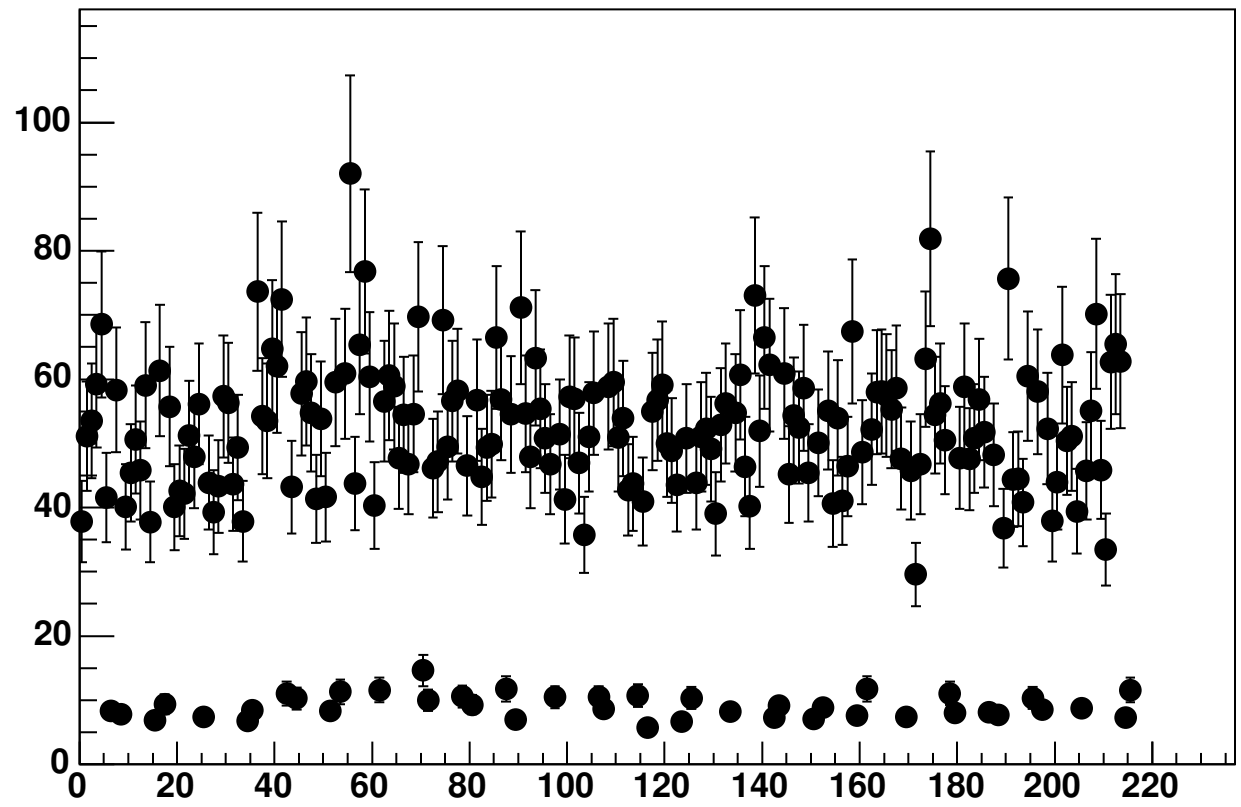
Enable 2, Hold=30, DAC=1800, ADC Noise vs 18*Chip+Chan



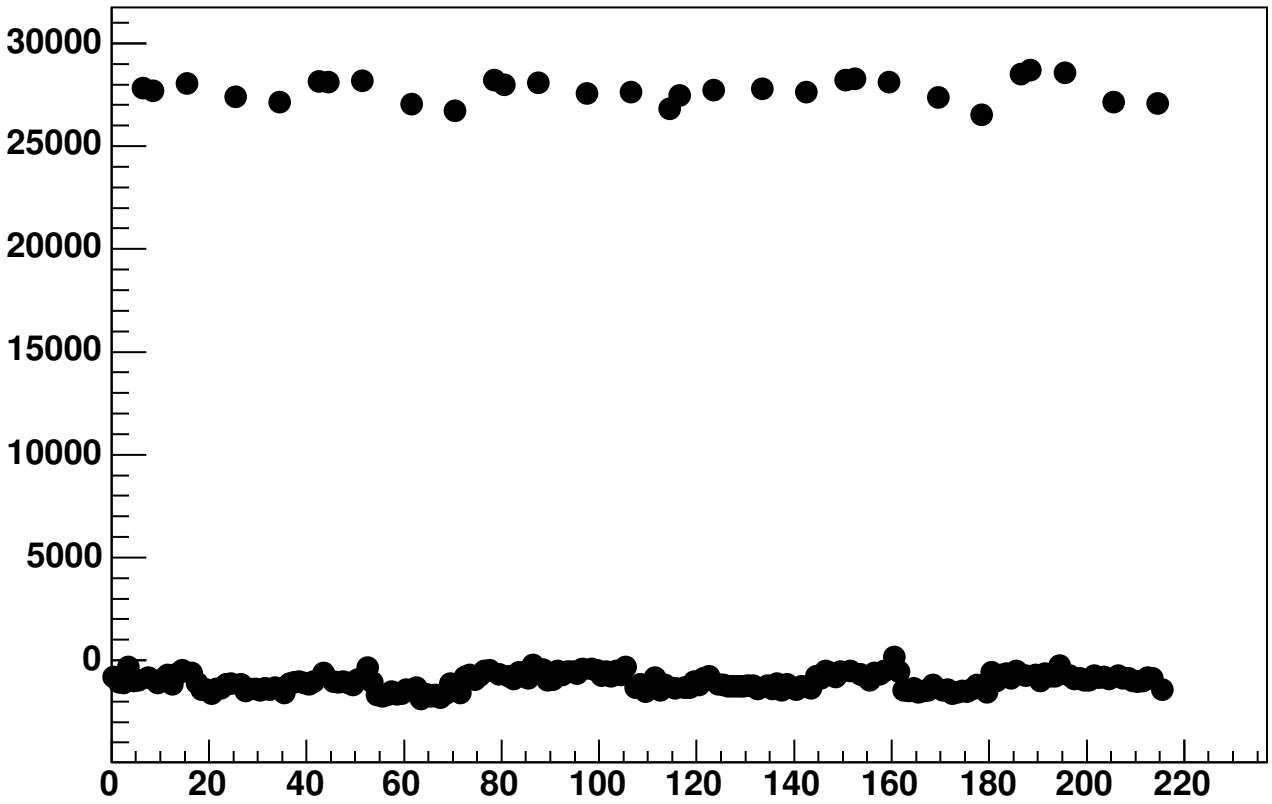
Enable 2, Hold=30, DAC=1850, ADC Mean vs 18*Chip+Chan



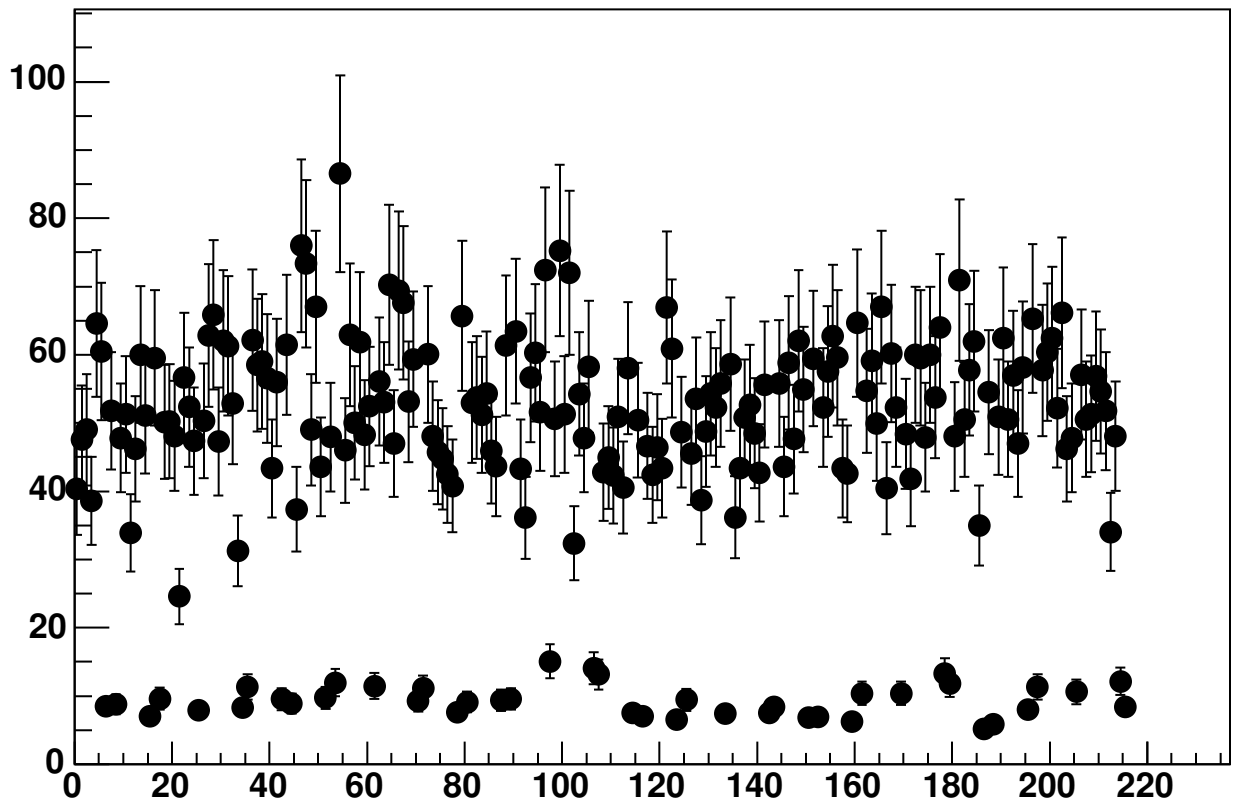
Enable 2, Hold=30, DAC=1850, ADC Noise vs 18*Chip+Chan



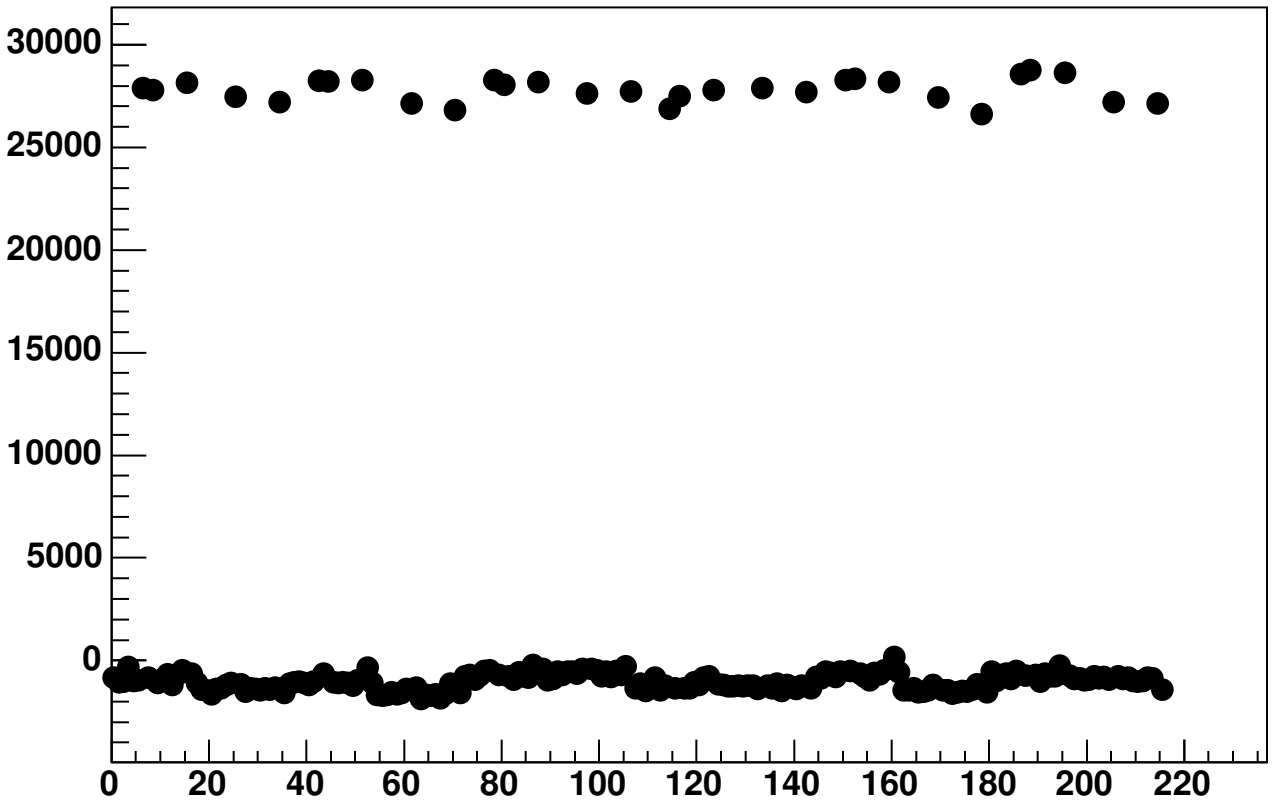
Enable 2, Hold=30, DAC=1900, ADC Mean vs 18*Chip+Chan



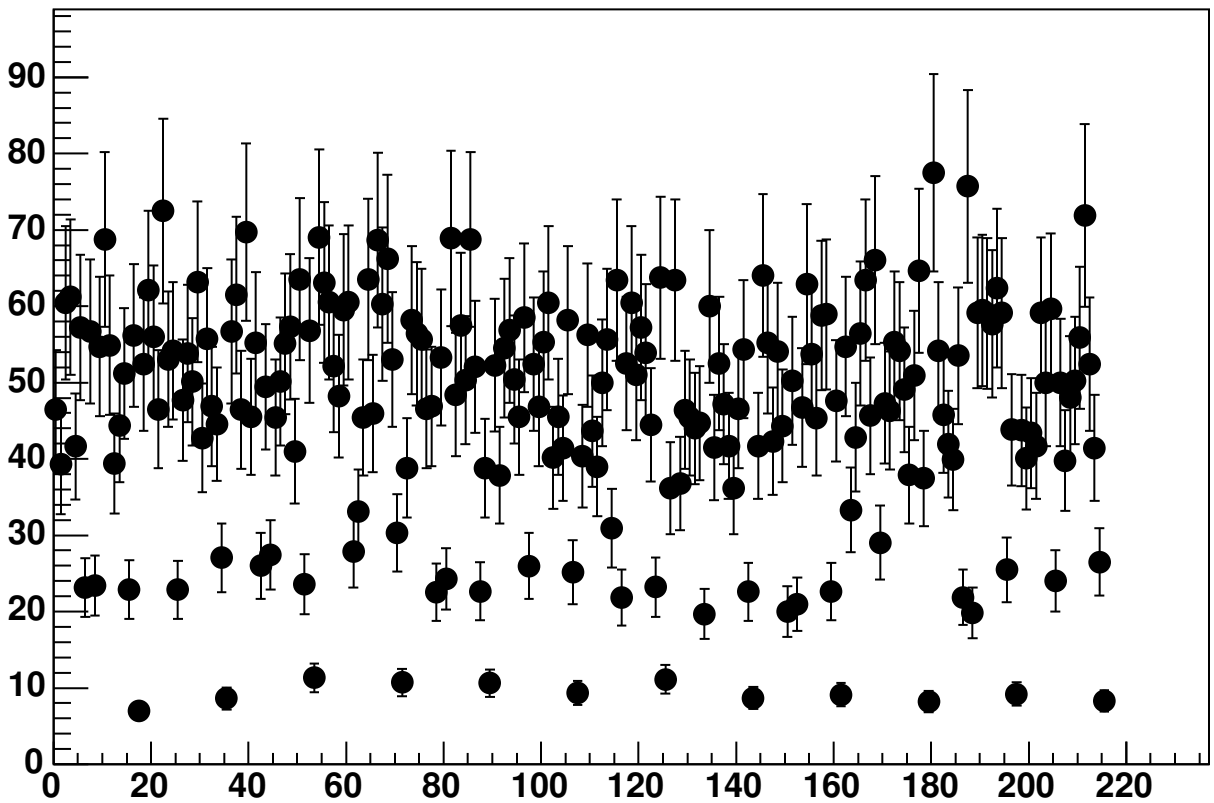
Enable 2, Hold=30, DAC=1900, ADC Noise vs 18*Chip+Chan



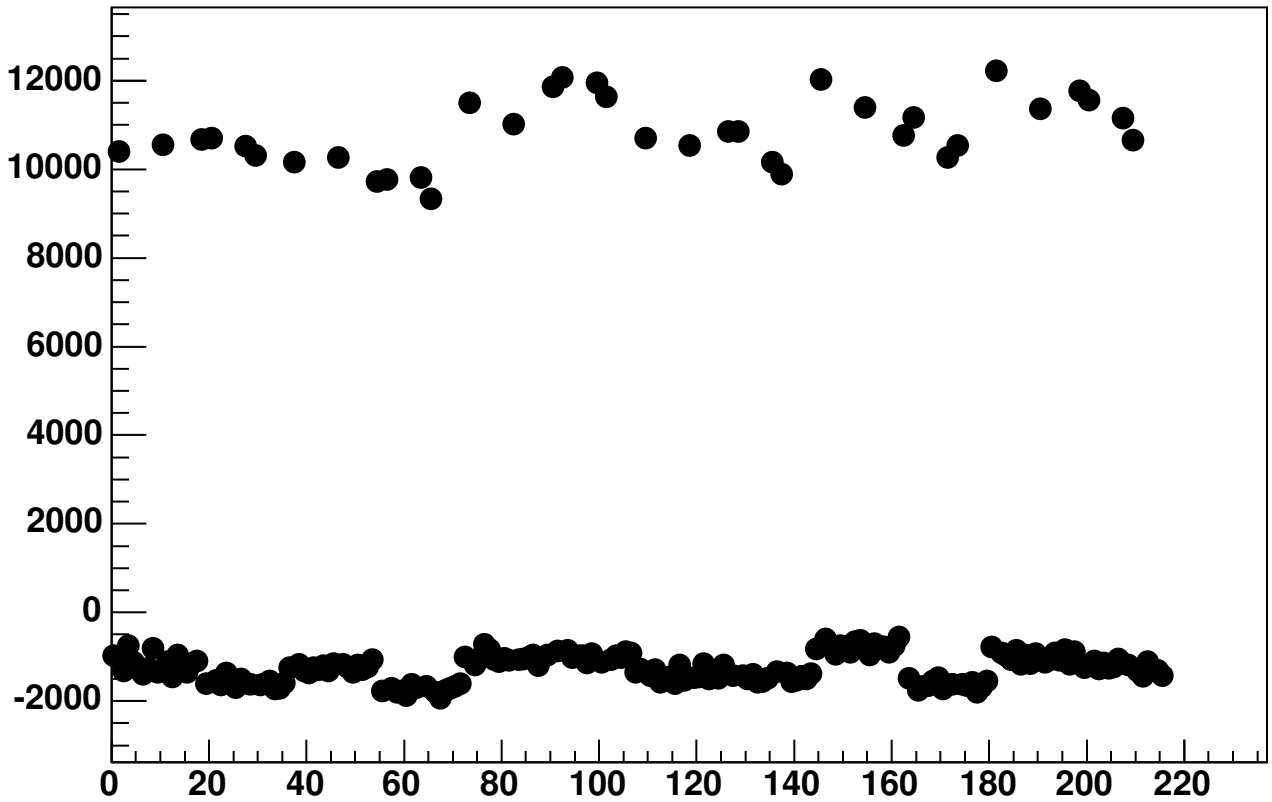
Enable 2, Hold=30, DAC=1950, ADC Mean vs 18*Chip+Chan



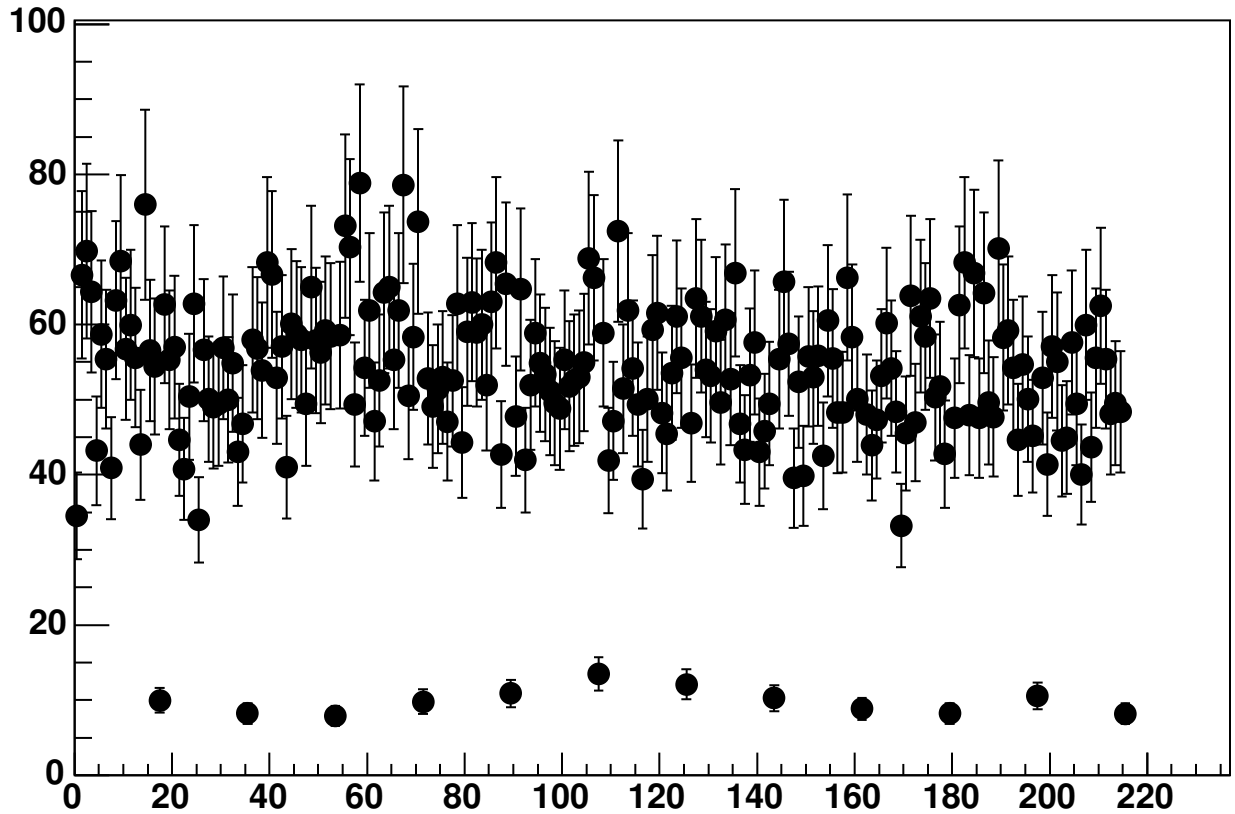
Enable 2, Hold=30, DAC=1950, ADC Noise vs 18*Chip+Chan



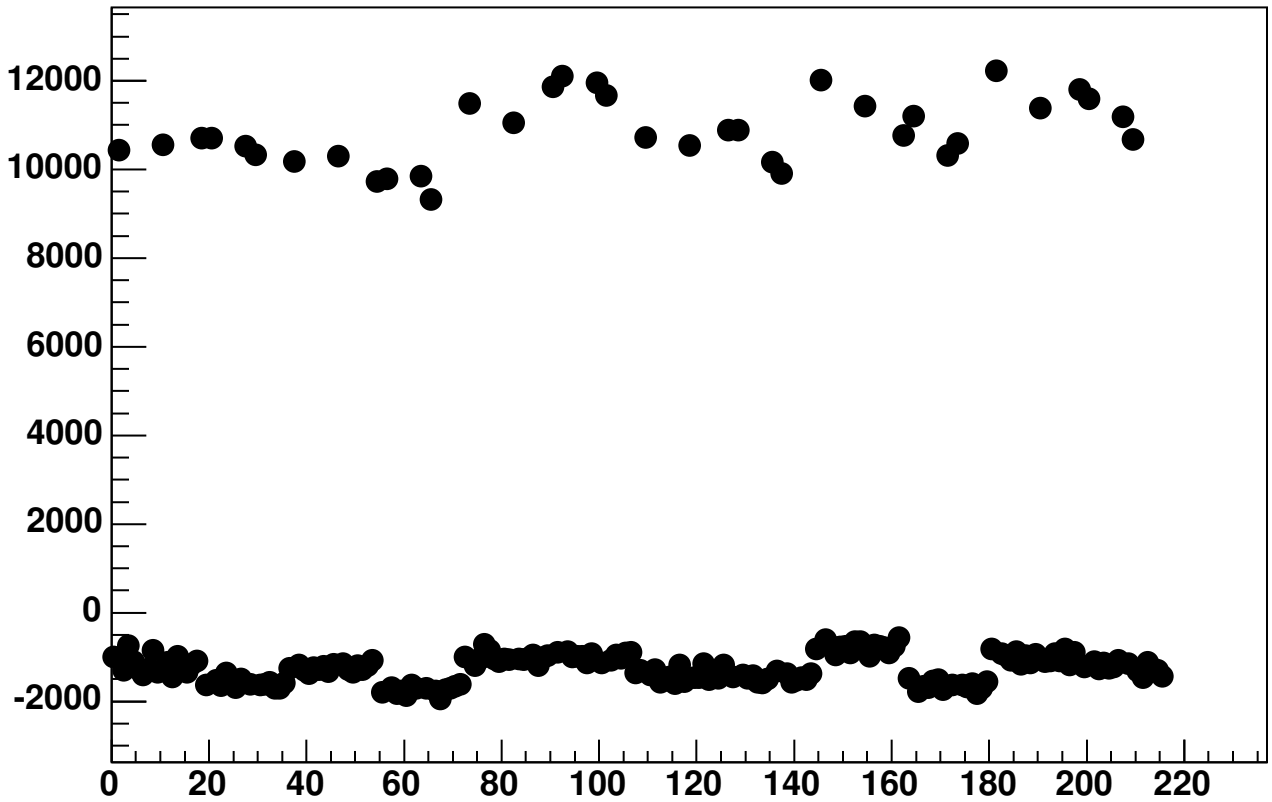
Enable 3, Hold=30, DAC=0, ADC Mean vs 18*Chip+Chan



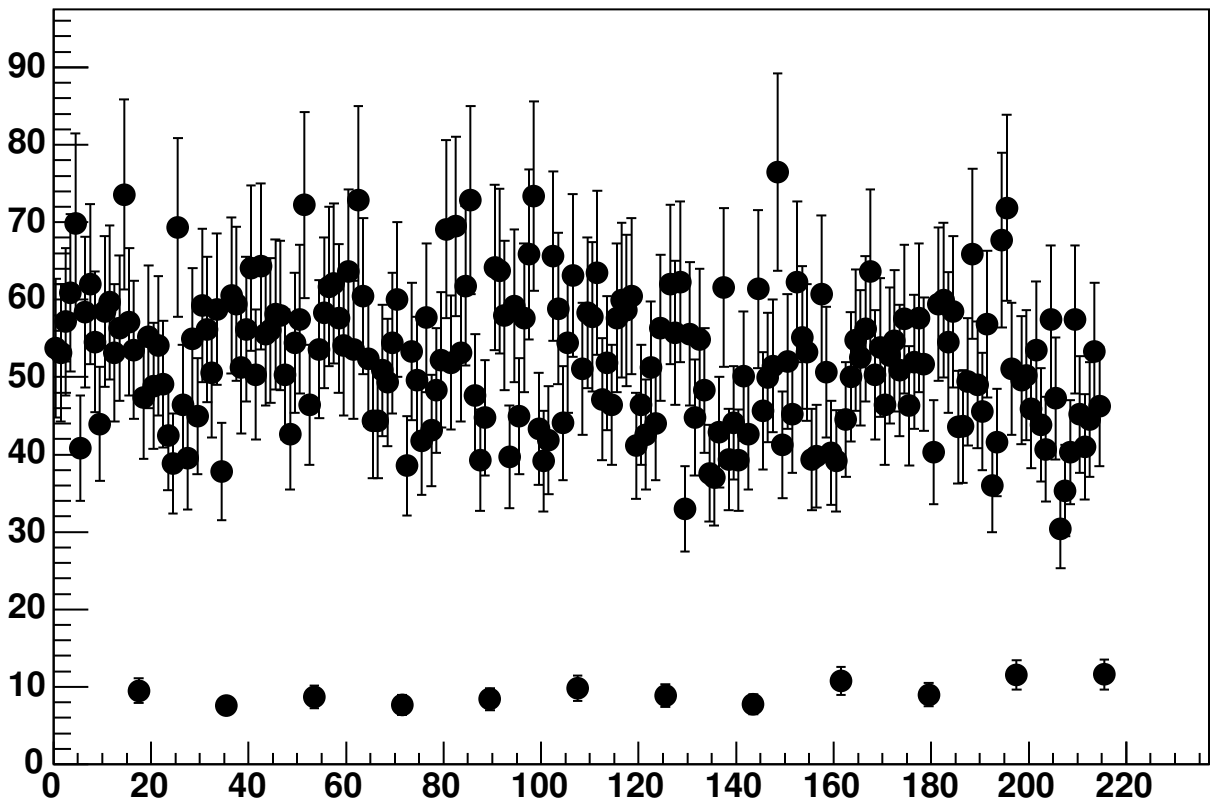
Enable 3, Hold=30, DAC=0, ADC Noise vs 18*Chip+Chan



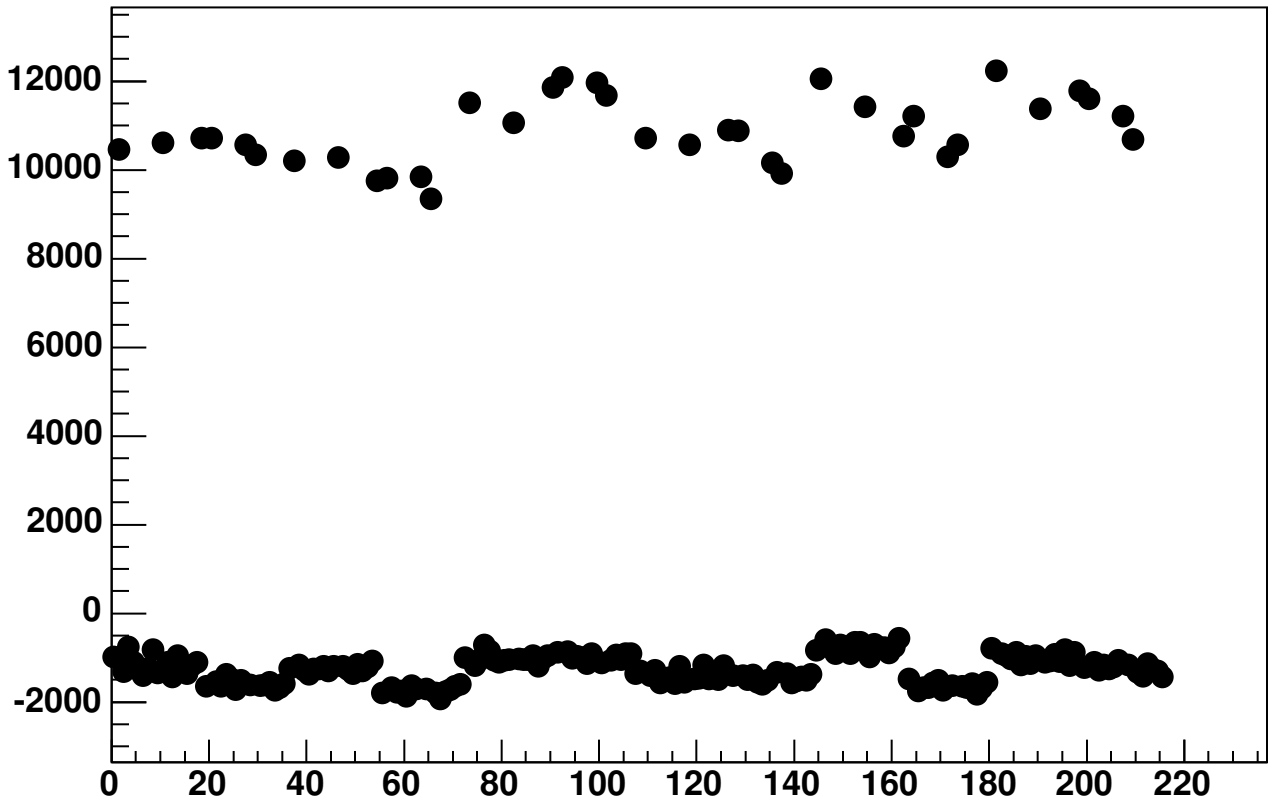
Enable 3, Hold=30, DAC=50, ADC Mean vs 18*Chip+Chan



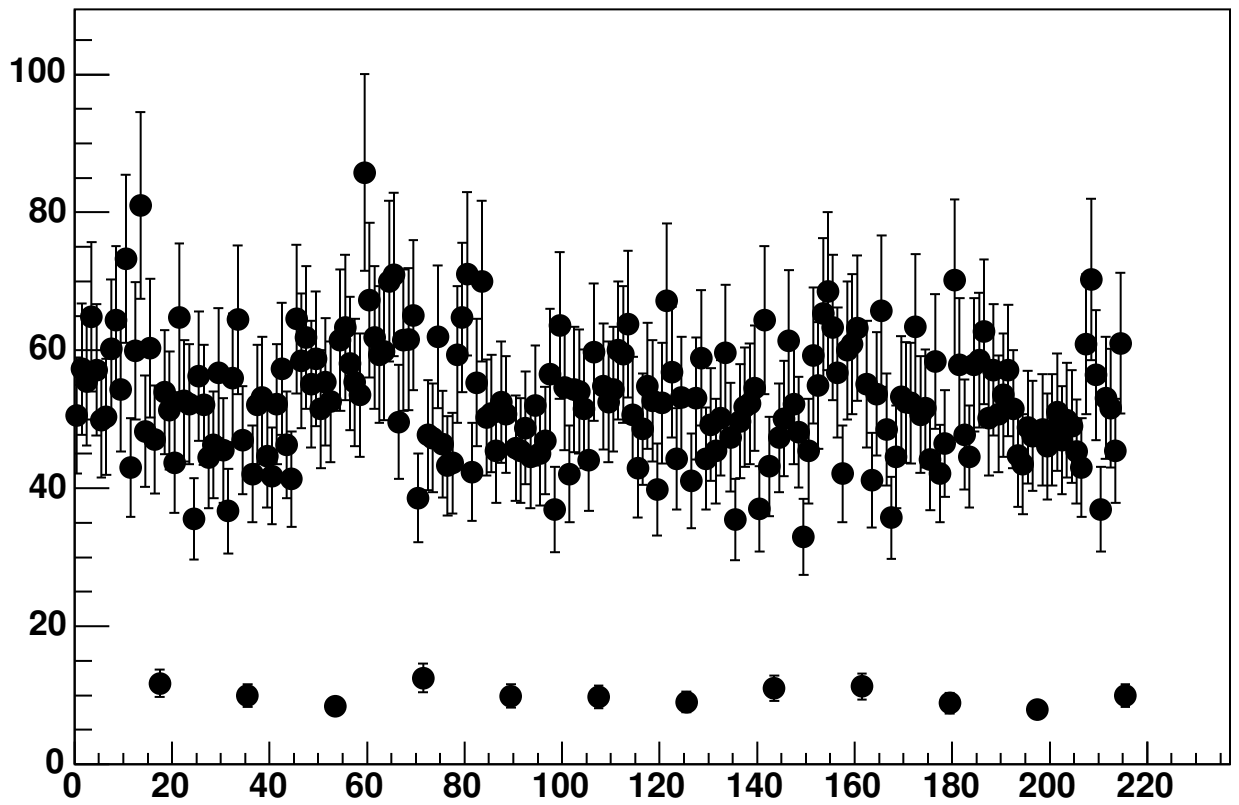
Enable 3, Hold=30, DAC=50, ADC Noise vs 18*Chip+Chan



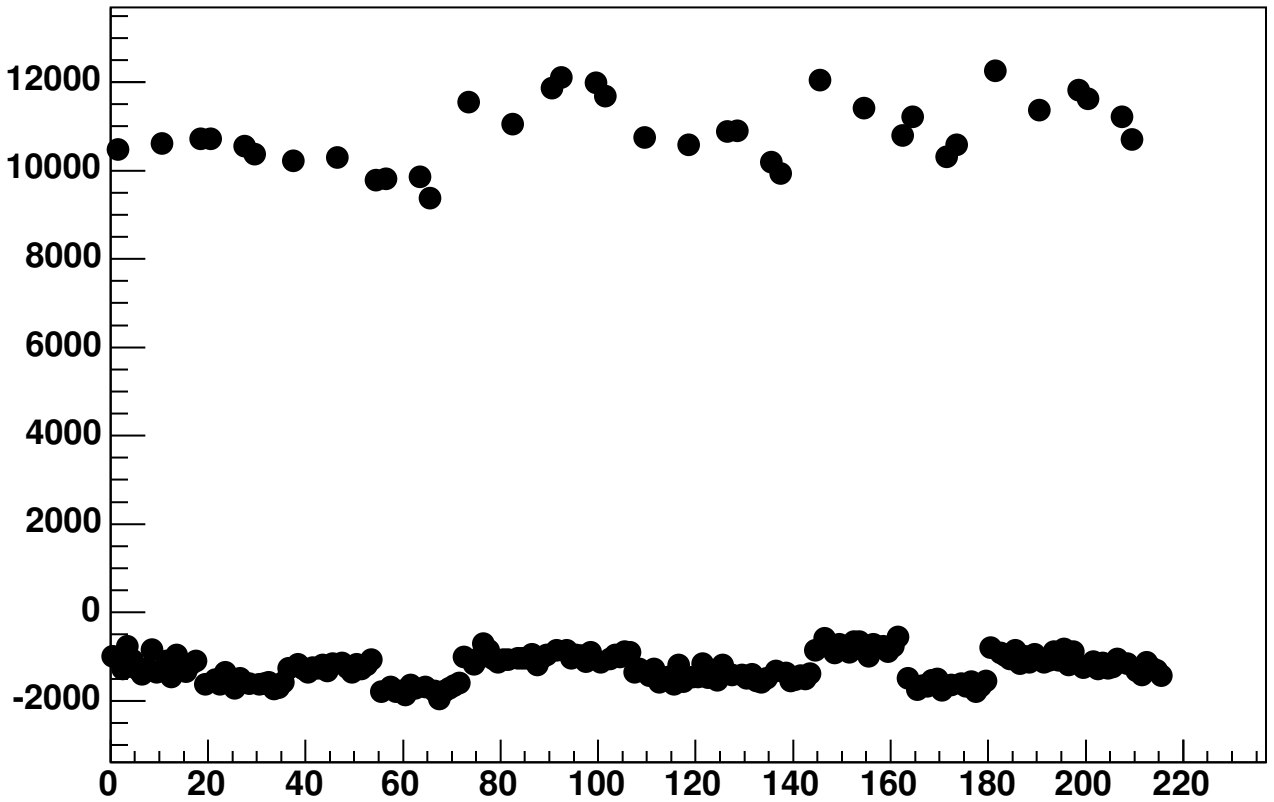
Enable 3, Hold=30, DAC=100, ADC Mean vs 18*Chip+Chan



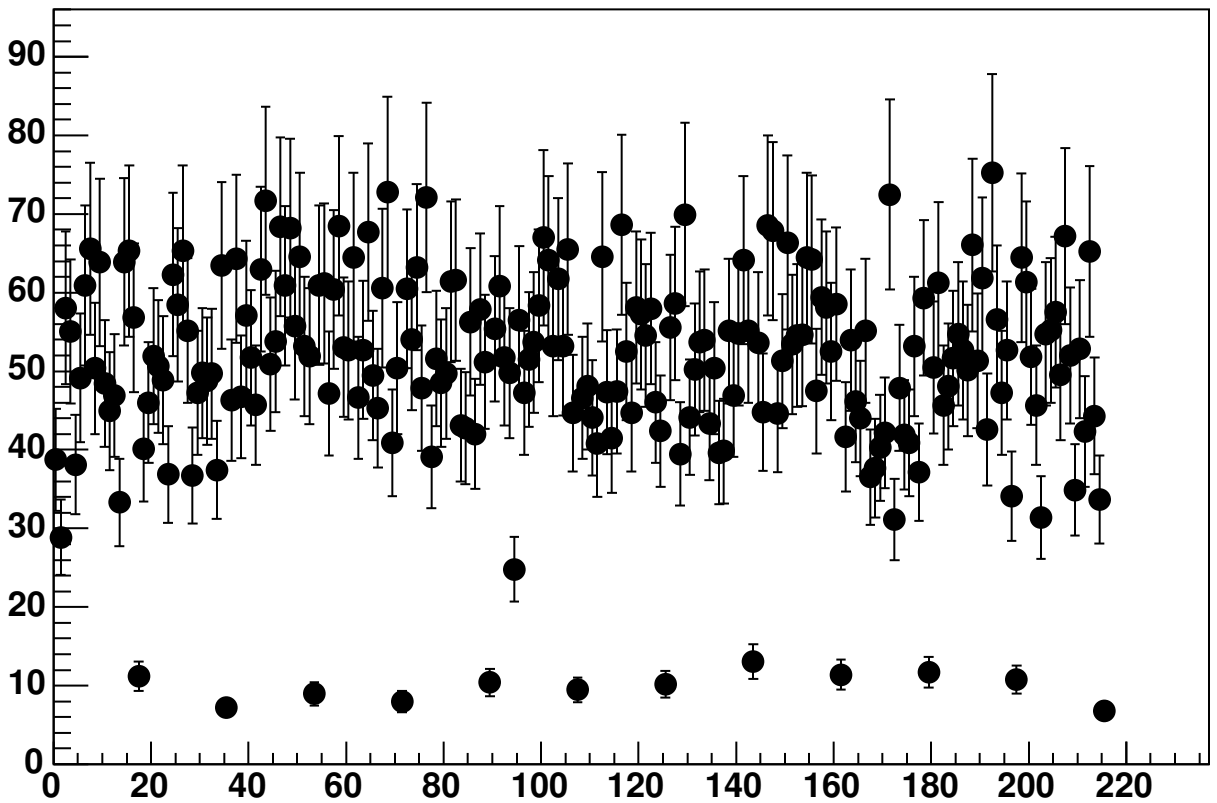
Enable 3, Hold=30, DAC=100, ADC Noise vs 18*Chip+Chan



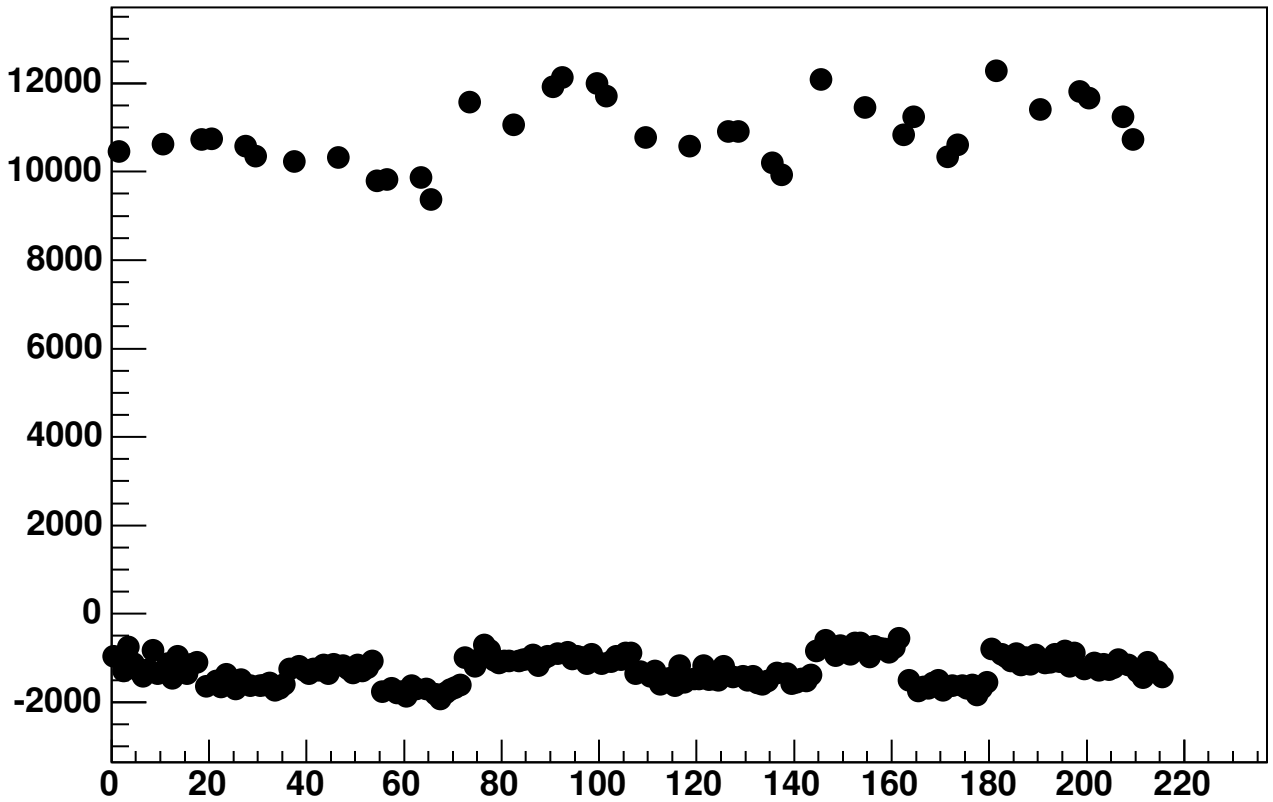
Enable 3, Hold=30, DAC=150, ADC Mean vs 18*Chip+Chan



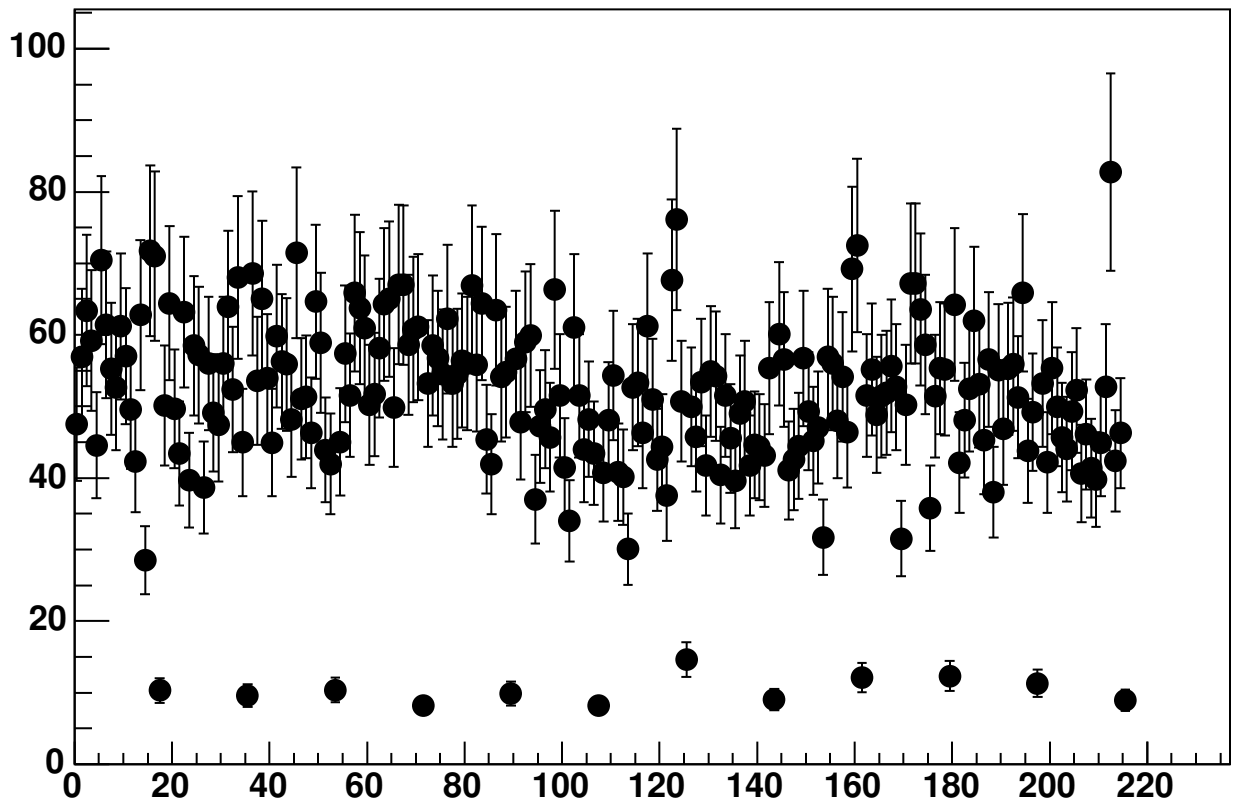
Enable 3, Hold=30, DAC=150, ADC Noise vs 18*Chip+Chan



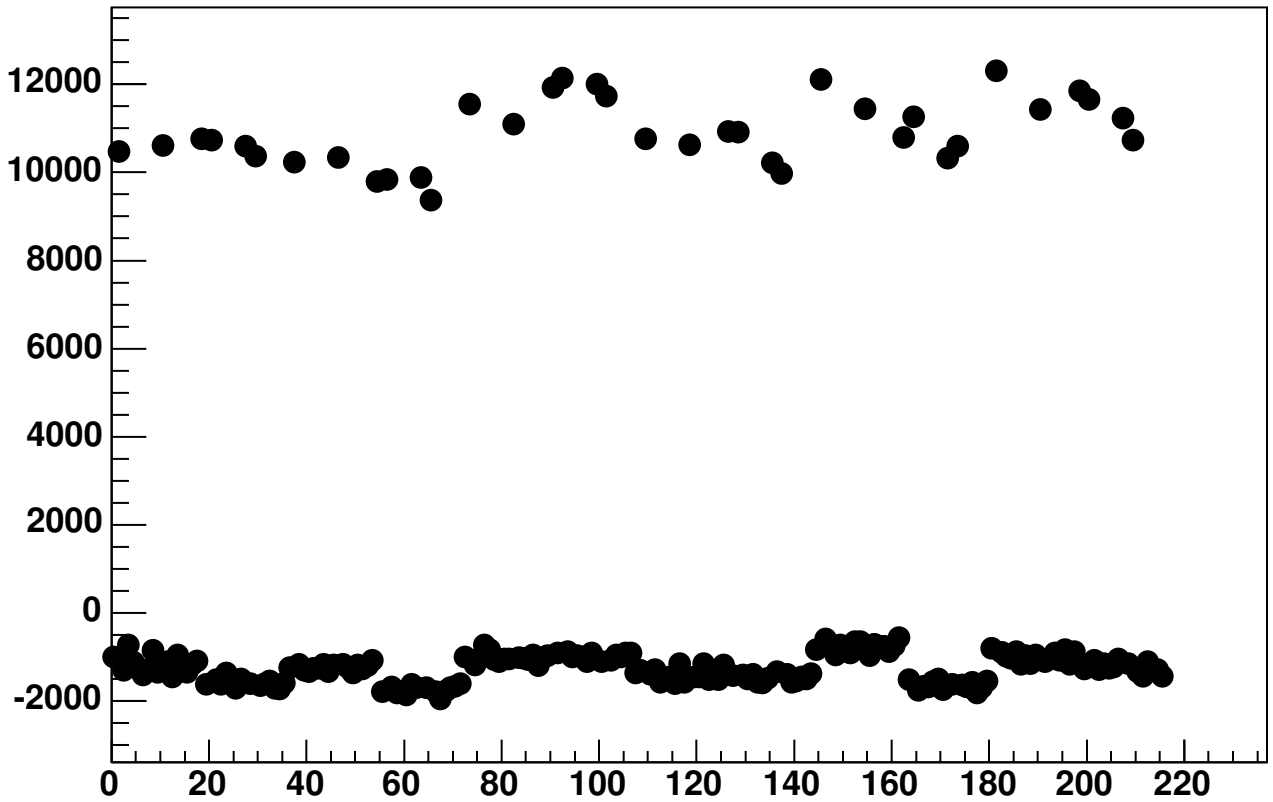
Enable 3, Hold=30, DAC=200, ADC Mean vs 18*Chip+Chan



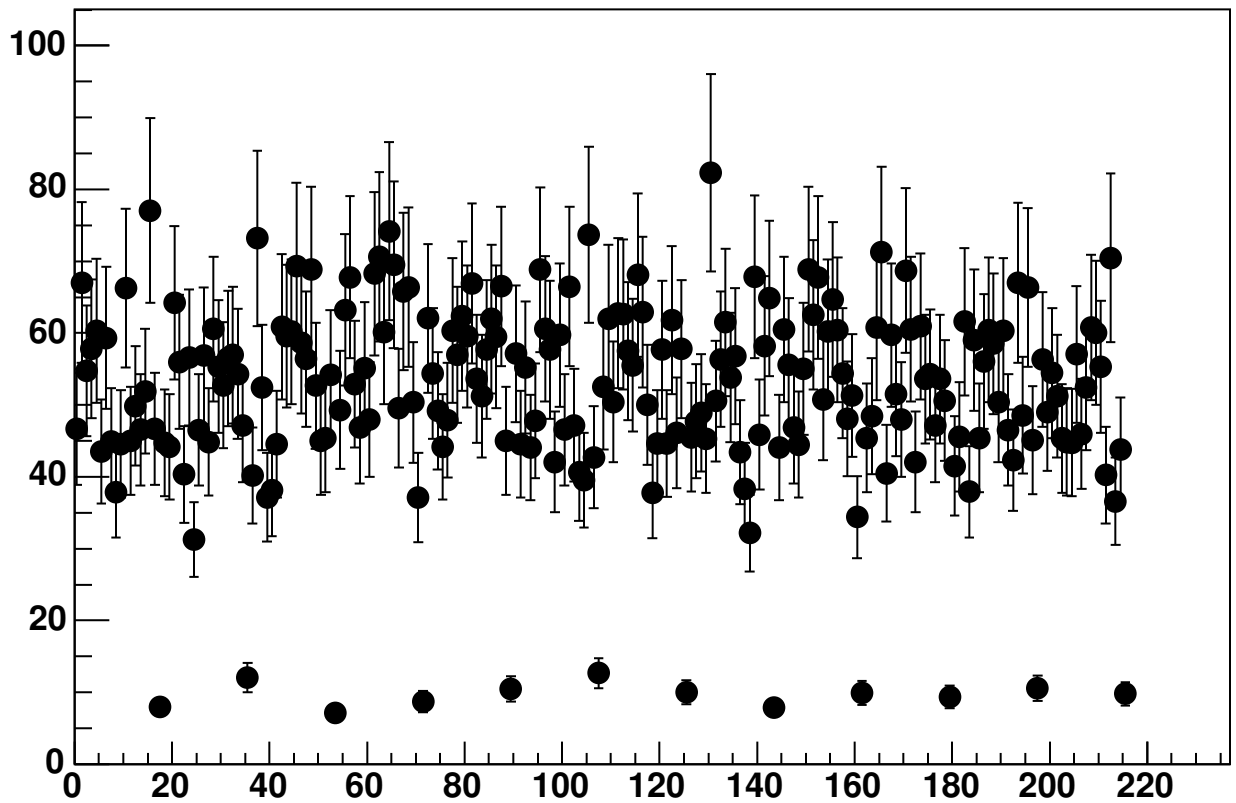
Enable 3, Hold=30, DAC=200, ADC Noise vs 18*Chip+Chan



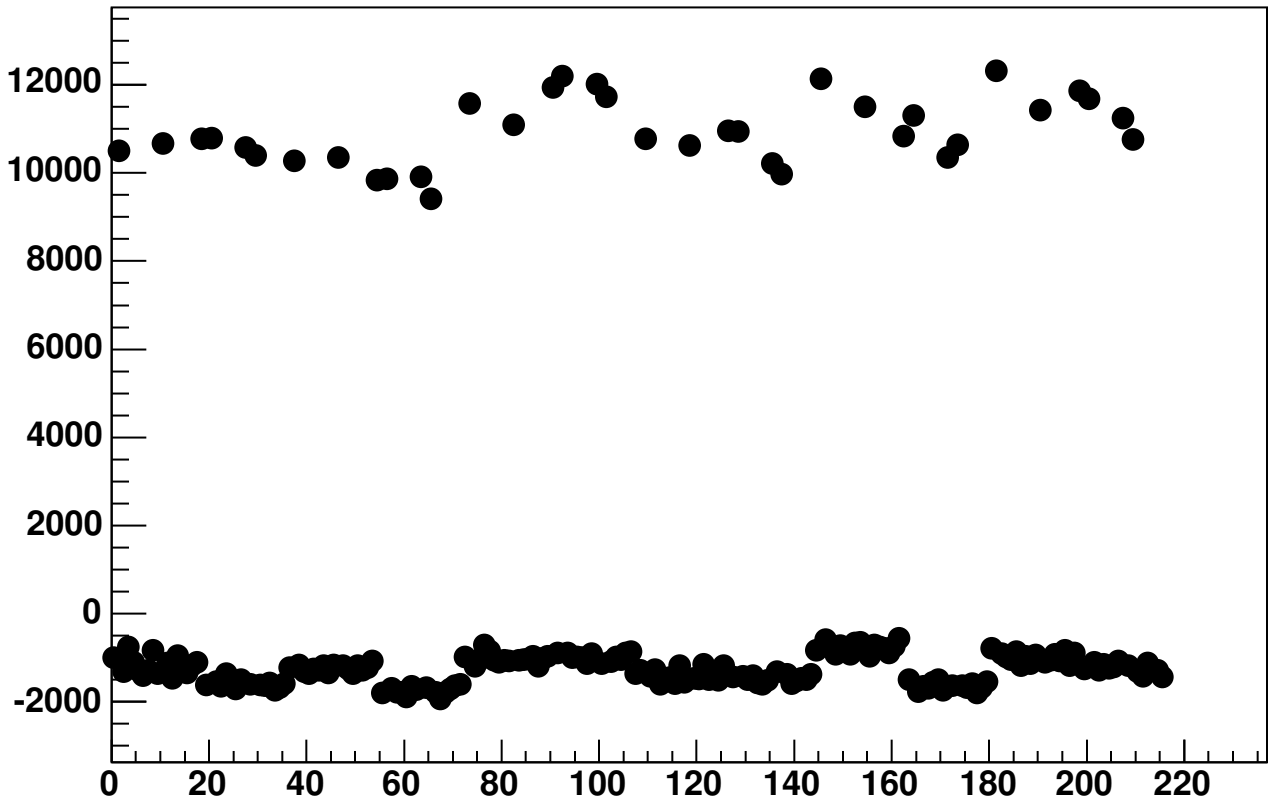
Enable 3, Hold=30, DAC=250, ADC Mean vs 18*Chip+Chan



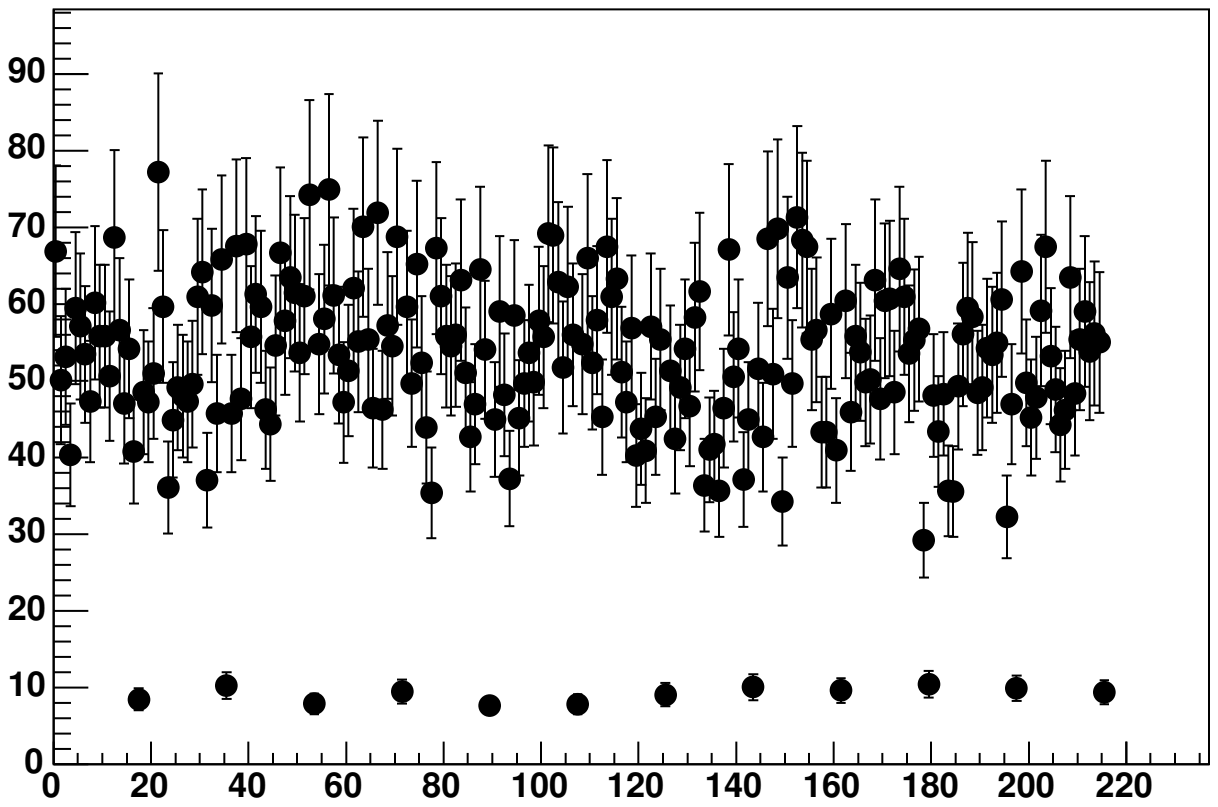
Enable 3, Hold=30, DAC=250, ADC Noise vs 18*Chip+Chan



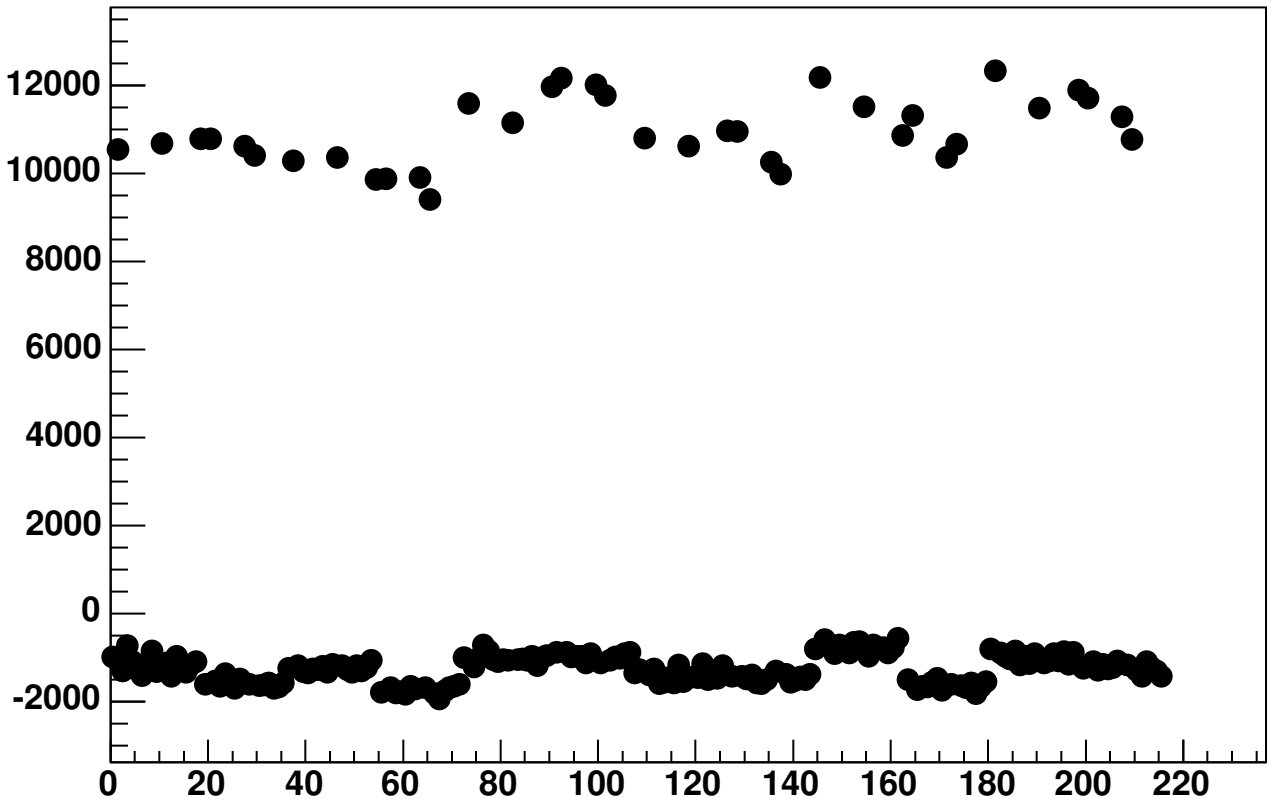
Enable 3, Hold=30, DAC=300, ADC Mean vs 18*Chip+Chan



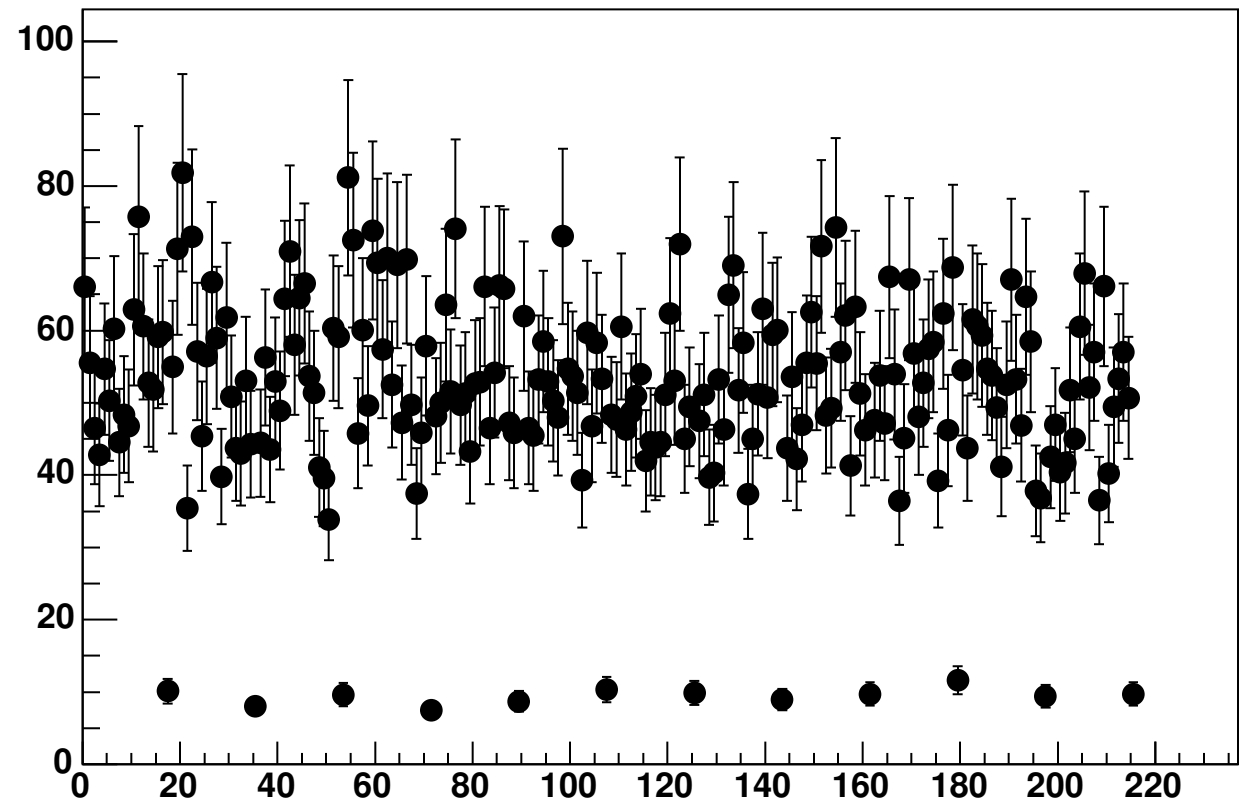
Enable 3, Hold=30, DAC=300, ADC Noise vs 18*Chip+Chan



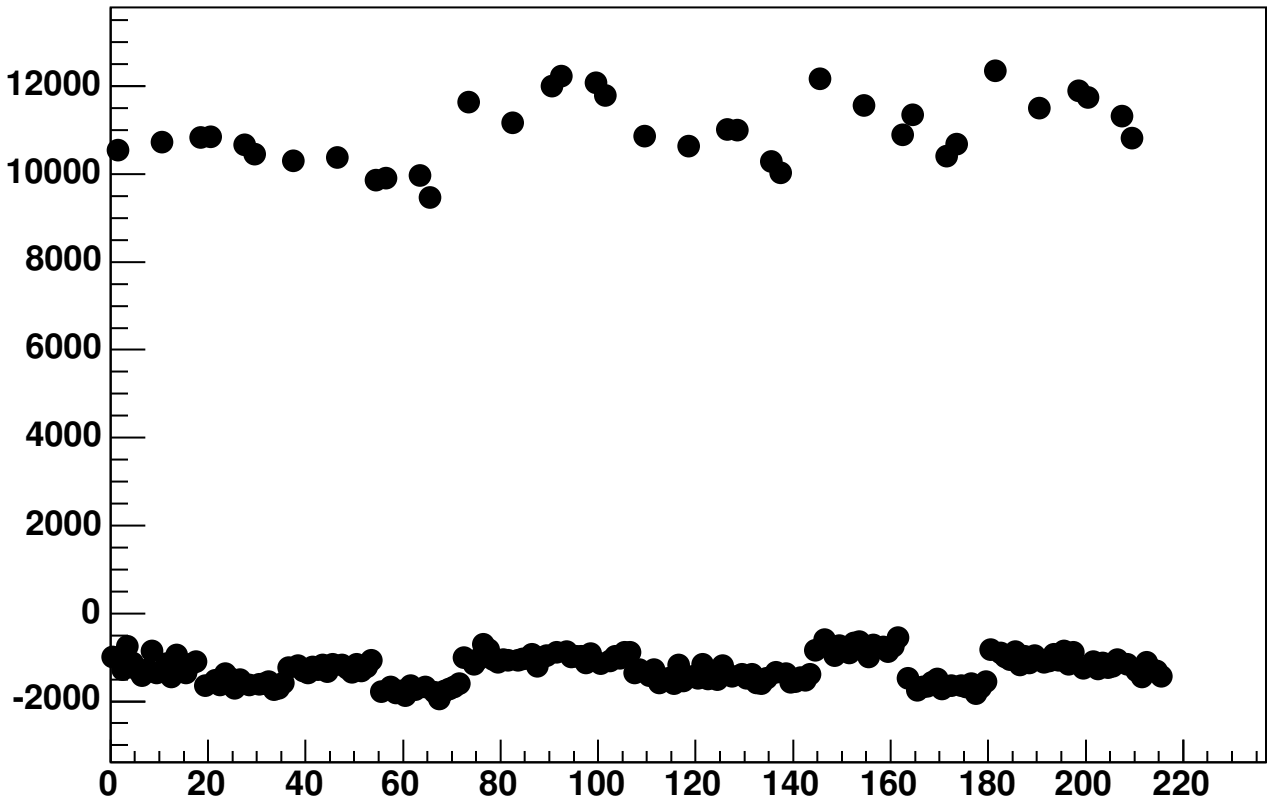
Enable 3, Hold=30, DAC=350, ADC Mean vs 18*Chip+Chan



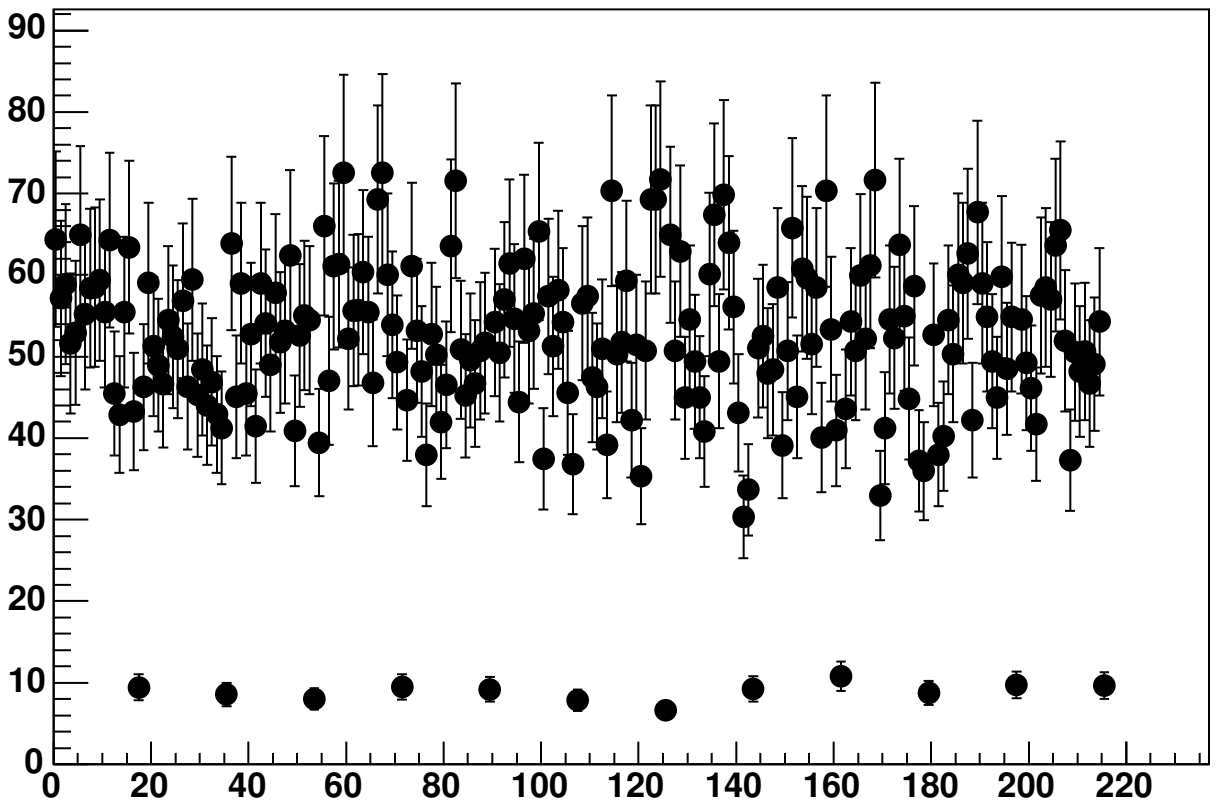
Enable 3, Hold=30, DAC=350, ADC Noise vs 18*Chip+Chan



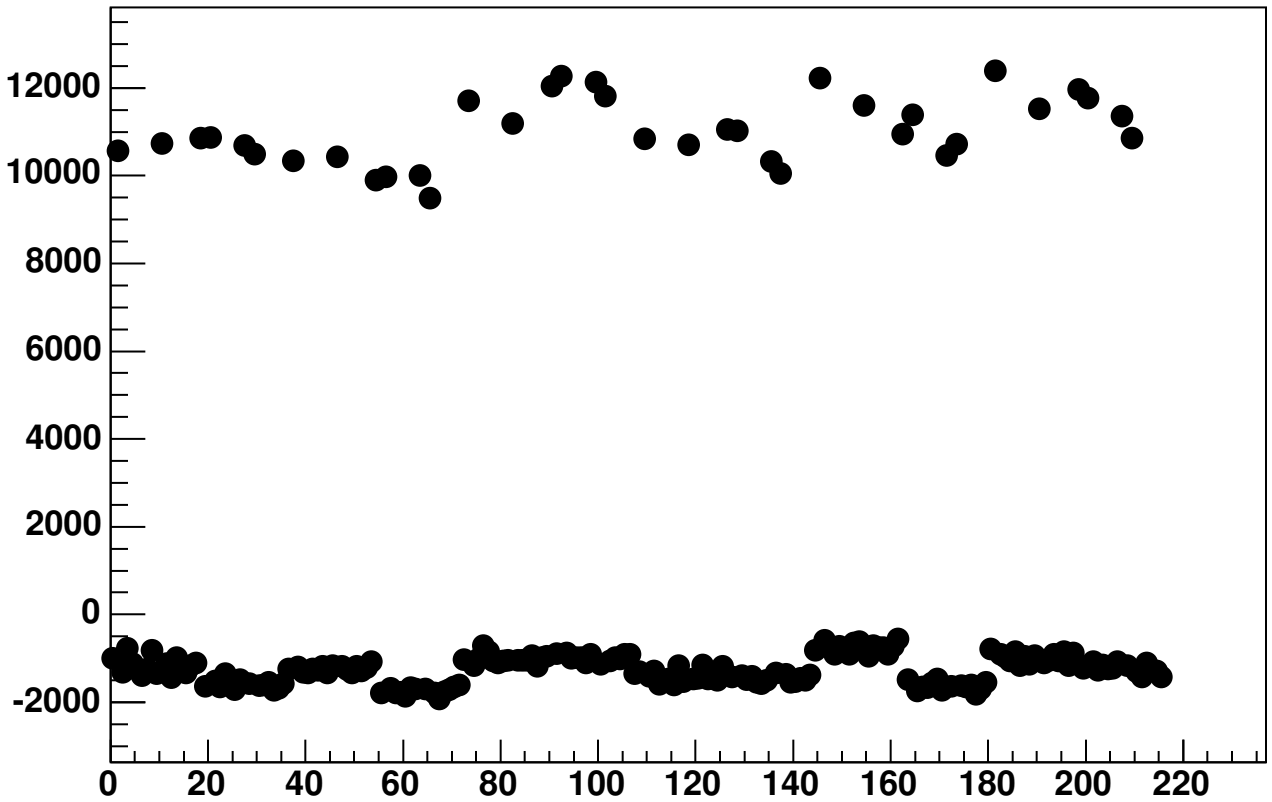
Enable 3, Hold=30, DAC=400, ADC Mean vs 18*Chip+Chan



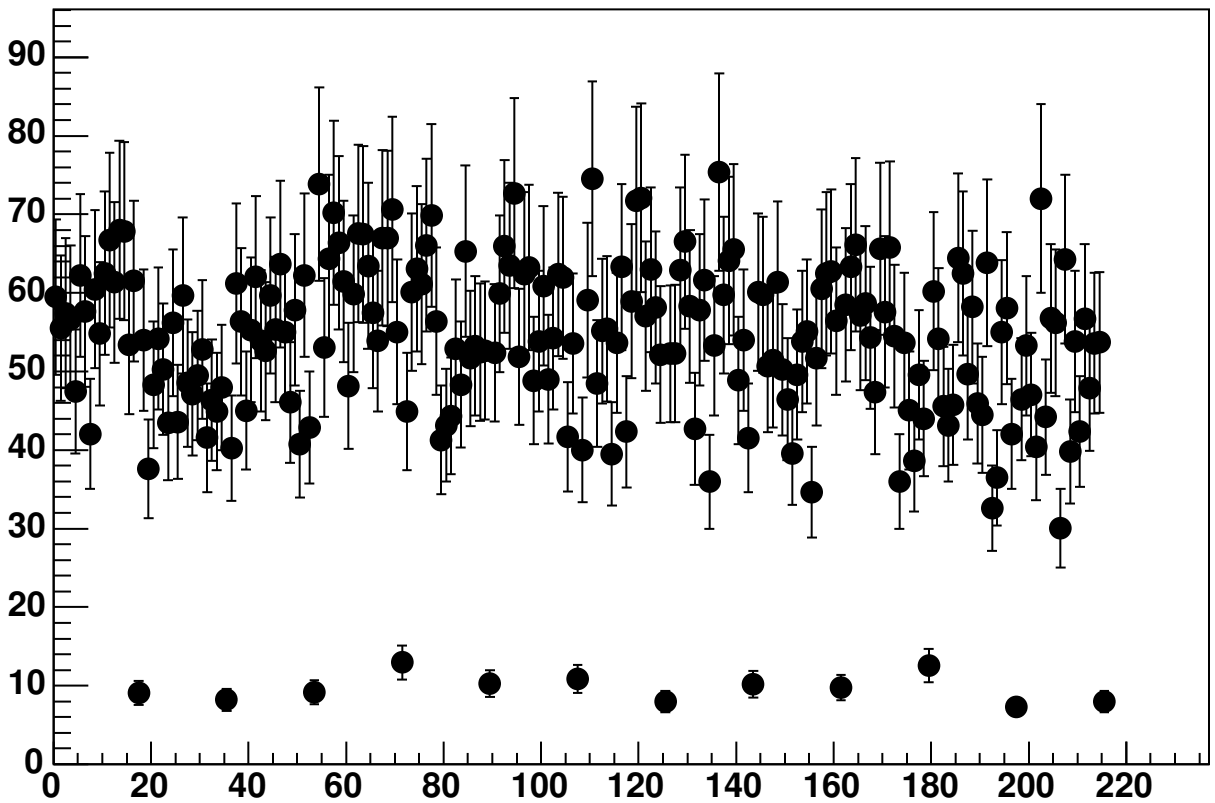
Enable 3, Hold=30, DAC=400, ADC Noise vs 18*Chip+Chan



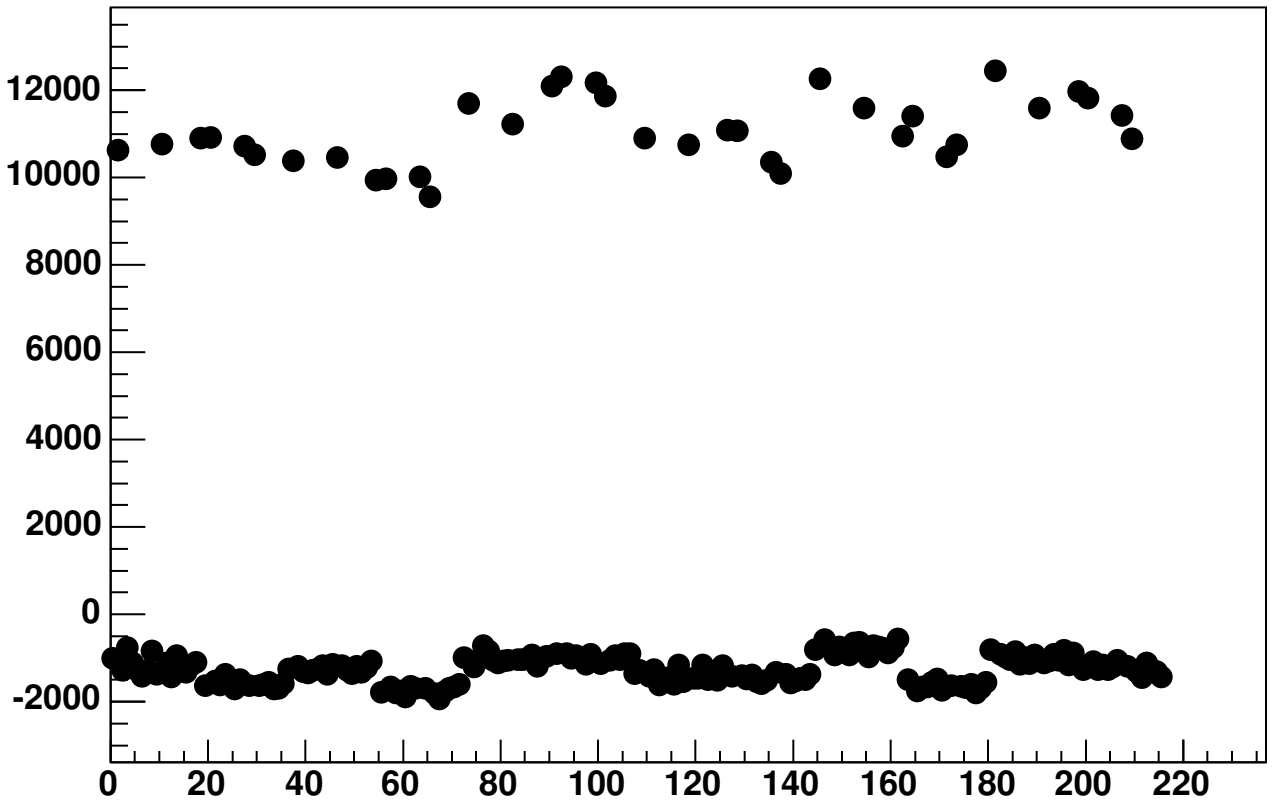
Enable 3, Hold=30, DAC=450, ADC Mean vs 18*Chip+Chan



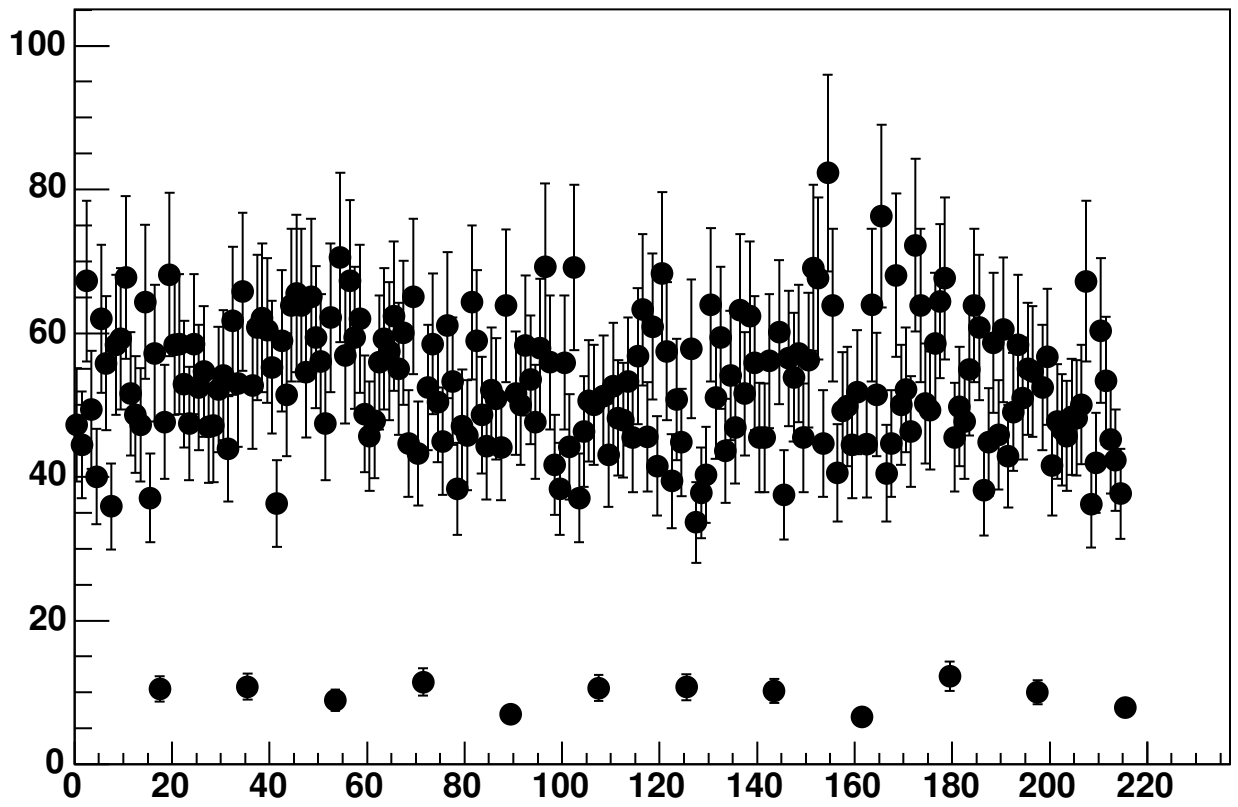
Enable 3, Hold=30, DAC=450, ADC Noise vs 18*Chip+Chan



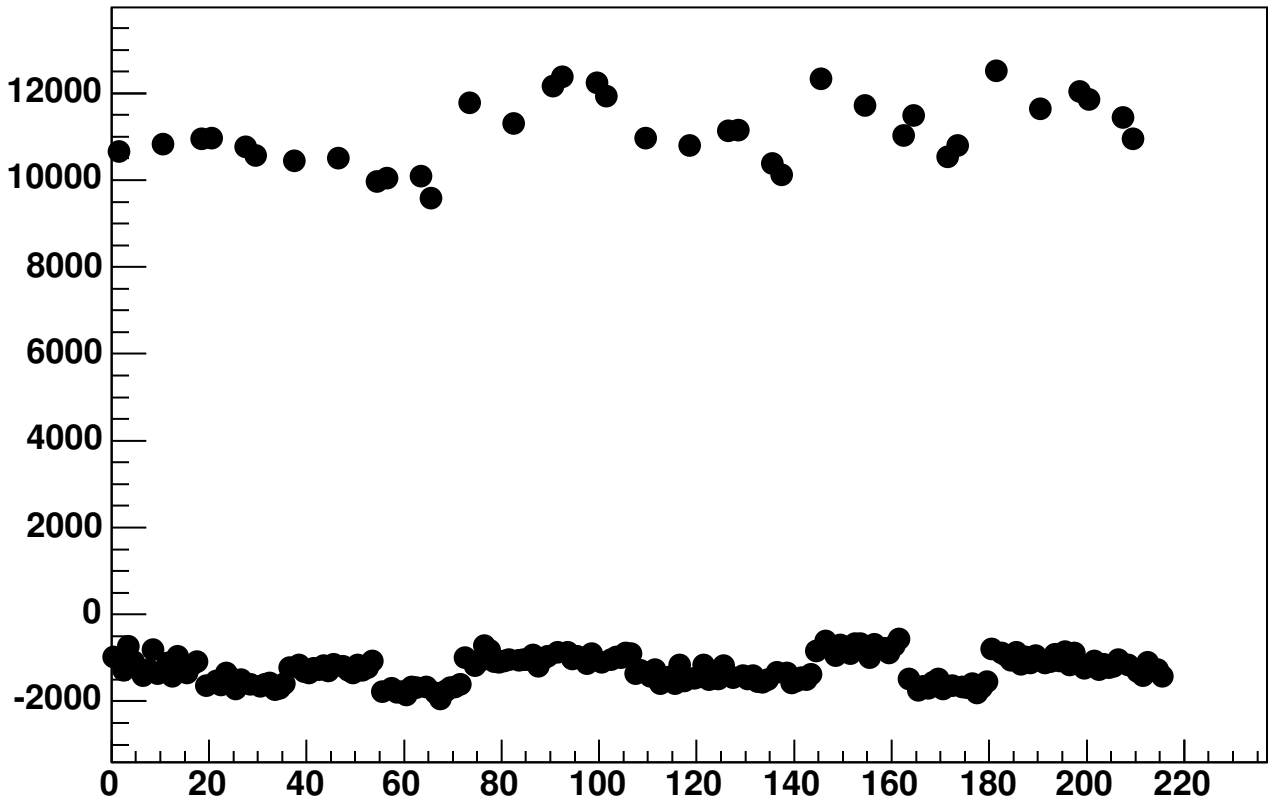
Enable 3, Hold=30, DAC=500, ADC Mean vs 18*Chip+Chan



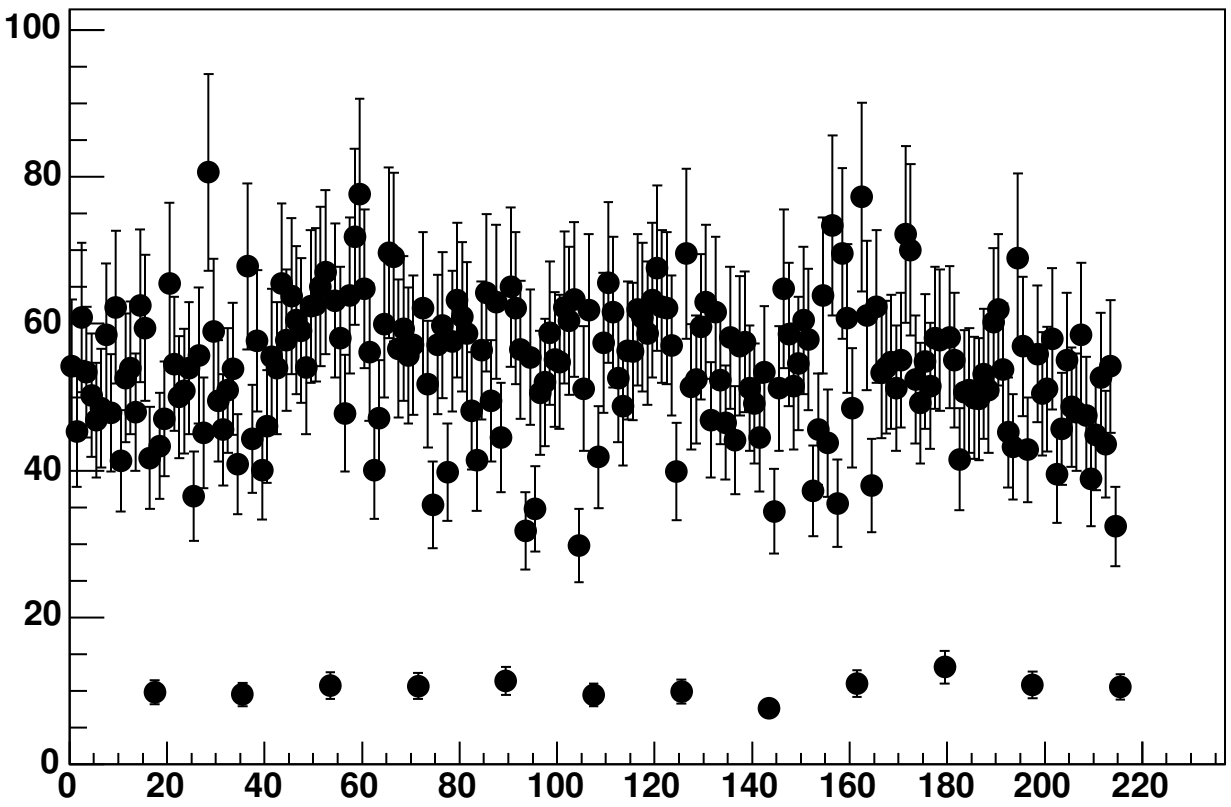
Enable 3, Hold=30, DAC=500, ADC Noise vs 18*Chip+Chan



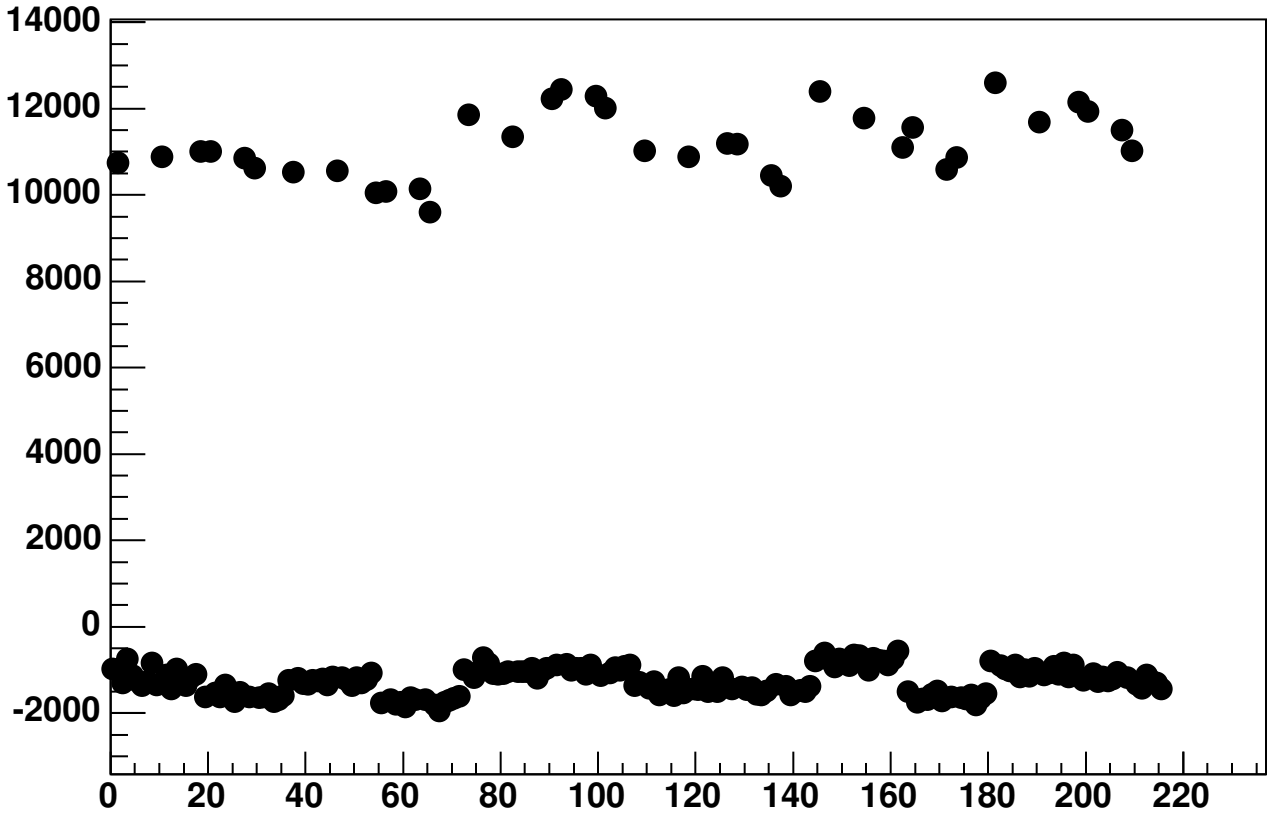
Enable 3, Hold=30, DAC=550, ADC Mean vs 18*Chip+Chan



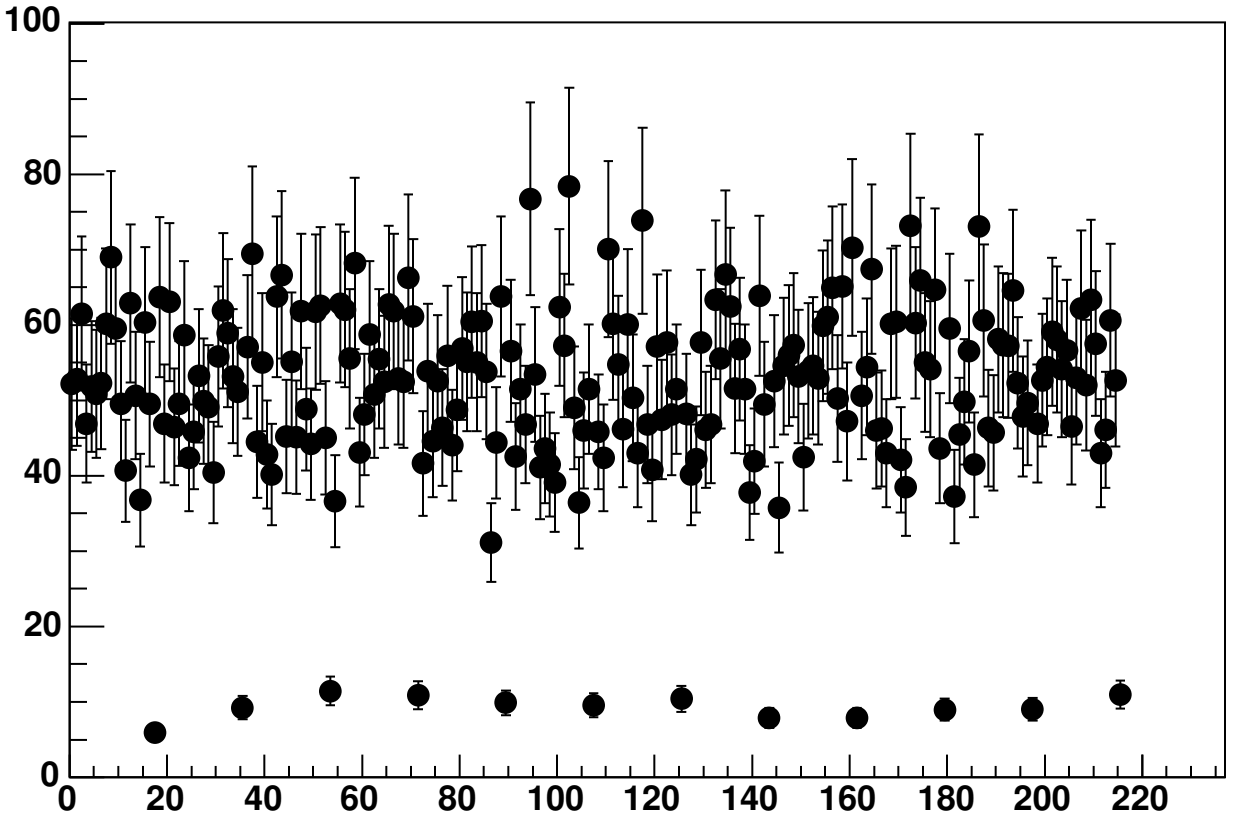
Enable 3, Hold=30, DAC=550, ADC Noise vs 18*Chip+Chan



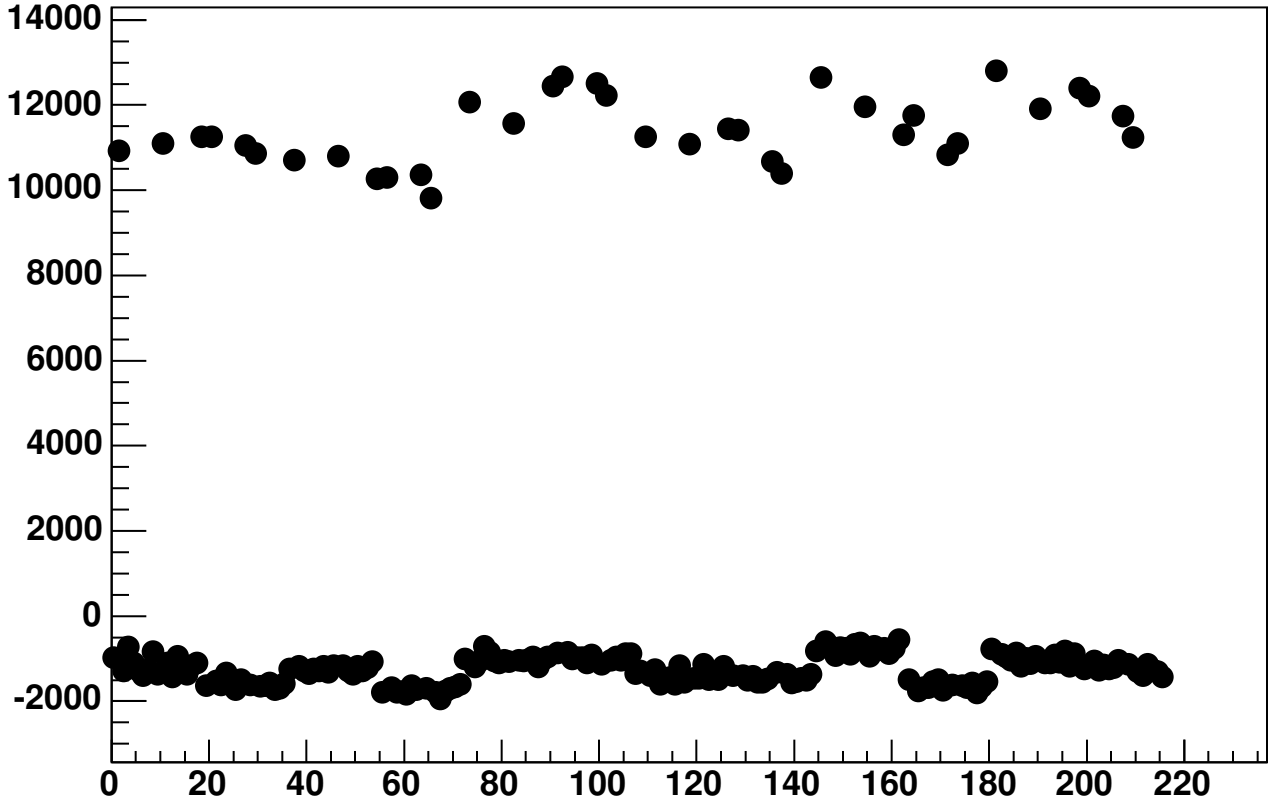
Enable 3, Hold=30, DAC=600, ADC Mean vs 18*Chip+Chan



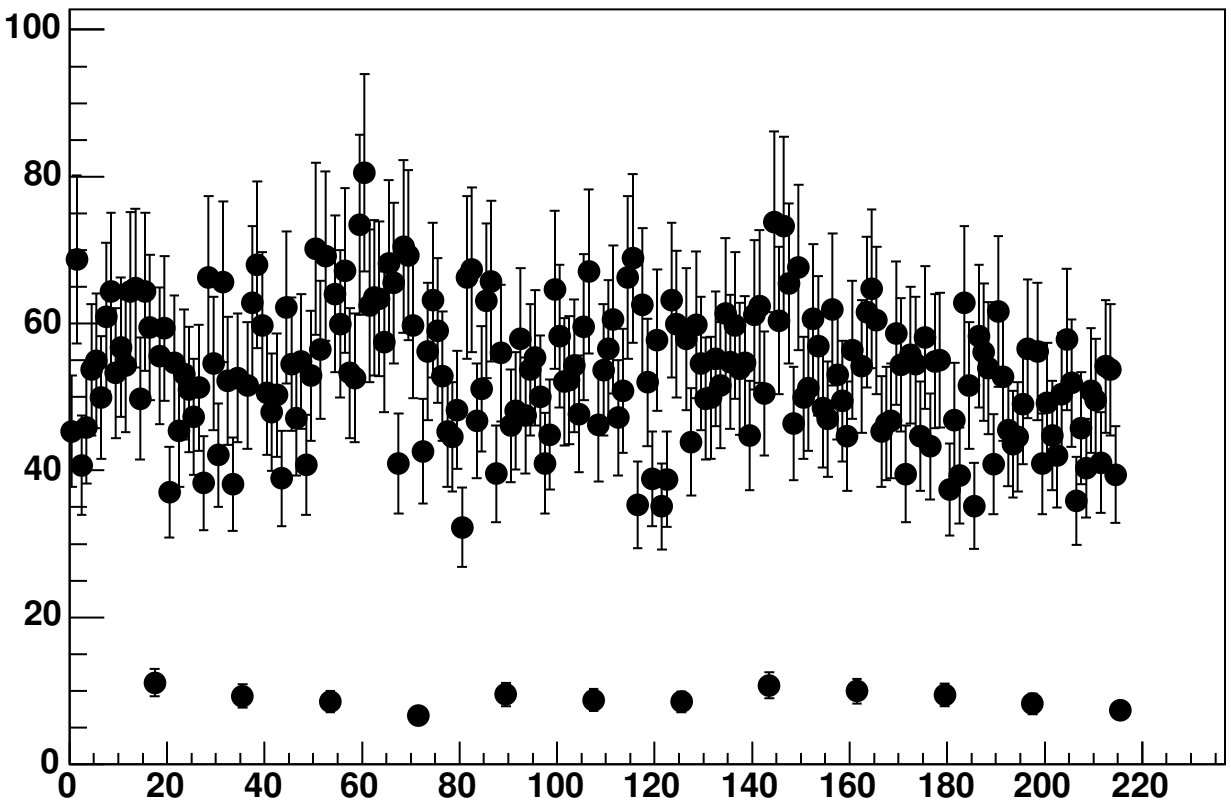
Enable 3, Hold=30, DAC=600, ADC Noise vs 18*Chip+Chan



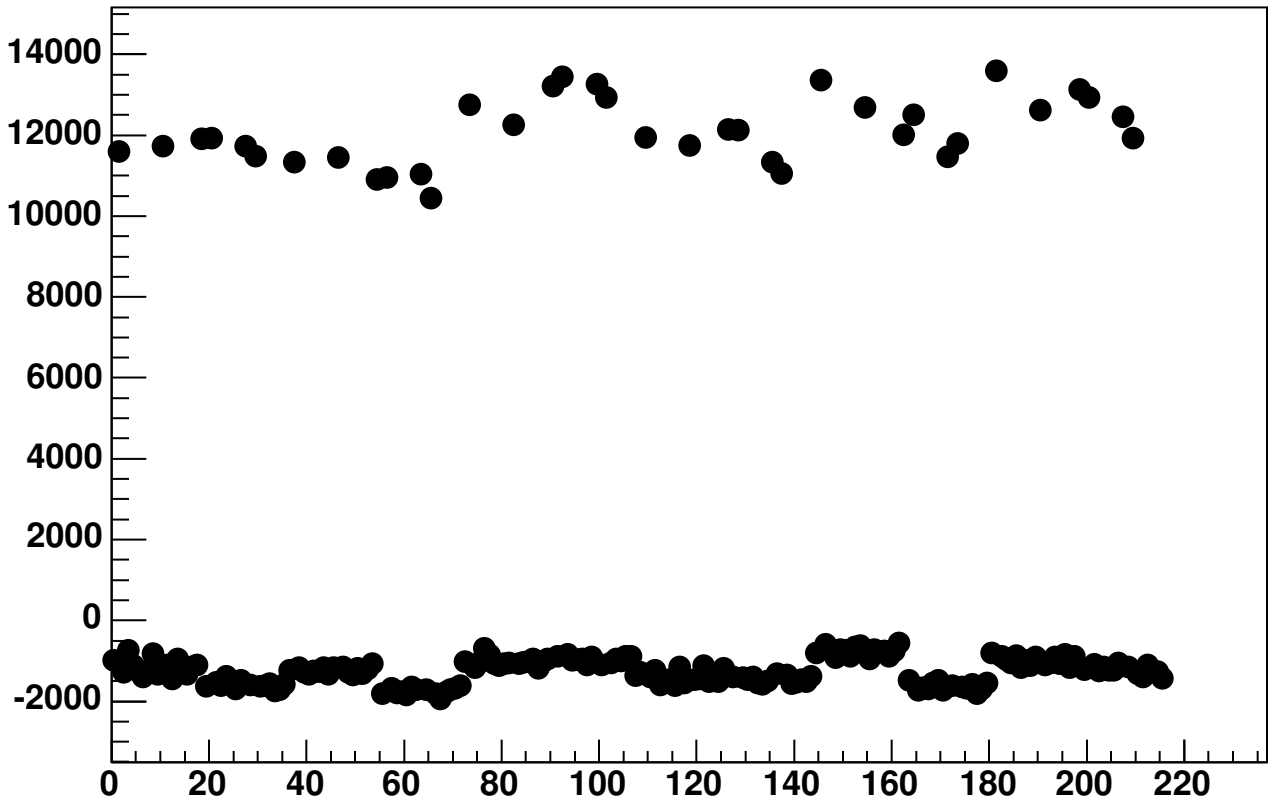
Enable 3, Hold=30, DAC=650, ADC Mean vs 18*Chip+Chan



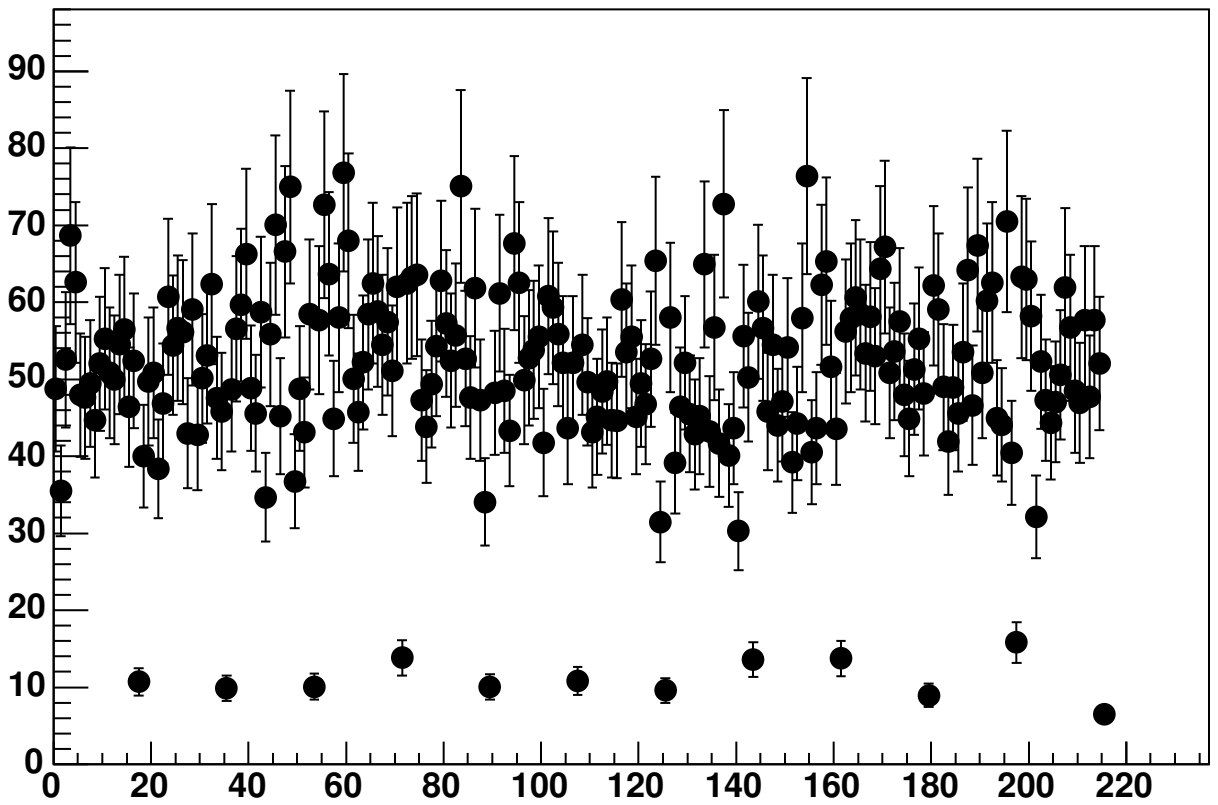
Enable 3, Hold=30, DAC=650, ADC Noise vs 18*Chip+Chan



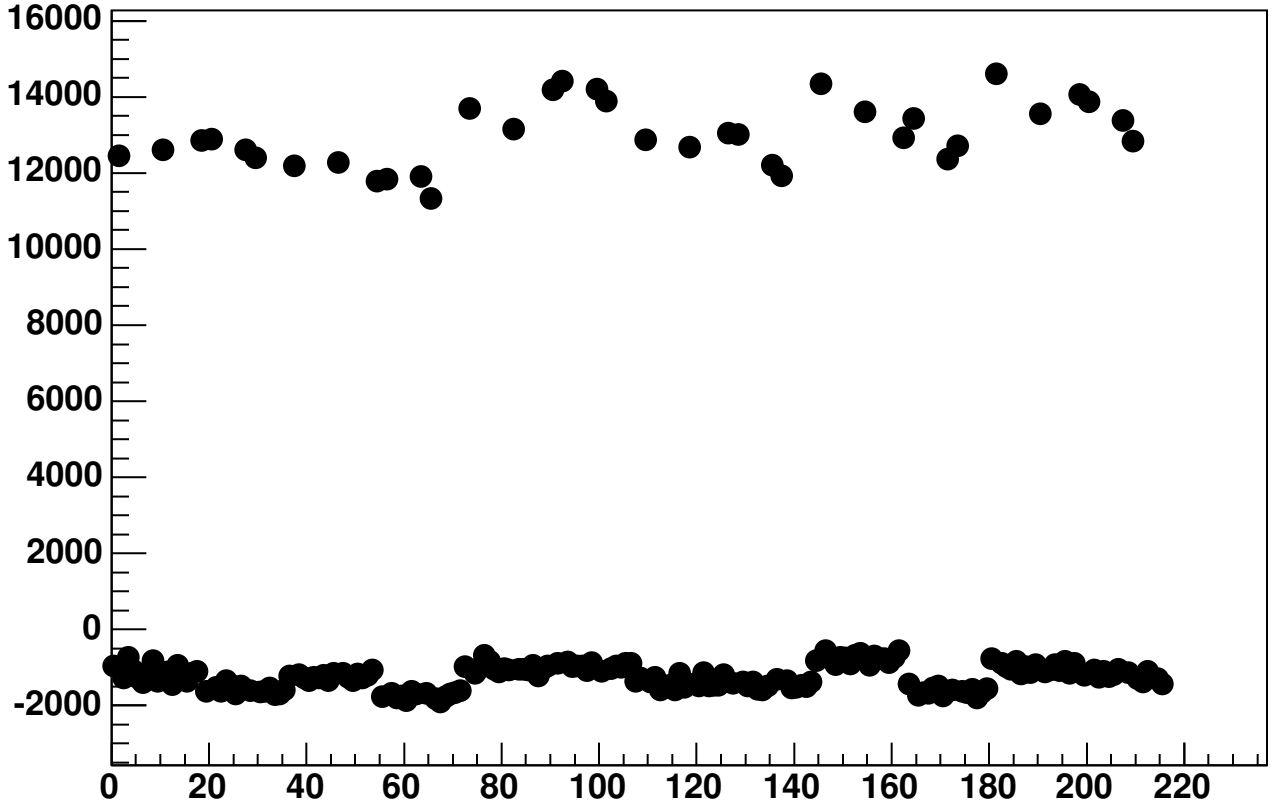
Enable 3, Hold=30, DAC=700, ADC Mean vs 18*Chip+Chan



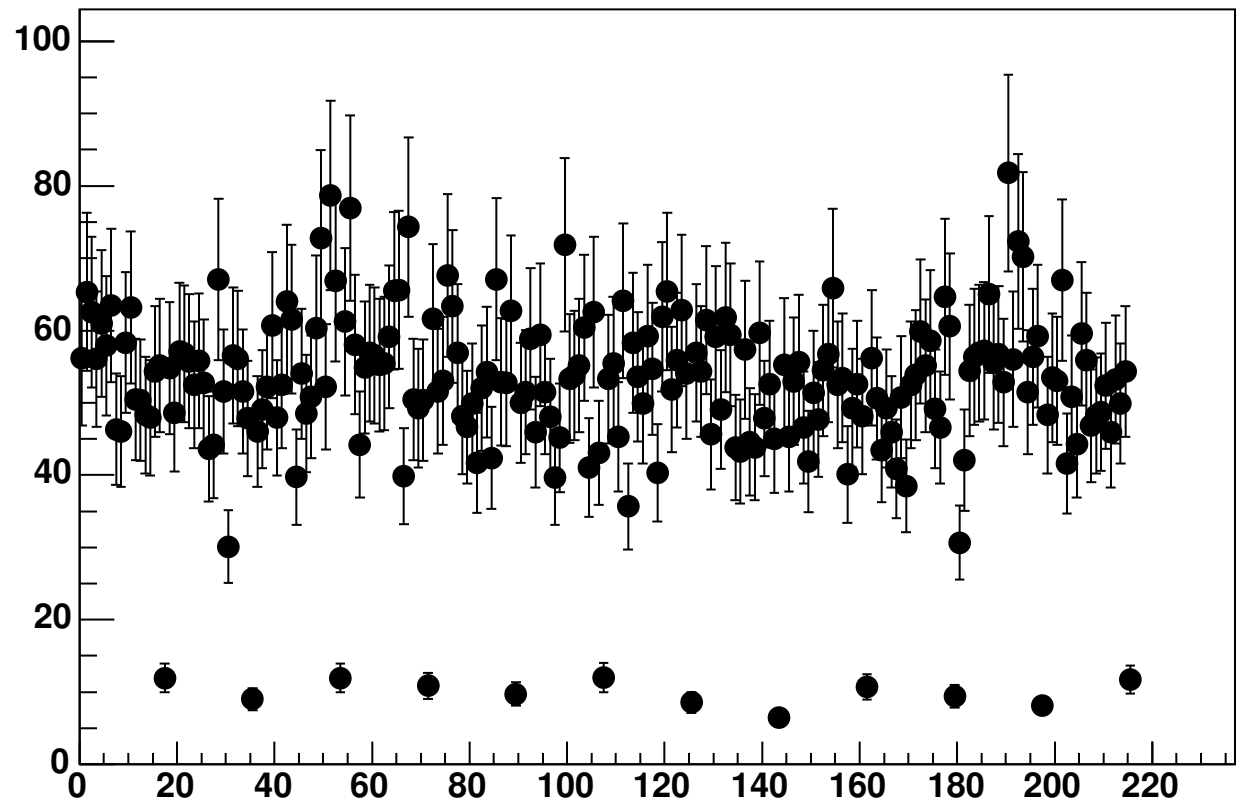
Enable 3, Hold=30, DAC=700, ADC Noise vs 18*Chip+Chan



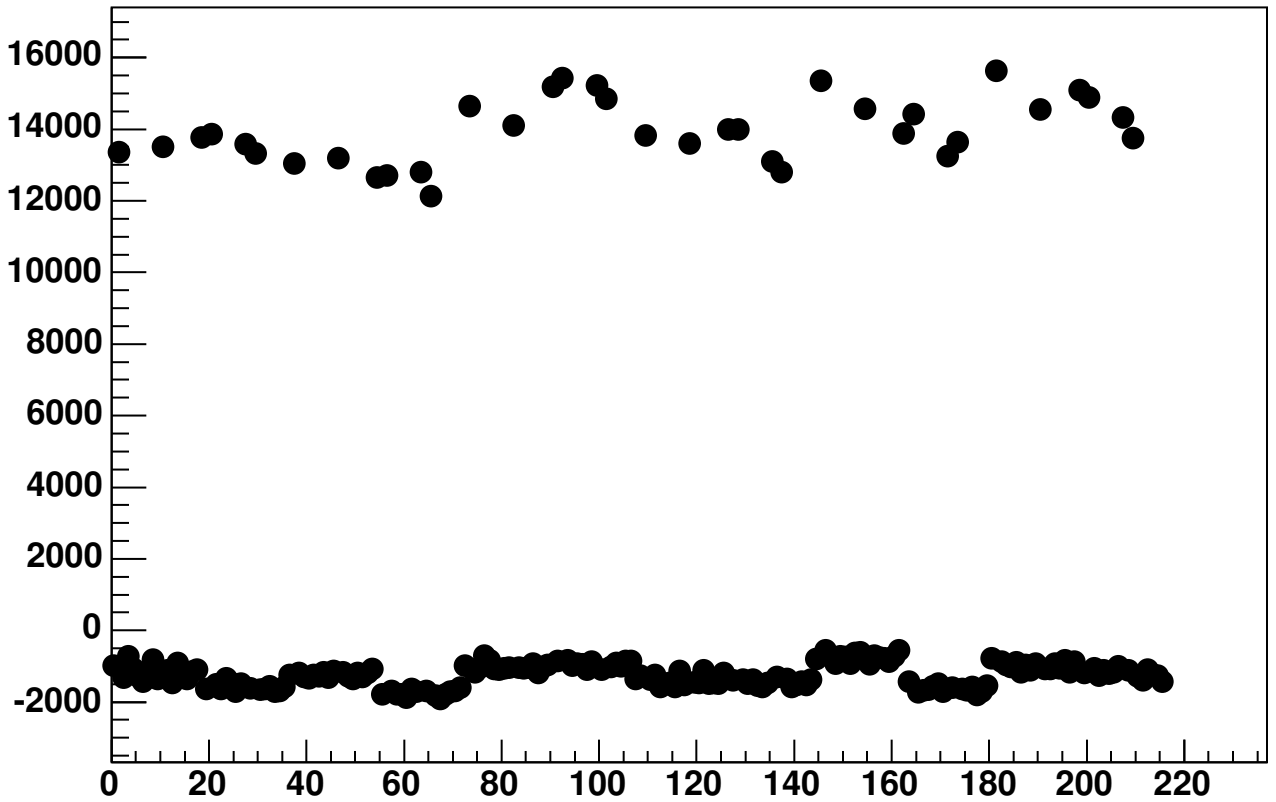
Enable 3, Hold=30, DAC=750, ADC Mean vs 18*Chip+Chan



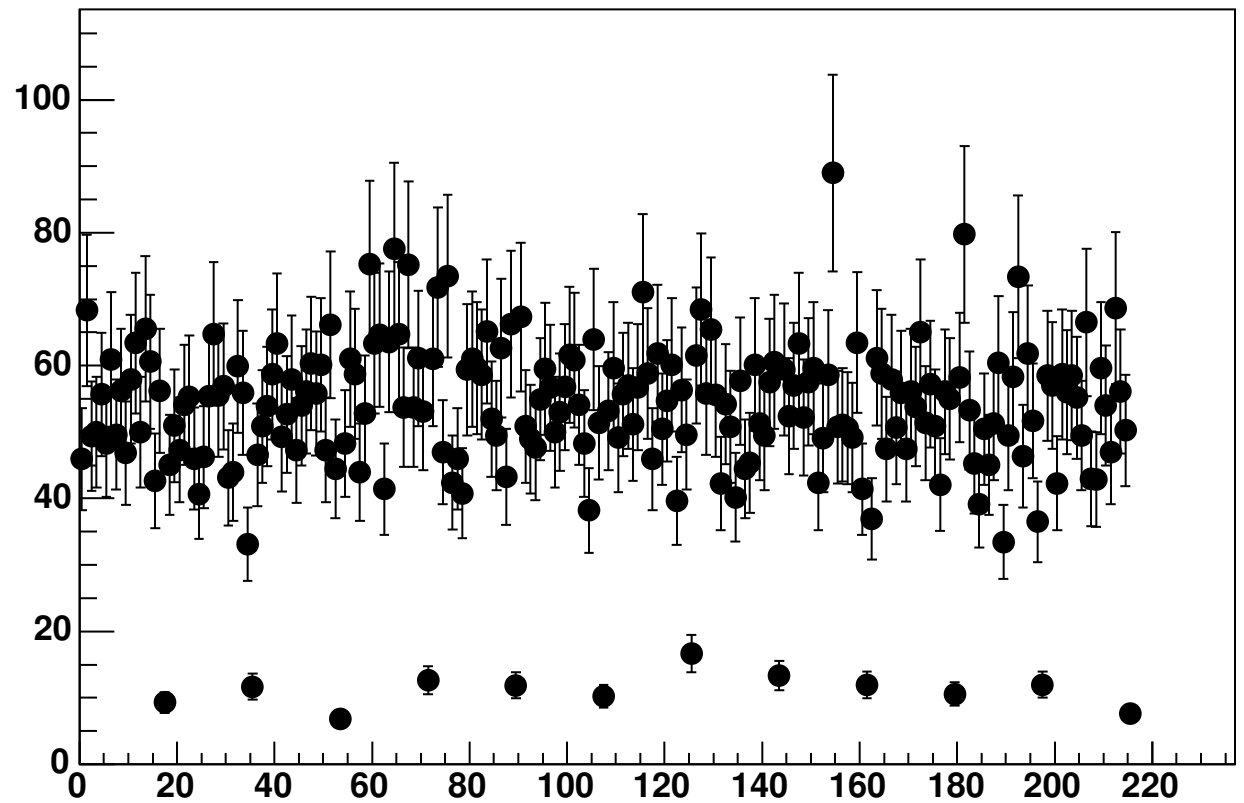
Enable 3, Hold=30, DAC=750, ADC Noise vs 18*Chip+Chan



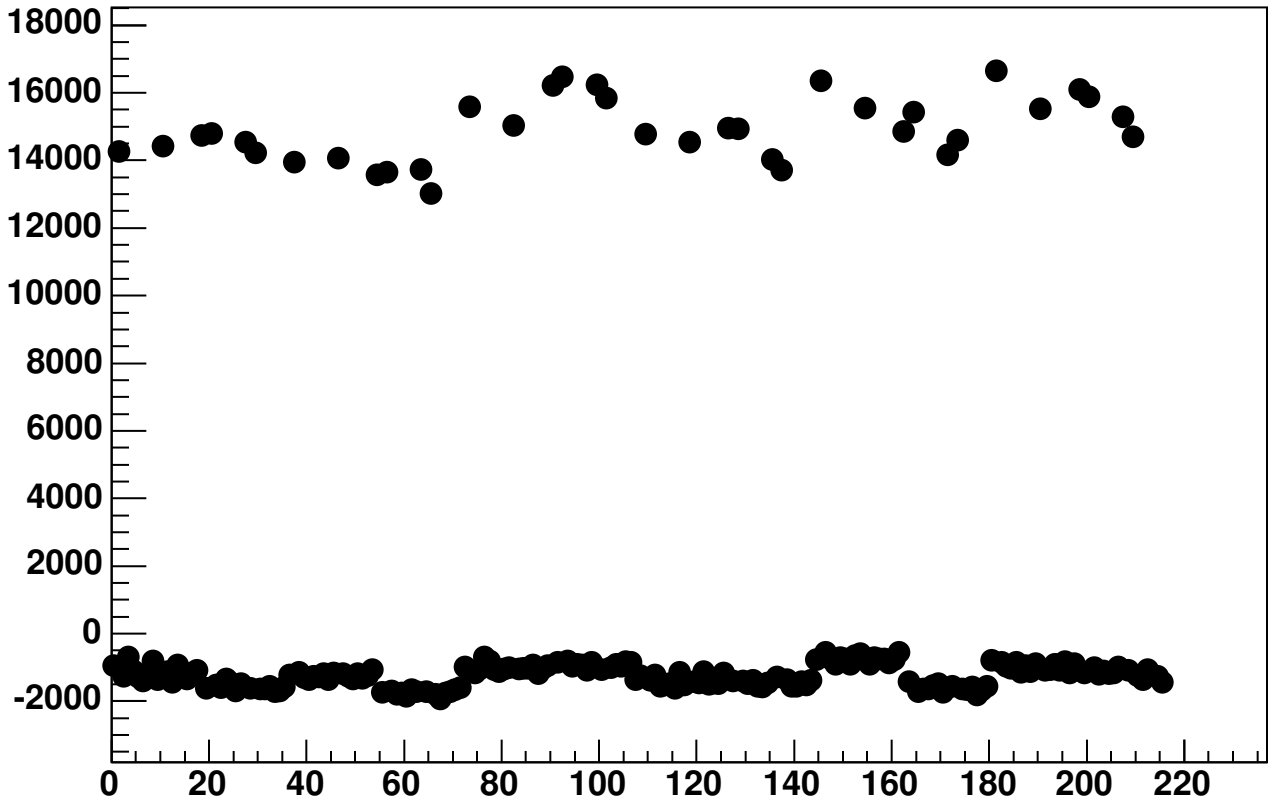
Enable 3, Hold=30, DAC=800, ADC Mean vs 18*Chip+Chan



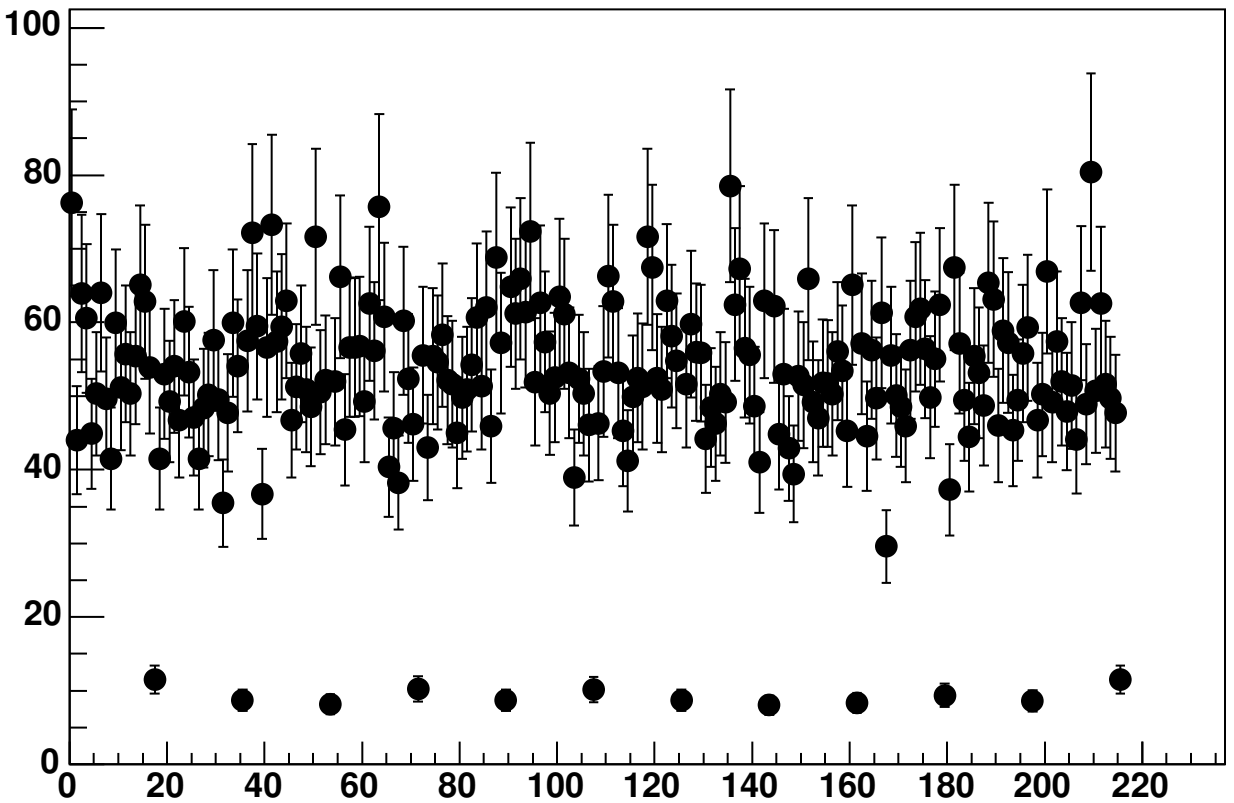
Enable 3, Hold=30, DAC=800, ADC Noise vs 18*Chip+Chan



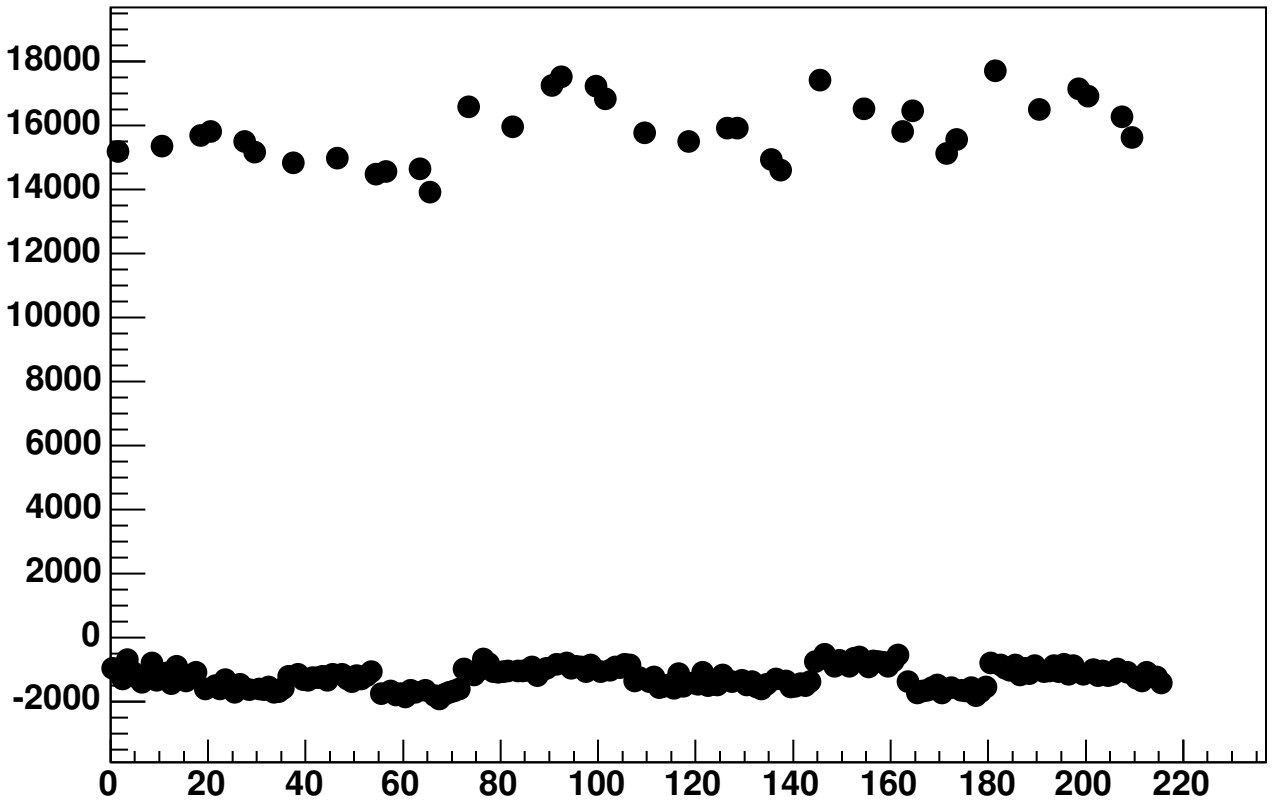
Enable 3, Hold=30, DAC=850, ADC Mean vs 18*Chip+Chan



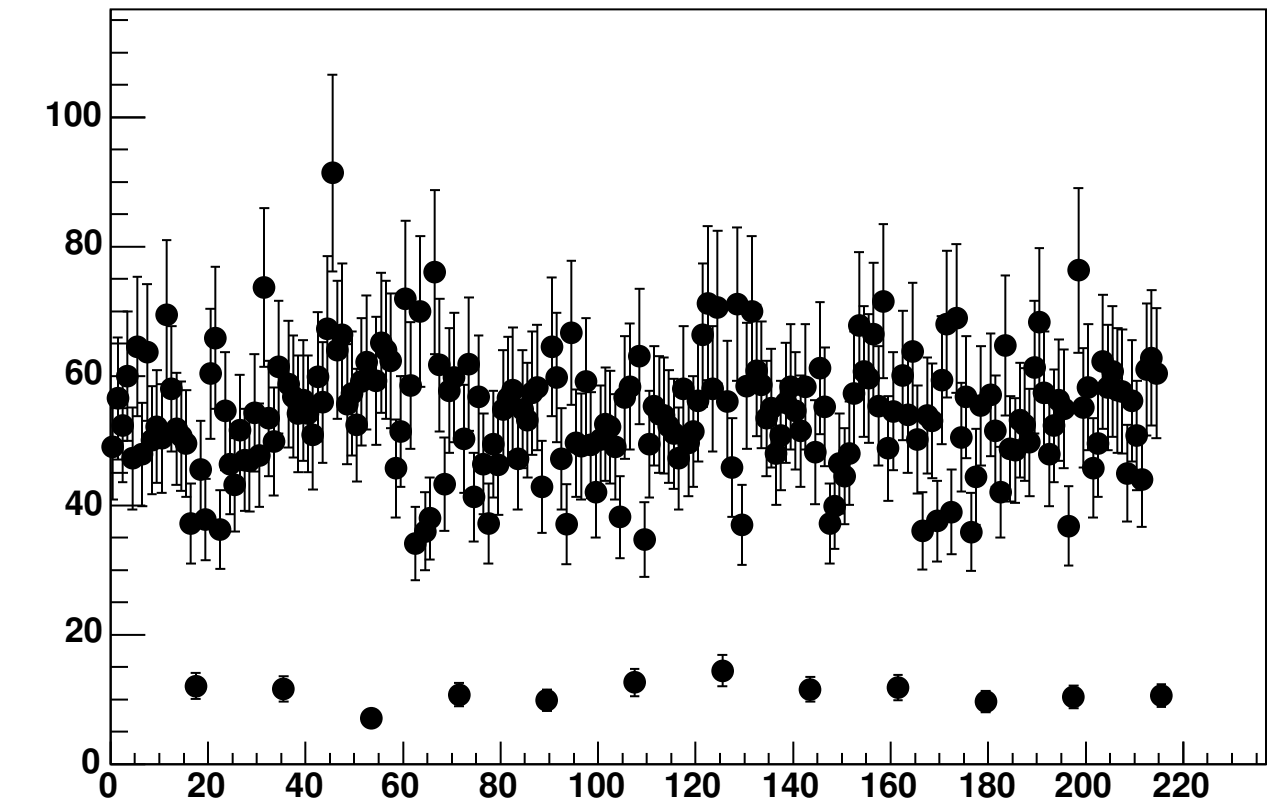
Enable 3, Hold=30, DAC=850, ADC Noise vs 18*Chip+Chan



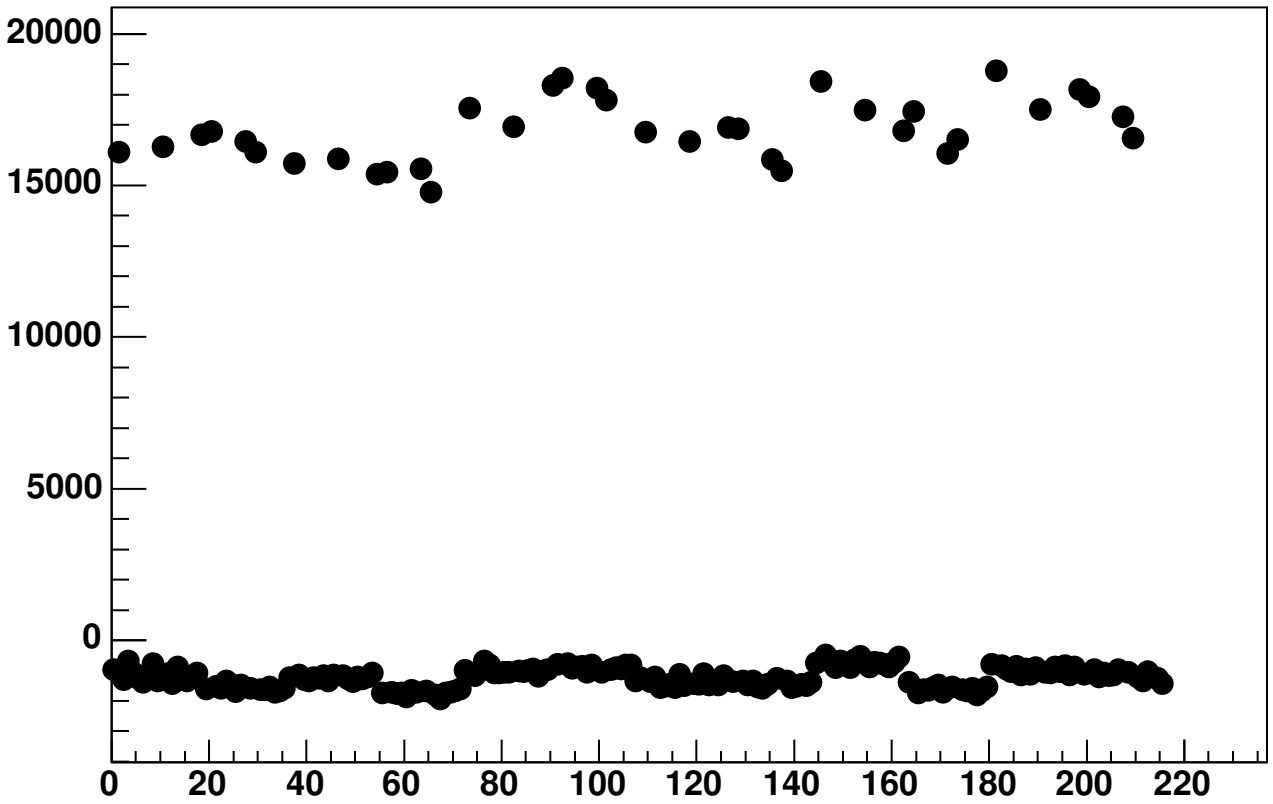
Enable 3, Hold=30, DAC=900, ADC Mean vs 18*Chip+Chan



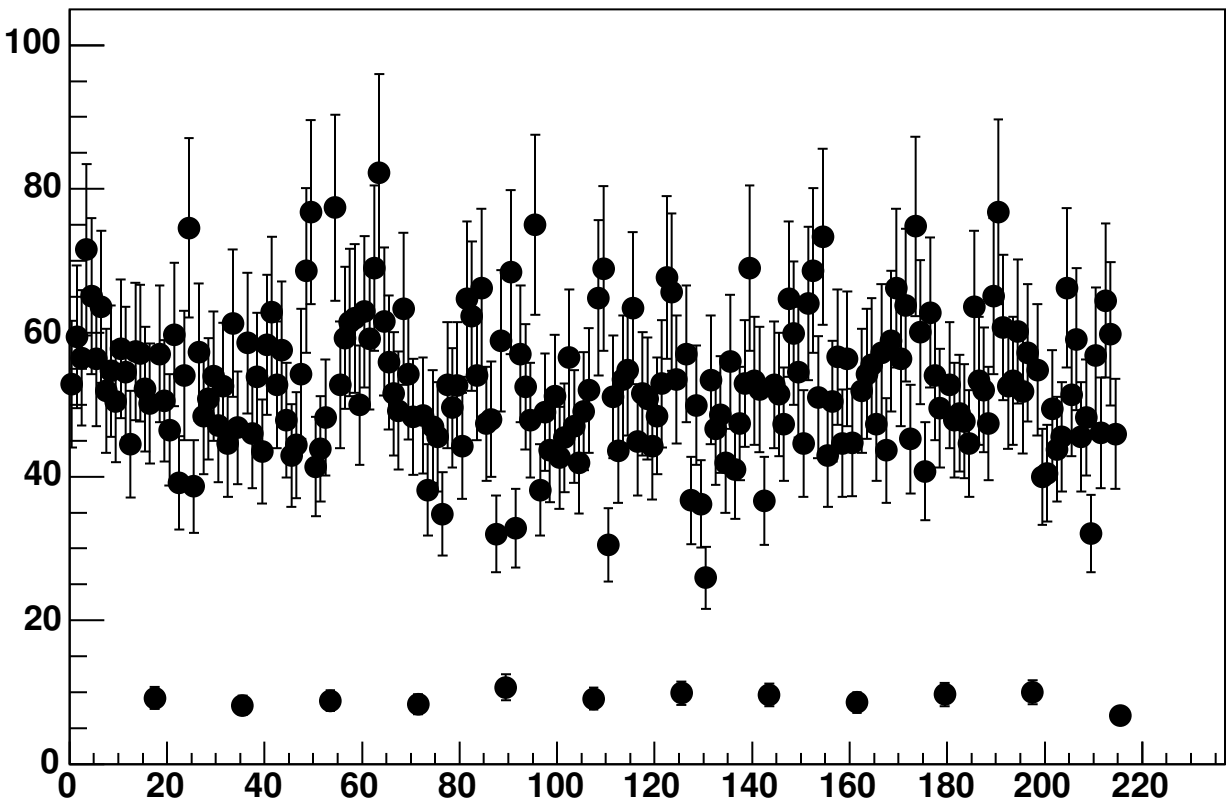
Enable 3, Hold=30, DAC=900, ADC Noise vs 18*Chip+Chan



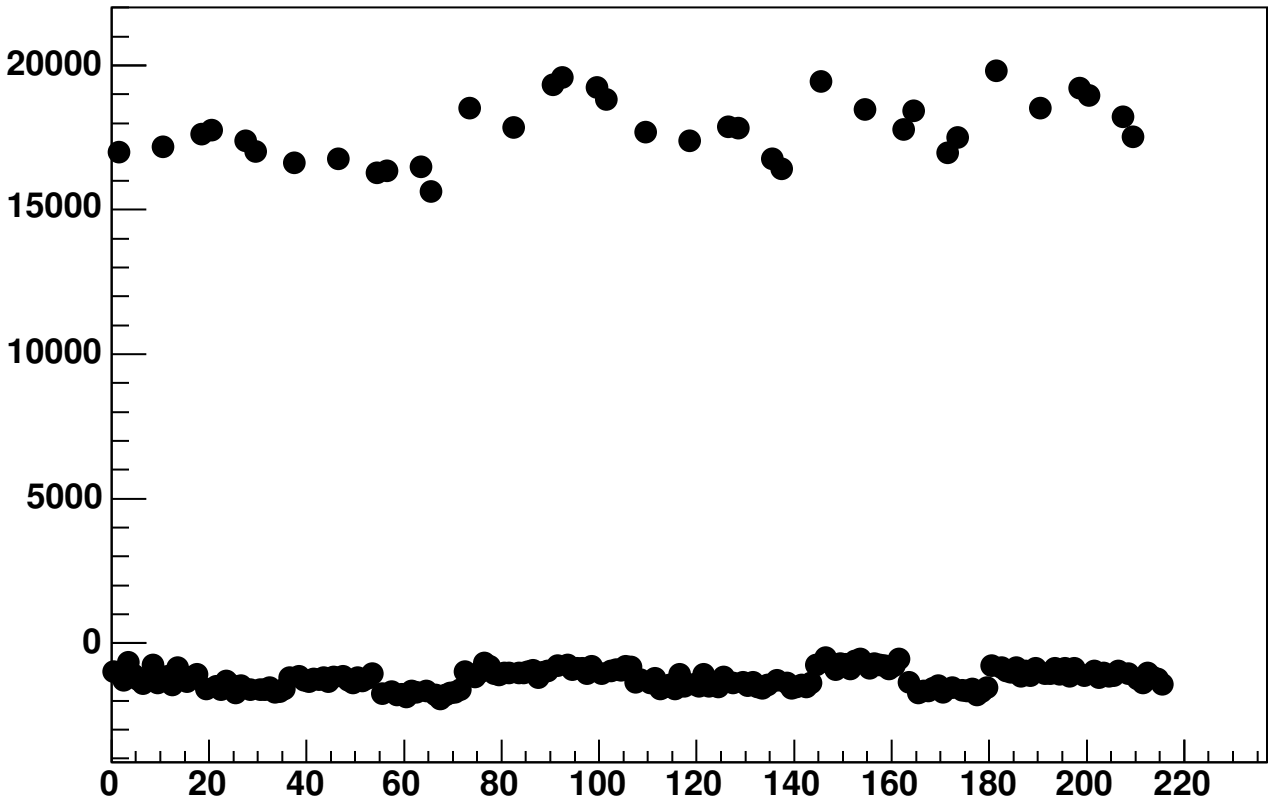
Enable 3, Hold=30, DAC=950, ADC Mean vs 18*Chip+Chan



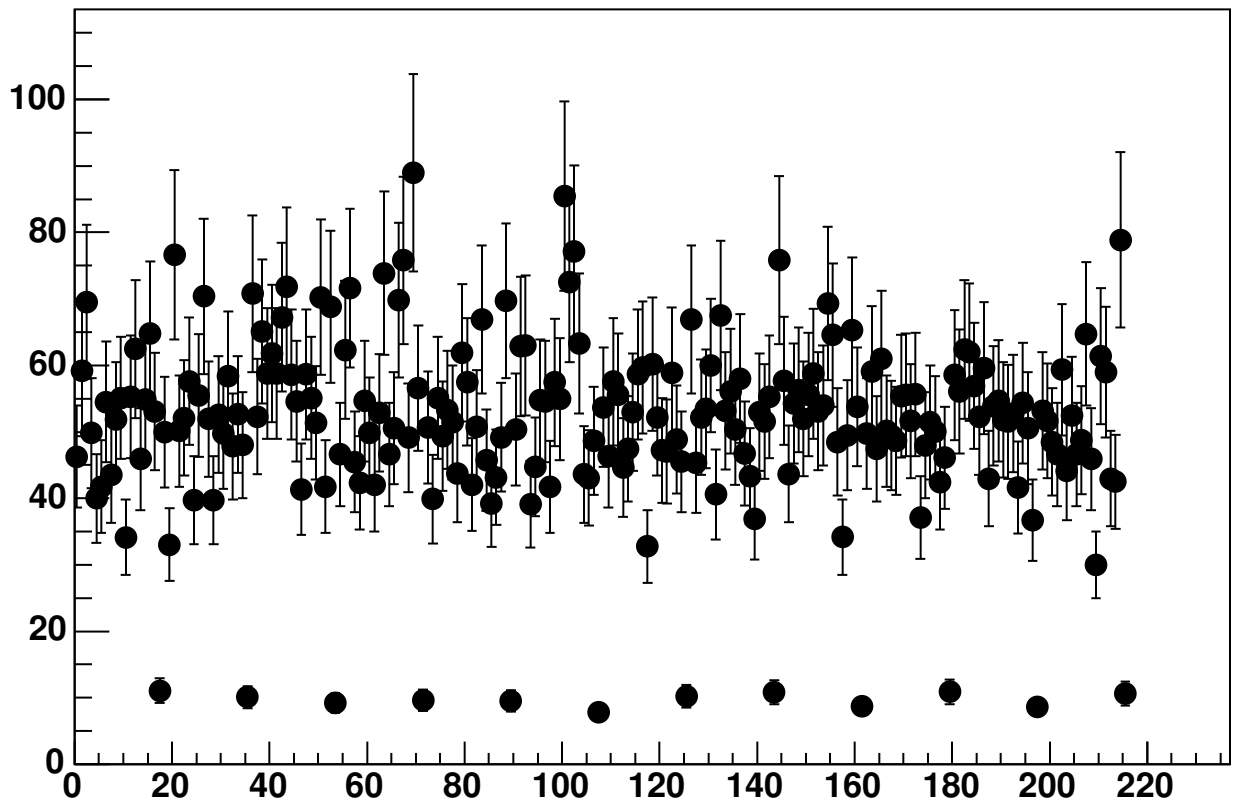
Enable 3, Hold=30, DAC=950, ADC Noise vs 18*Chip+Chan



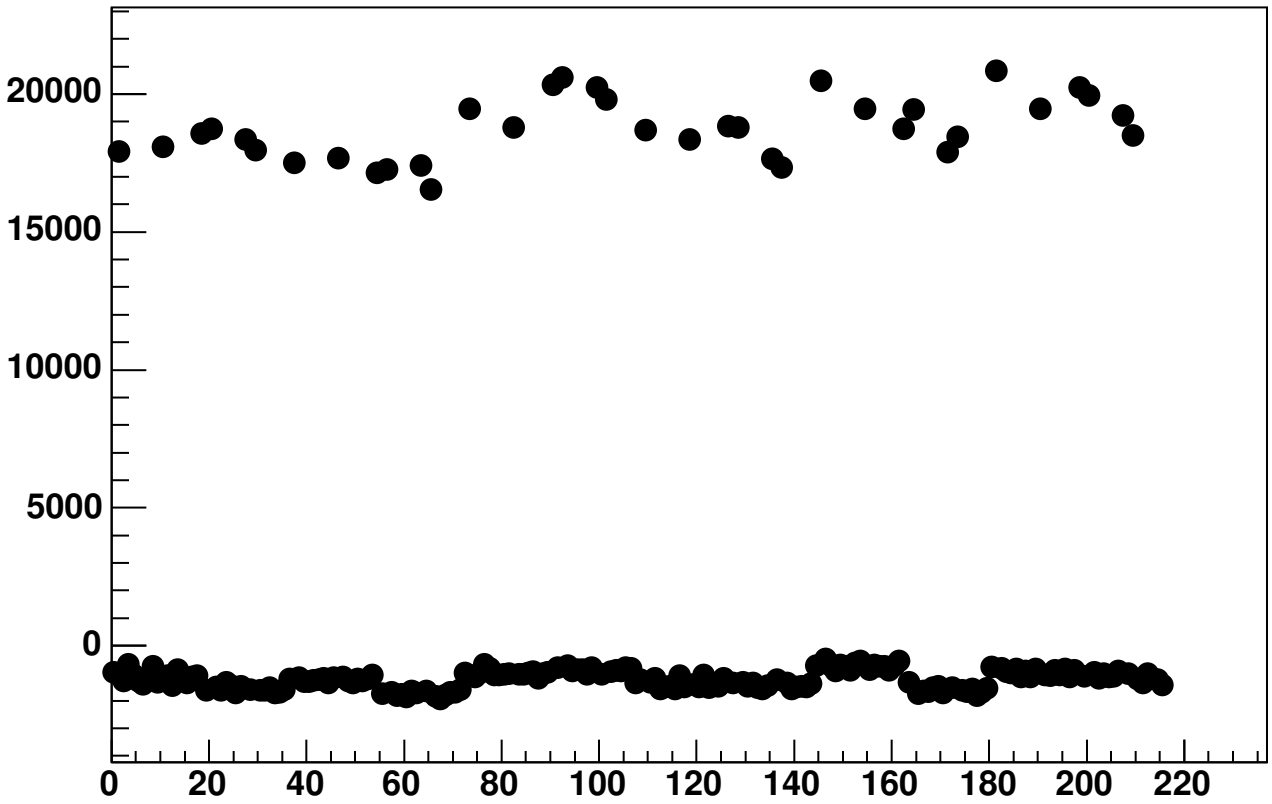
Enable 3, Hold=30, DAC=1000, ADC Mean vs 18*Chip+Chan



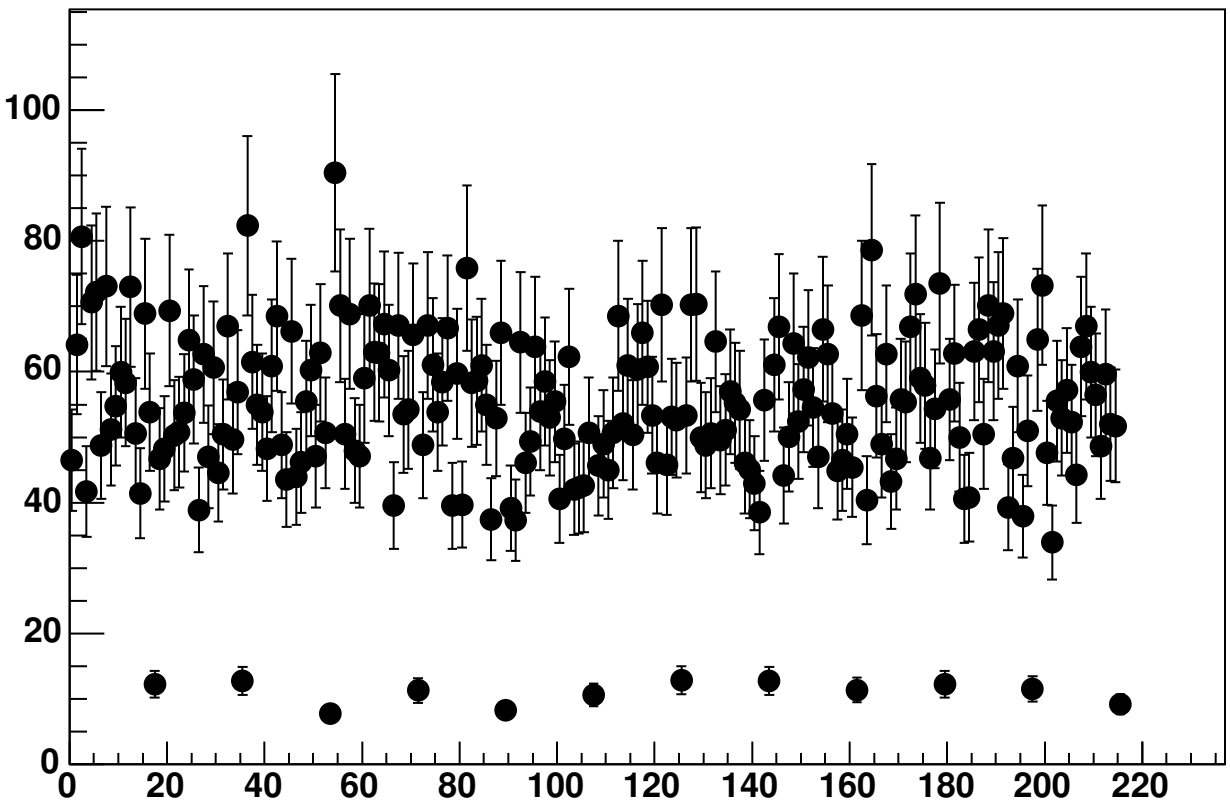
Enable 3, Hold=30, DAC=1000, ADC Noise vs 18*Chip+Chan



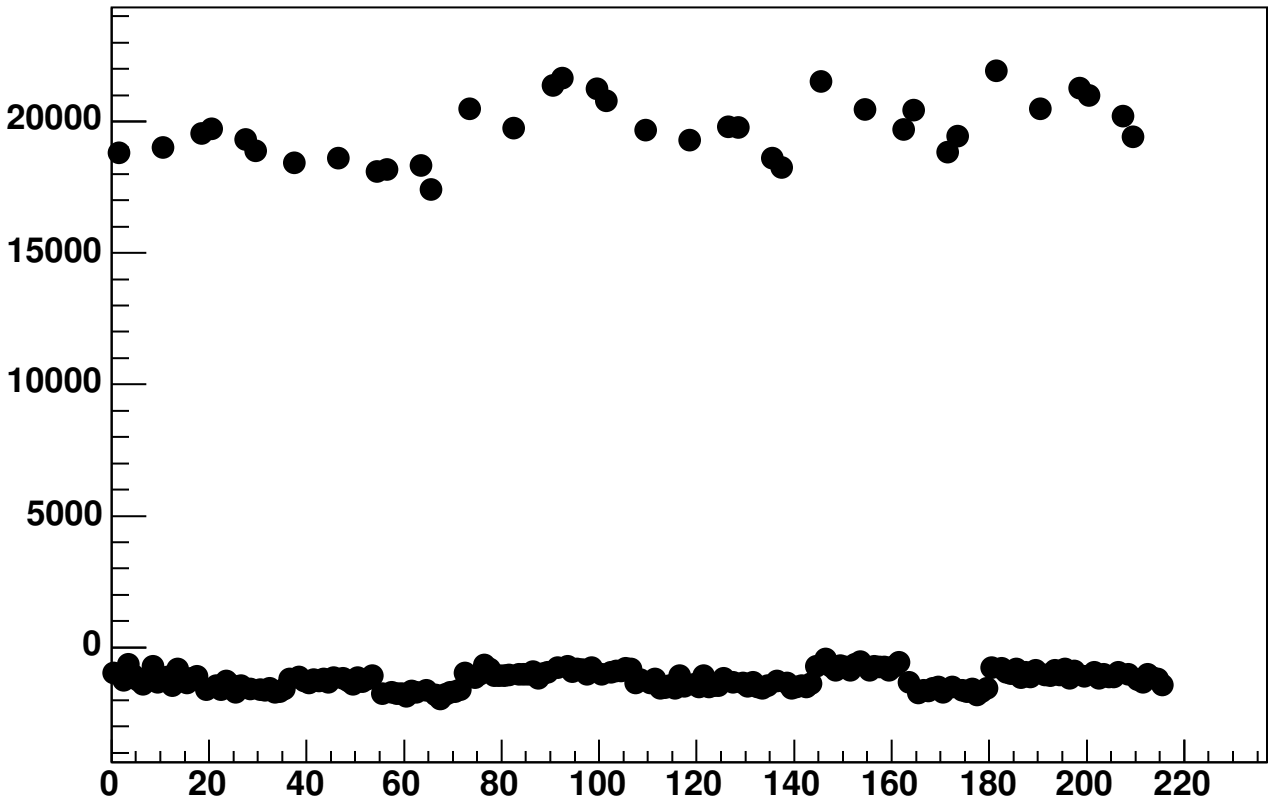
Enable 3, Hold=30, DAC=1050, ADC Mean vs 18*Chip+Chan



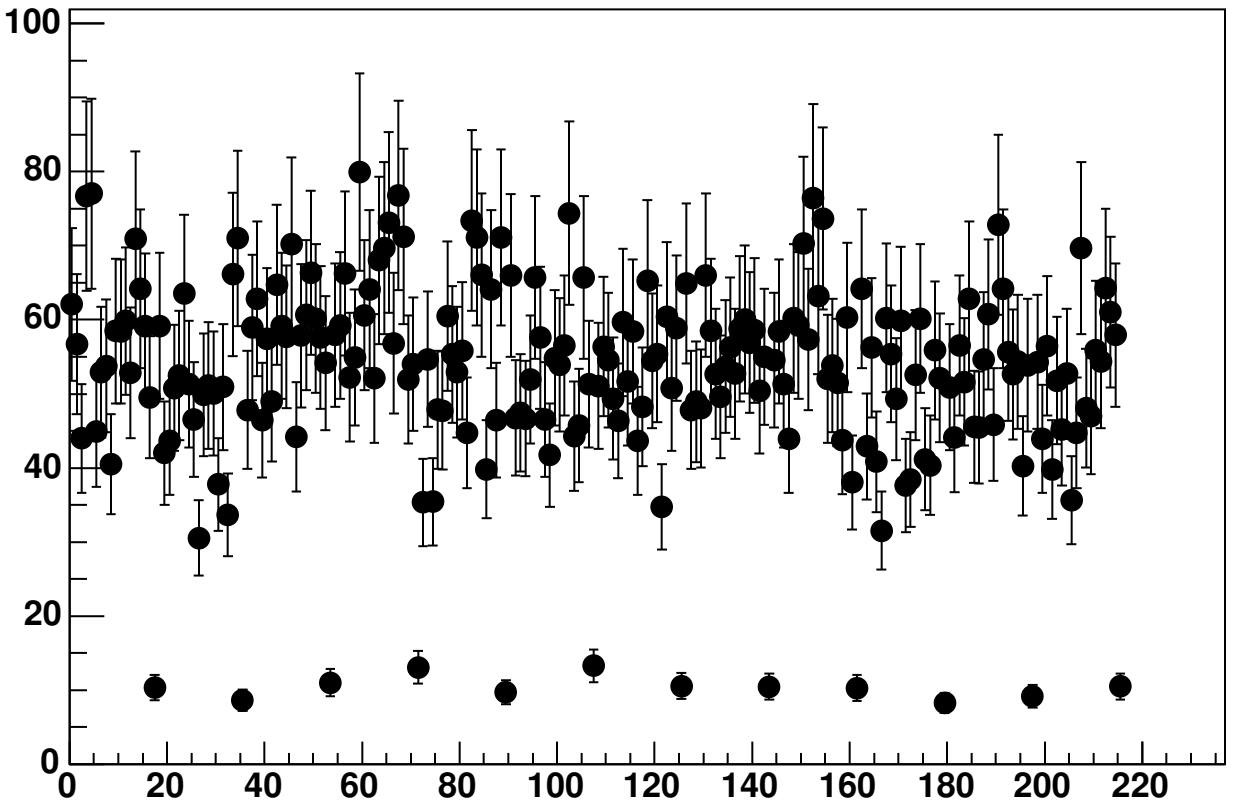
Enable 3, Hold=30, DAC=1050, ADC Noise vs 18*Chip+Chan



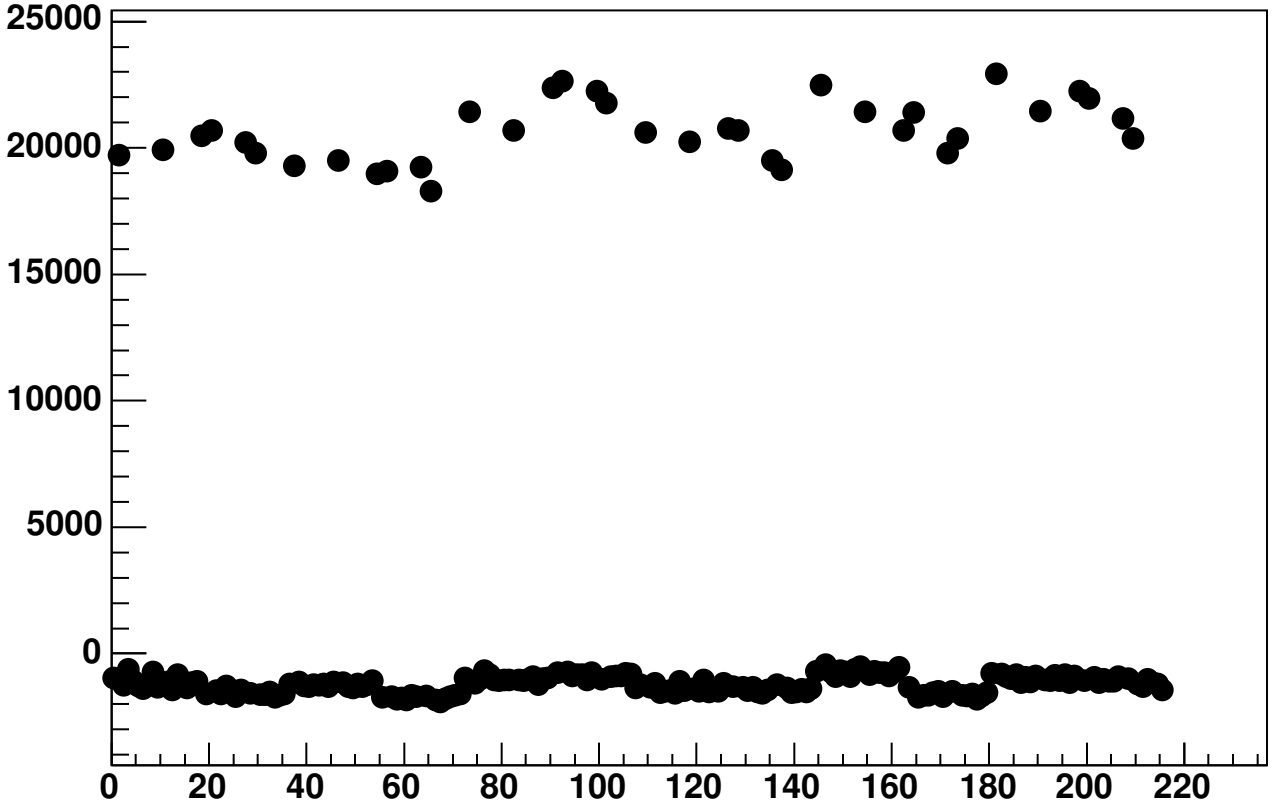
Enable 3, Hold=30, DAC=1100, ADC Mean vs 18*Chip+Chan



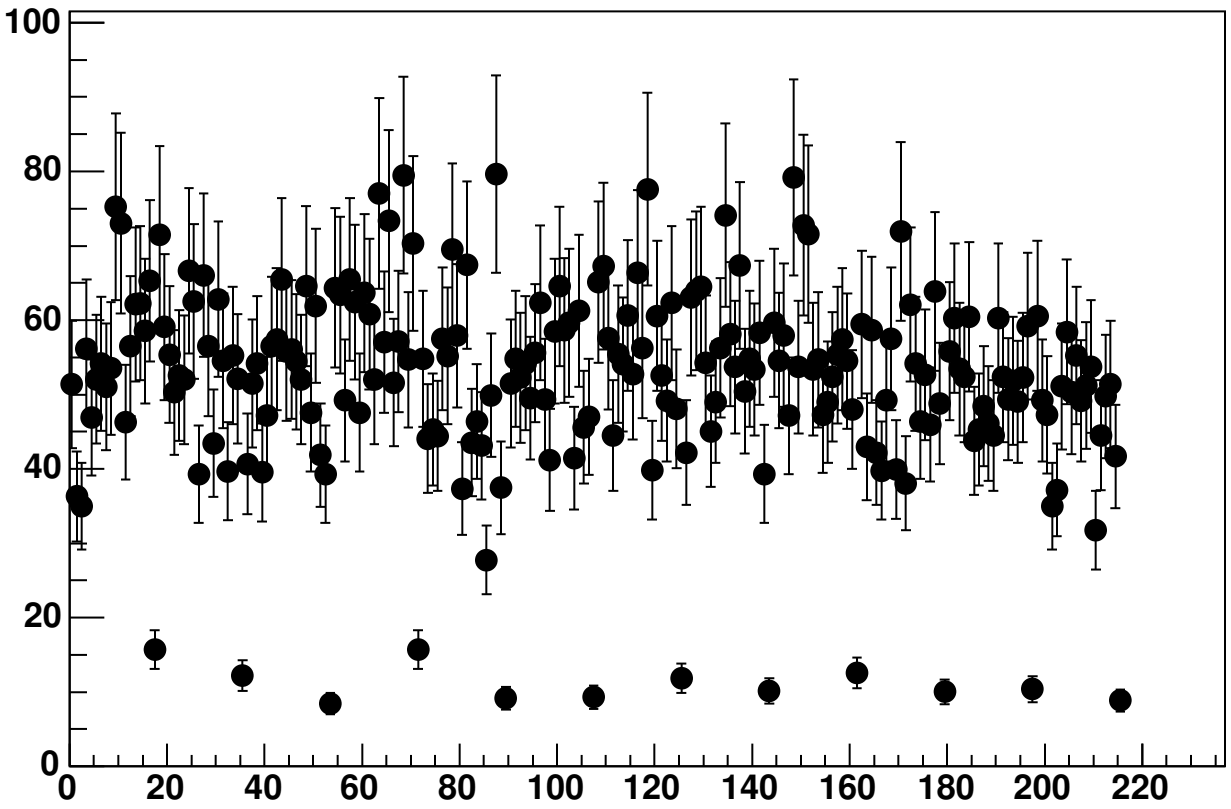
Enable 3, Hold=30, DAC=1100, ADC Noise vs 18*Chip+Chan



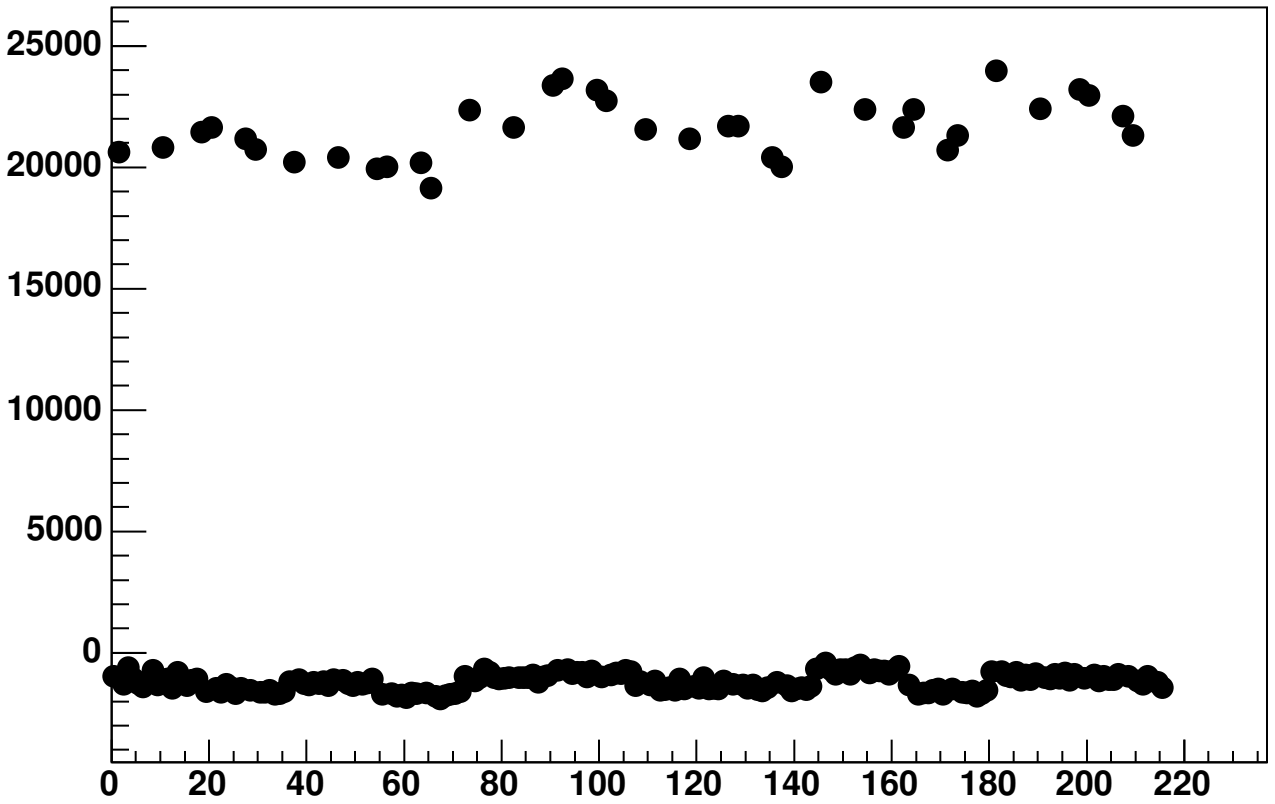
Enable 3, Hold=30, DAC=1150, ADC Mean vs 18*Chip+Chan



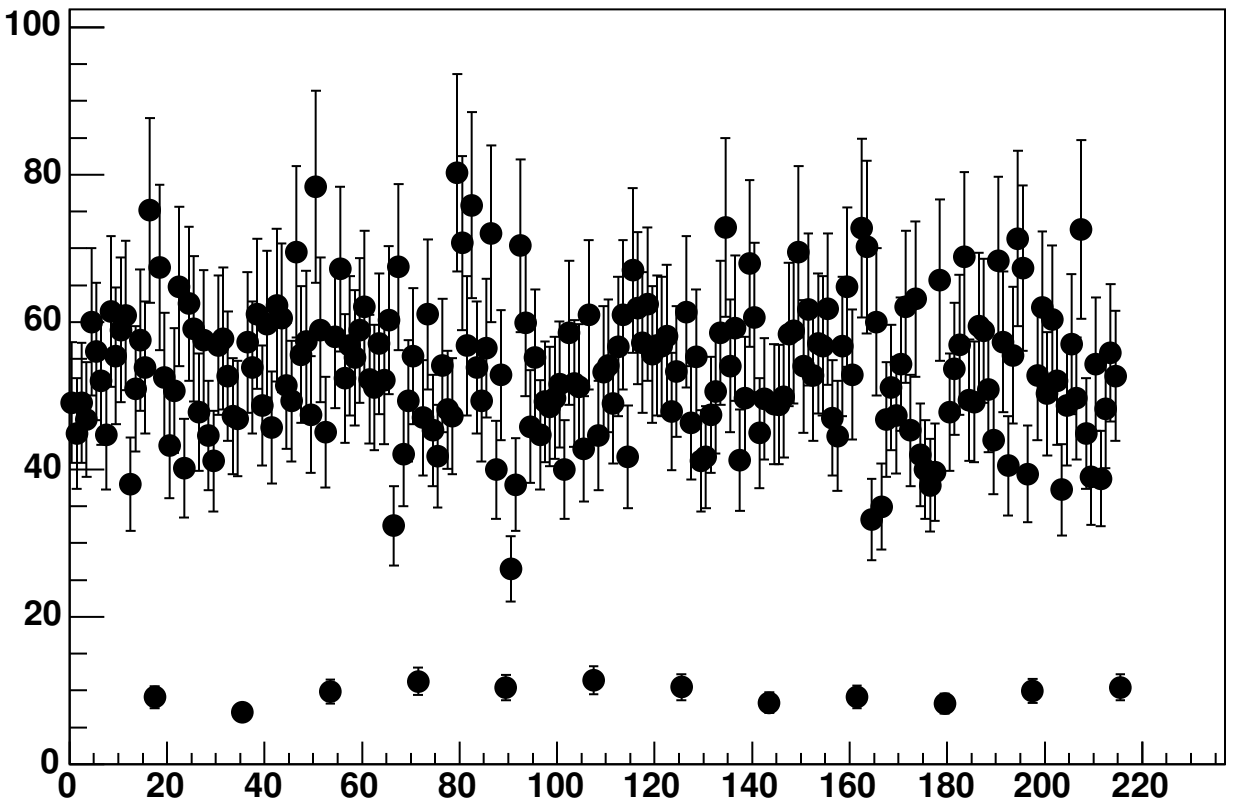
Enable 3, Hold=30, DAC=1150, ADC Noise vs 18*Chip+Chan



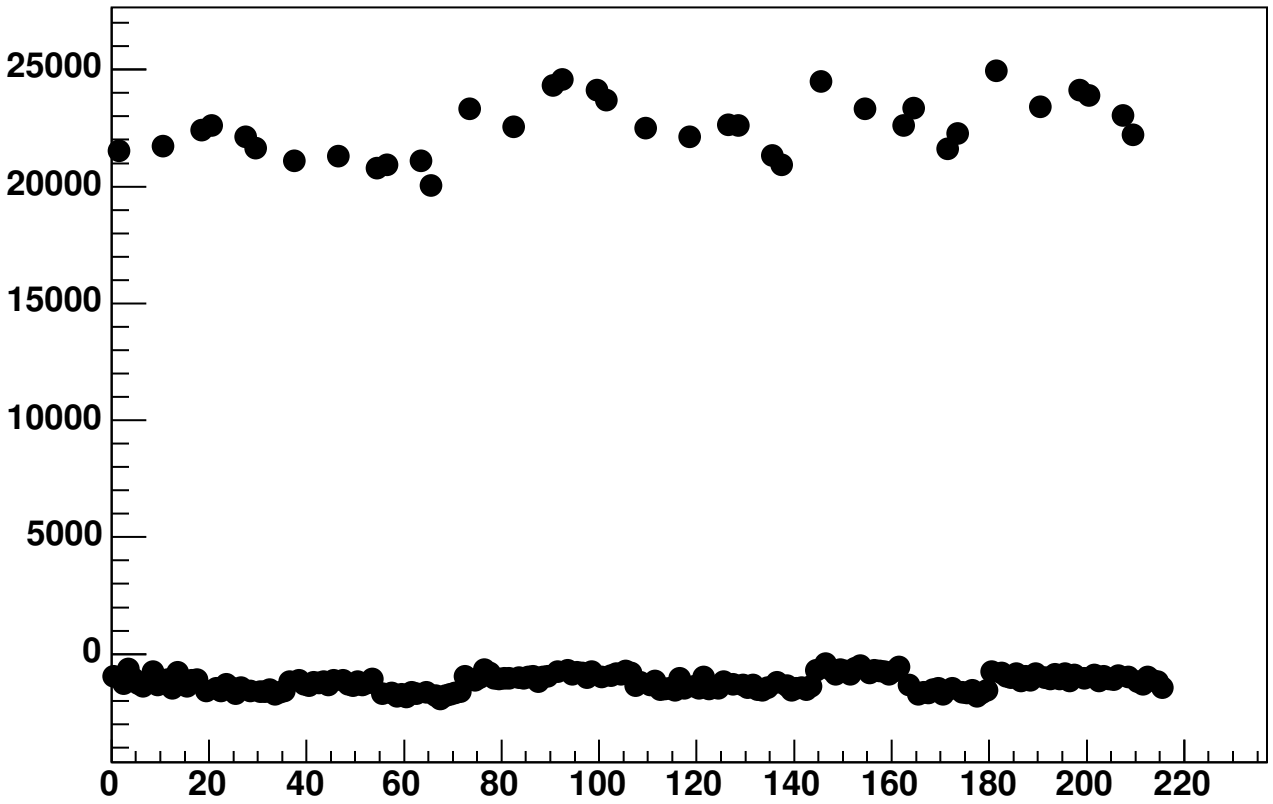
Enable 3, Hold=30, DAC=1200, ADC Mean vs 18*Chip+Chan



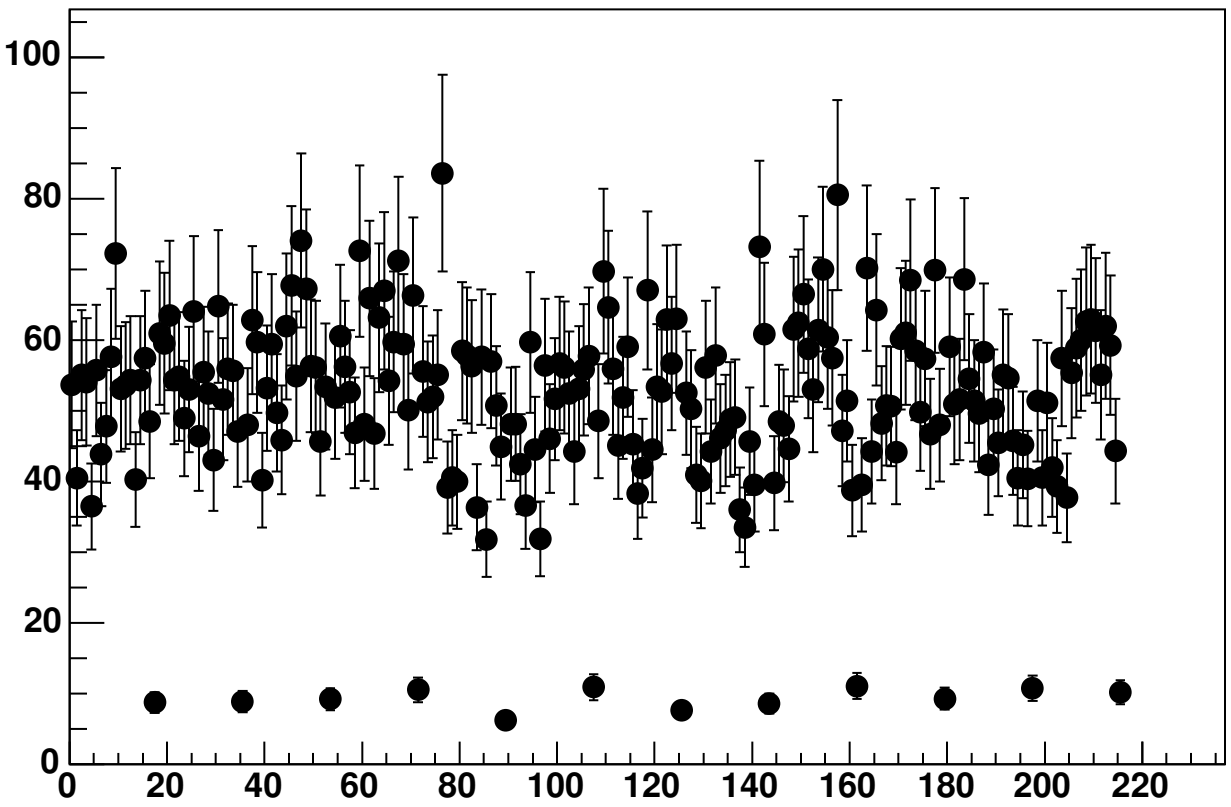
Enable 3, Hold=30, DAC=1200, ADC Noise vs 18*Chip+Chan



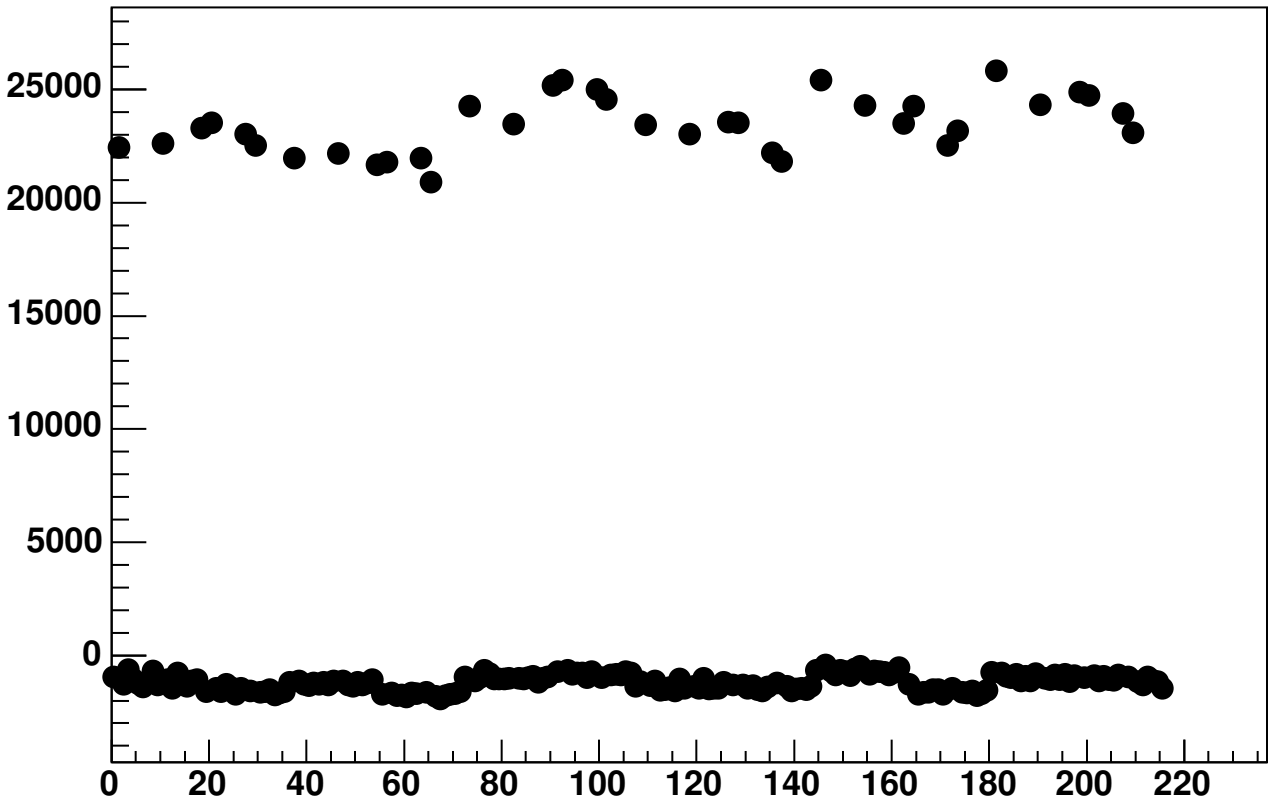
Enable 3, Hold=30, DAC=1250, ADC Mean vs 18*Chip+Chan



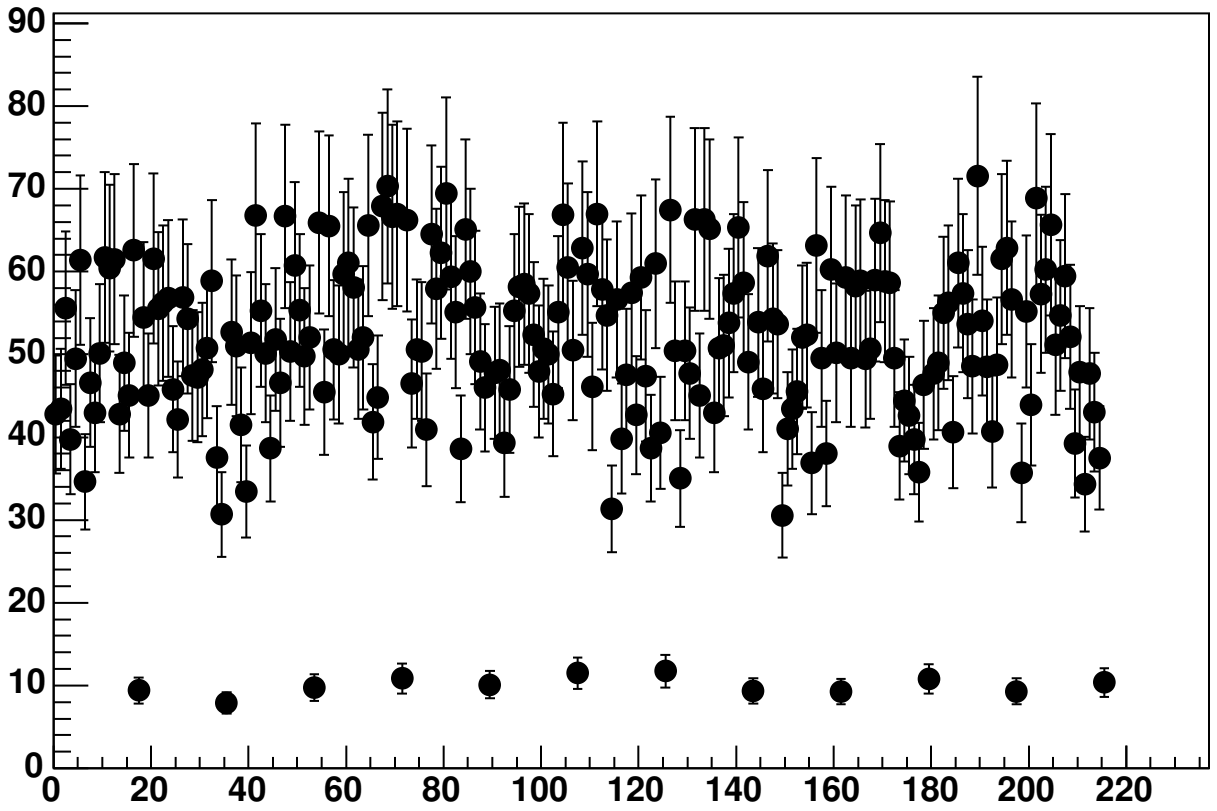
Enable 3, Hold=30, DAC=1250, ADC Noise vs 18*Chip+Chan



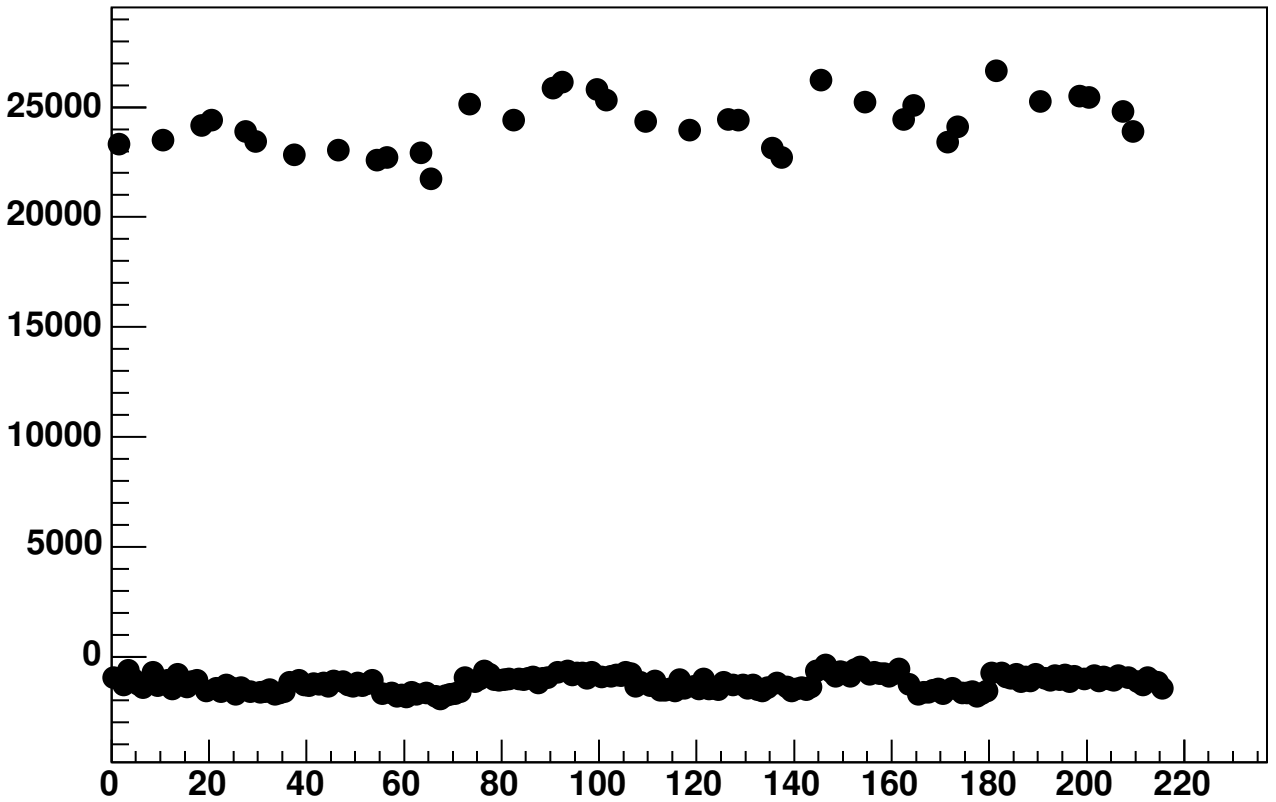
Enable 3, Hold=30, DAC=1300, ADC Mean vs 18*Chip+Chan



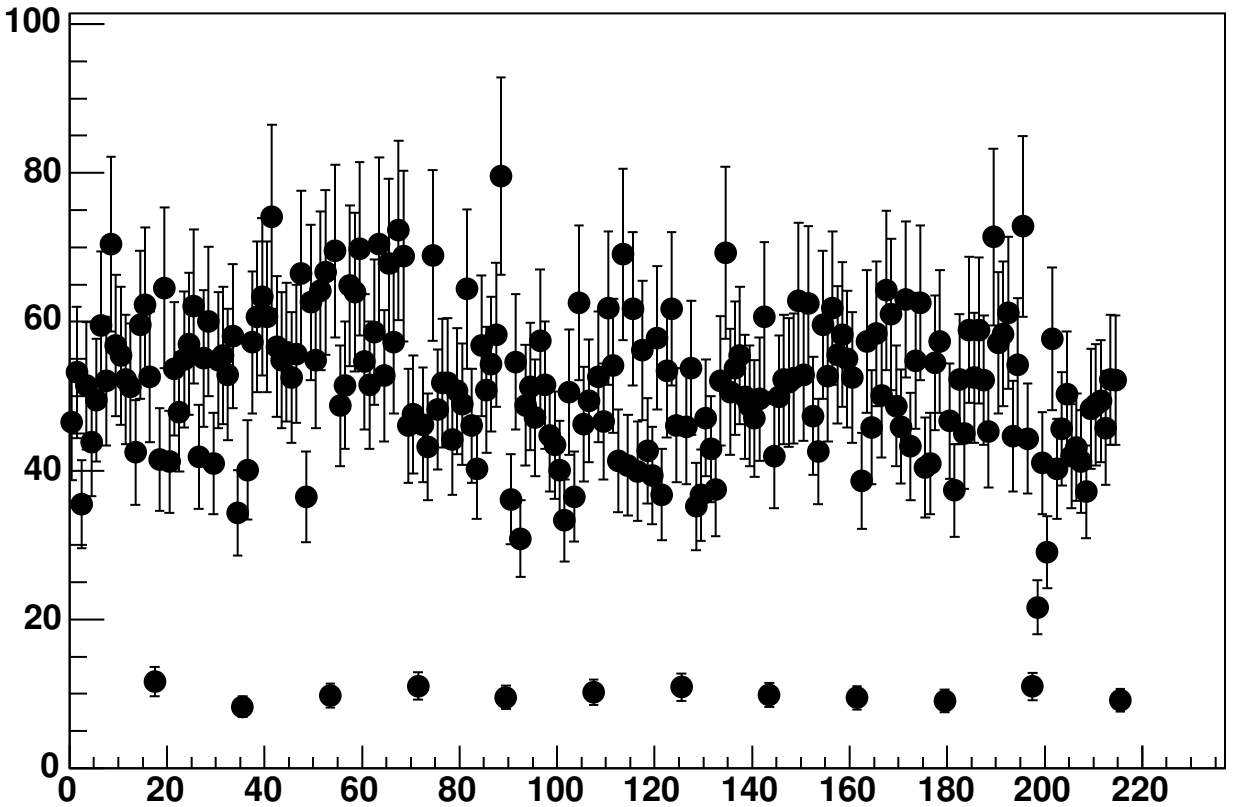
Enable 3, Hold=30, DAC=1300, ADC Noise vs 18*Chip+Chan



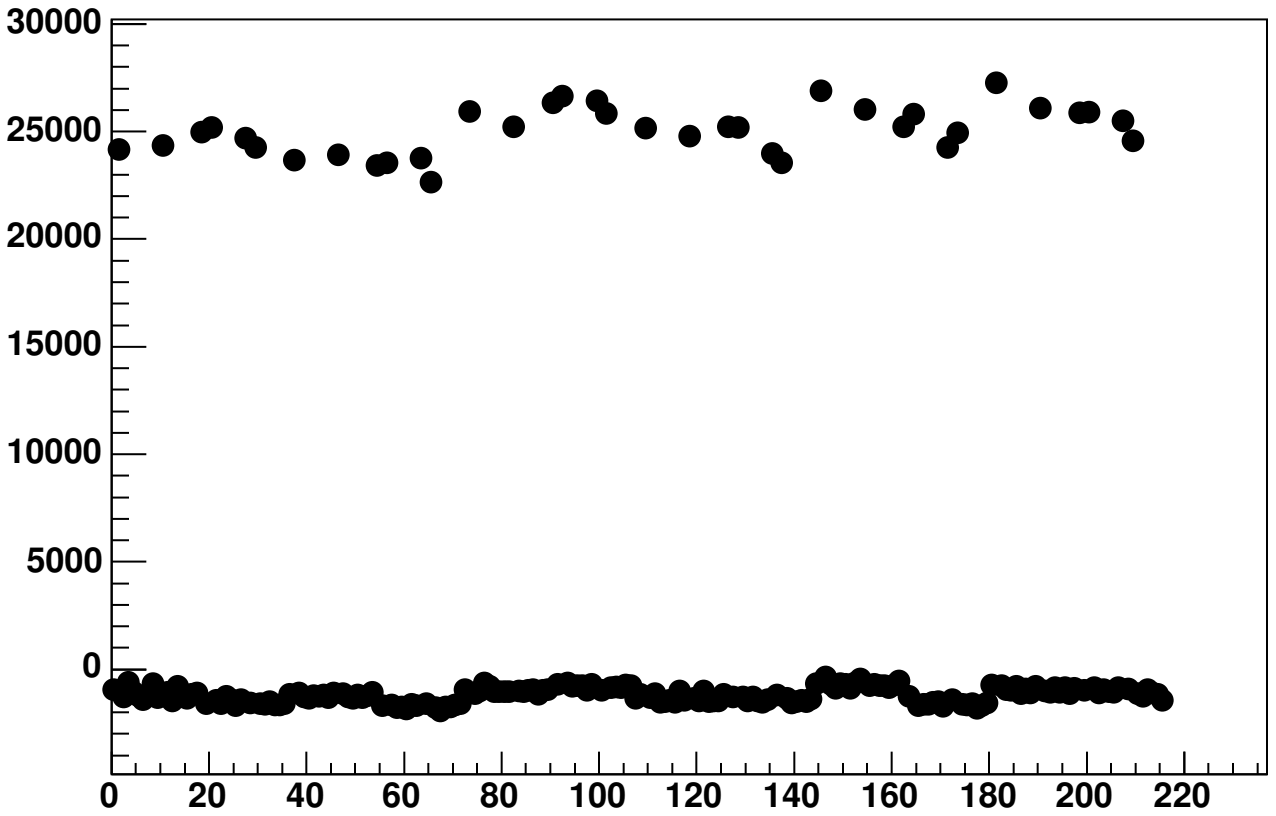
Enable 3, Hold=30, DAC=1350, ADC Mean vs 18*Chip+Chan



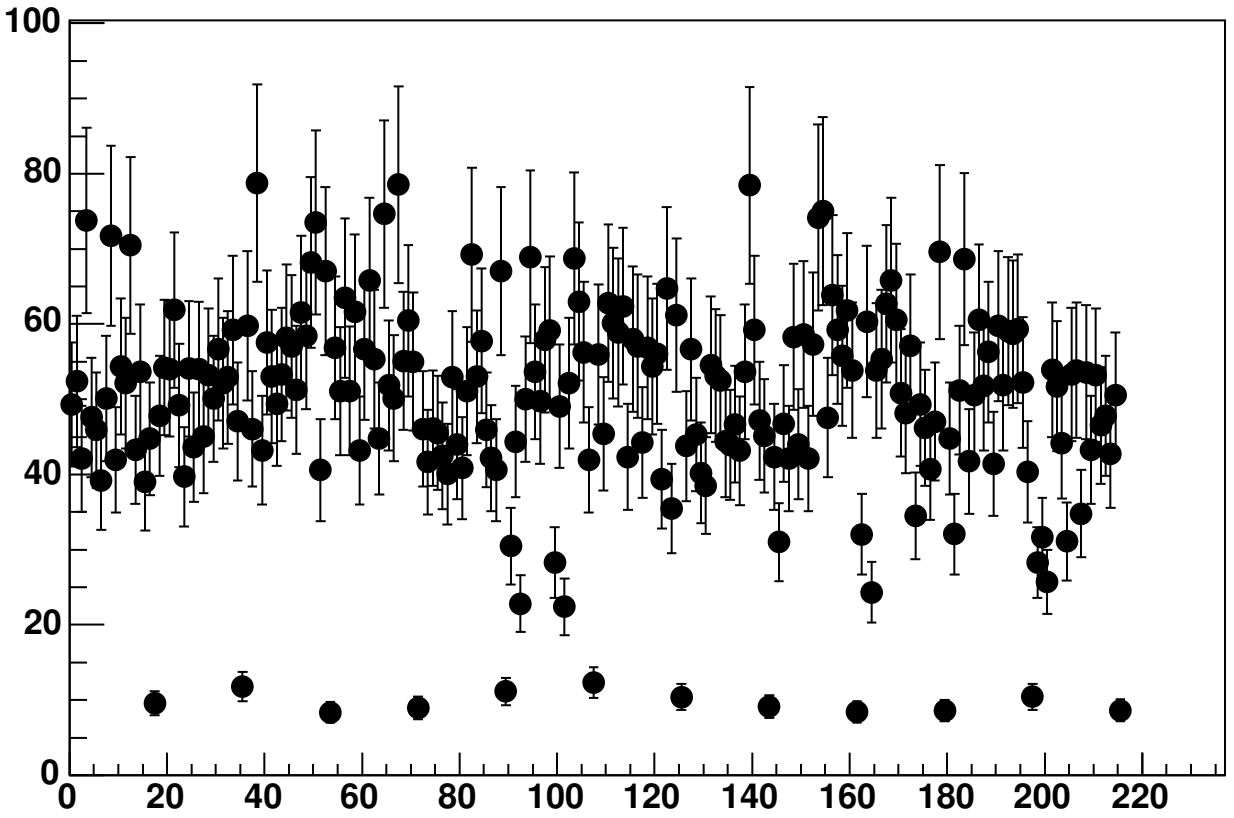
Enable 3, Hold=30, DAC=1350, ADC Noise vs 18*Chip+Chan



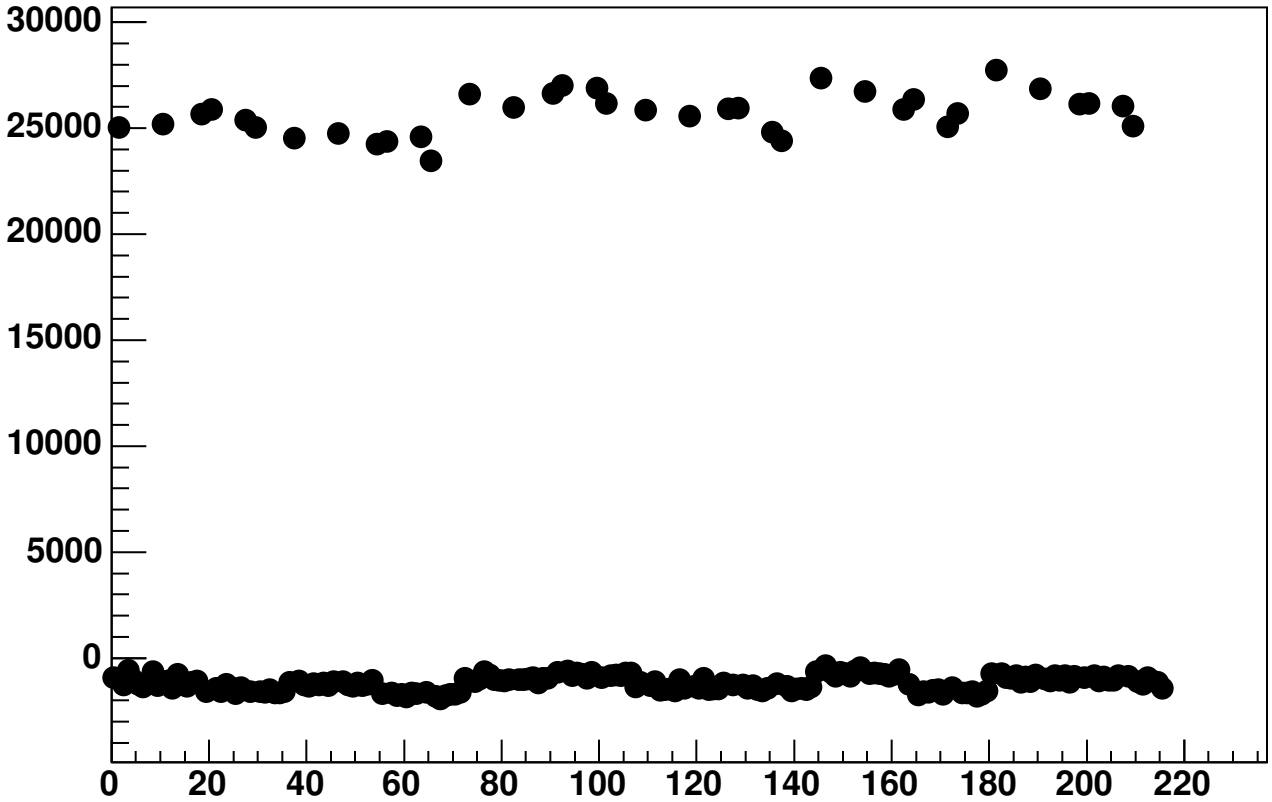
Enable 3, Hold=30, DAC=1400, ADC Mean vs 18*Chip+Chan



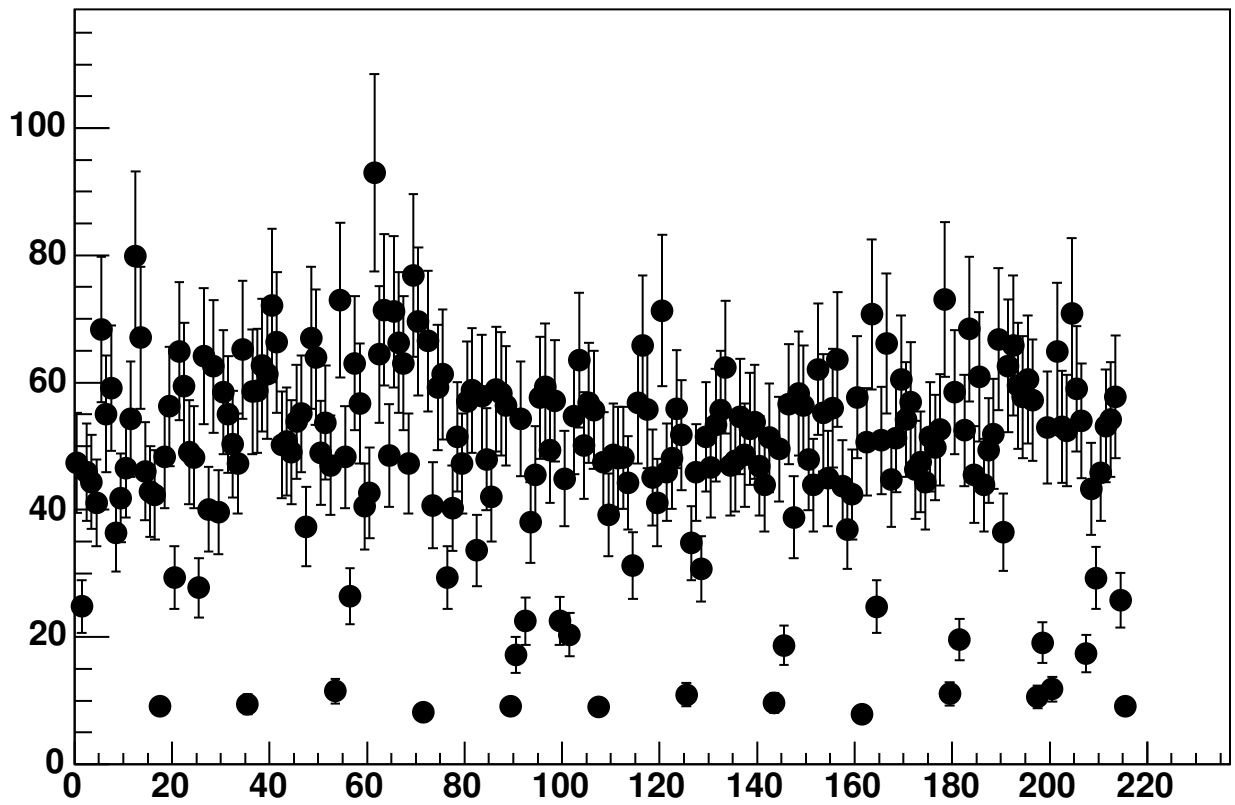
Enable 3, Hold=30, DAC=1400, ADC Noise vs 18*Chip+Chan



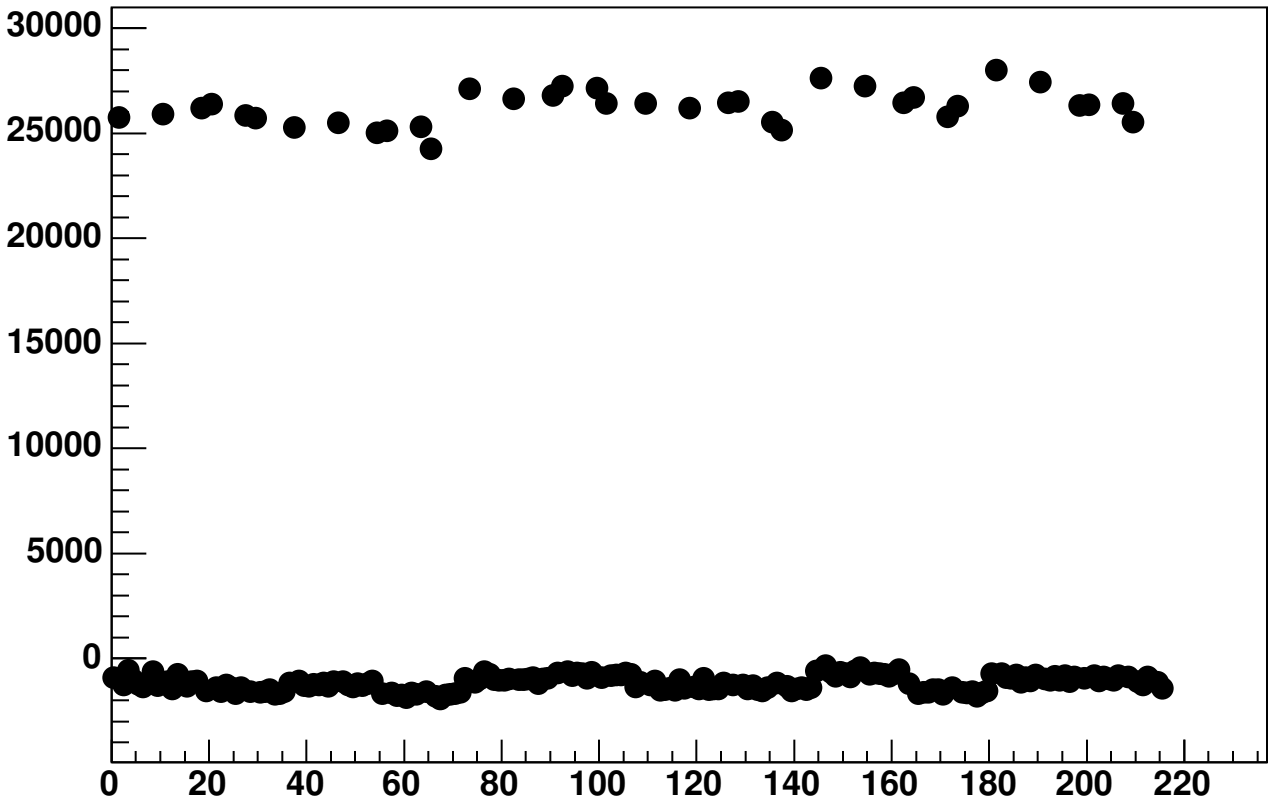
Enable 3, Hold=30, DAC=1450, ADC Mean vs 18*Chip+Chan



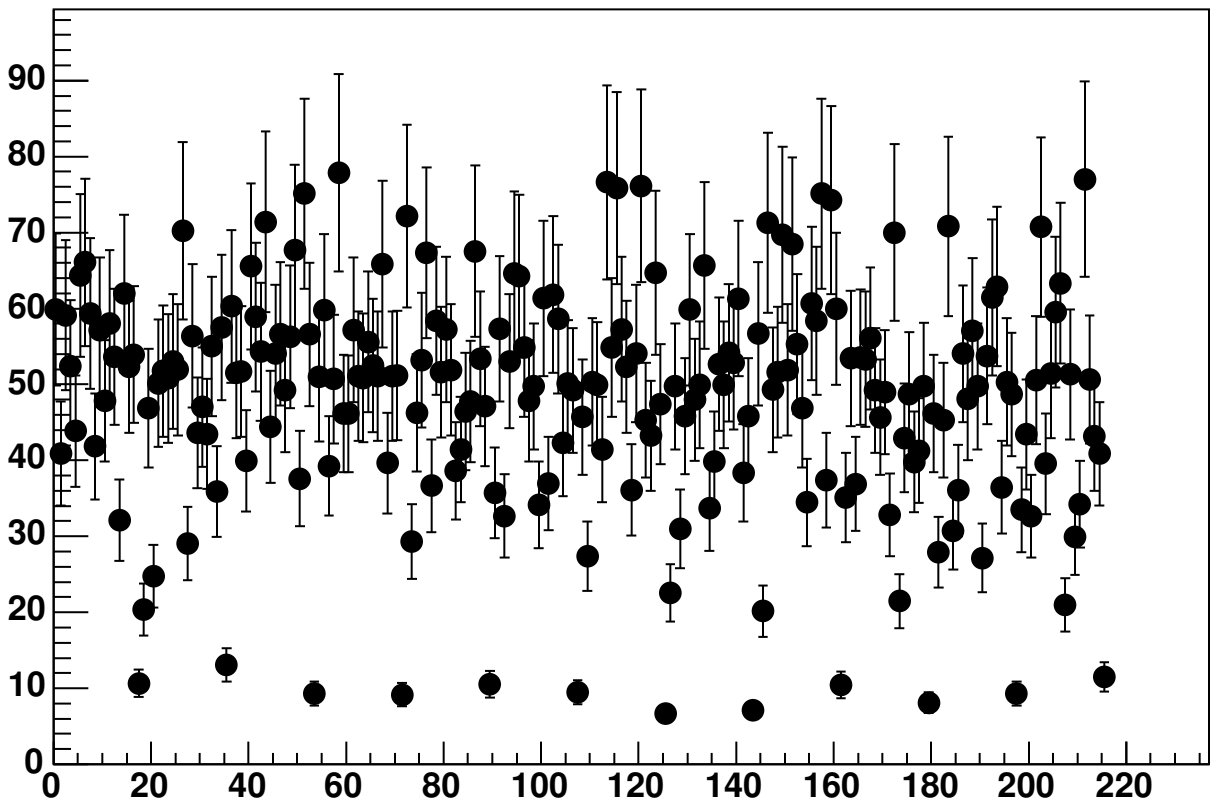
Enable 3, Hold=30, DAC=1450, ADC Noise vs 18*Chip+Chan



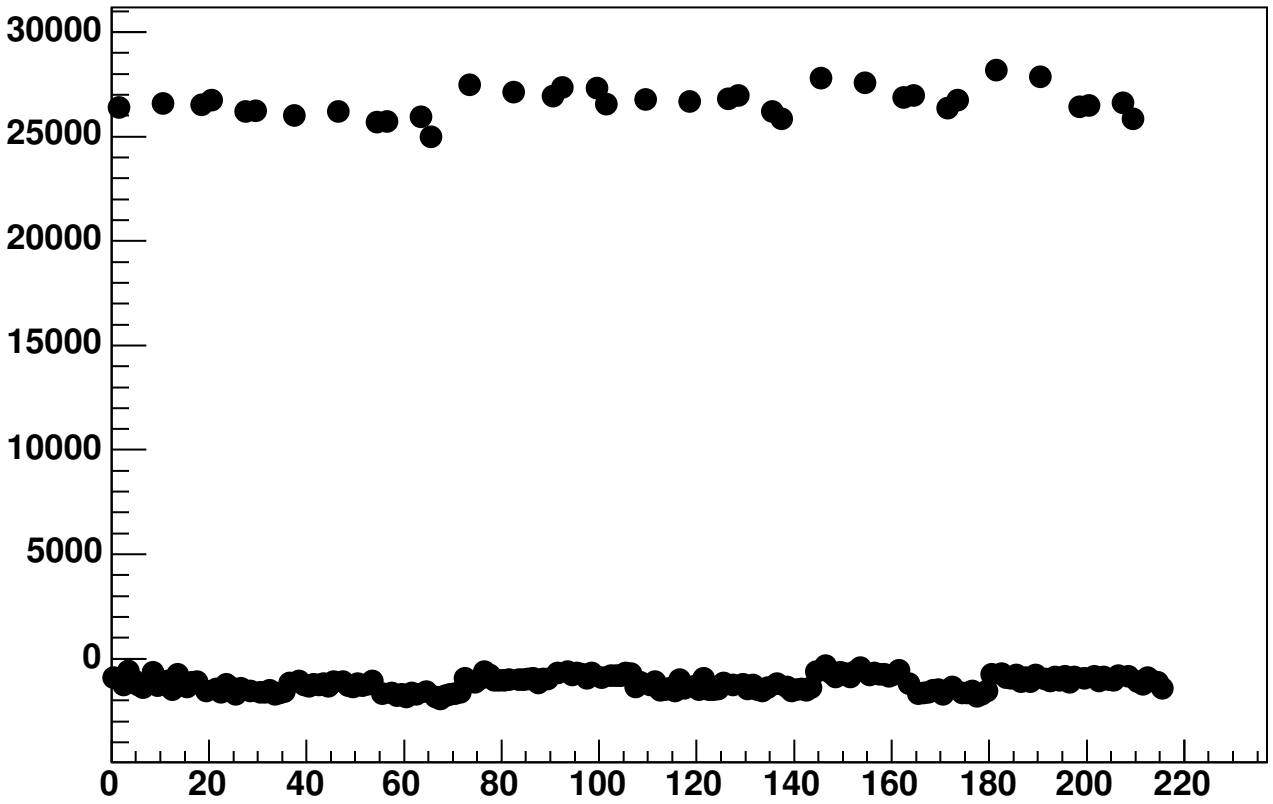
Enable 3, Hold=30, DAC=1500, ADC Mean vs 18*Chip+Chan



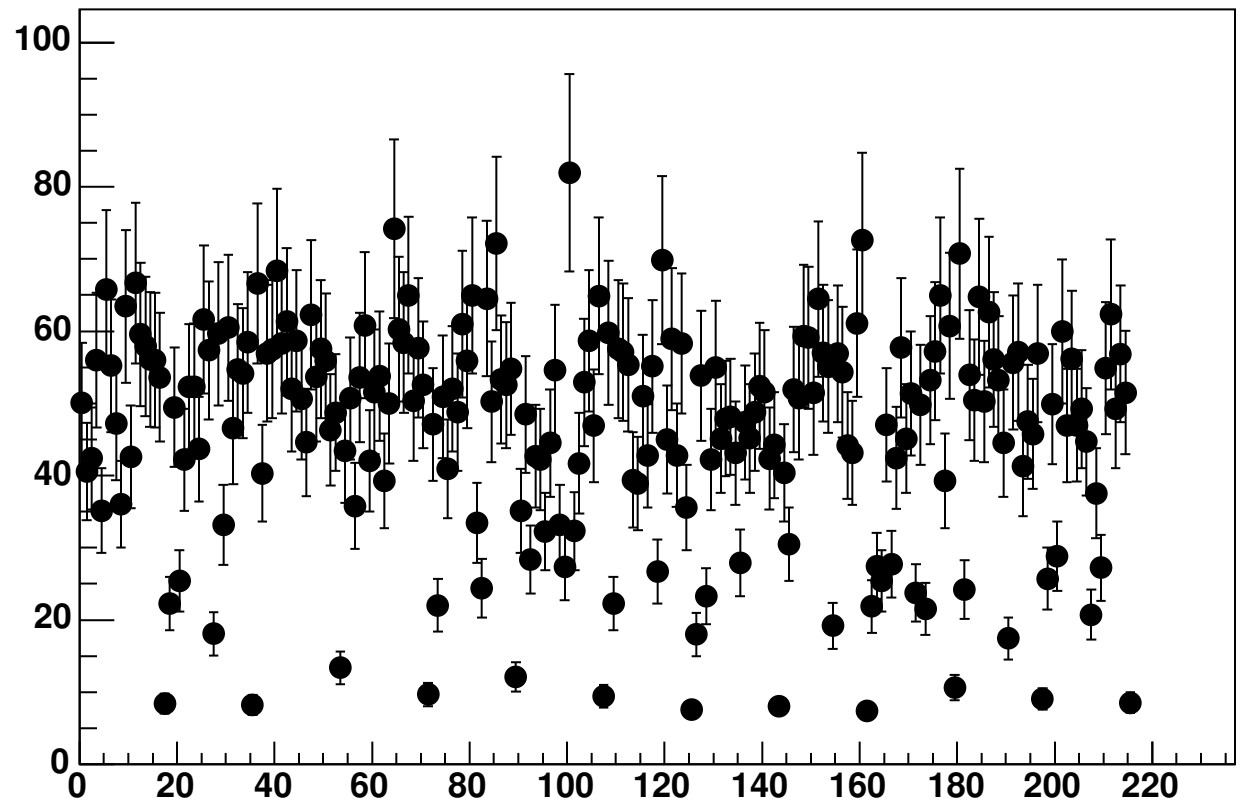
Enable 3, Hold=30, DAC=1500, ADC Noise vs 18*Chip+Chan



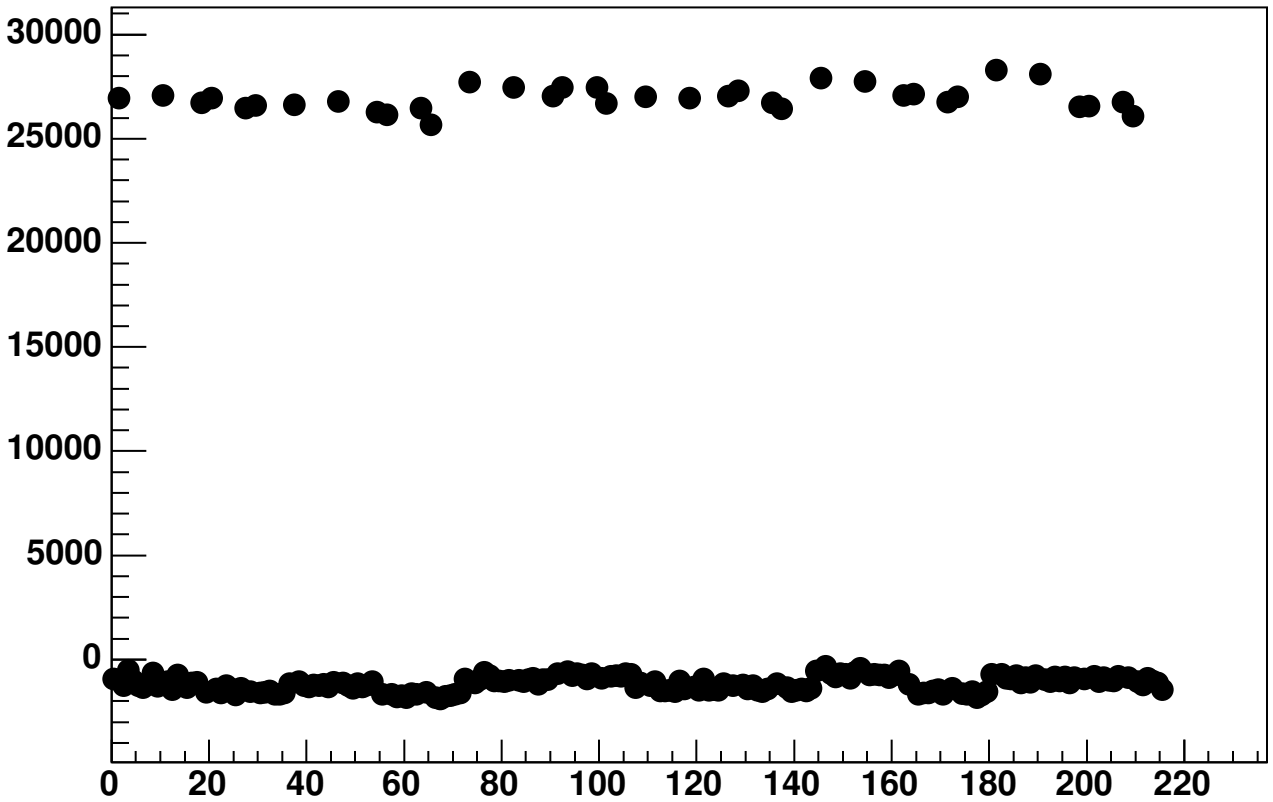
Enable 3, Hold=30, DAC=1550, ADC Mean vs 18*Chip+Chan



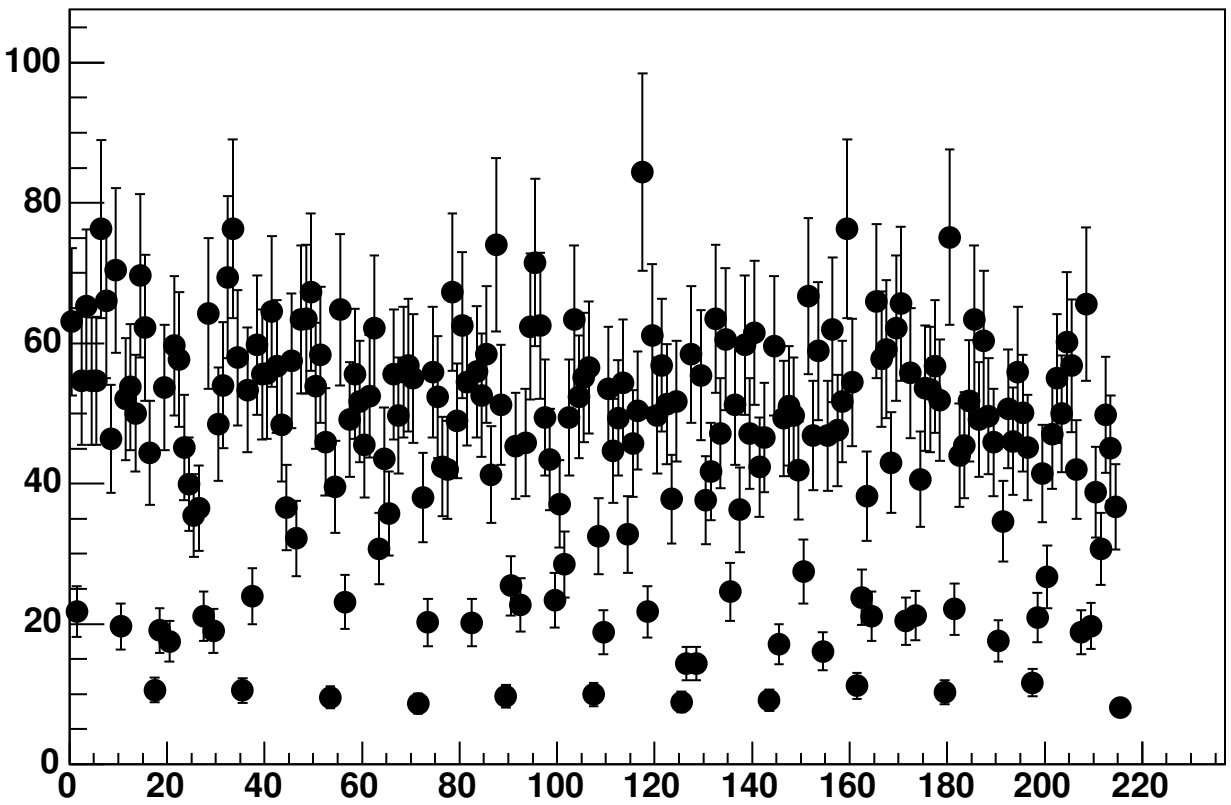
Enable 3, Hold=30, DAC=1550, ADC Noise vs 18*Chip+Chan



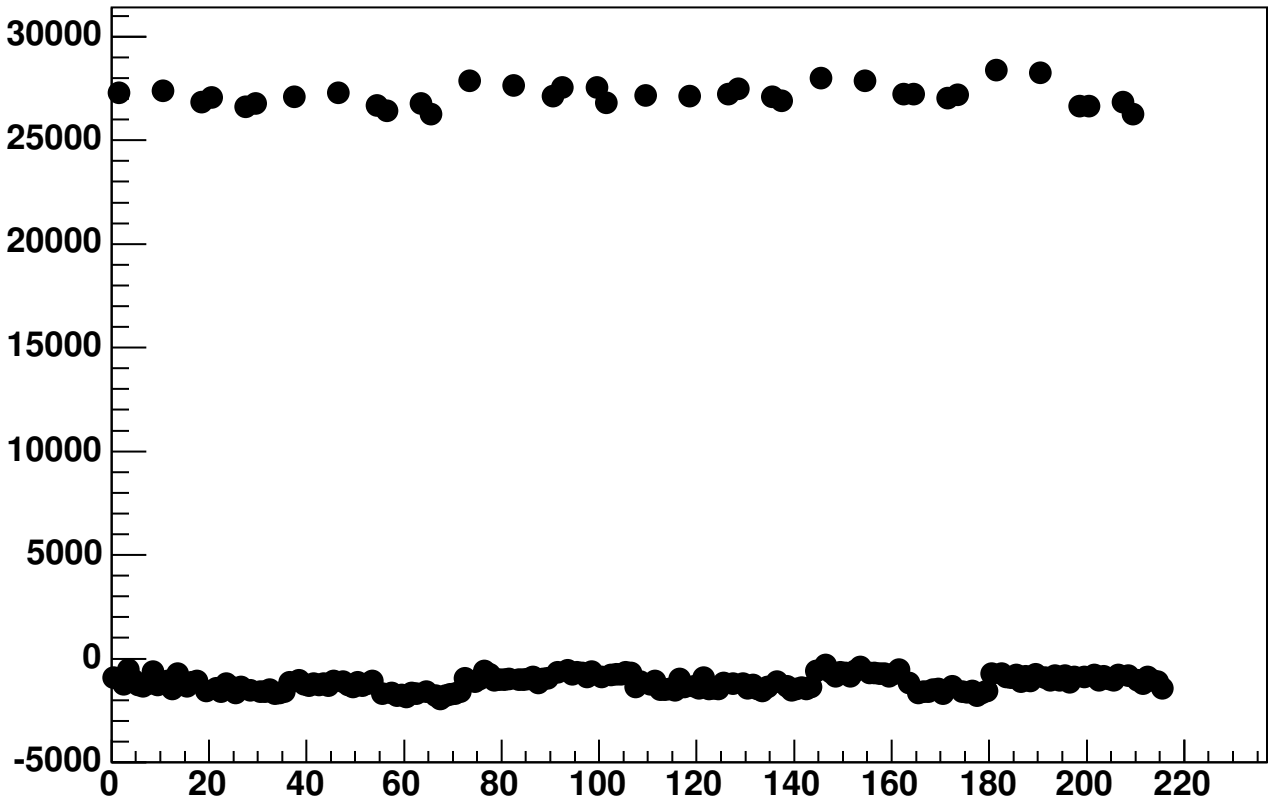
Enable 3, Hold=30, DAC=1600, ADC Mean vs 18*Chip+Chan



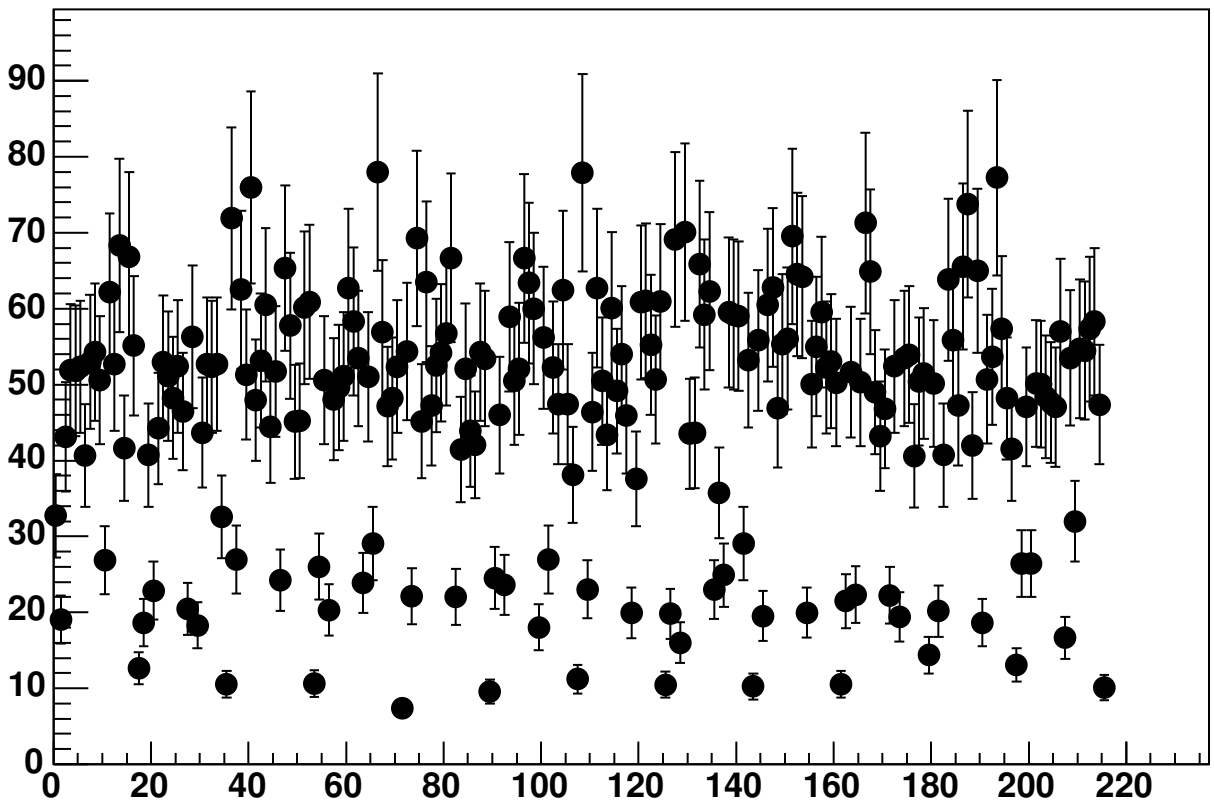
Enable 3, Hold=30, DAC=1600, ADC Noise vs 18*Chip+Chan



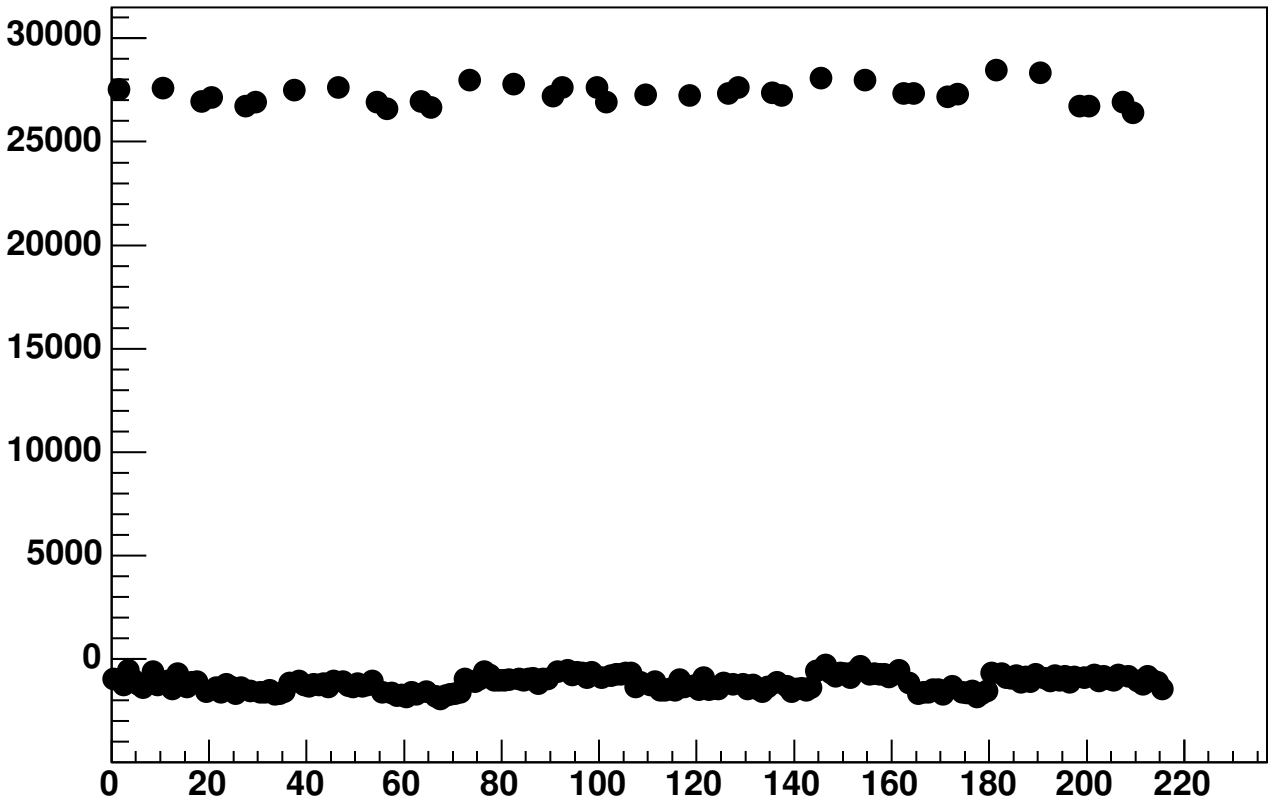
Enable 3, Hold=30, DAC=1650, ADC Mean vs 18*Chip+Chan



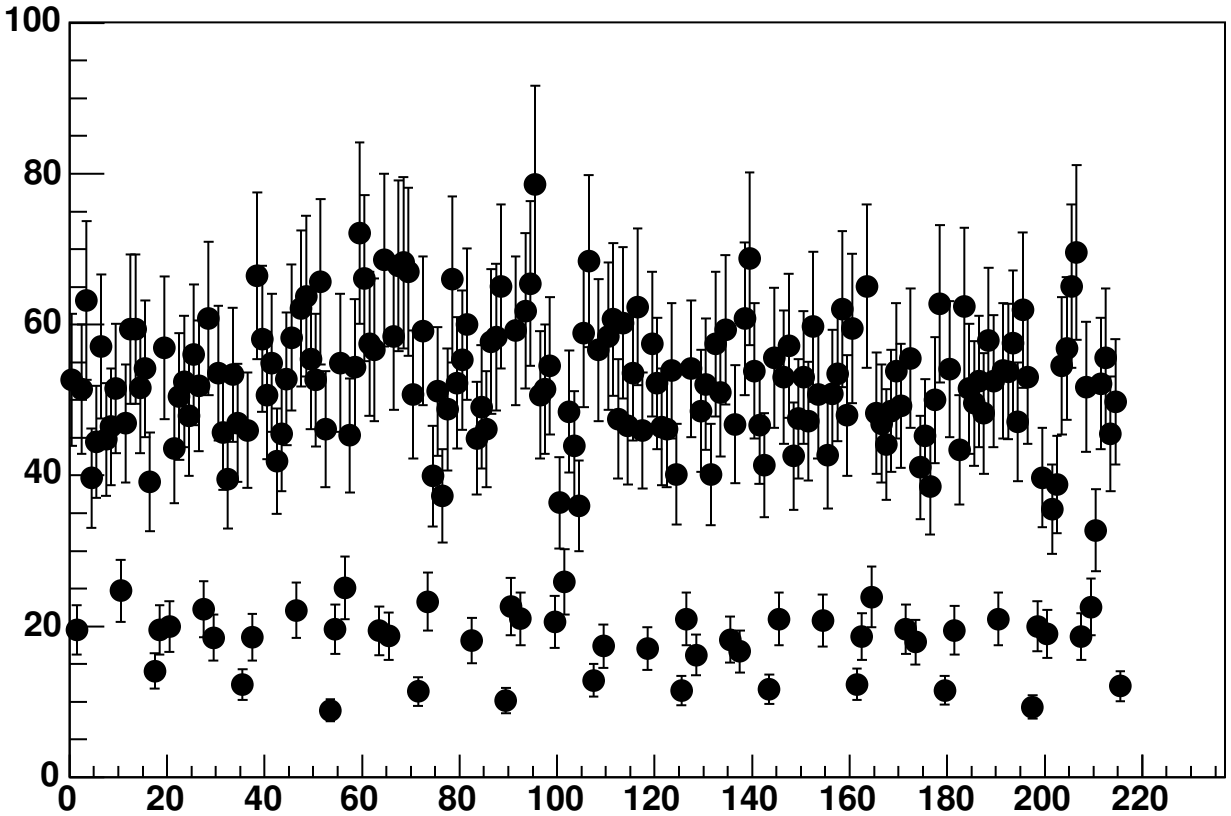
Enable 3, Hold=30, DAC=1650, ADC Noise vs 18*Chip+Chan



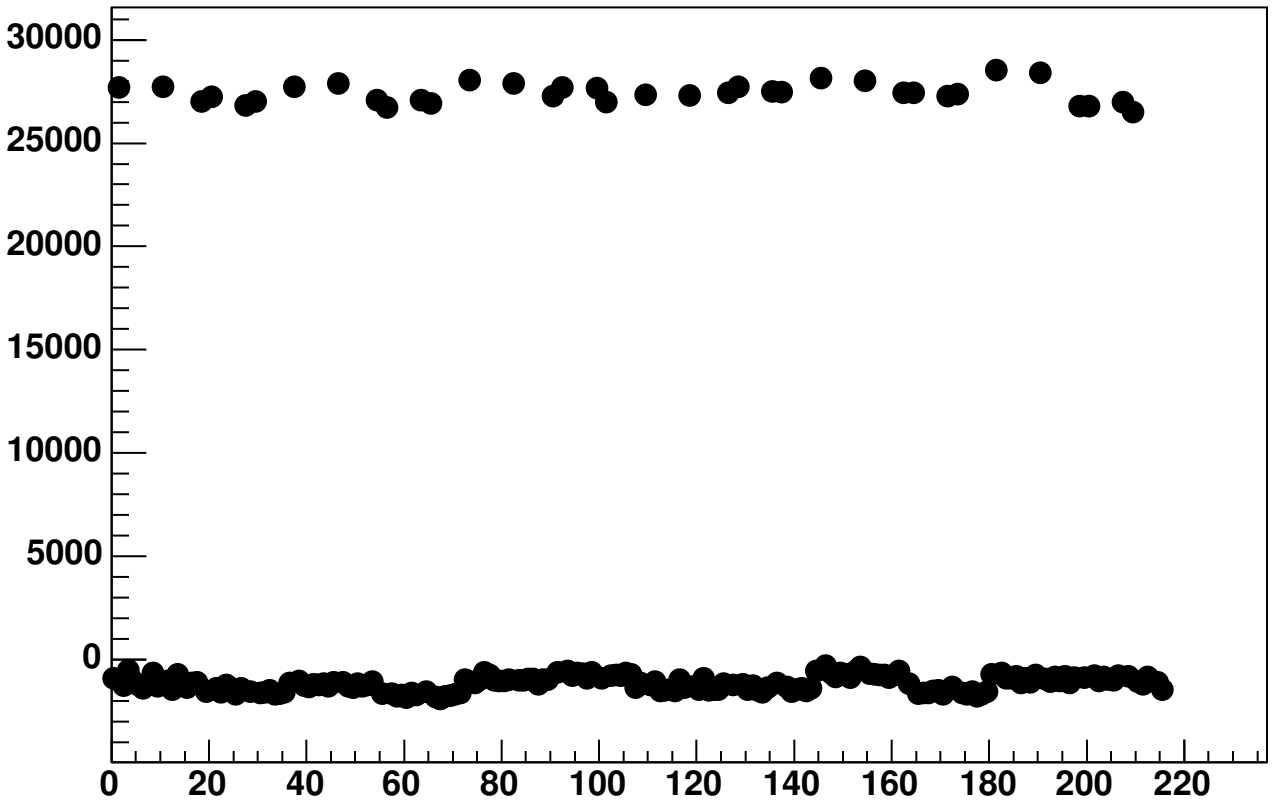
Enable 3, Hold=30, DAC=1700, ADC Mean vs 18*Chip+Chan



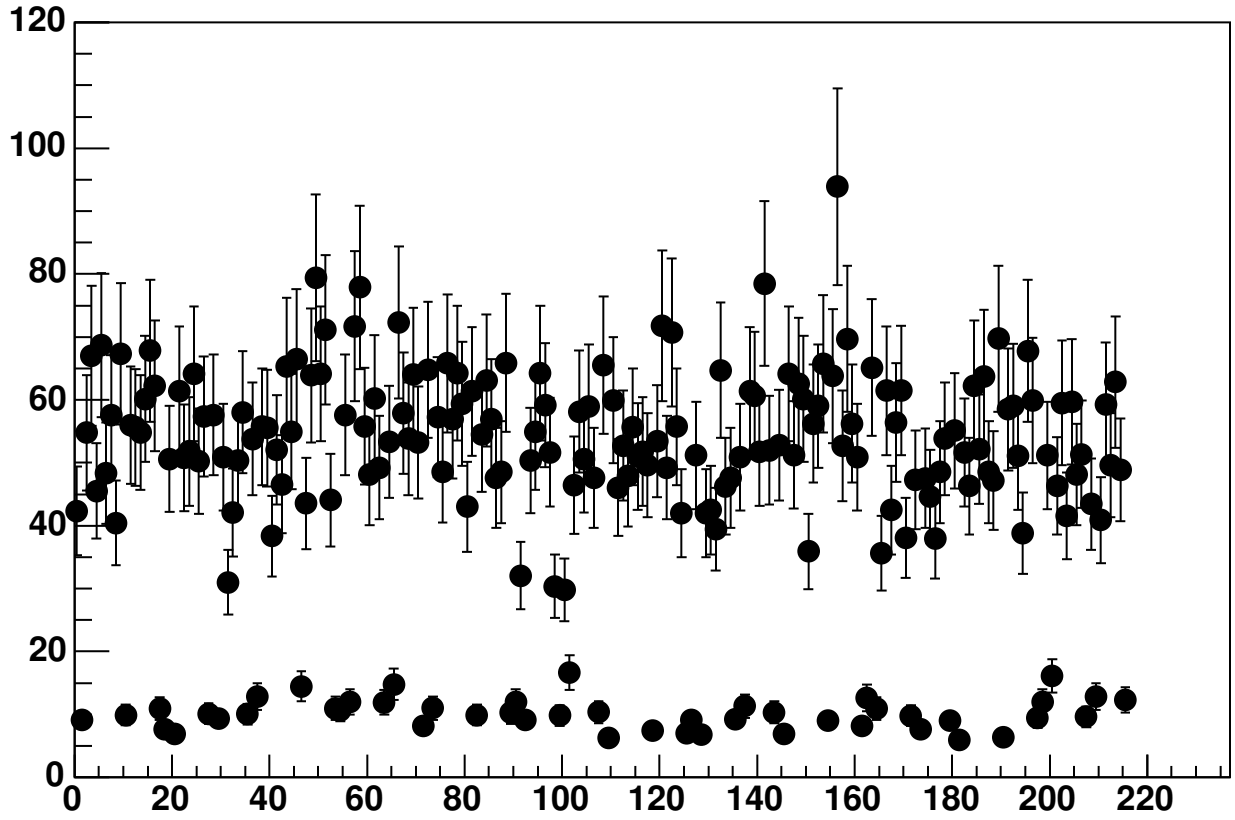
Enable 3, Hold=30, DAC=1700, ADC Noise vs 18*Chip+Chan



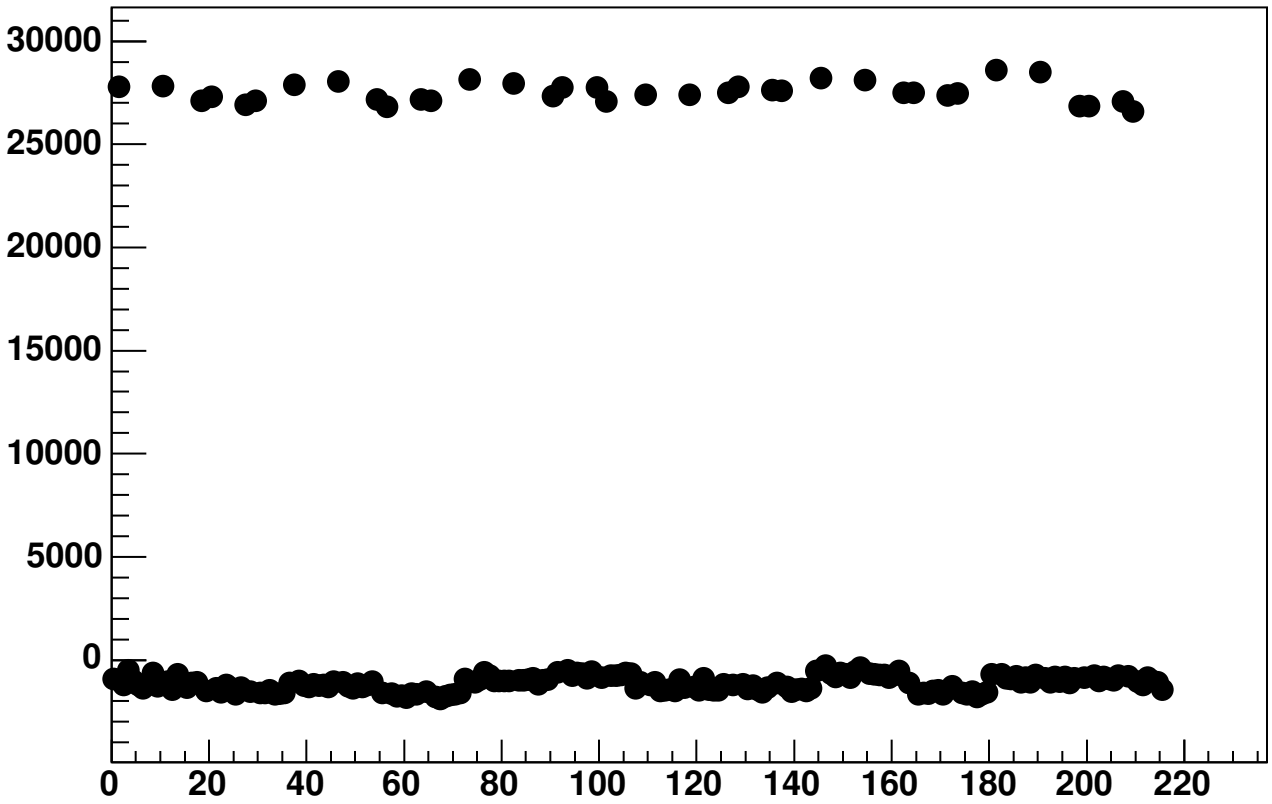
Enable 3, Hold=30, DAC=1750, ADC Mean vs 18*Chip+Chan



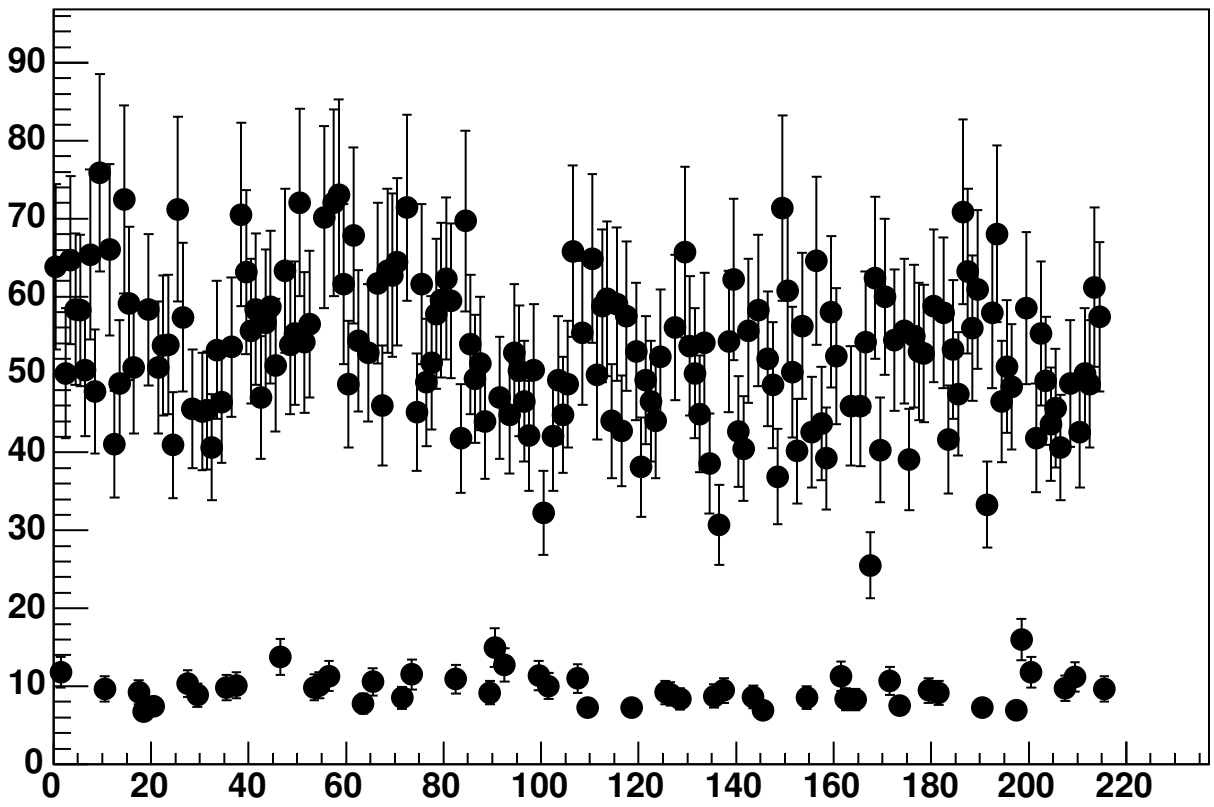
Enable 3, Hold=30, DAC=1750, ADC Noise vs 18*Chip+Chan



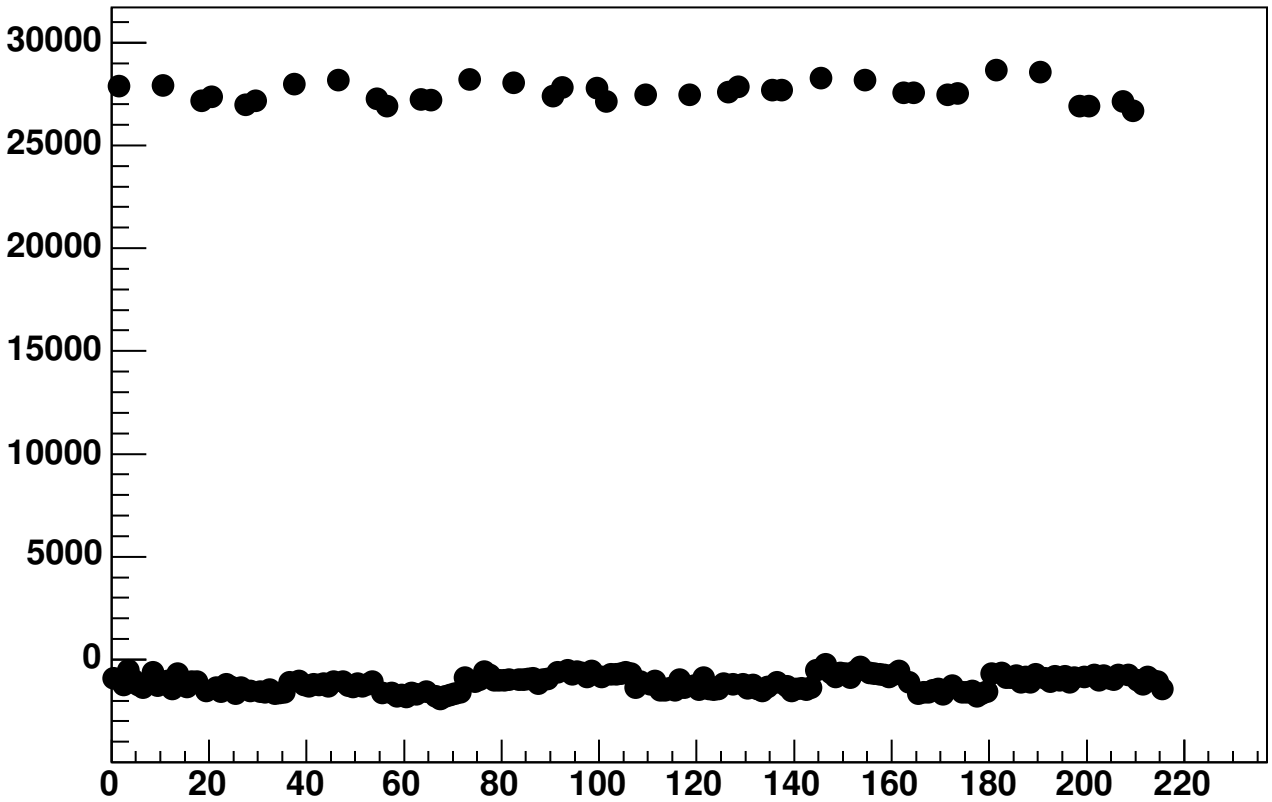
Enable 3, Hold=30, DAC=1800, ADC Mean vs 18*Chip+Chan



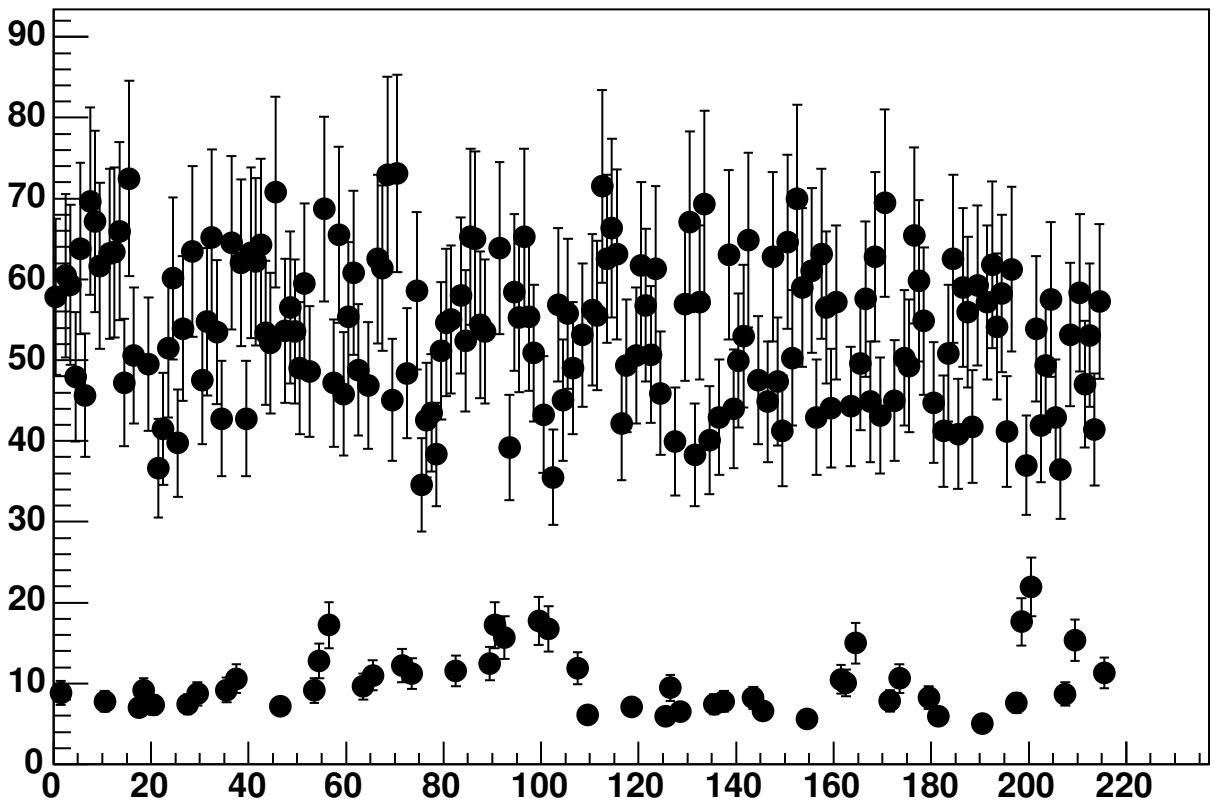
Enable 3, Hold=30, DAC=1800, ADC Noise vs 18*Chip+Chan



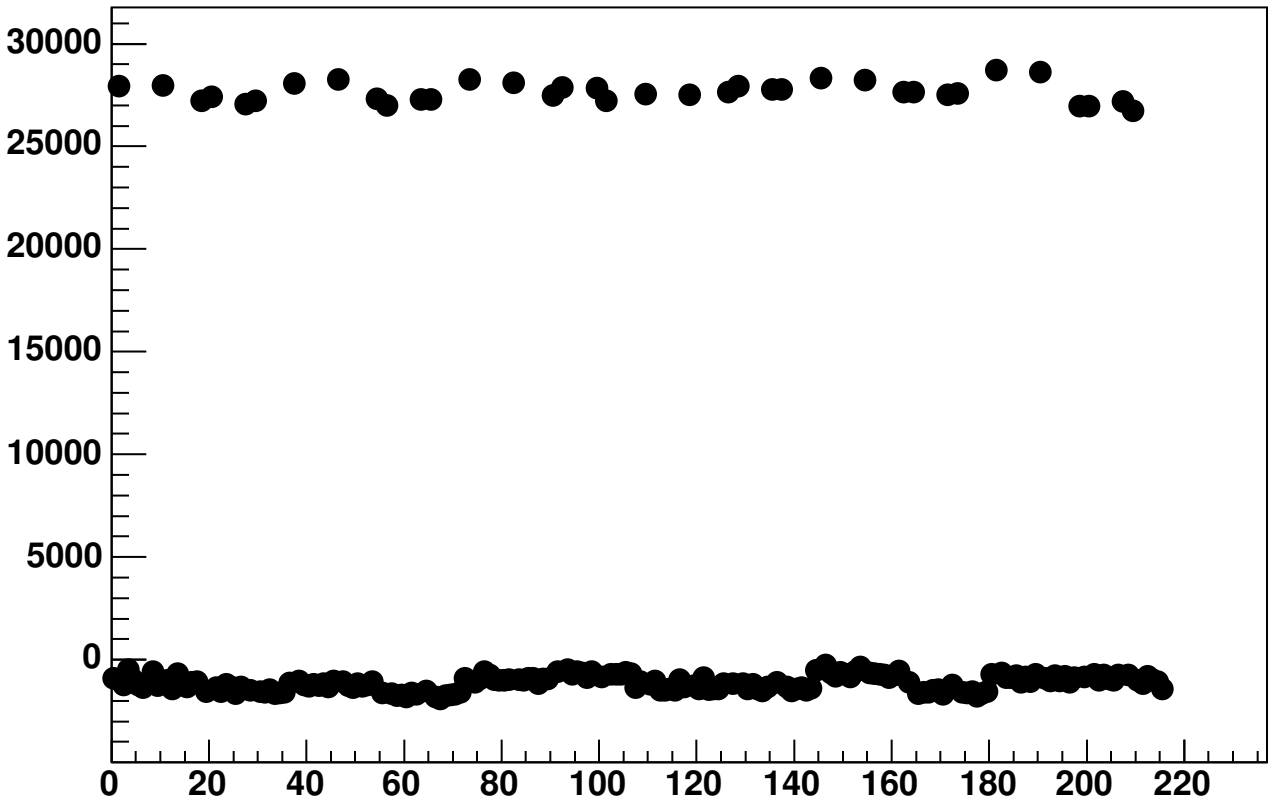
Enable 3, Hold=30, DAC=1850, ADC Mean vs 18*Chip+Chan



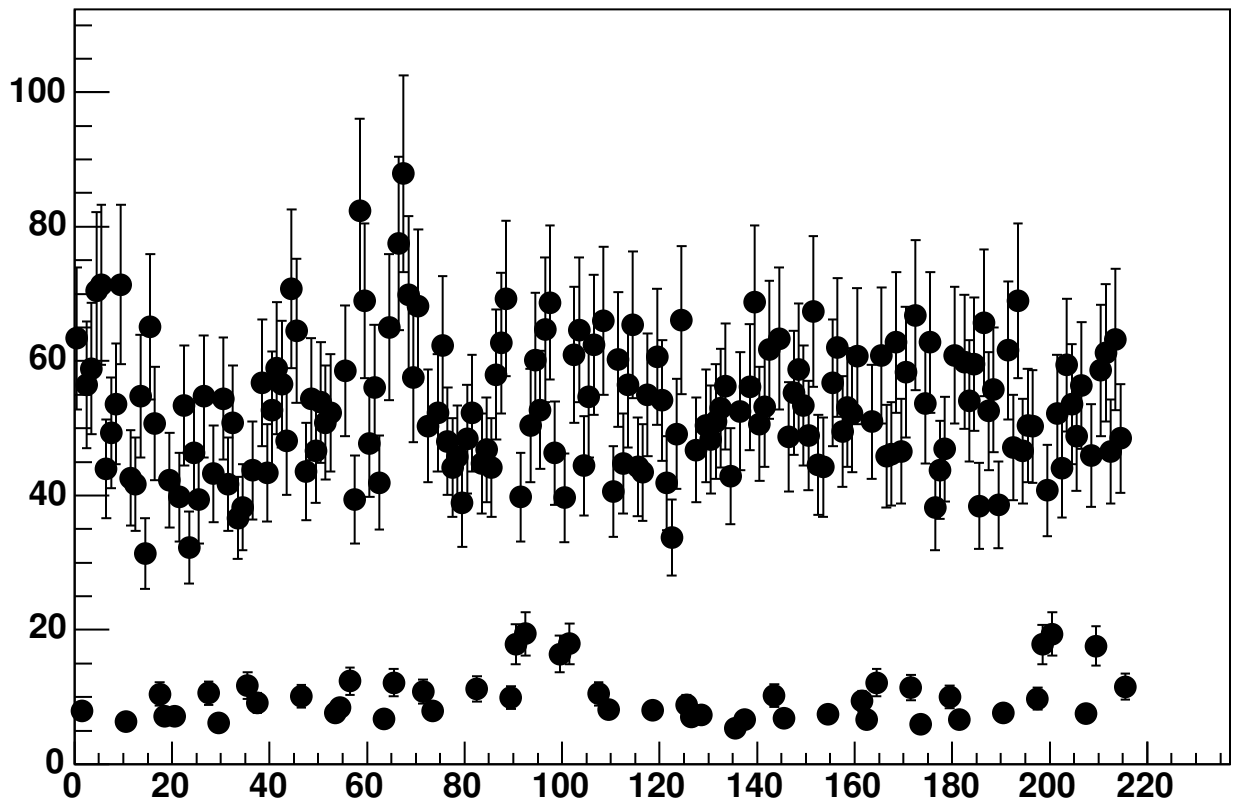
Enable 3, Hold=30, DAC=1850, ADC Noise vs 18*Chip+Chan



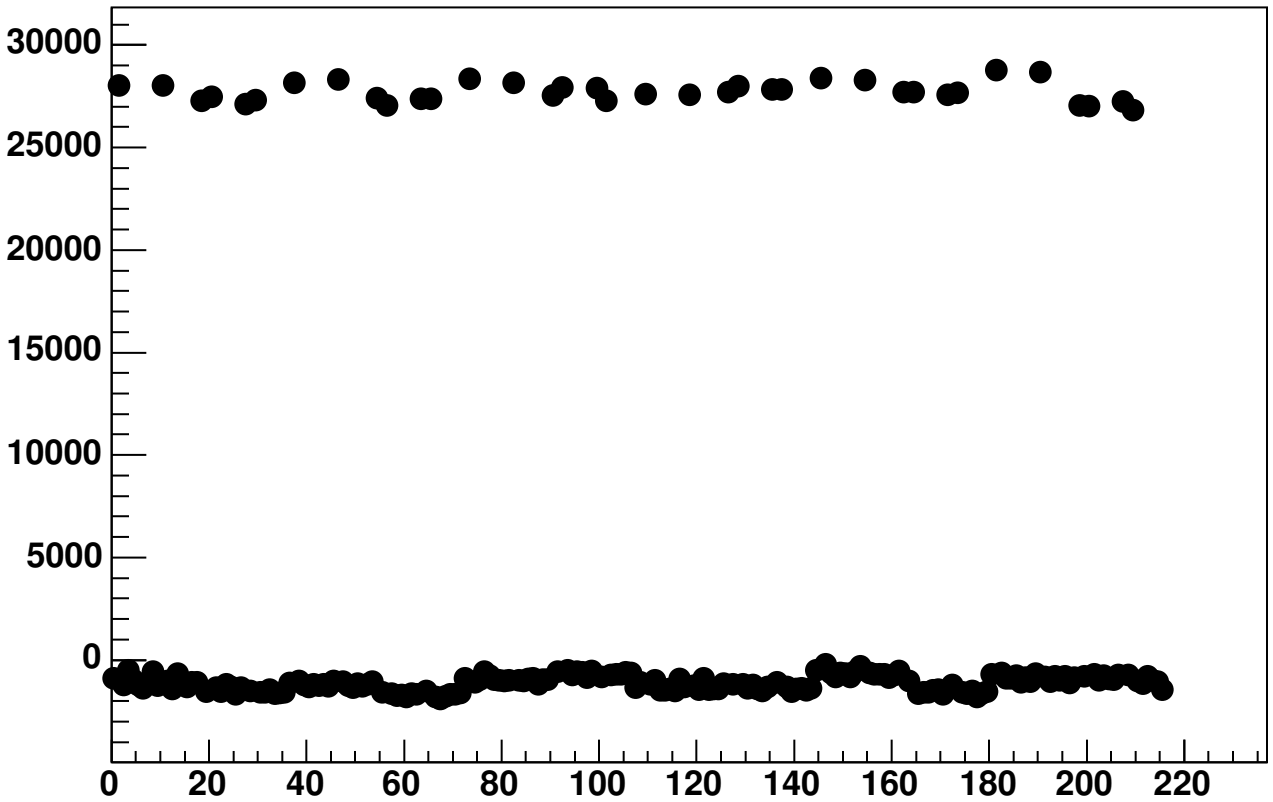
Enable 3, Hold=30, DAC=1900, ADC Mean vs 18*Chip+Chan



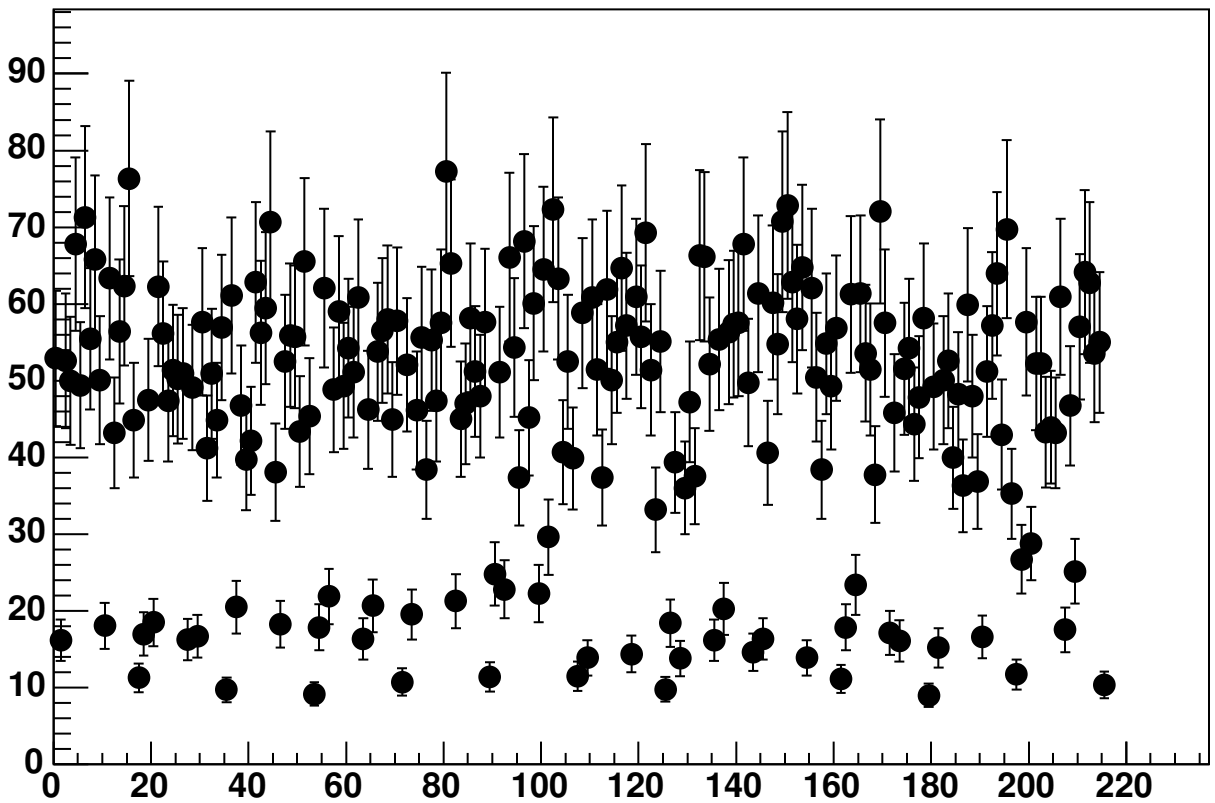
Enable 3, Hold=30, DAC=1900, ADC Noise vs 18*Chip+Chan



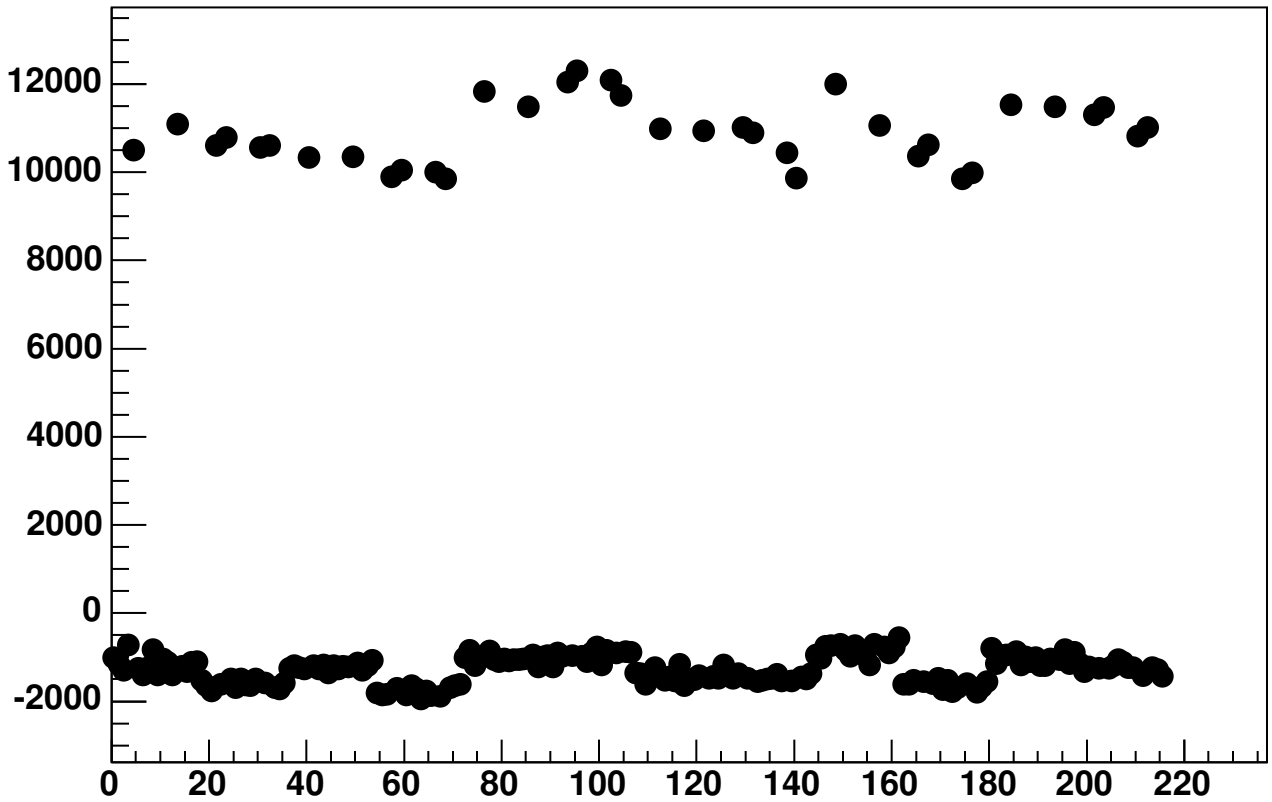
Enable 3, Hold=30, DAC=1950, ADC Mean vs 18*Chip+Chan



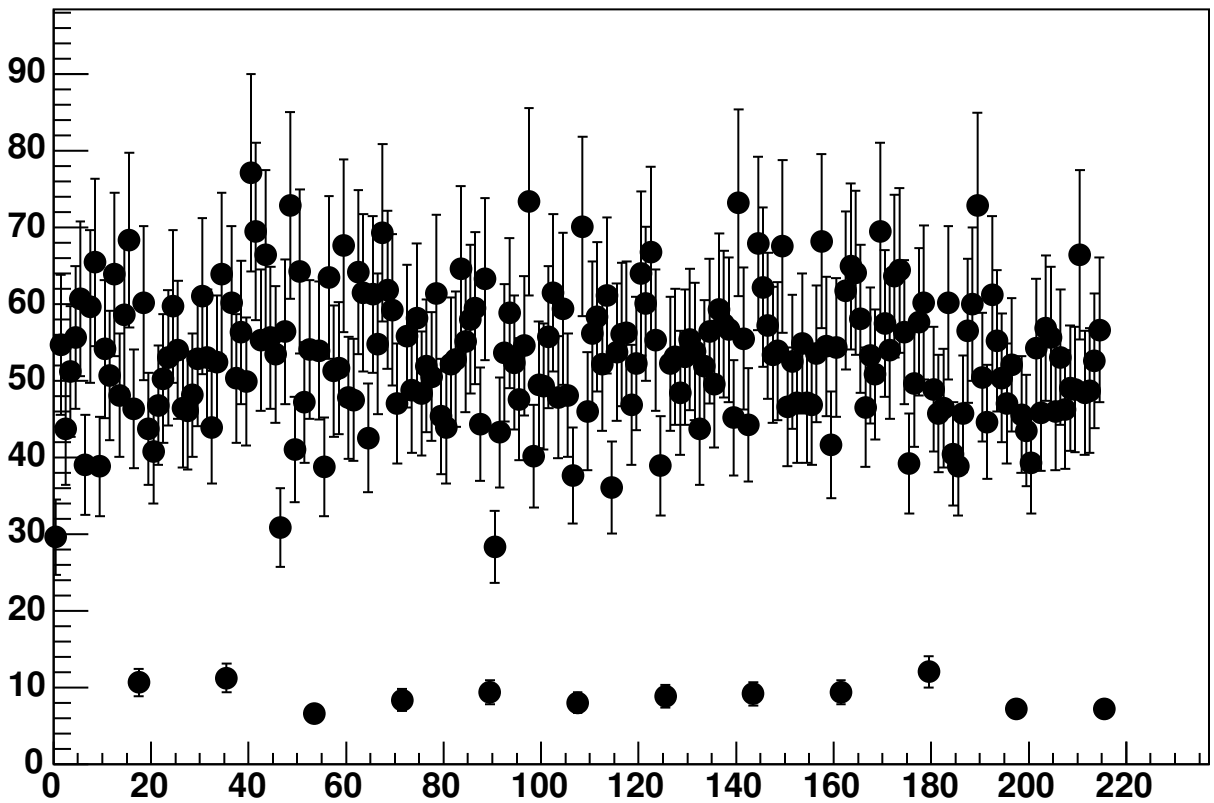
Enable 3, Hold=30, DAC=1950, ADC Noise vs 18*Chip+Chan



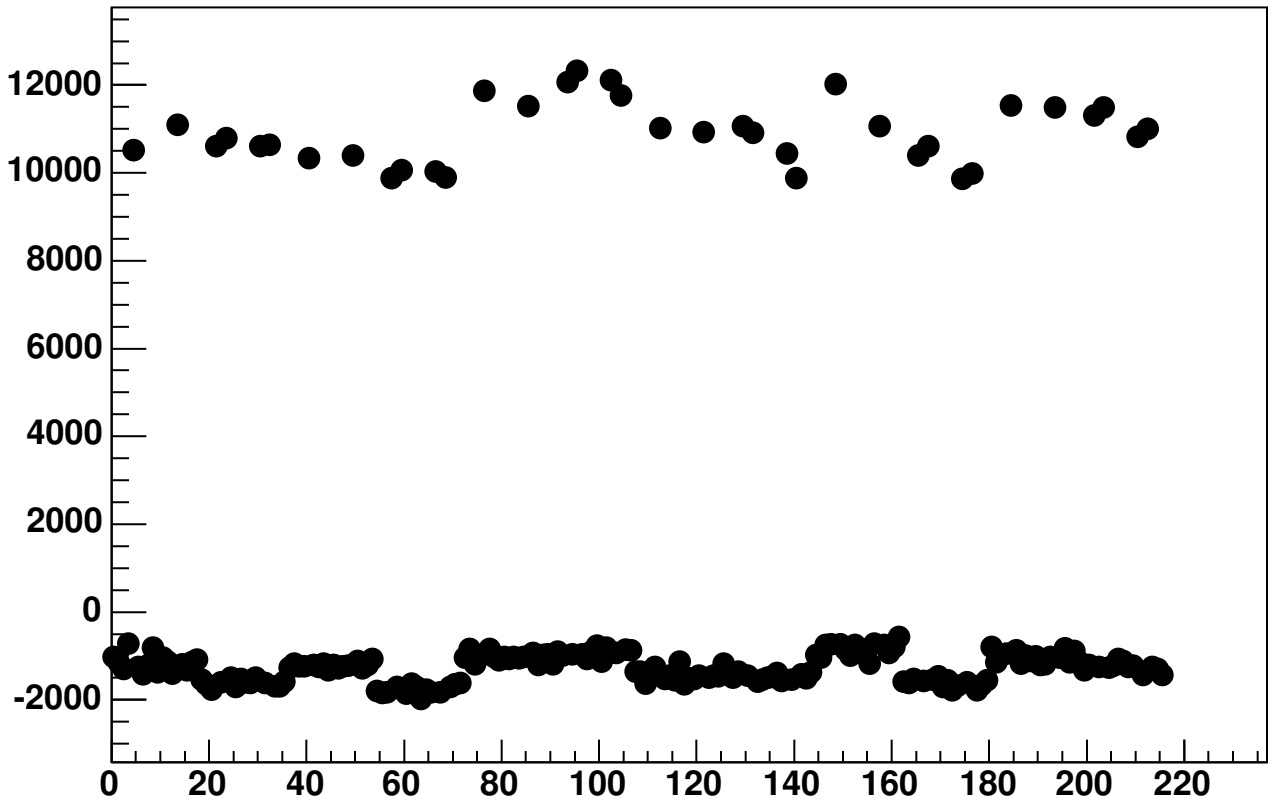
Enable 4, Hold=30, DAC=0, ADC Mean vs 18*Chip+Chan



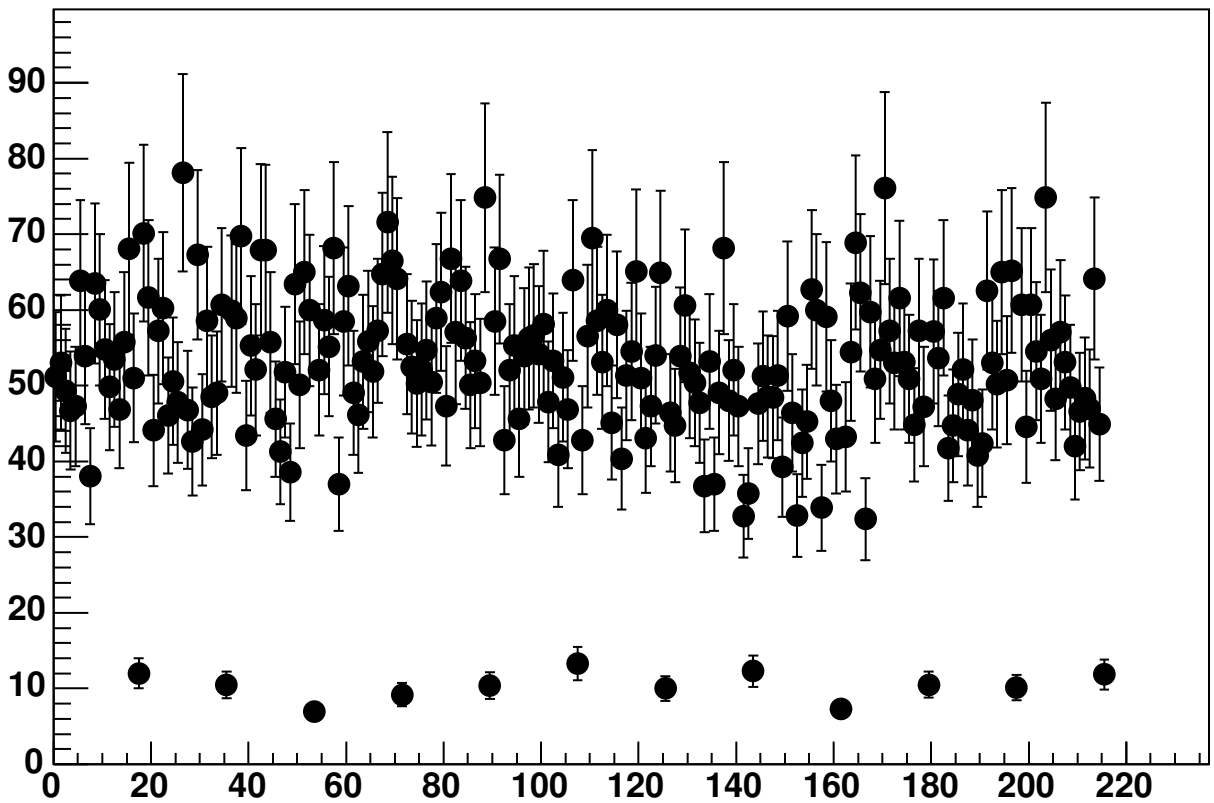
Enable 4, Hold=30, DAC=0, ADC Noise vs 18*Chip+Chan



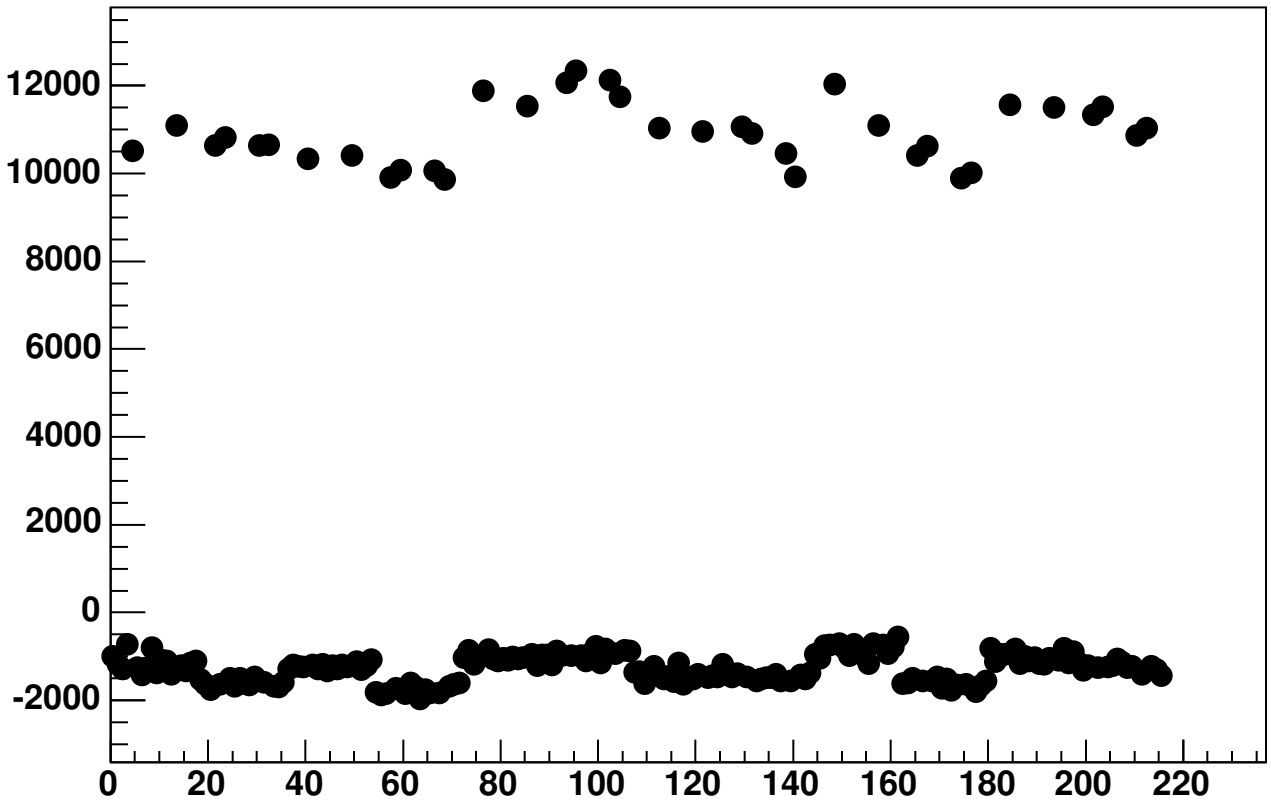
Enable 4, Hold=30, DAC=50, ADC Mean vs 18*Chip+Chan



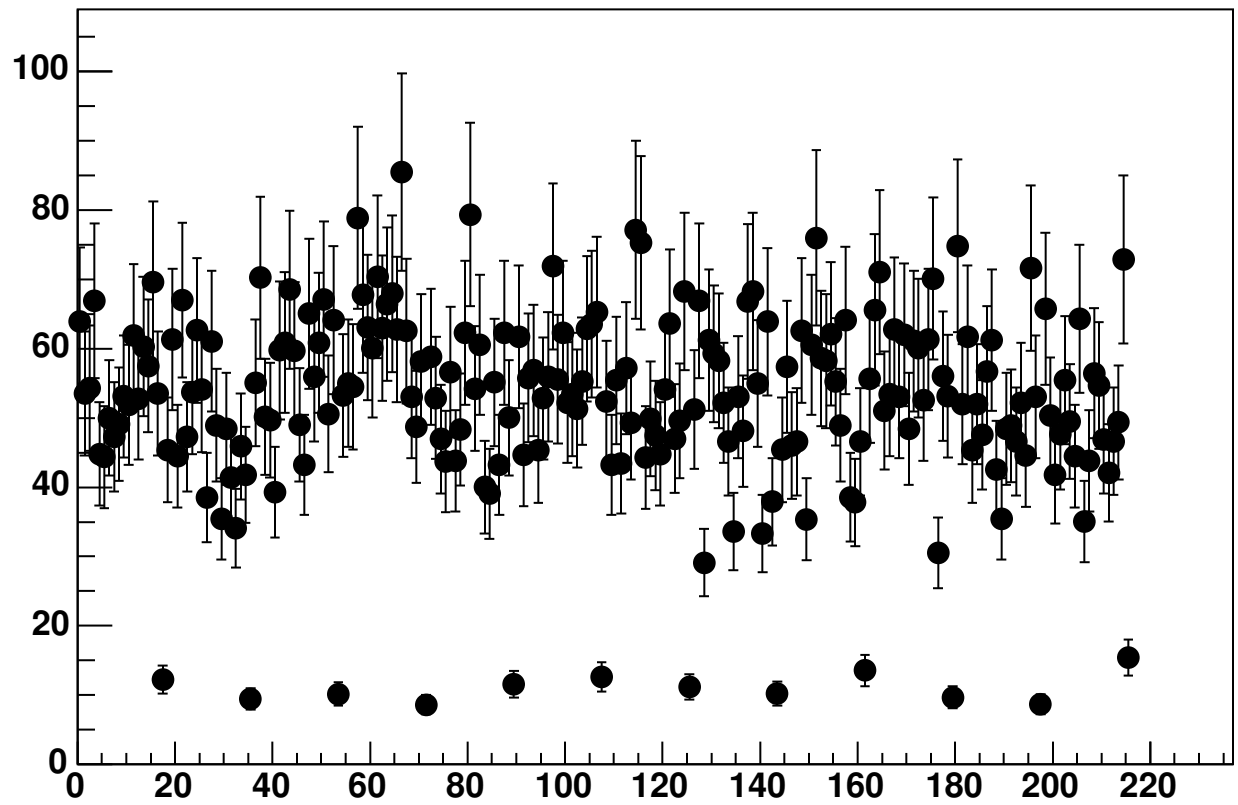
Enable 4, Hold=30, DAC=50, ADC Noise vs 18*Chip+Chan



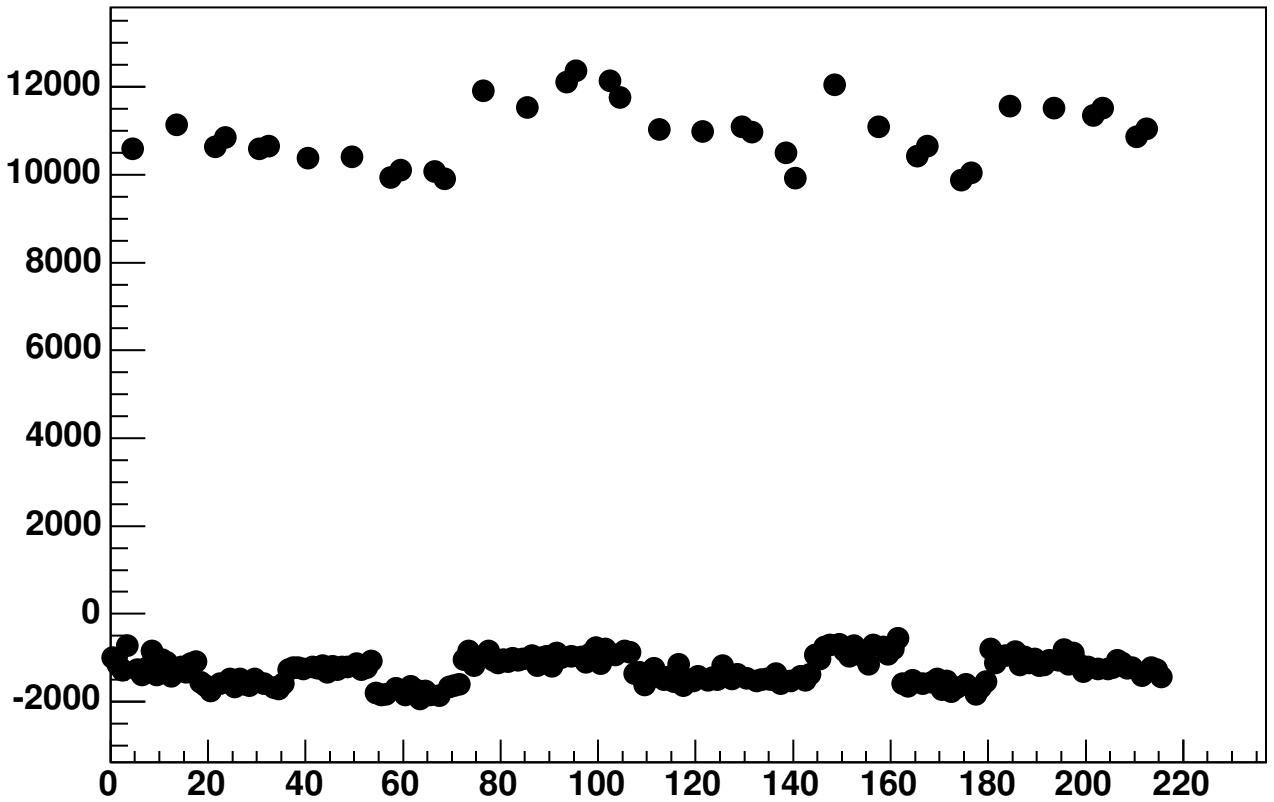
Enable 4, Hold=30, DAC=100, ADC Mean vs 18*Chip+Chan



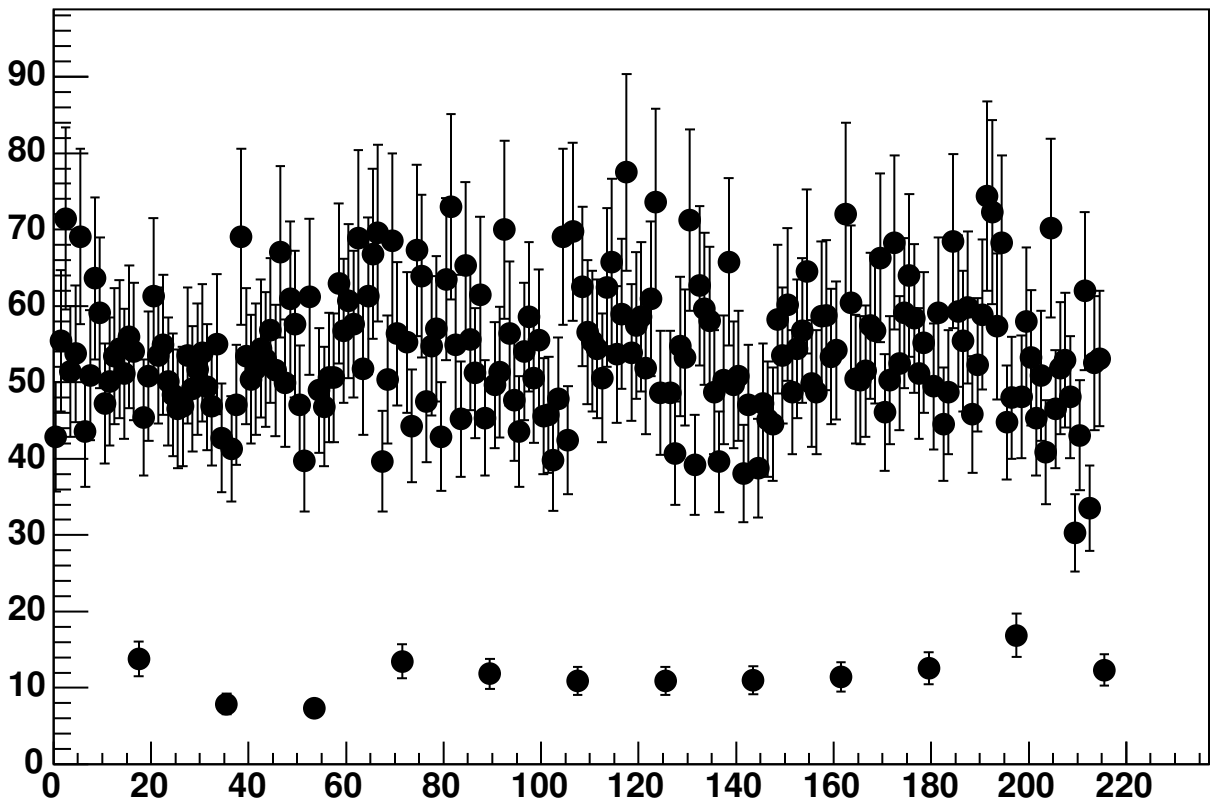
Enable 4, Hold=30, DAC=100, ADC Noise vs 18*Chip+Chan



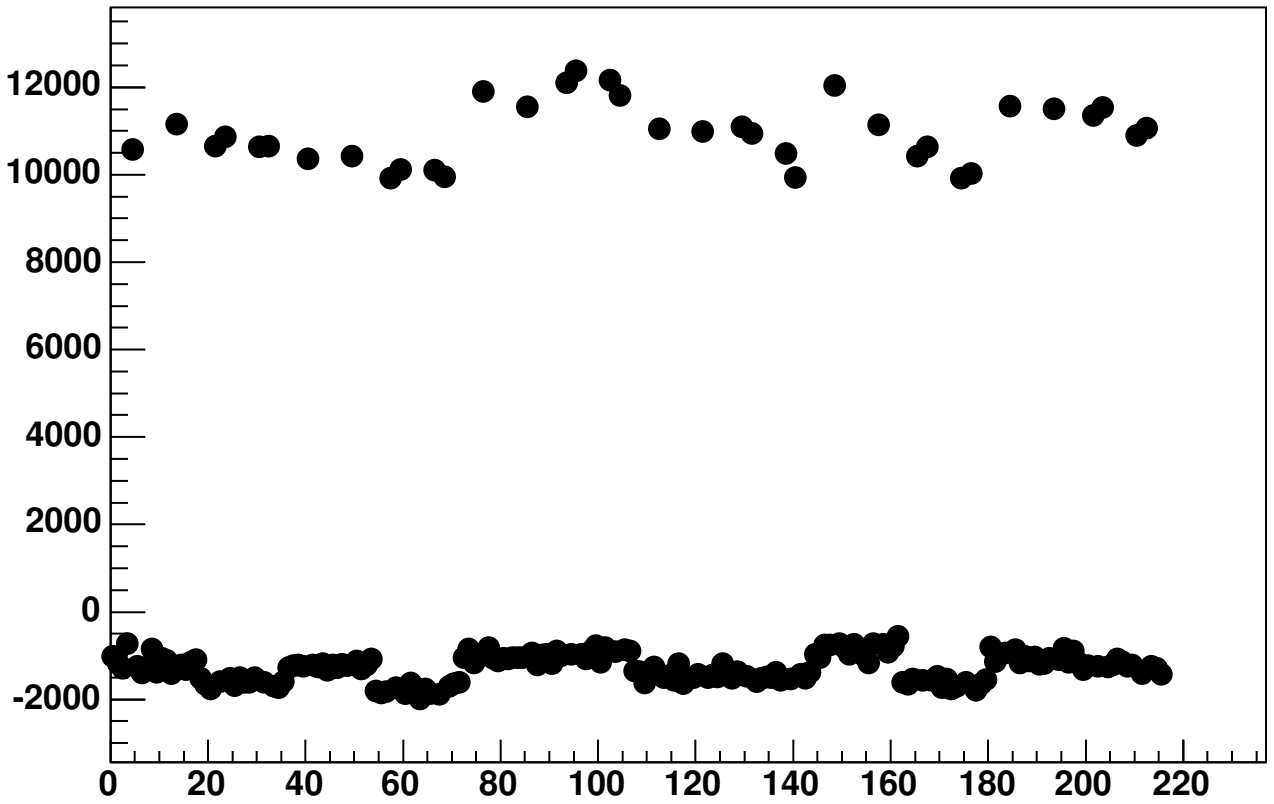
Enable 4, Hold=30, DAC=150, ADC Mean vs 18*Chip+Chan



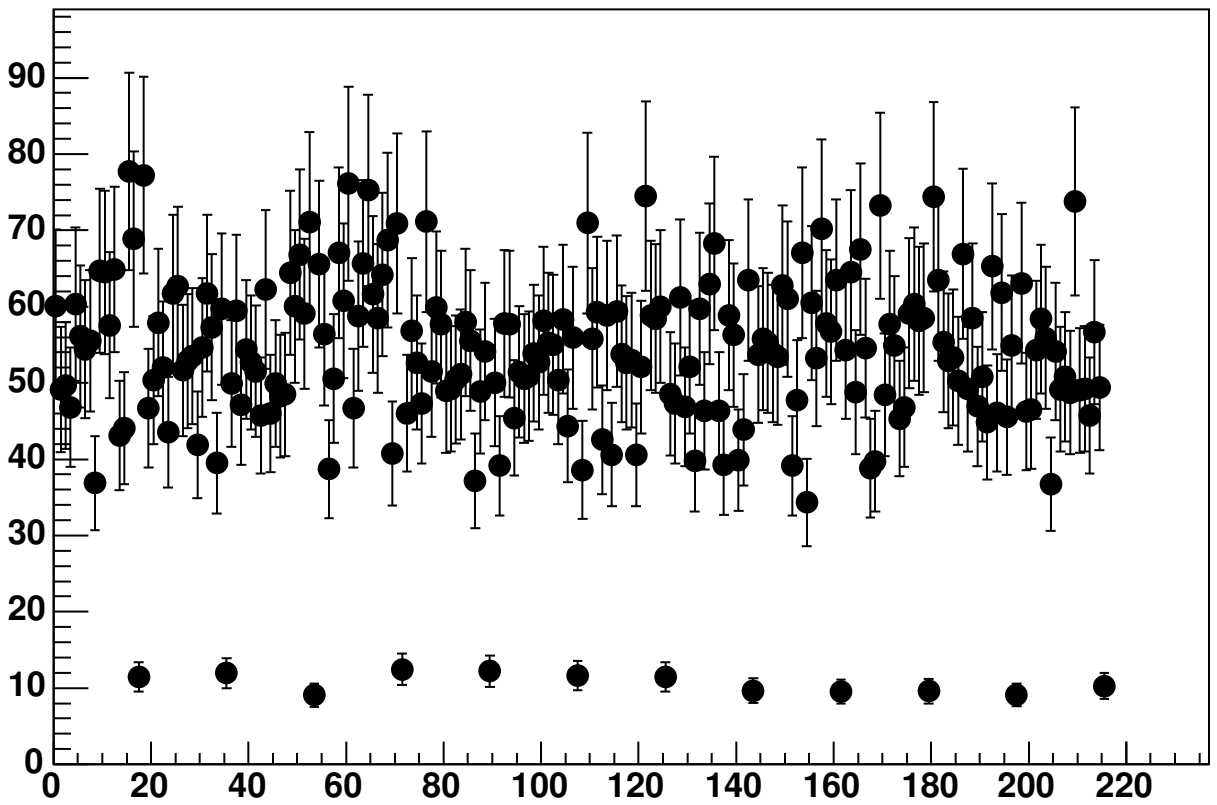
Enable 4, Hold=30, DAC=150, ADC Noise vs 18*Chip+Chan



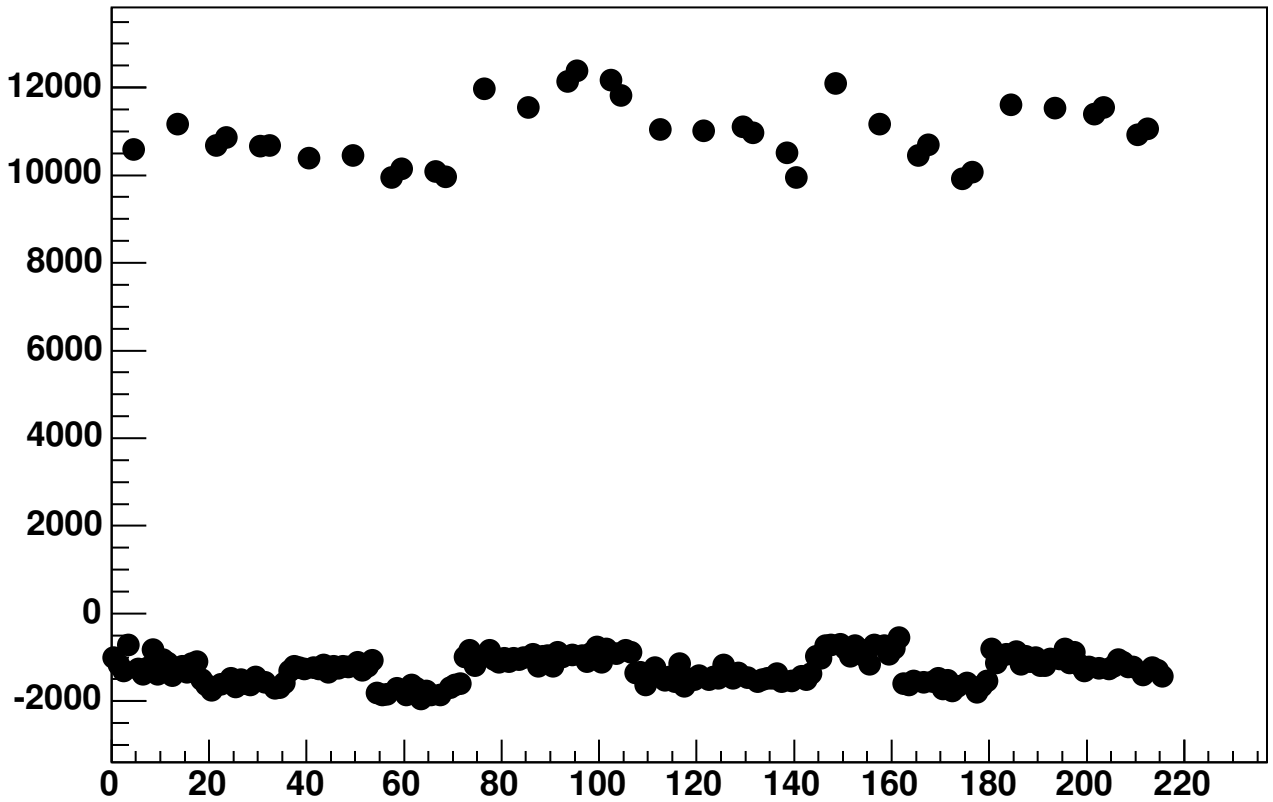
Enable 4, Hold=30, DAC=200, ADC Mean vs 18*Chip+Chan



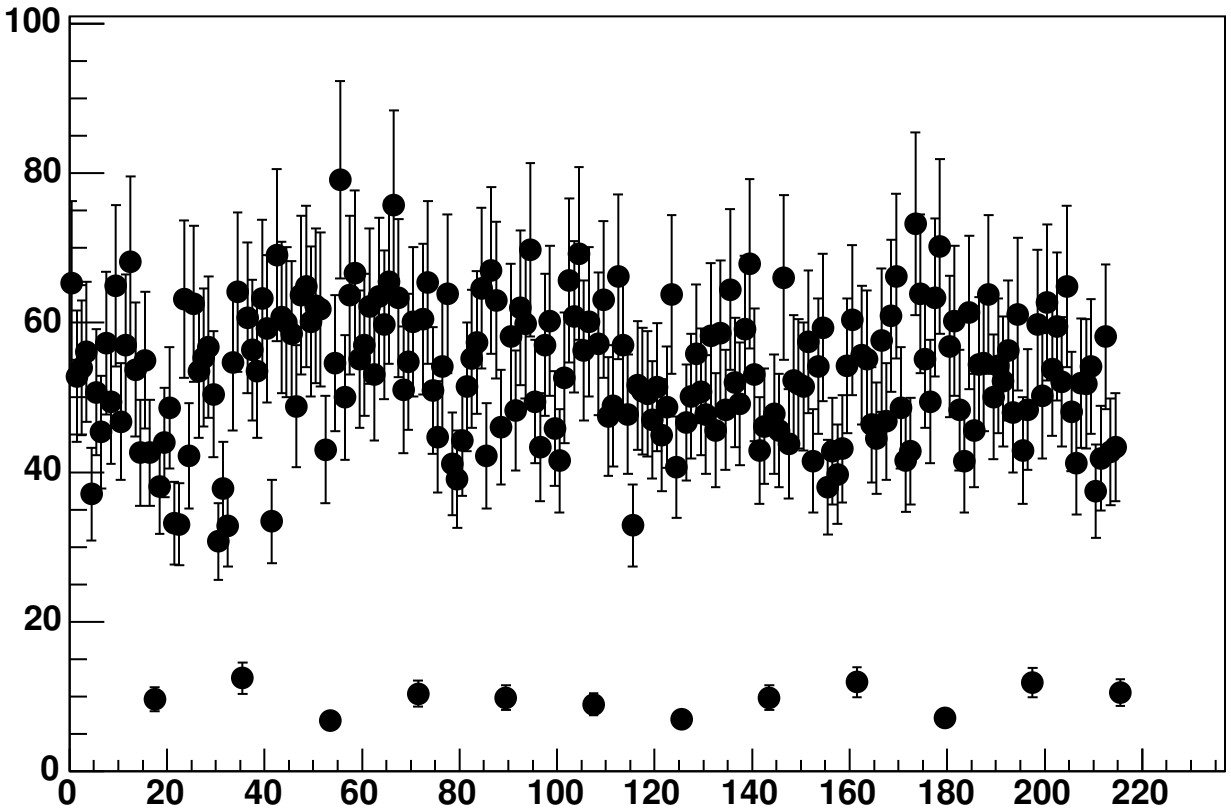
Enable 4, Hold=30, DAC=200, ADC Noise vs 18*Chip+Chan



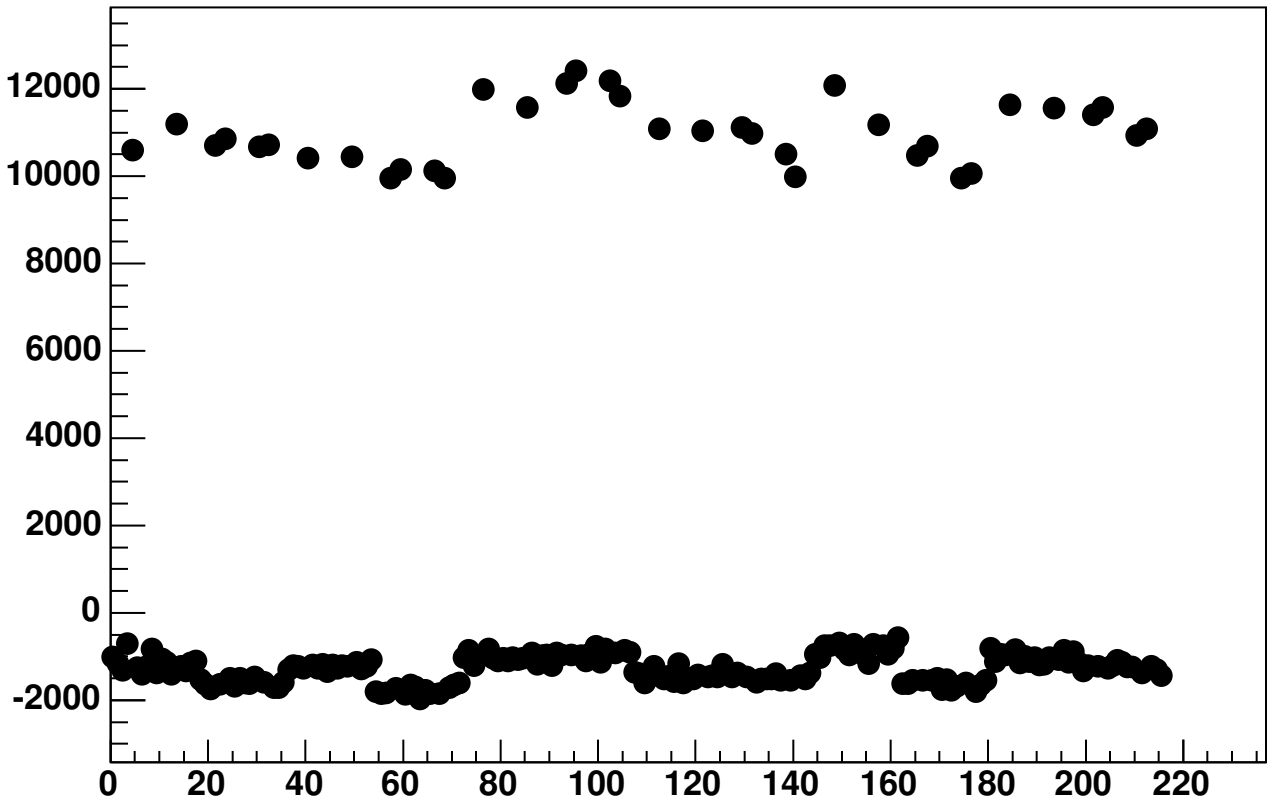
Enable 4, Hold=30, DAC=250, ADC Mean vs 18*Chip+Chan



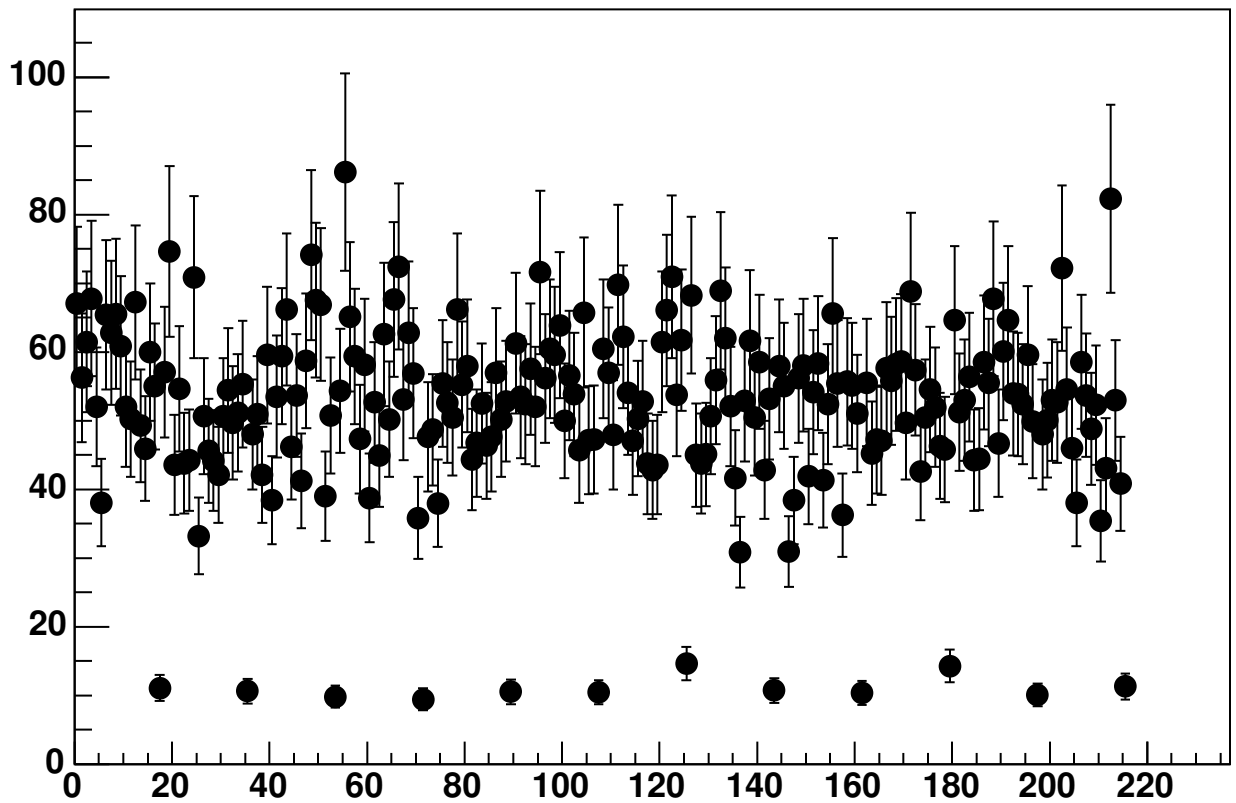
Enable 4, Hold=30, DAC=250, ADC Noise vs 18*Chip+Chan



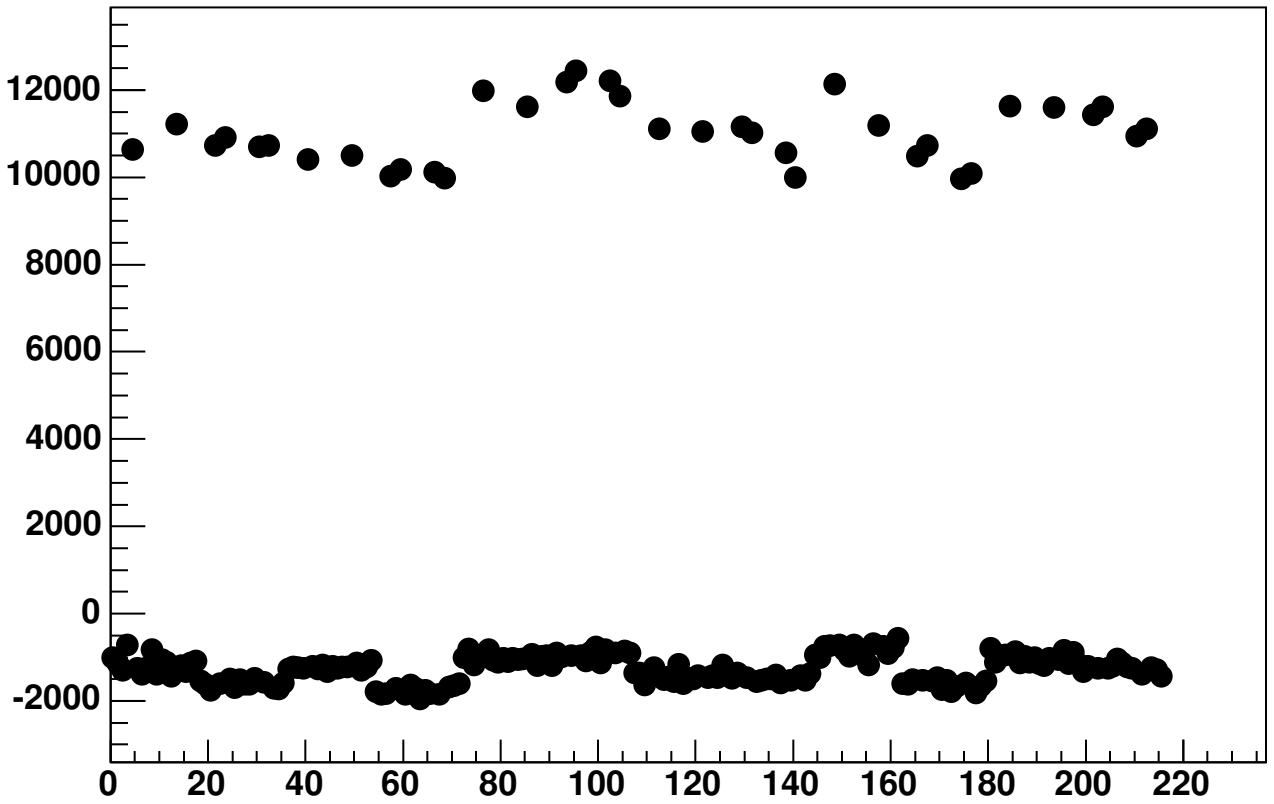
Enable 4, Hold=30, DAC=300, ADC Mean vs 18*Chip+Chan



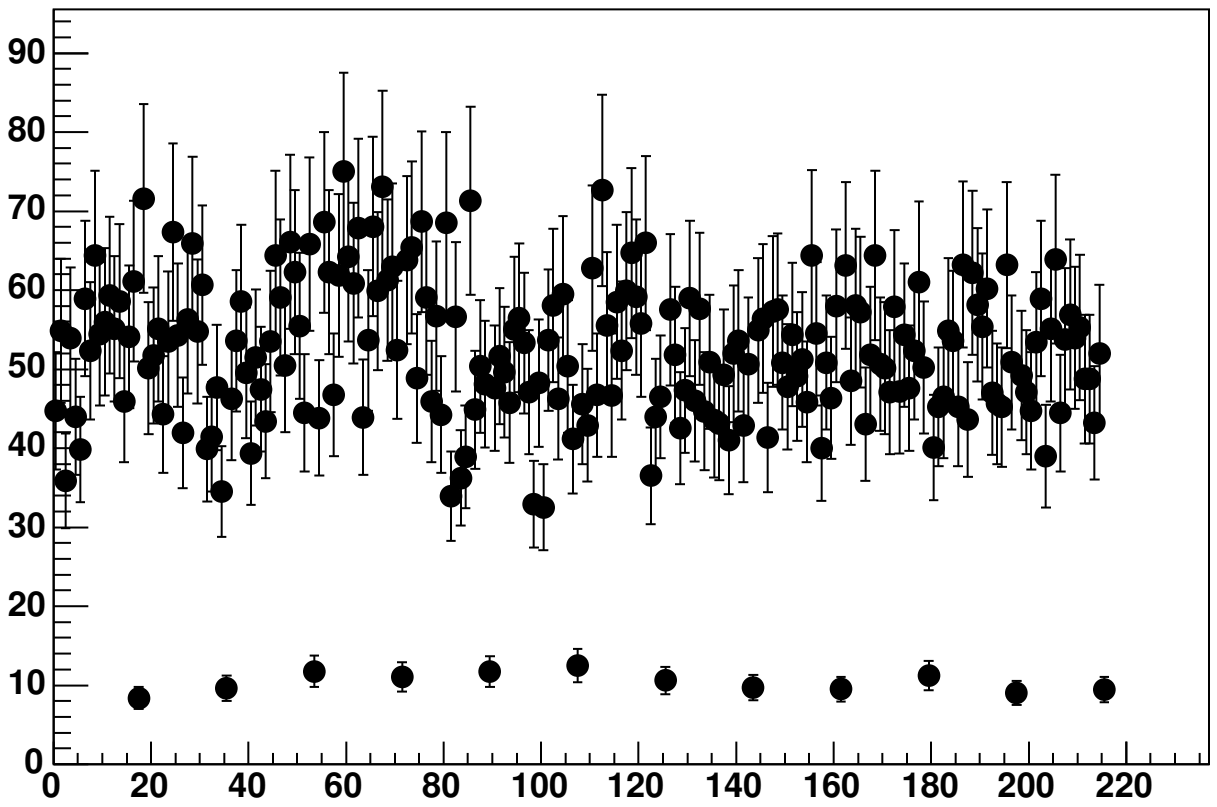
Enable 4, Hold=30, DAC=300, ADC Noise vs 18*Chip+Chan



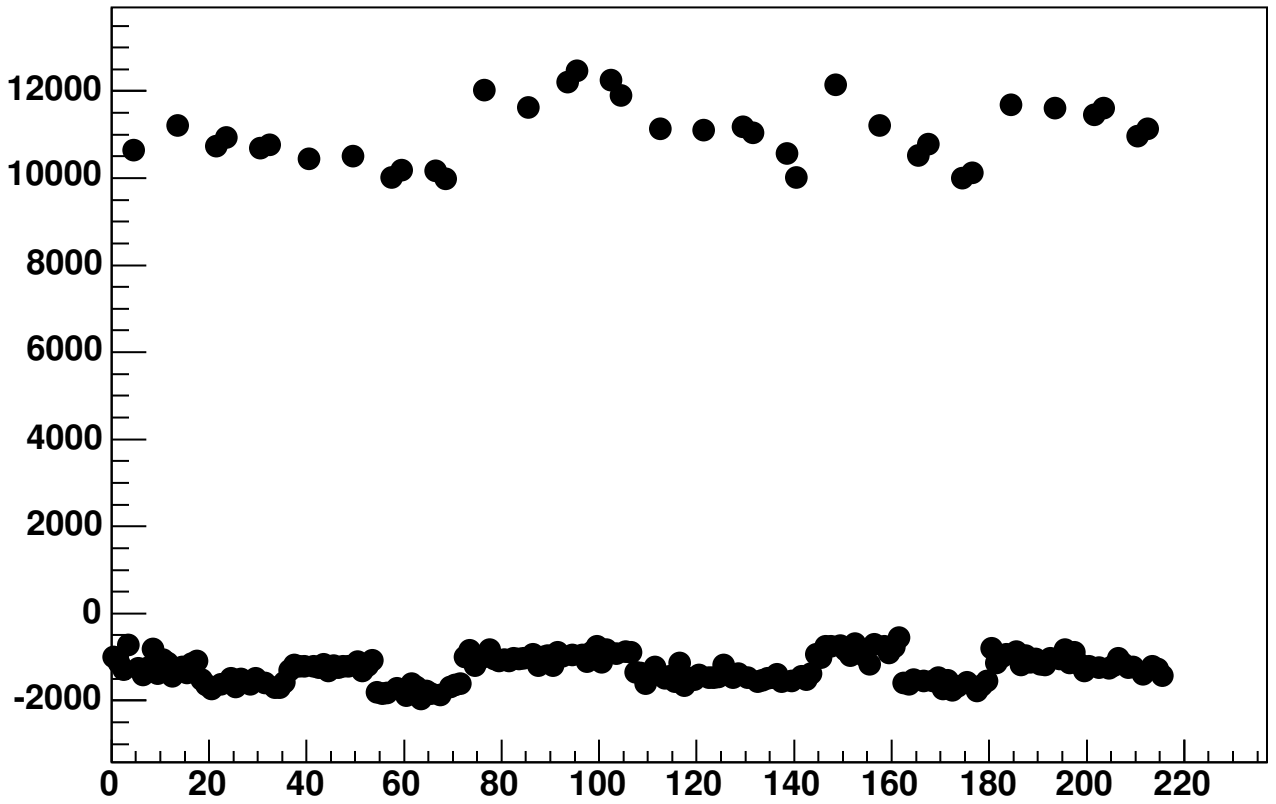
Enable 4, Hold=30, DAC=350, ADC Mean vs 18*Chip+Chan



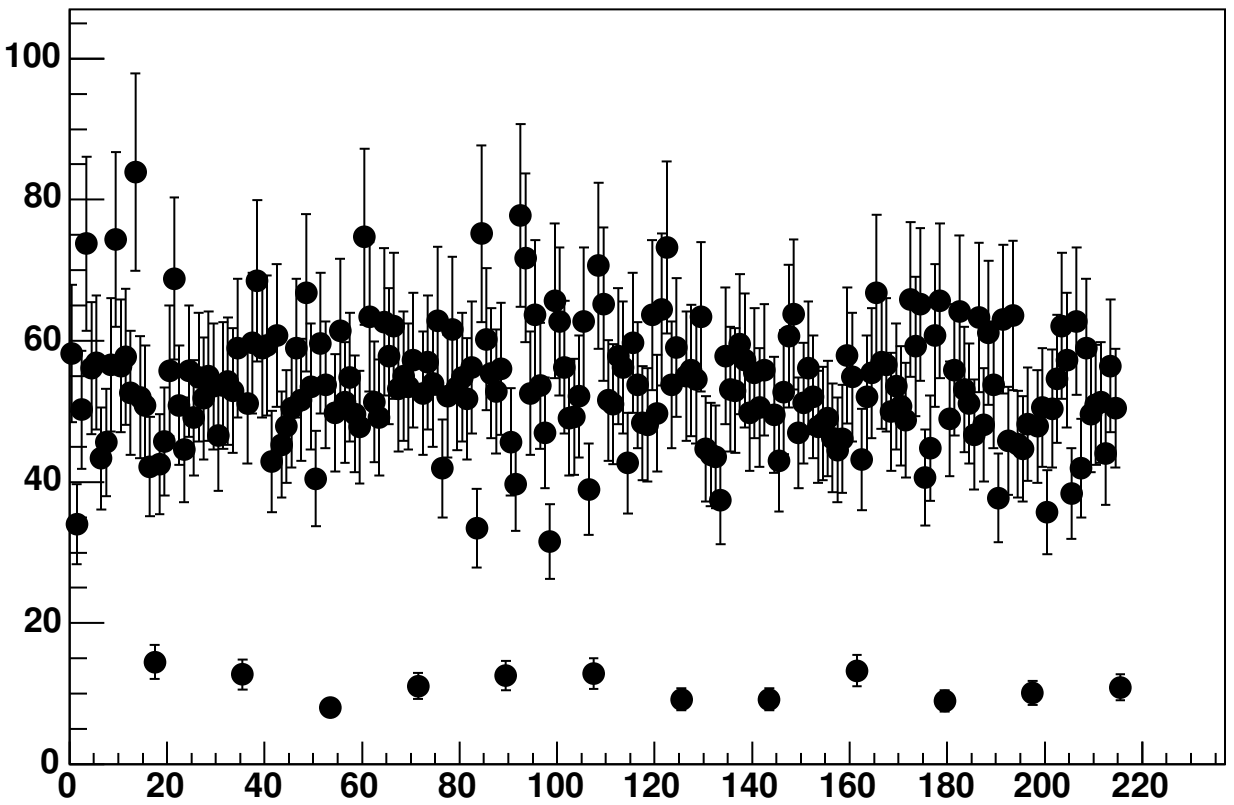
Enable 4, Hold=30, DAC=350, ADC Noise vs 18*Chip+Chan



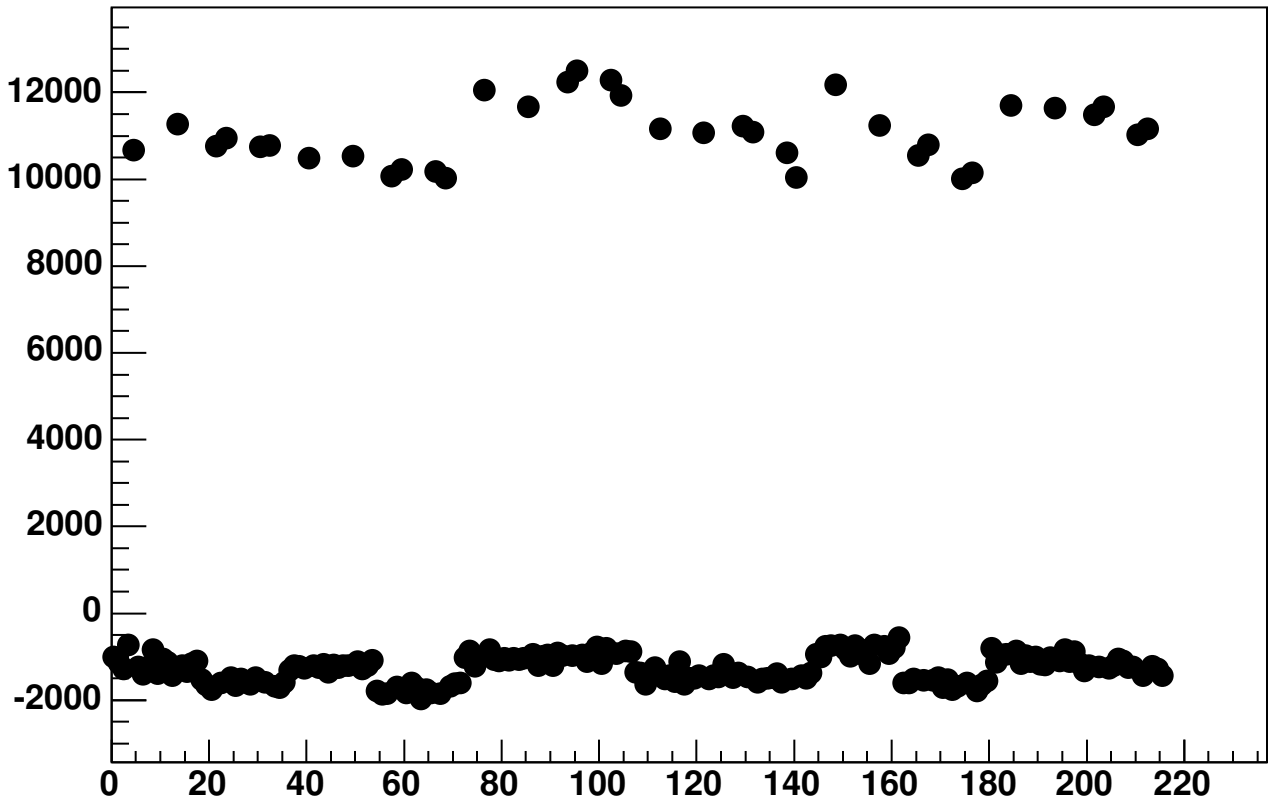
Enable 4, Hold=30, DAC=400, ADC Mean vs 18*Chip+Chan



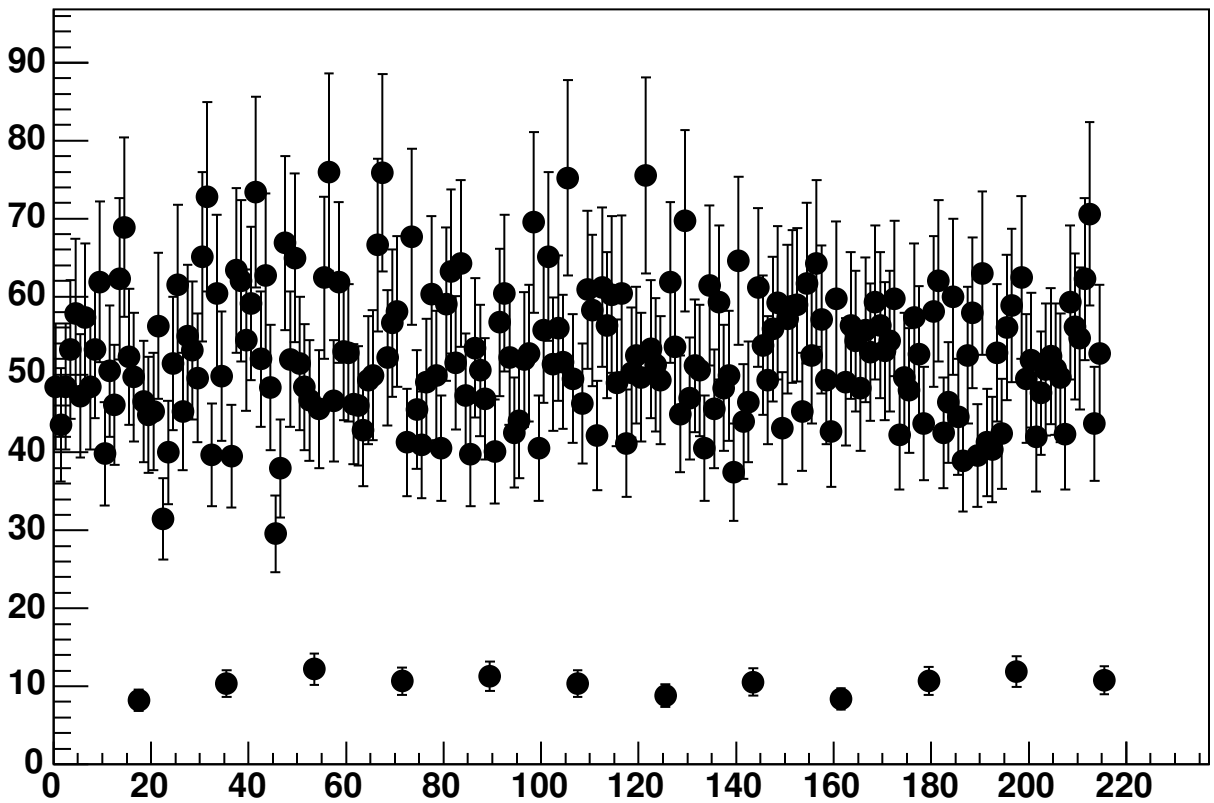
Enable 4, Hold=30, DAC=400, ADC Noise vs 18*Chip+Chan



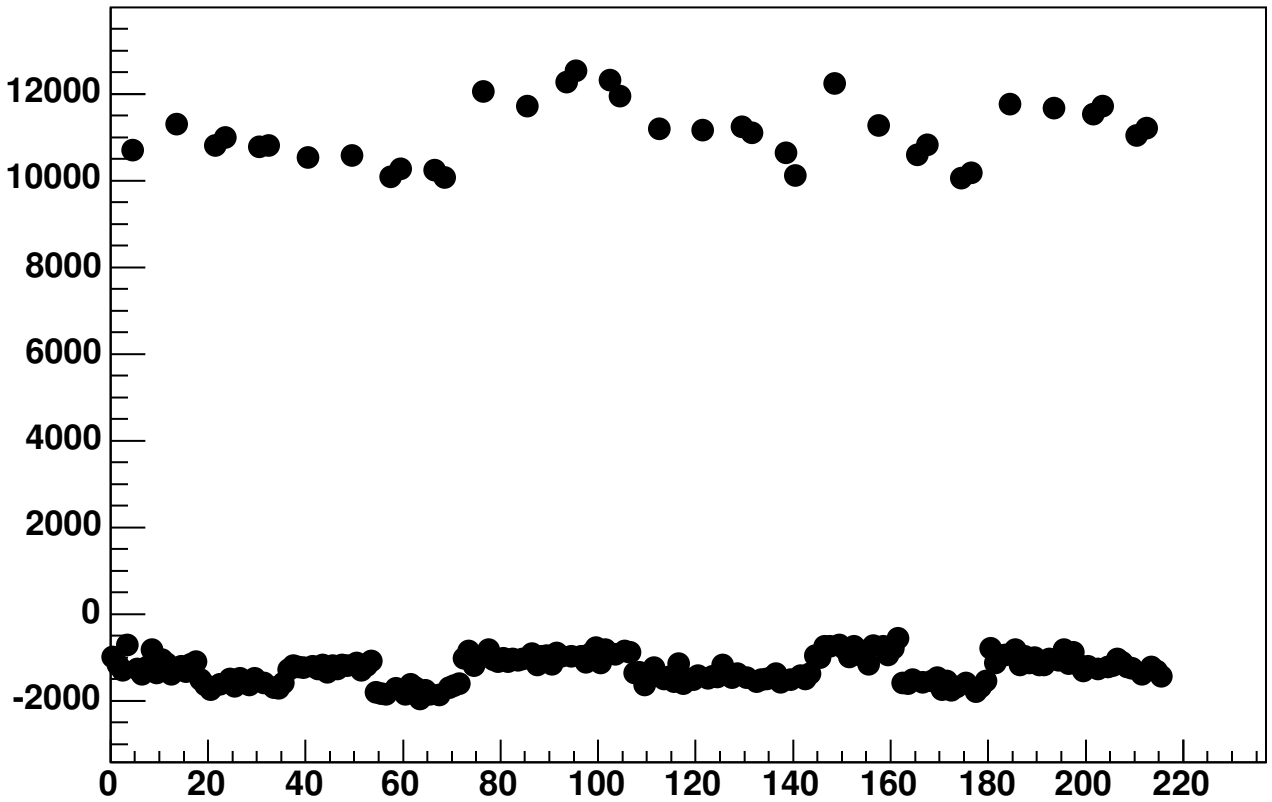
Enable 4, Hold=30, DAC=450, ADC Mean vs 18*Chip+Chan



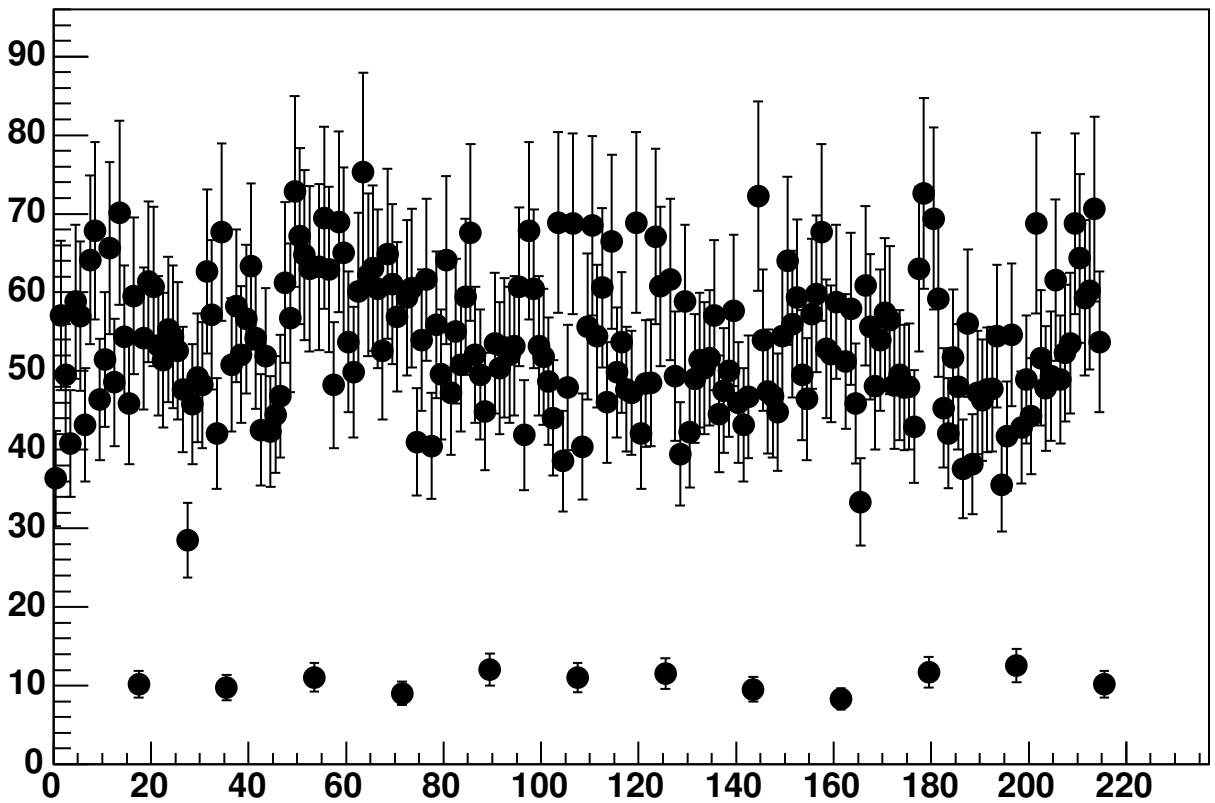
Enable 4, Hold=30, DAC=450, ADC Noise vs 18*Chip+Chan



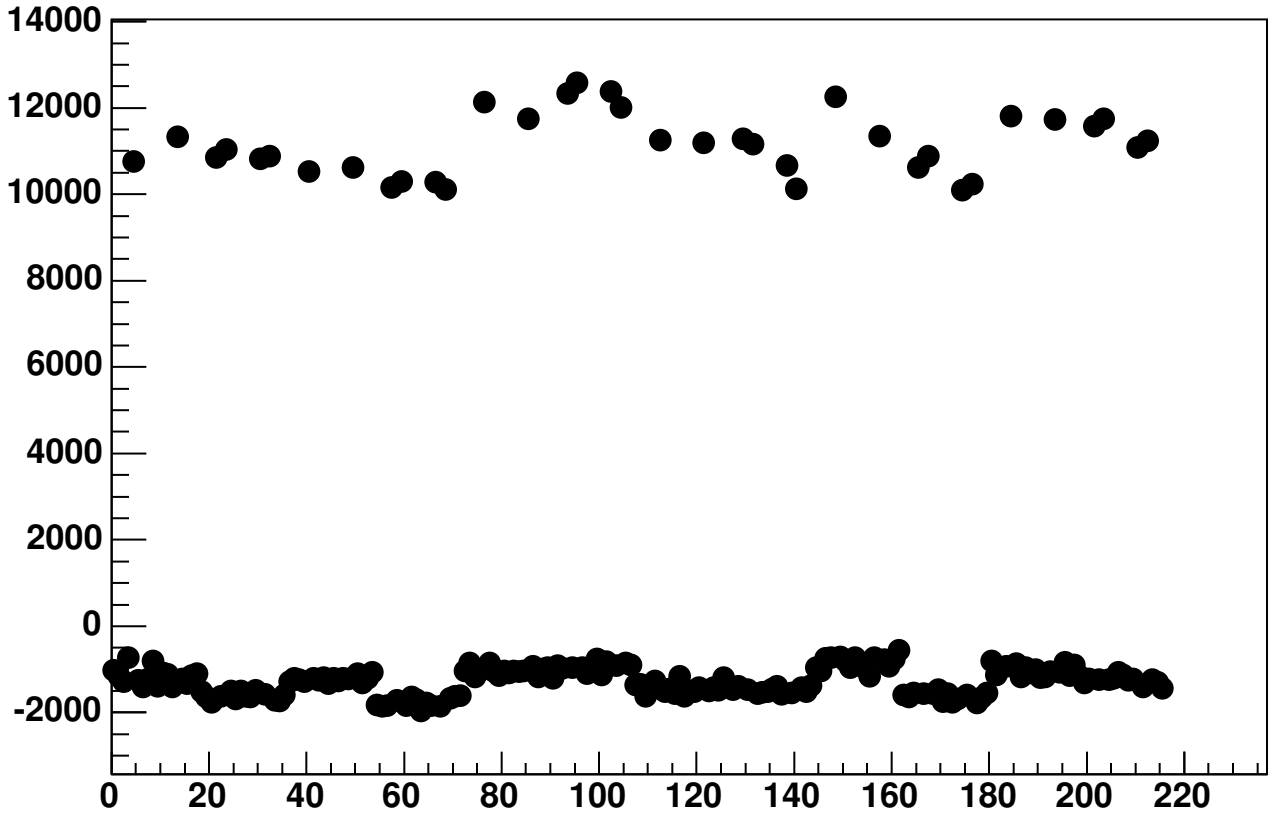
Enable 4, Hold=30, DAC=500, ADC Mean vs 18*Chip+Chan



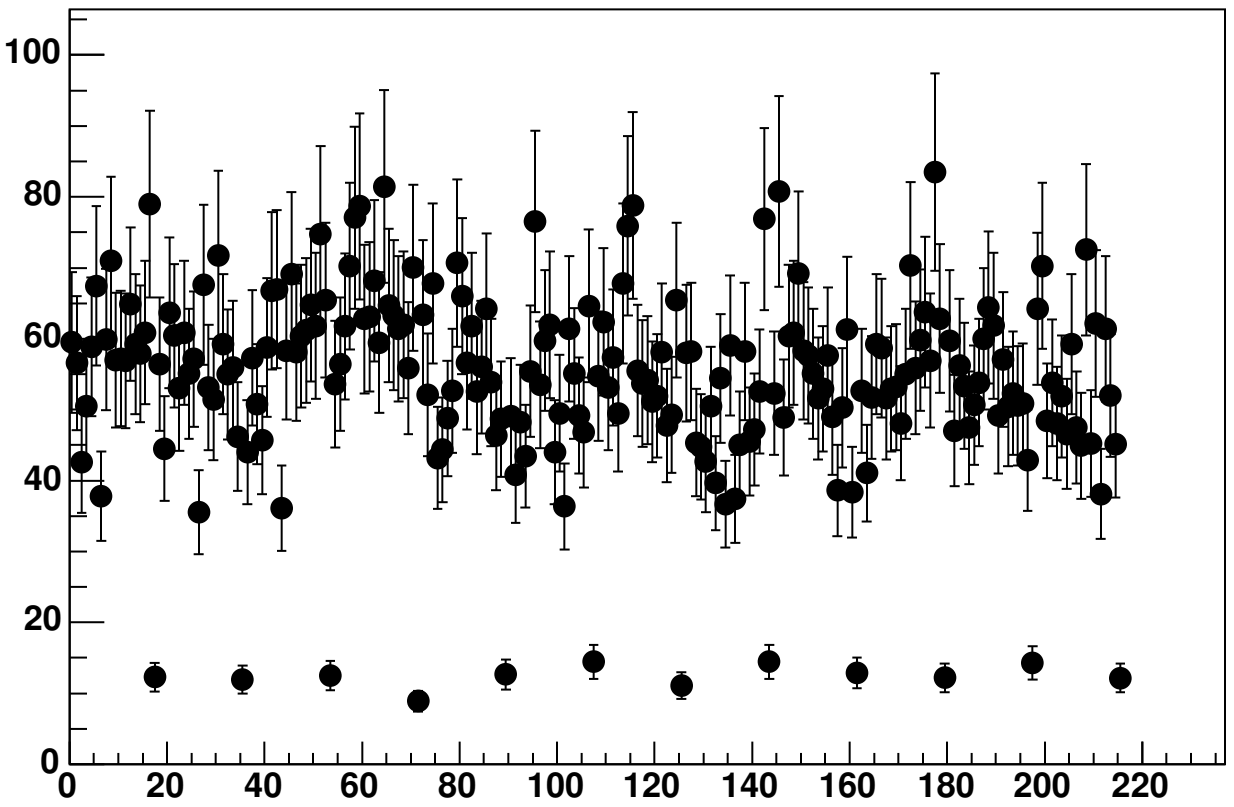
Enable 4, Hold=30, DAC=500, ADC Noise vs 18*Chip+Chan



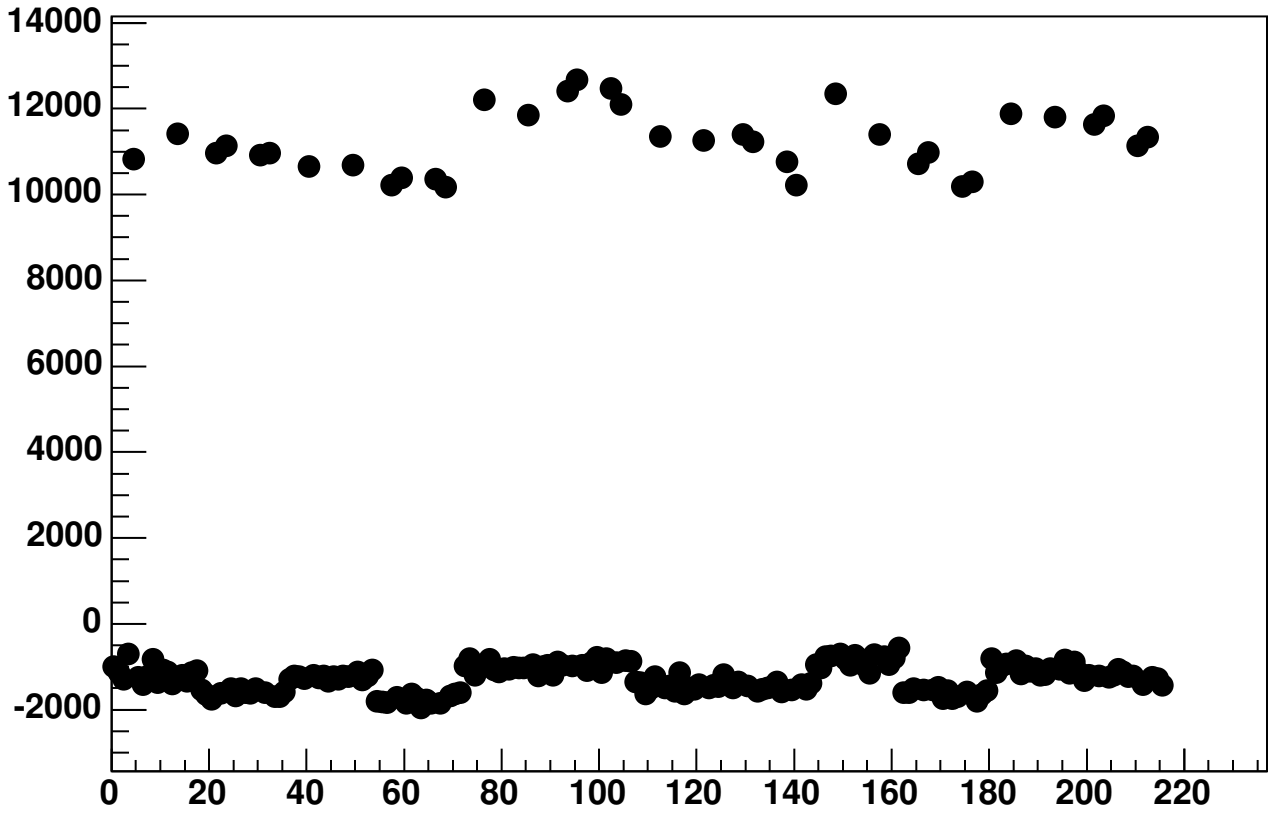
Enable 4, Hold=30, DAC=550, ADC Mean vs 18*Chip+Chan



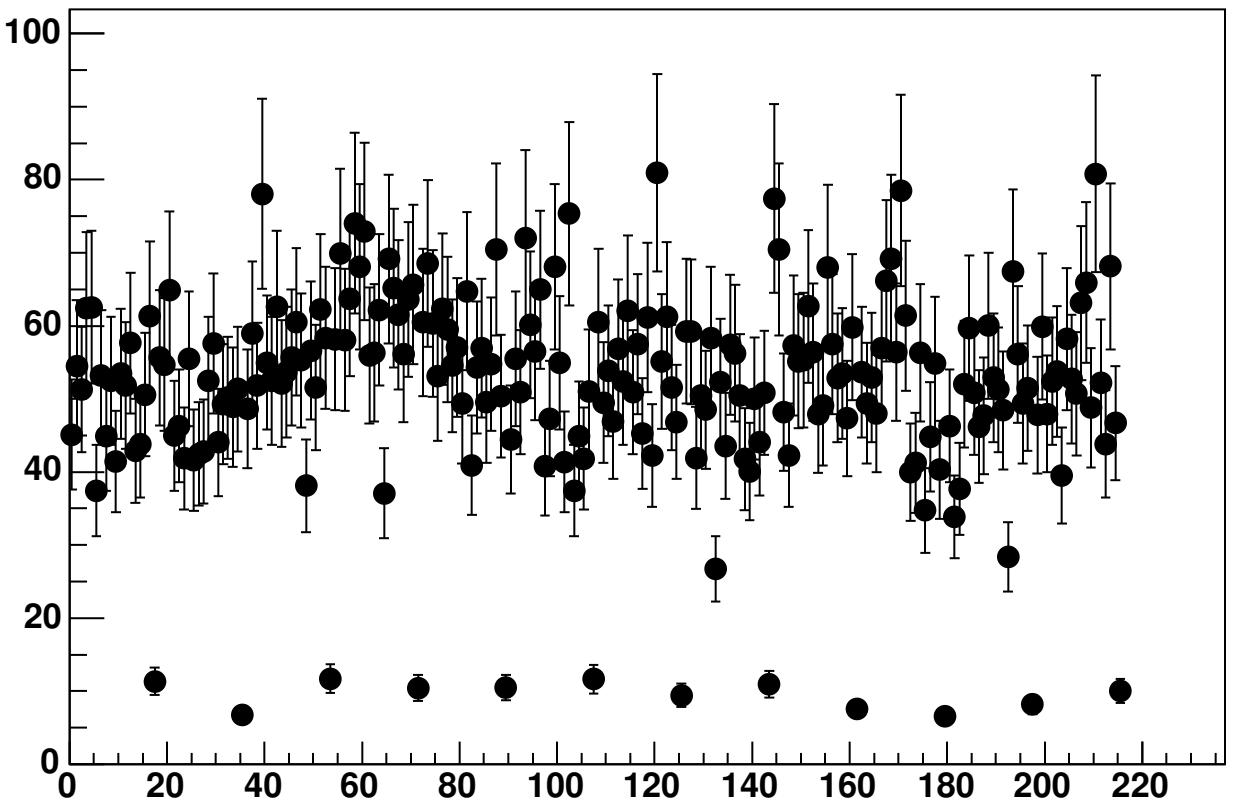
Enable 4, Hold=30, DAC=550, ADC Noise vs 18*Chip+Chan



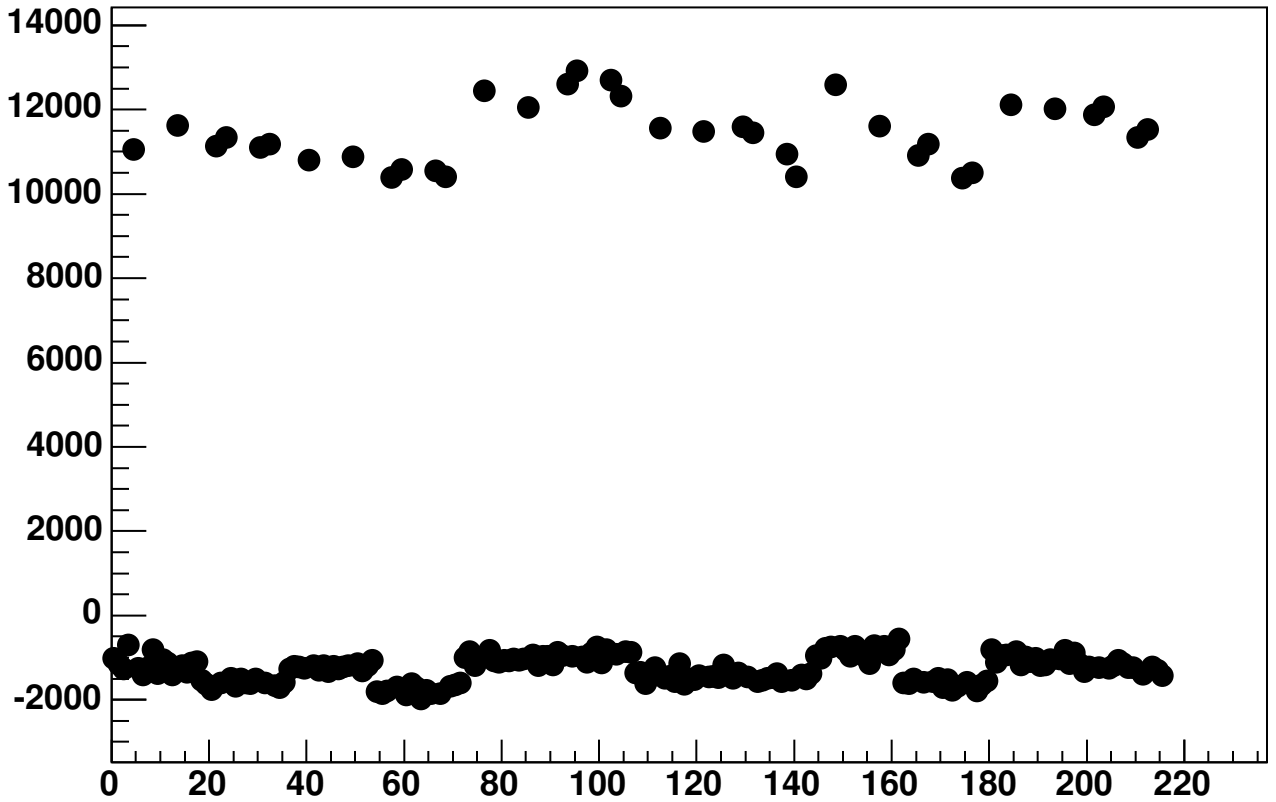
Enable 4, Hold=30, DAC=600, ADC Mean vs 18*Chip+Chan



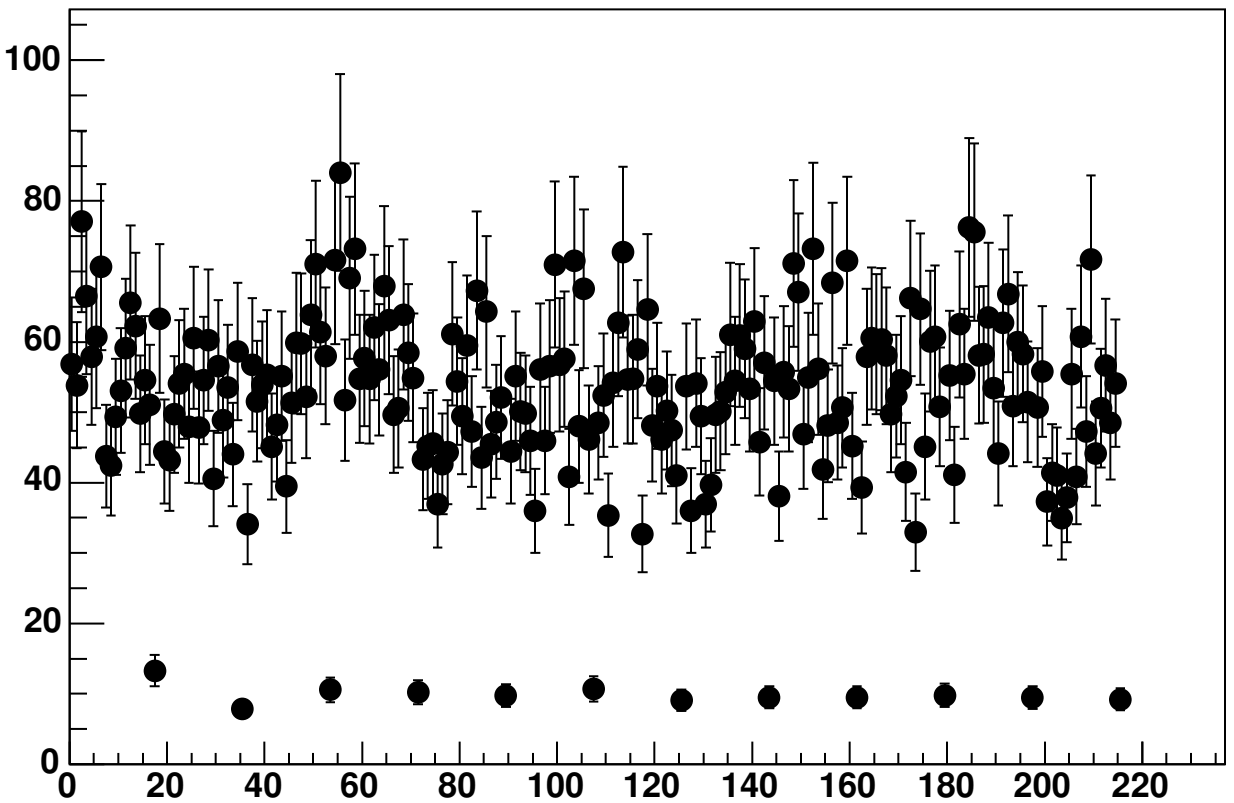
Enable 4, Hold=30, DAC=600, ADC Noise vs 18*Chip+Chan



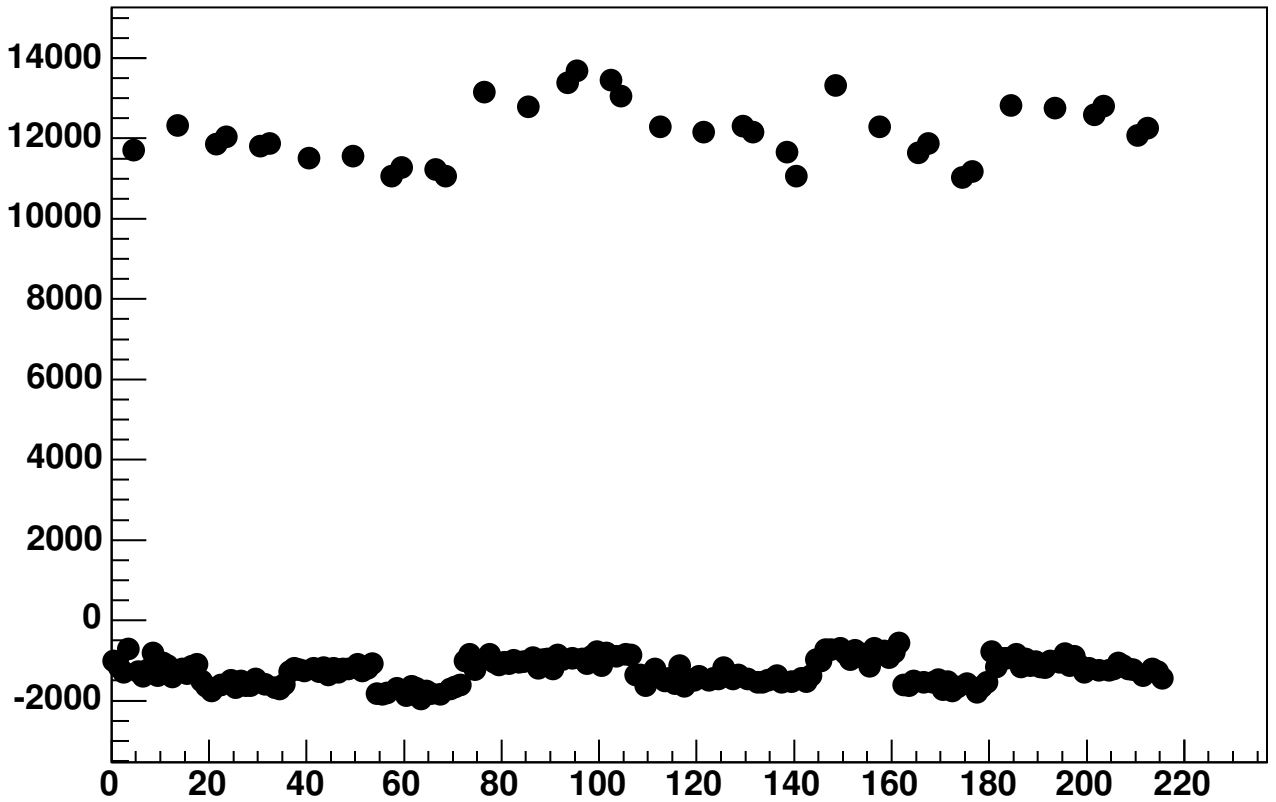
Enable 4, Hold=30, DAC=650, ADC Mean vs 18*Chip+Chan



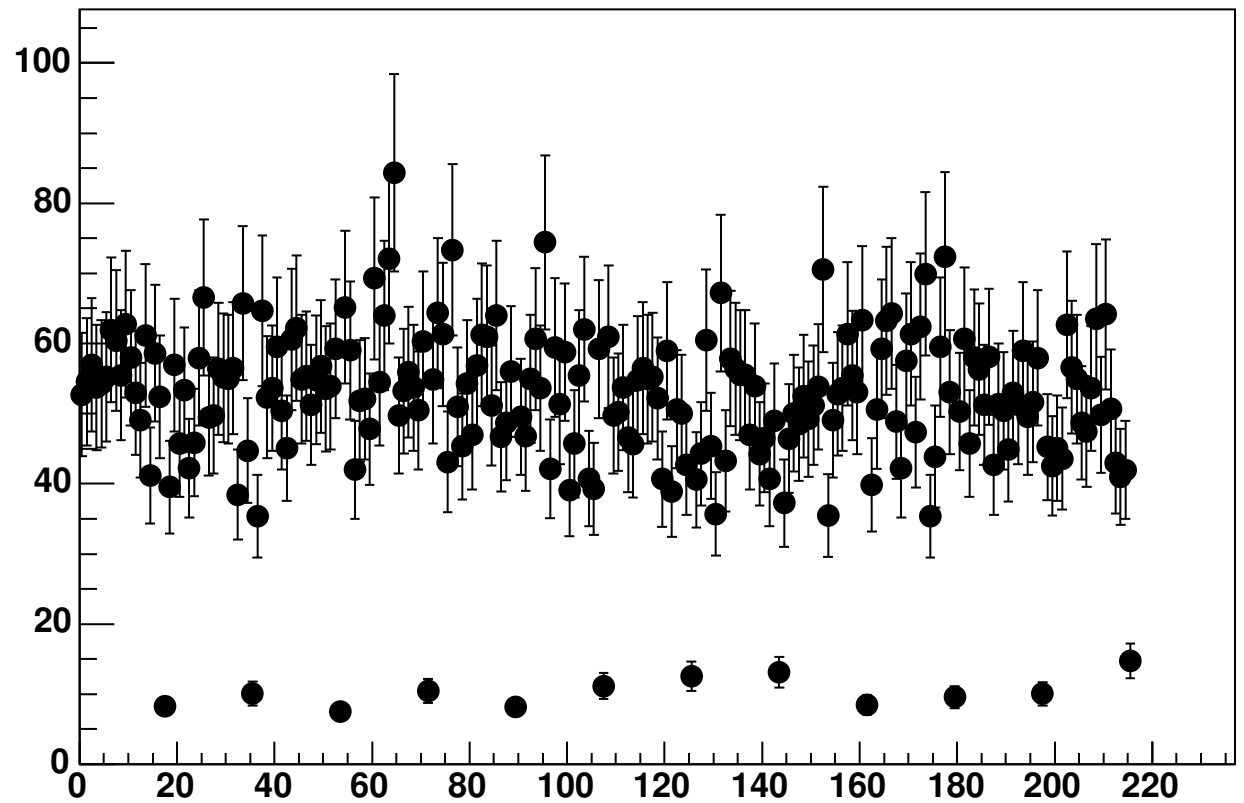
Enable 4, Hold=30, DAC=650, ADC Noise vs 18*Chip+Chan



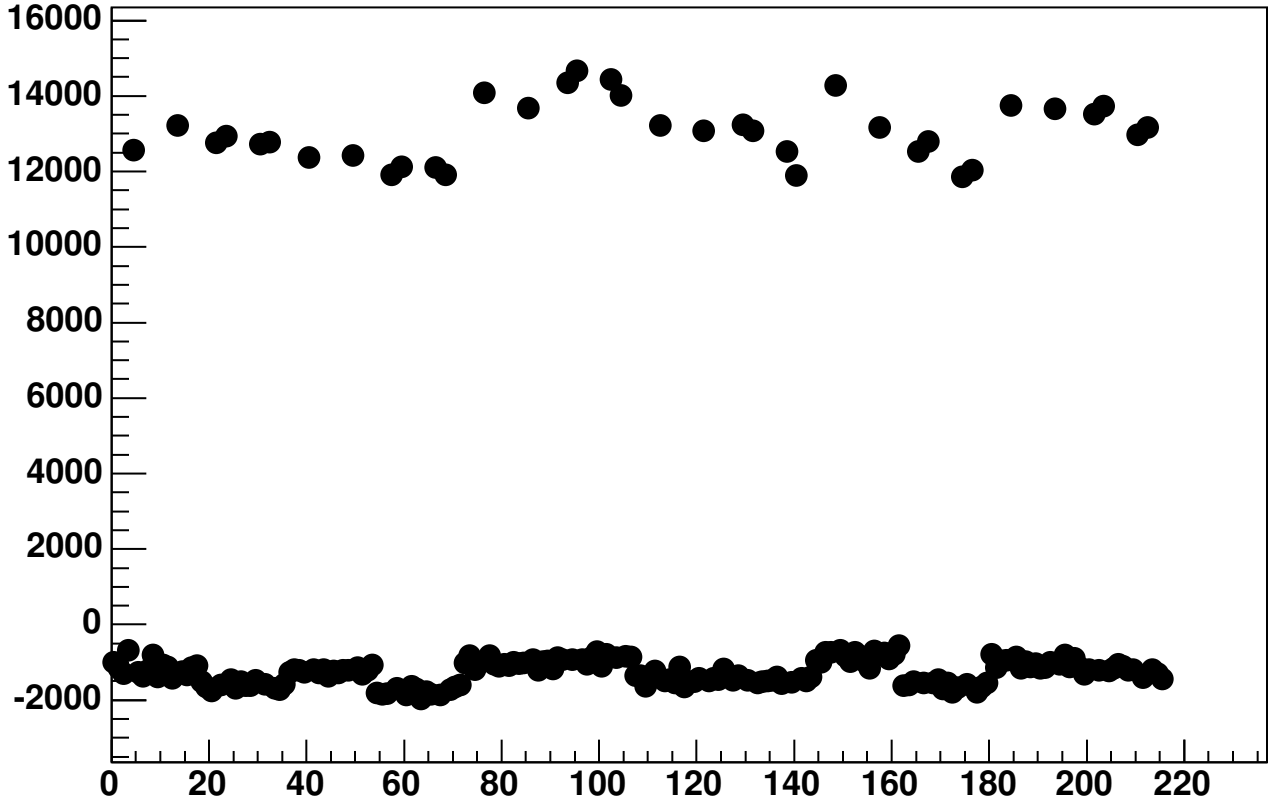
Enable 4, Hold=30, DAC=700, ADC Mean vs 18*Chip+Chan



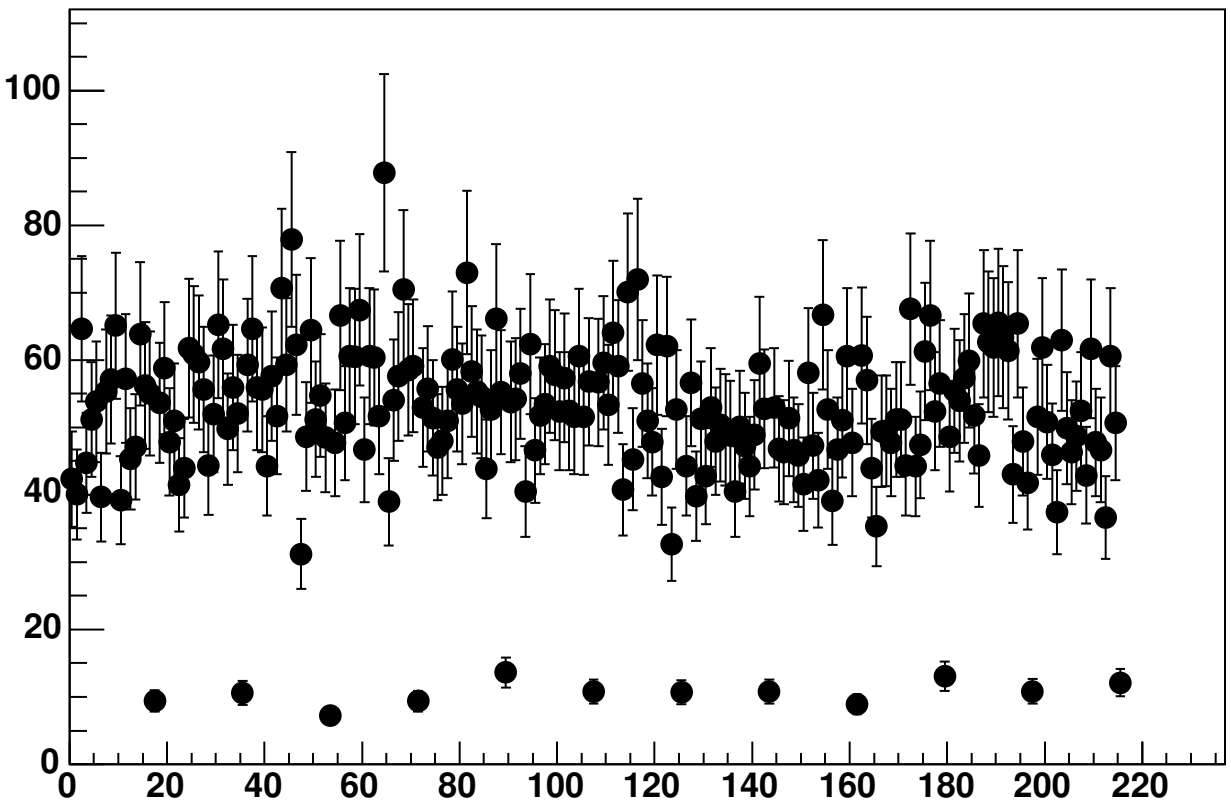
Enable 4, Hold=30, DAC=700, ADC Noise vs 18*Chip+Chan



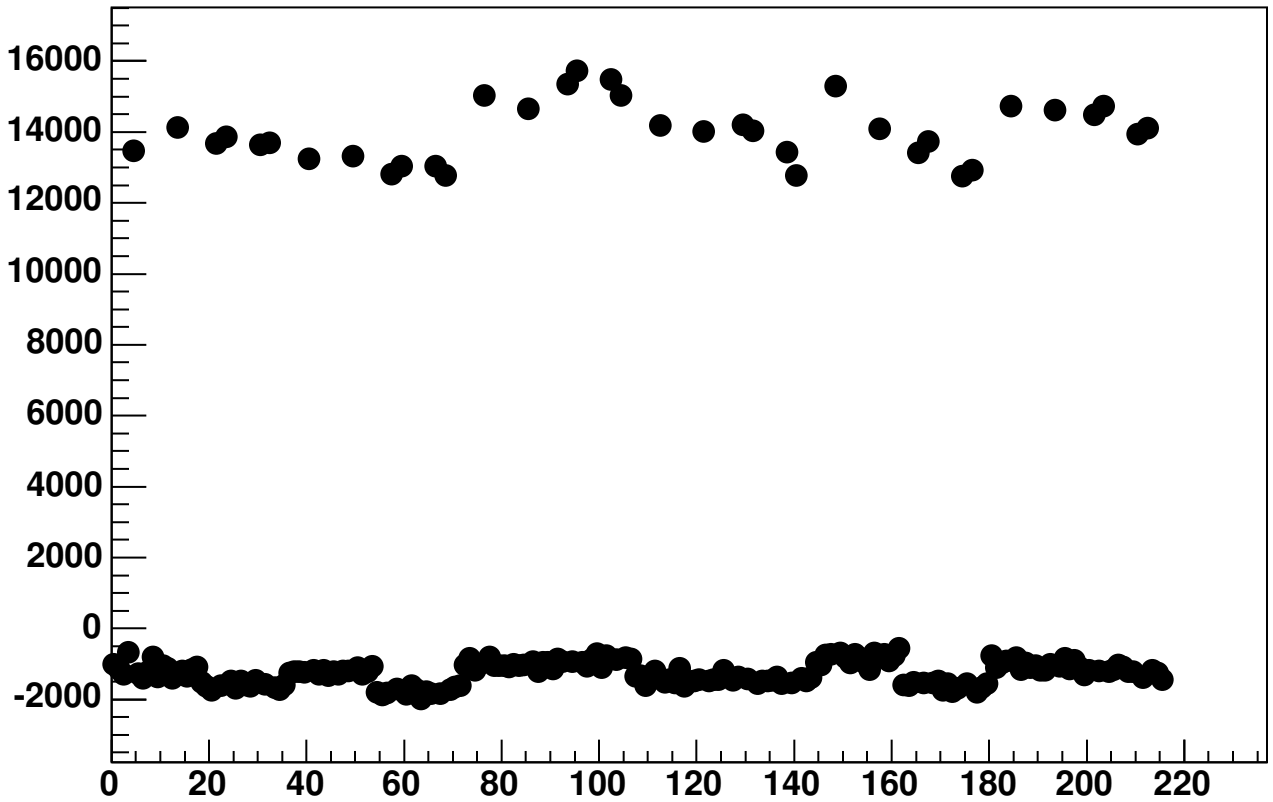
Enable 4, Hold=30, DAC=750, ADC Mean vs 18*Chip+Chan



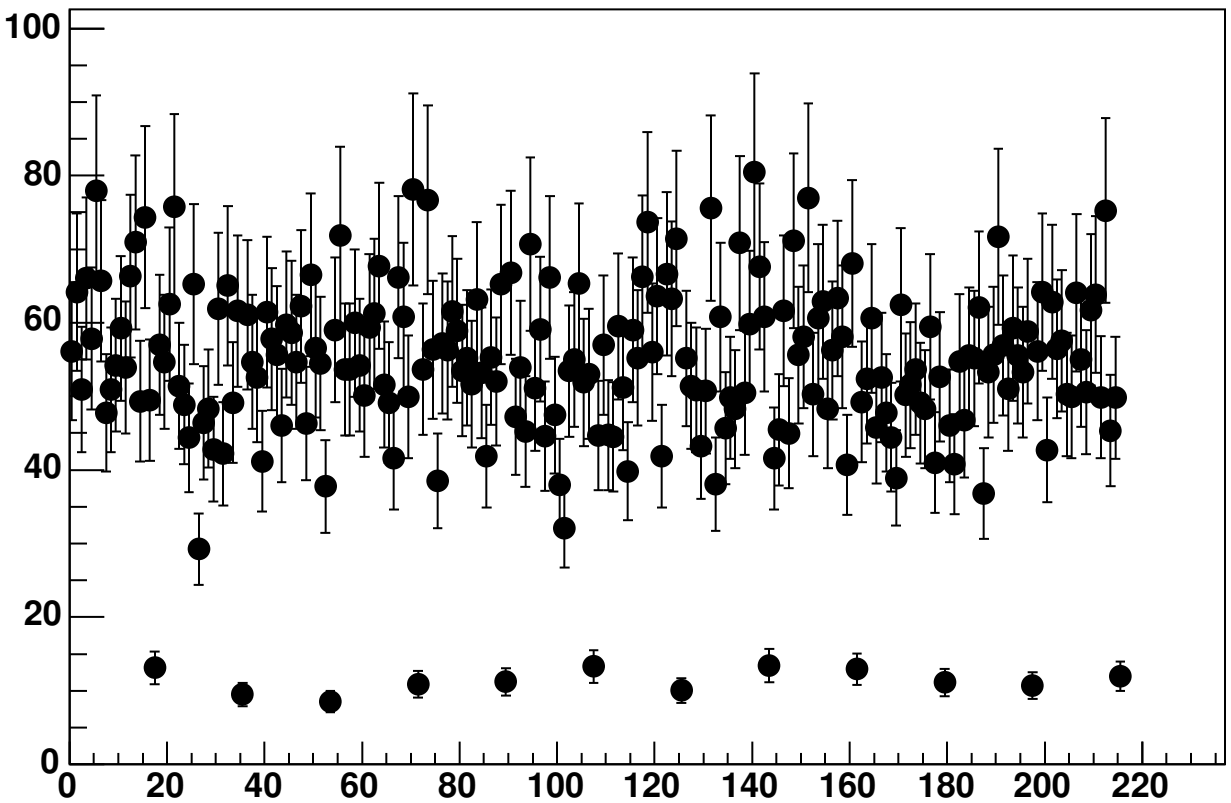
Enable 4, Hold=30, DAC=750, ADC Noise vs 18*Chip+Chan



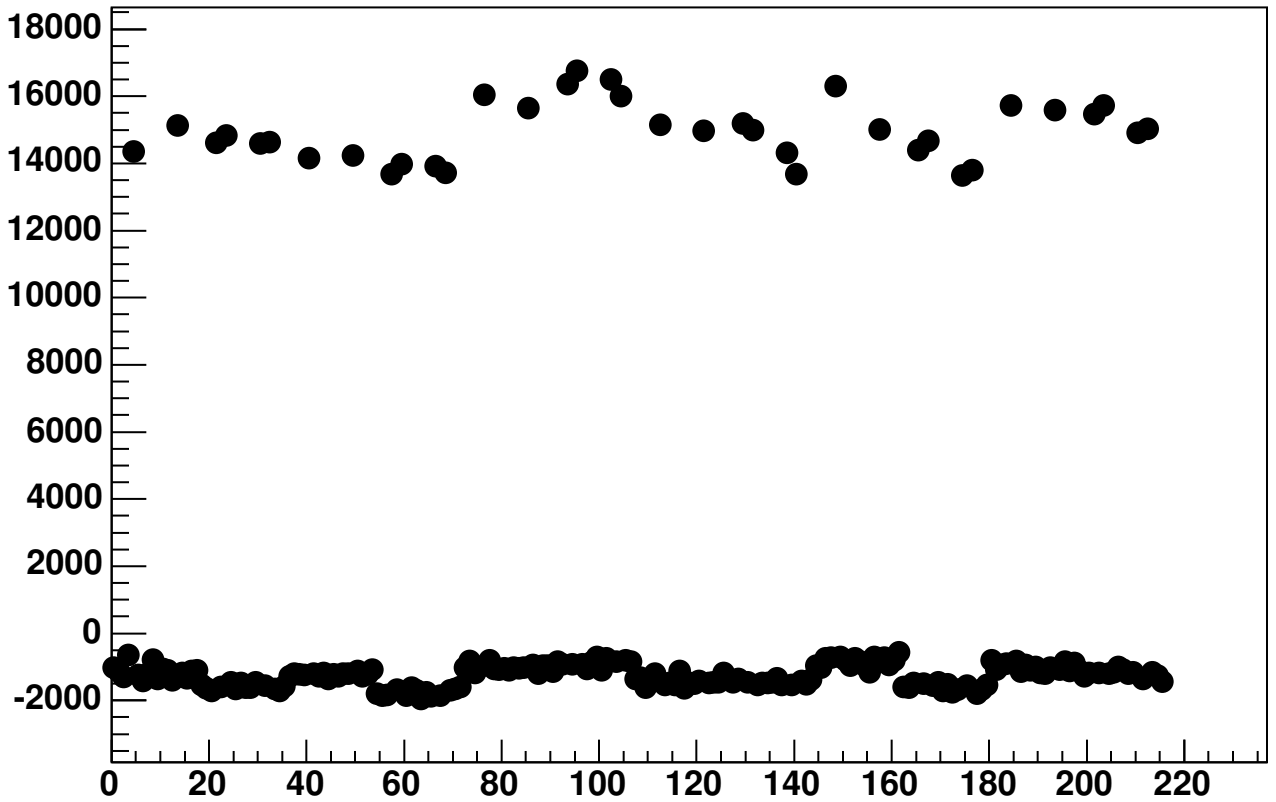
Enable 4, Hold=30, DAC=800, ADC Mean vs 18*Chip+Chan



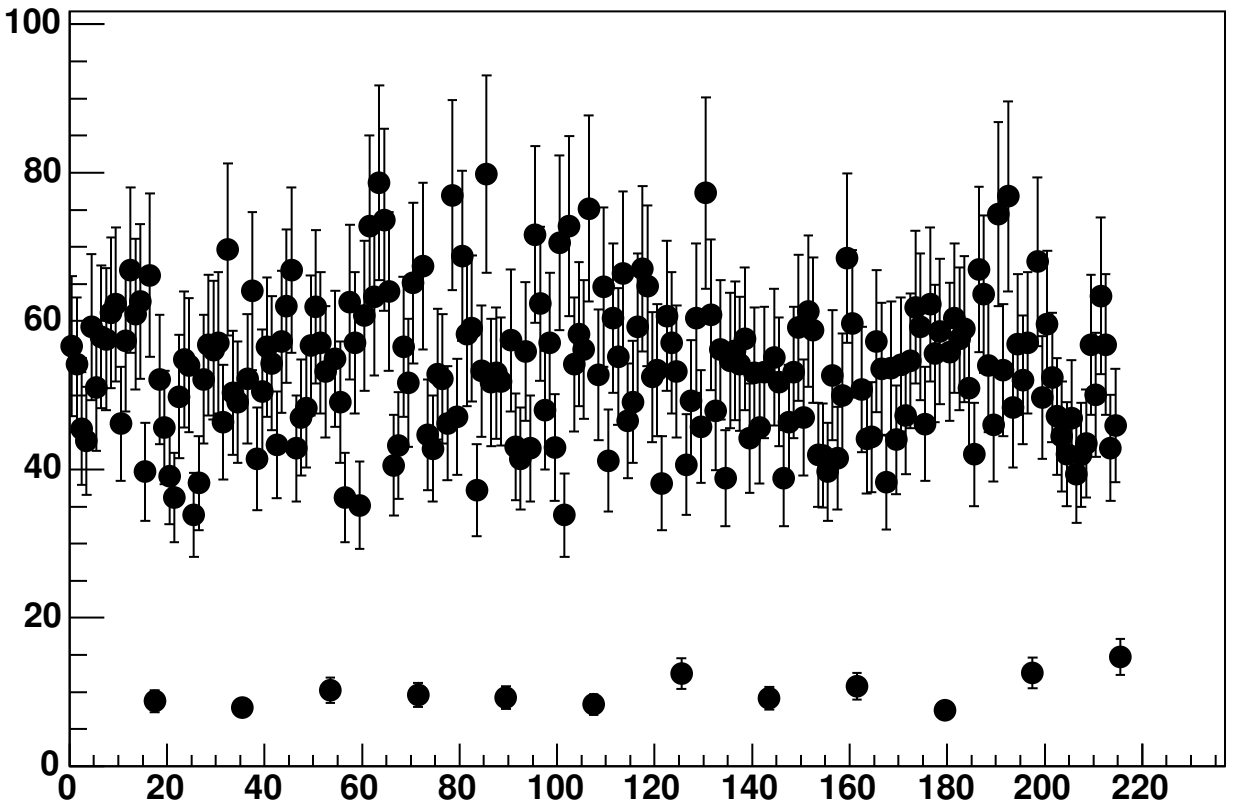
Enable 4, Hold=30, DAC=800, ADC Noise vs 18*Chip+Chan



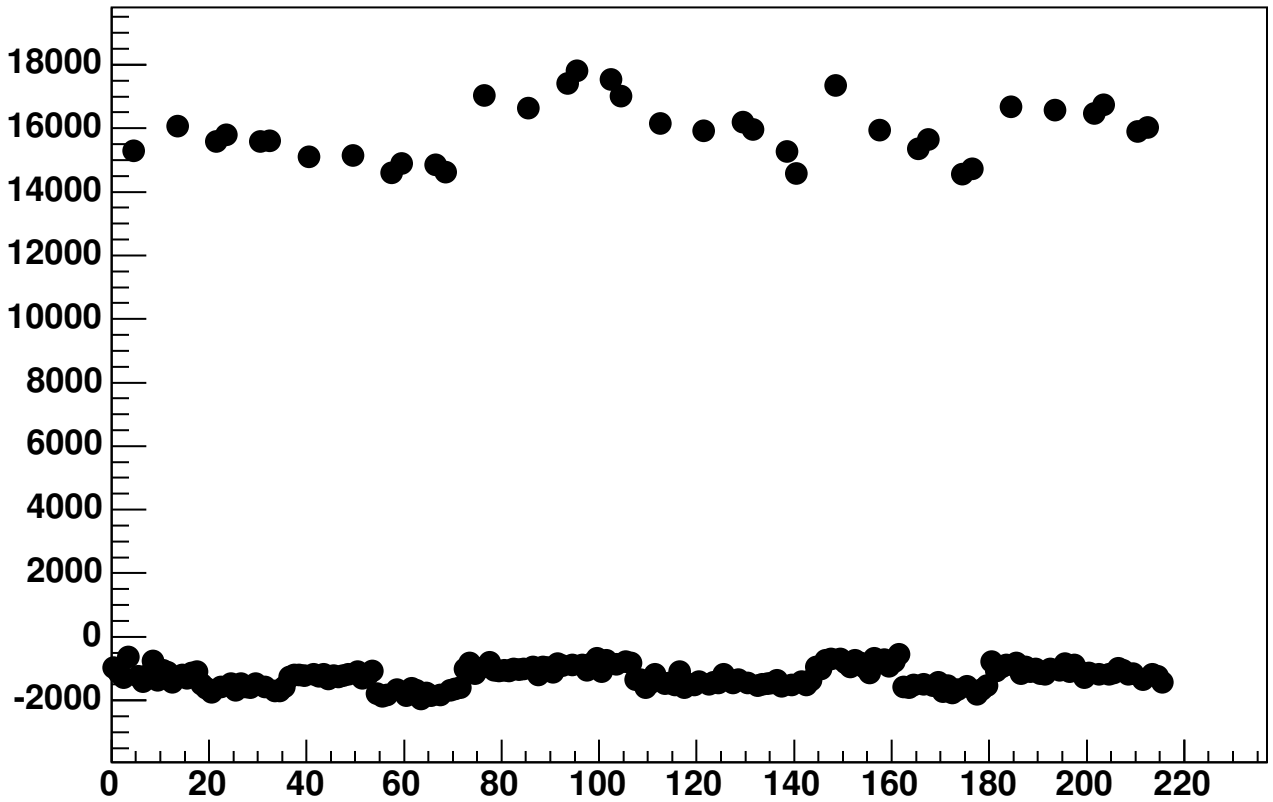
Enable 4, Hold=30, DAC=850, ADC Mean vs 18*Chip+Chan



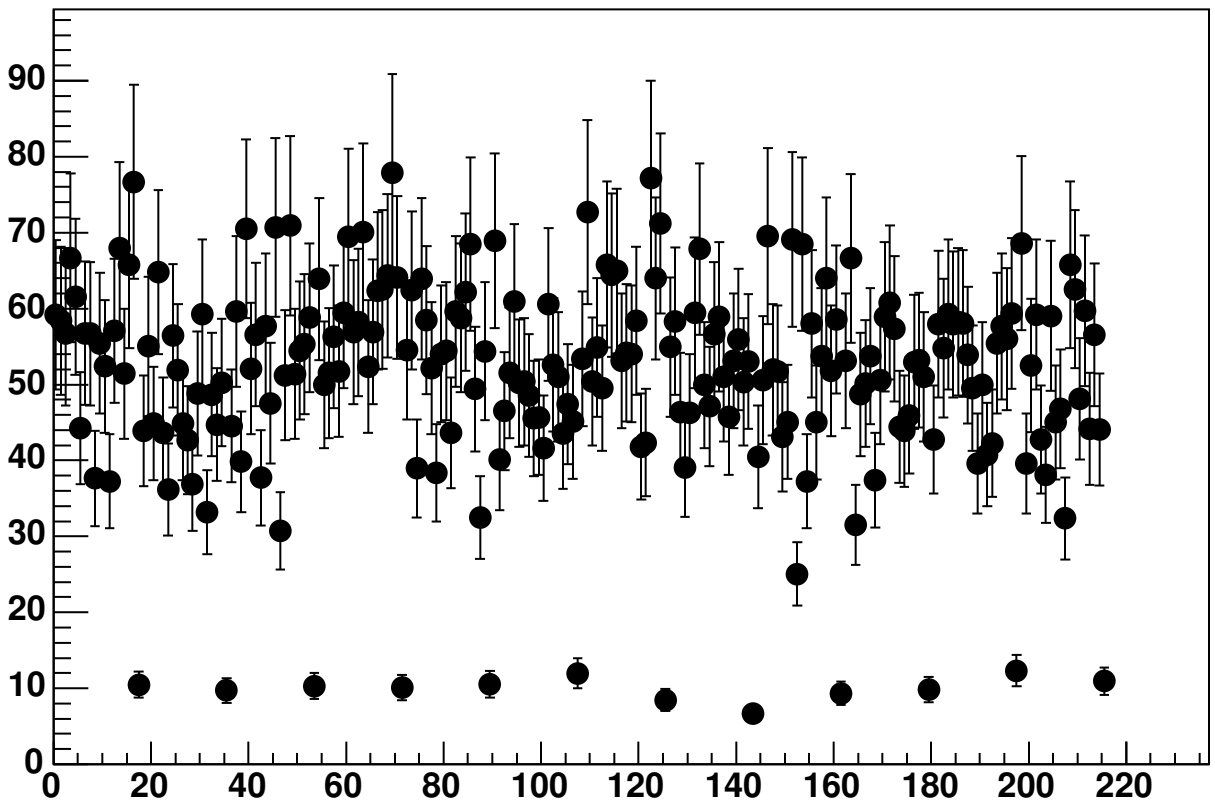
Enable 4, Hold=30, DAC=850, ADC Noise vs 18*Chip+Chan



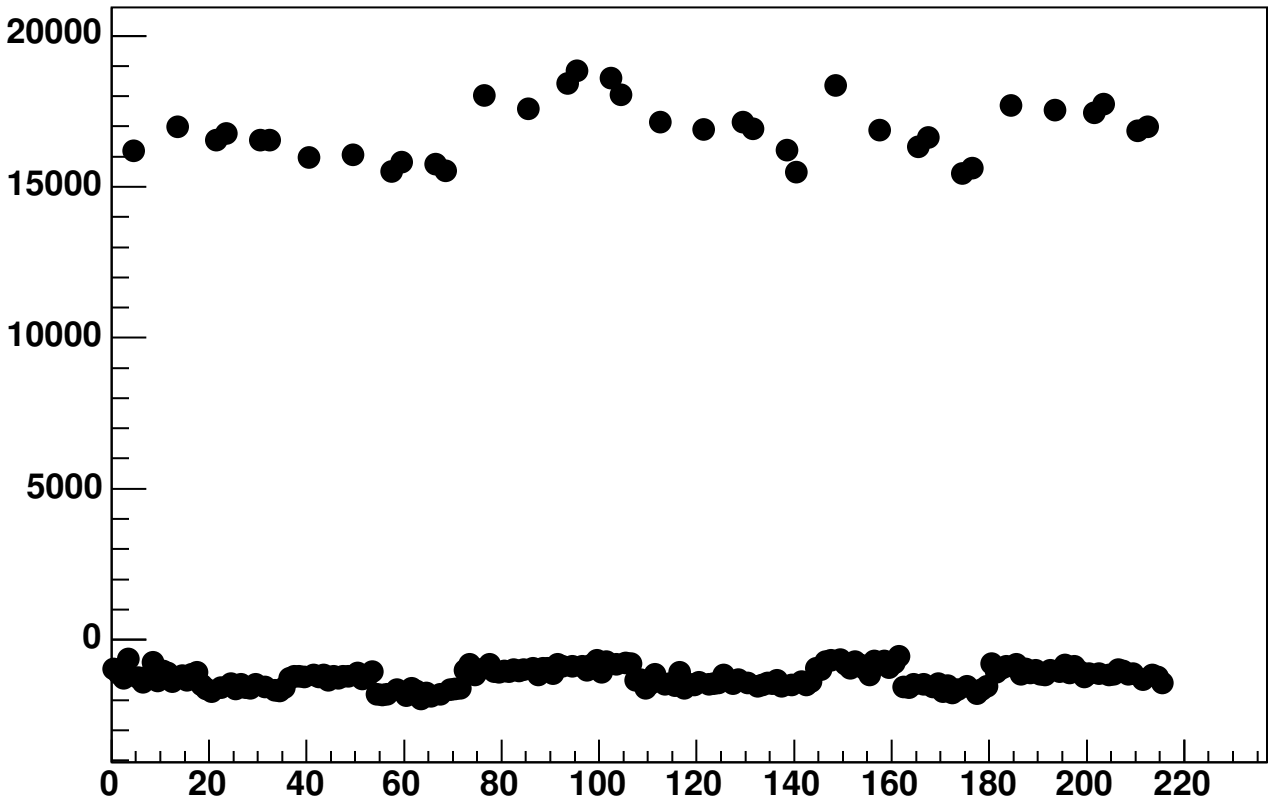
Enable 4, Hold=30, DAC=900, ADC Mean vs 18*Chip+Chan



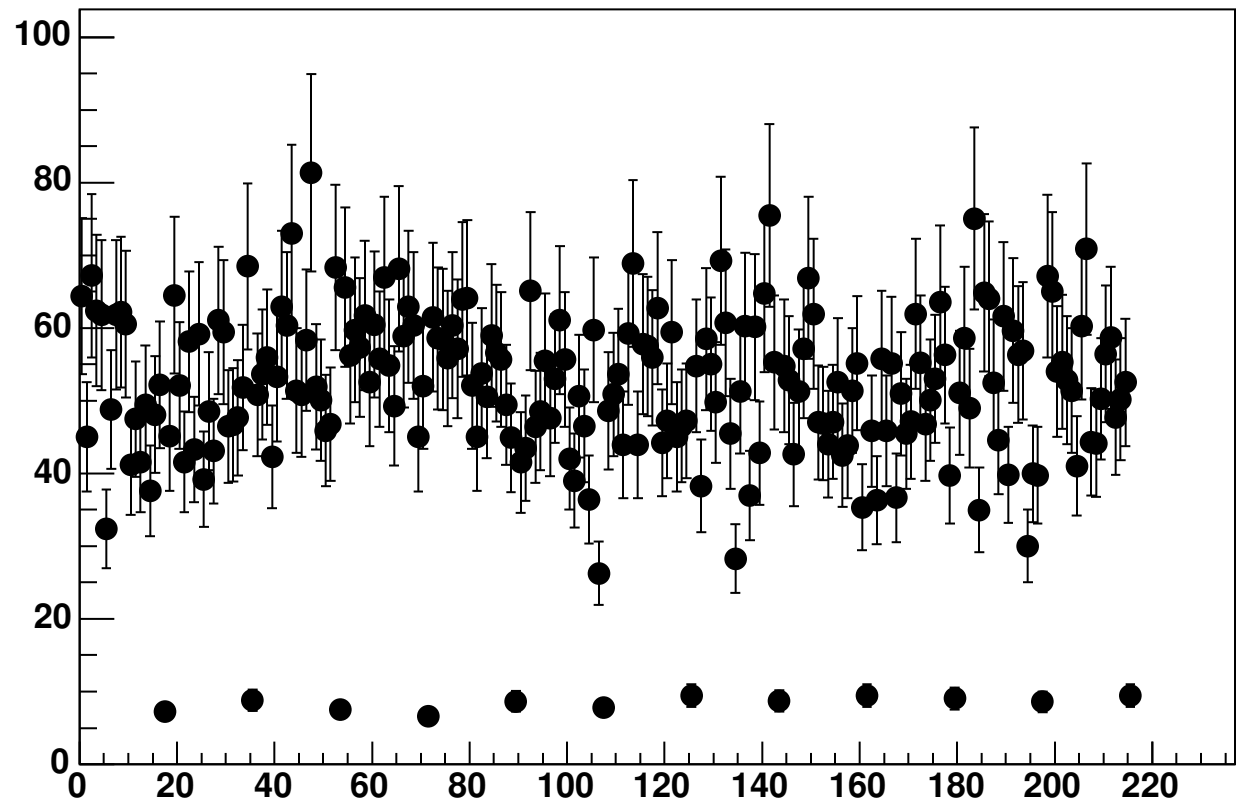
Enable 4, Hold=30, DAC=900, ADC Noise vs 18*Chip+Chan



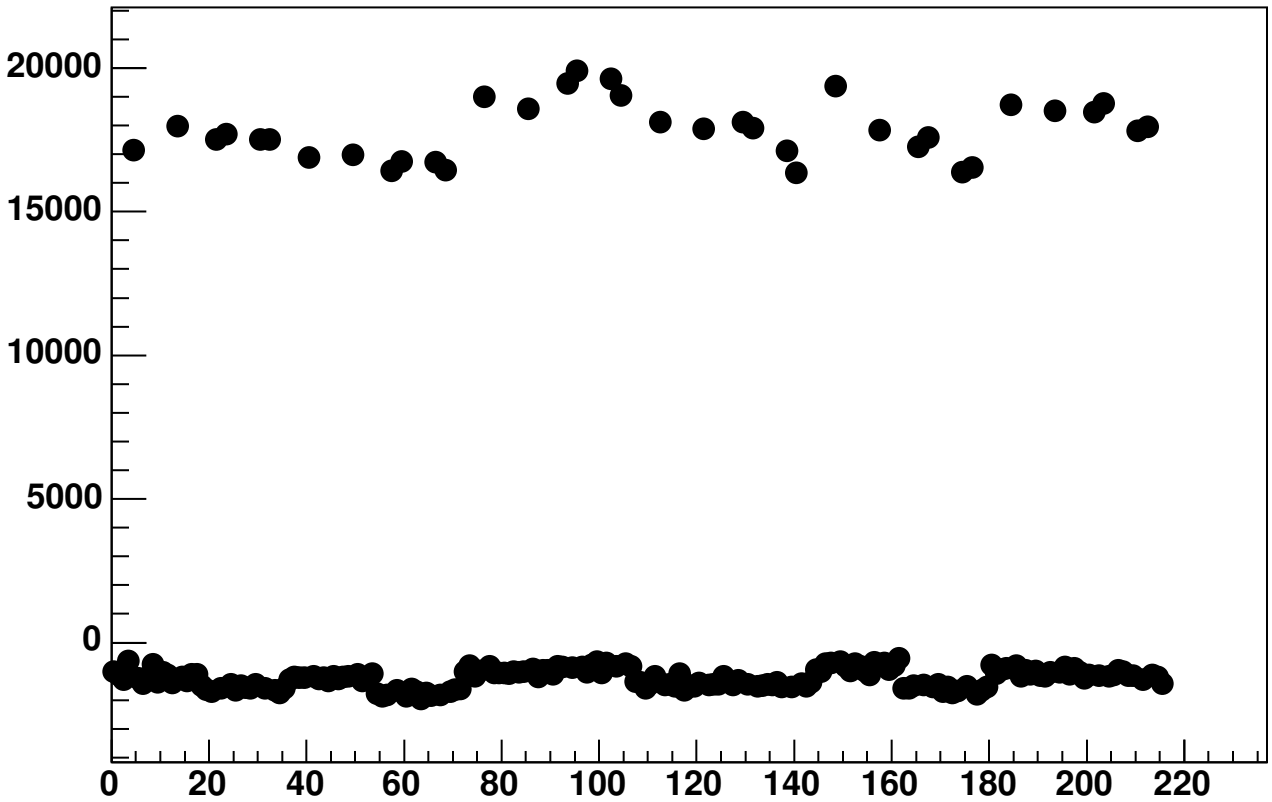
Enable 4, Hold=30, DAC=950, ADC Mean vs 18*Chip+Chan



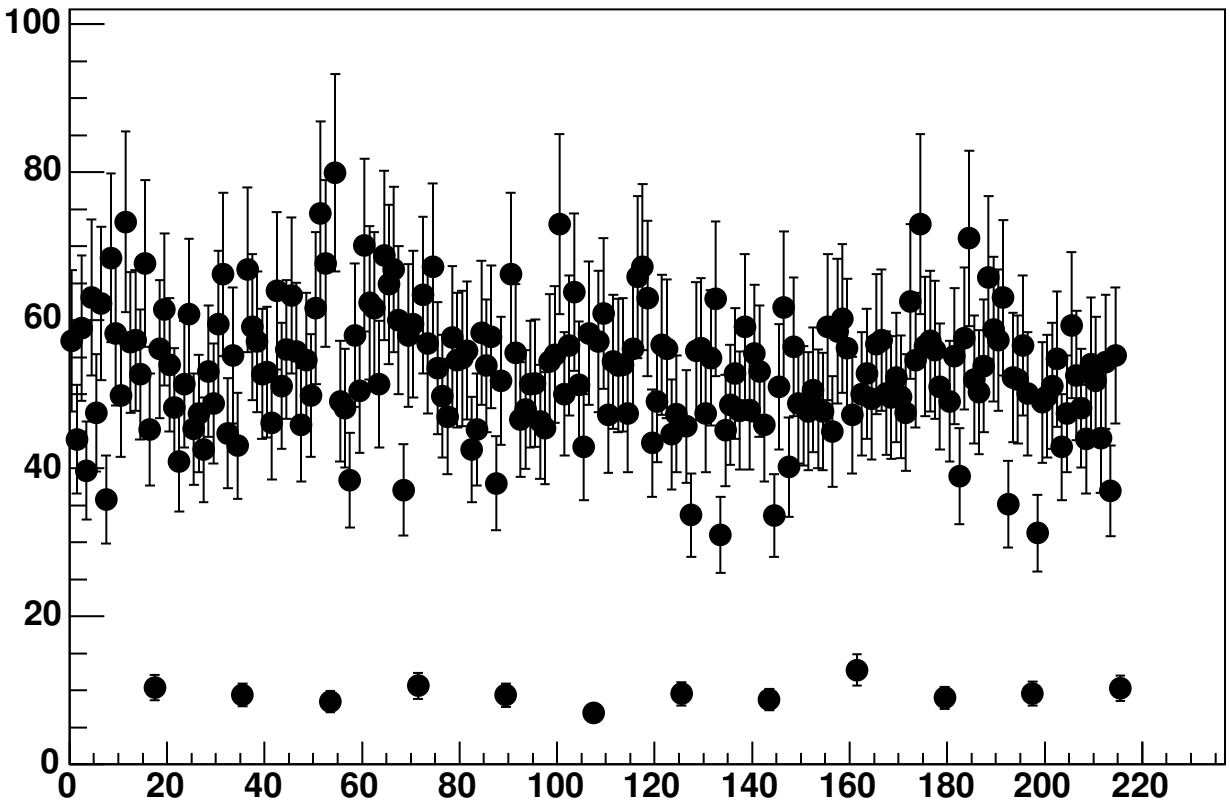
Enable 4, Hold=30, DAC=950, ADC Noise vs 18*Chip+Chan



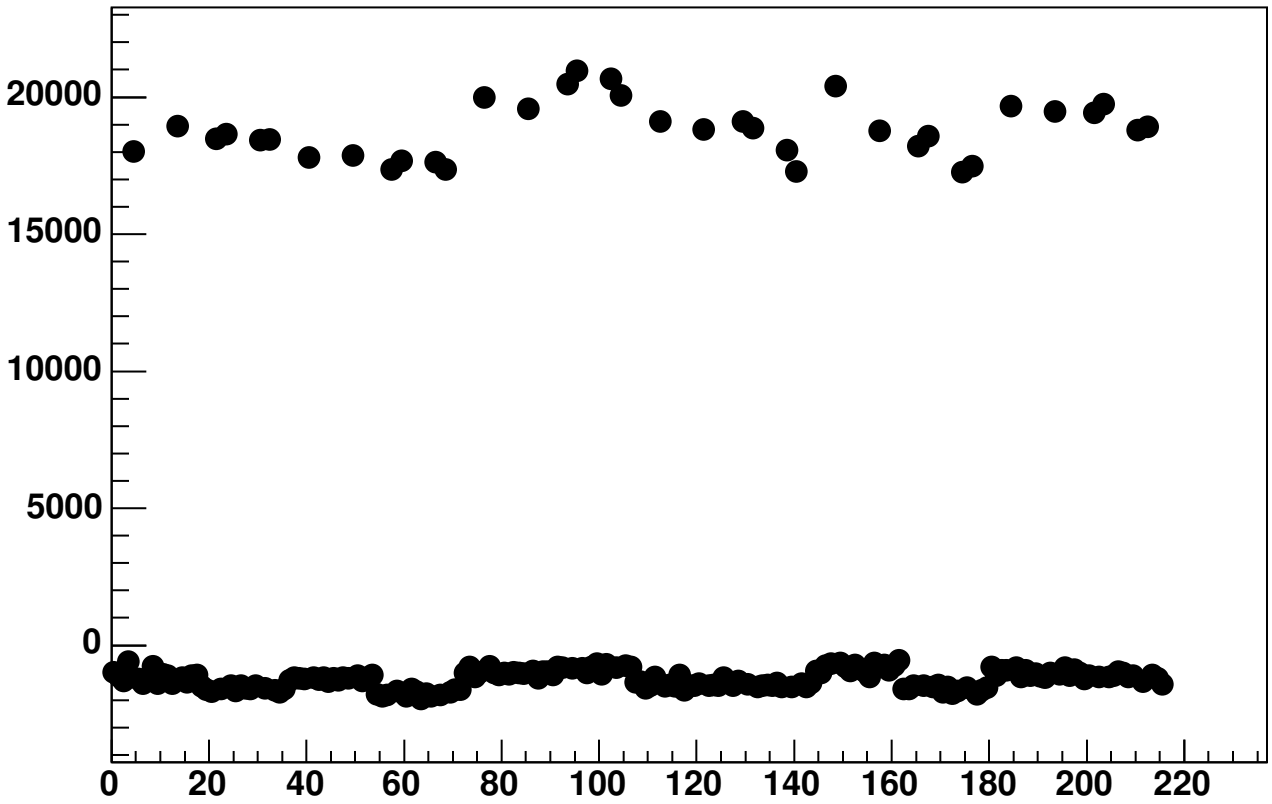
Enable 4, Hold=30, DAC=1000, ADC Mean vs 18*Chip+Chan



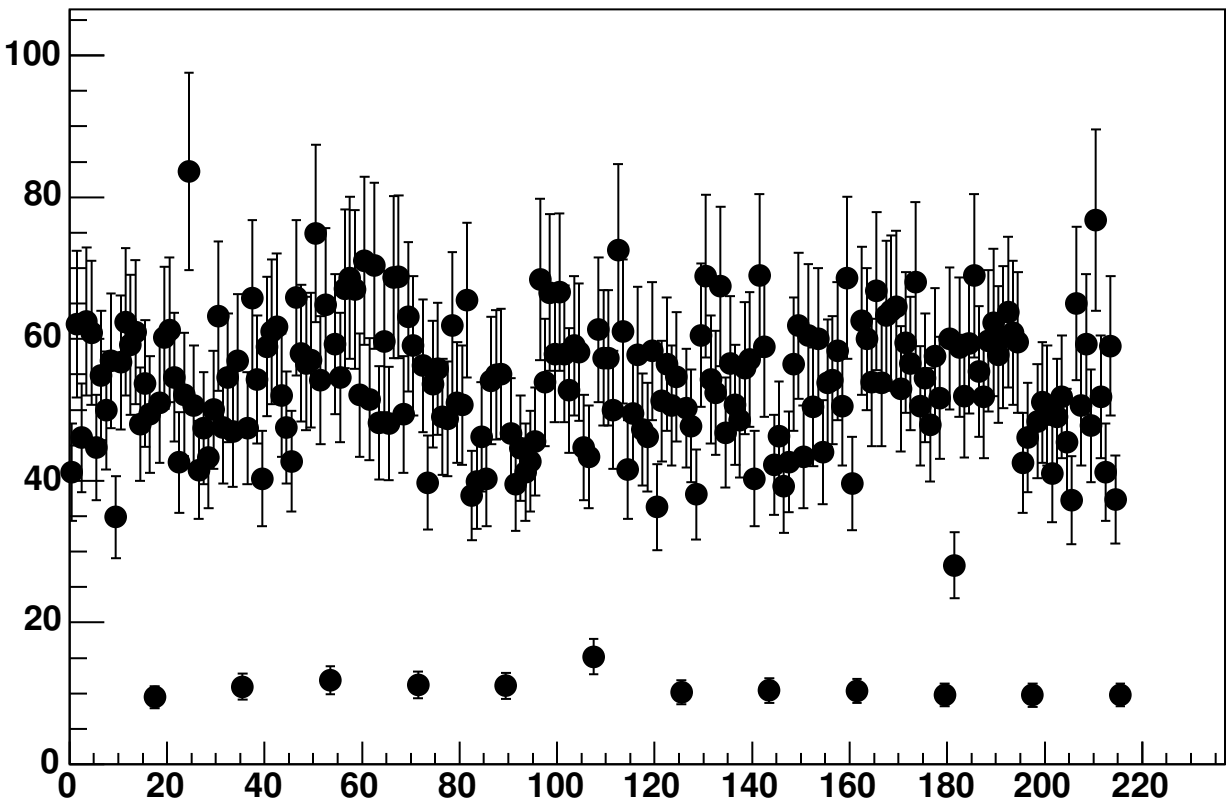
Enable 4, Hold=30, DAC=1000, ADC Noise vs 18*Chip+Chan



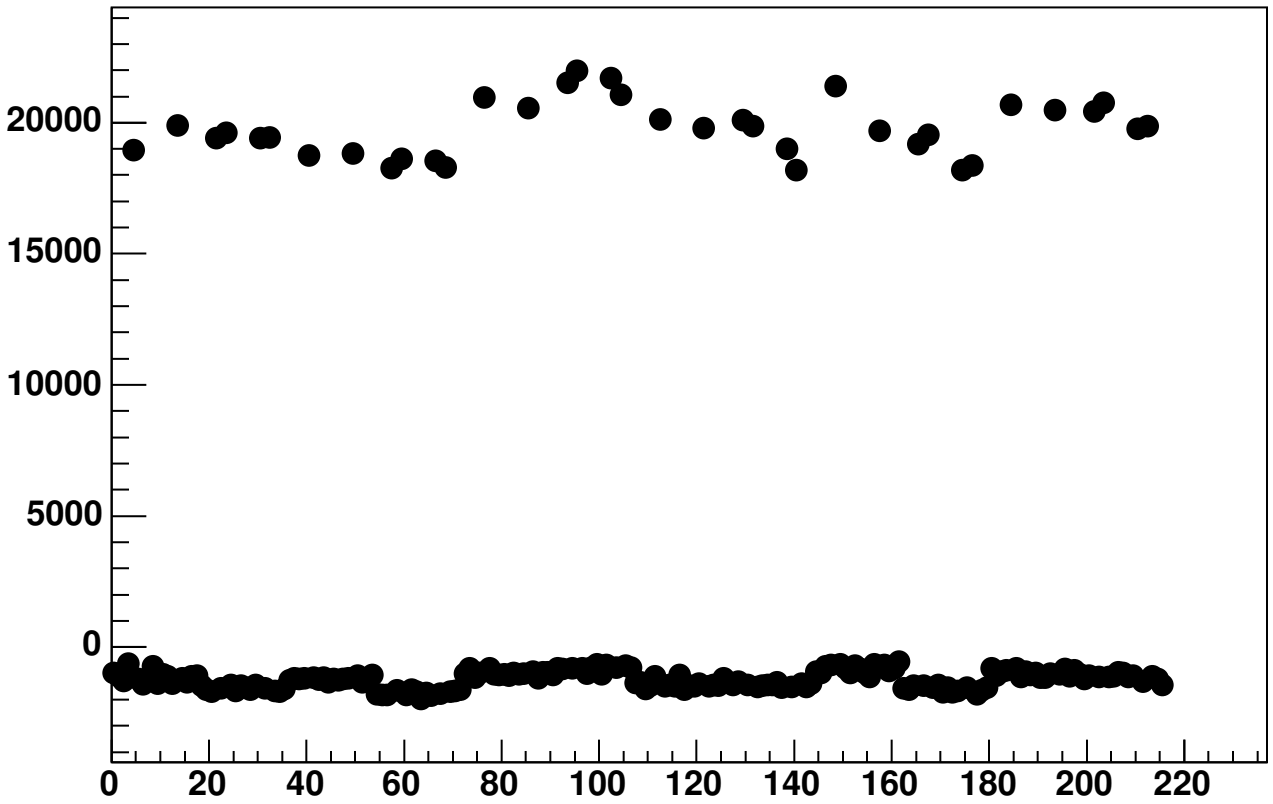
Enable 4, Hold=30, DAC=1050, ADC Mean vs 18*Chip+Chan



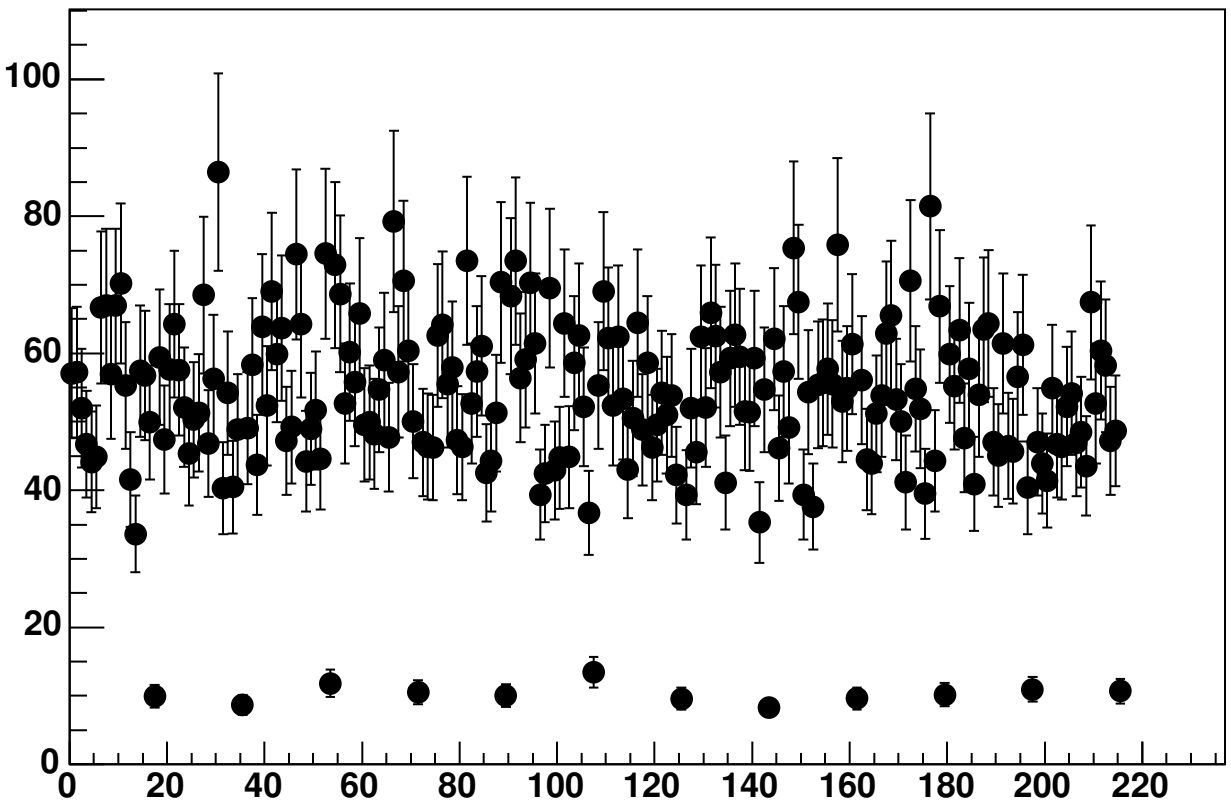
Enable 4, Hold=30, DAC=1050, ADC Noise vs 18*Chip+Chan



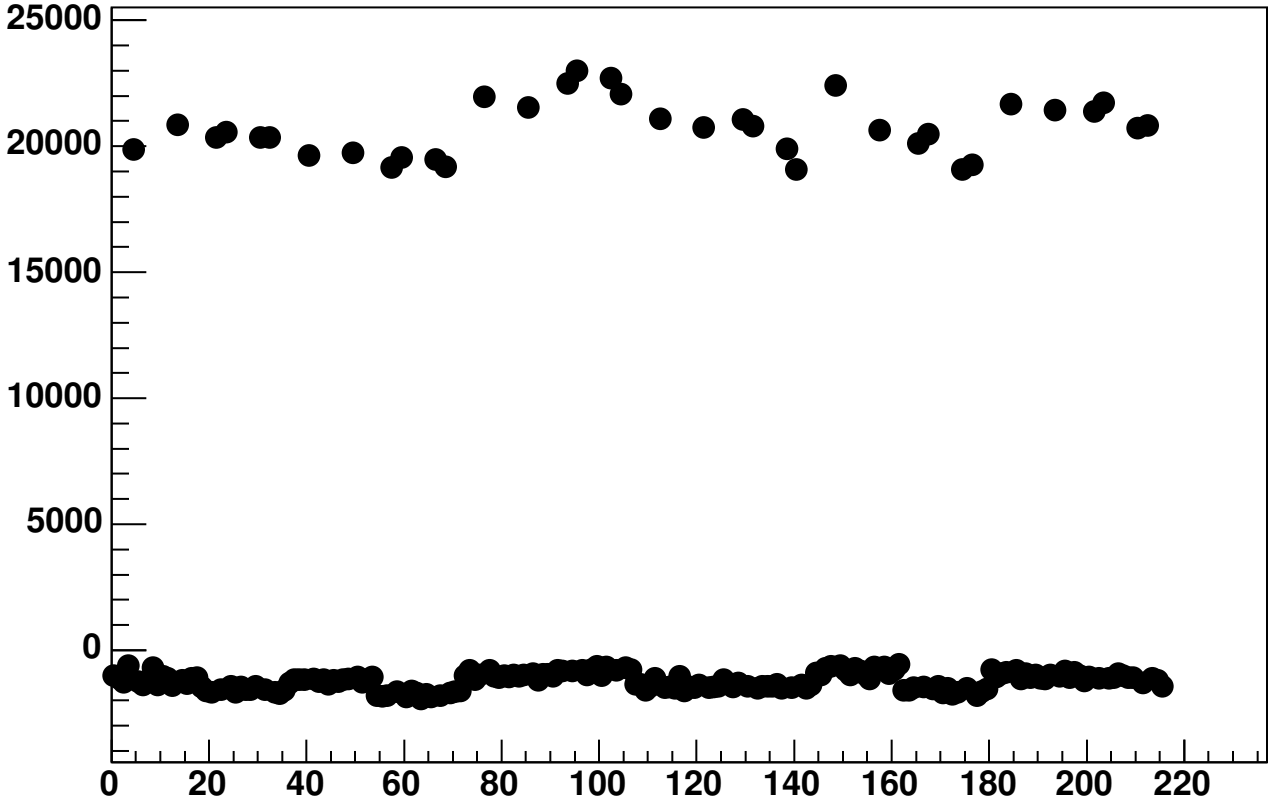
Enable 4, Hold=30, DAC=1100, ADC Mean vs 18*Chip+Chan



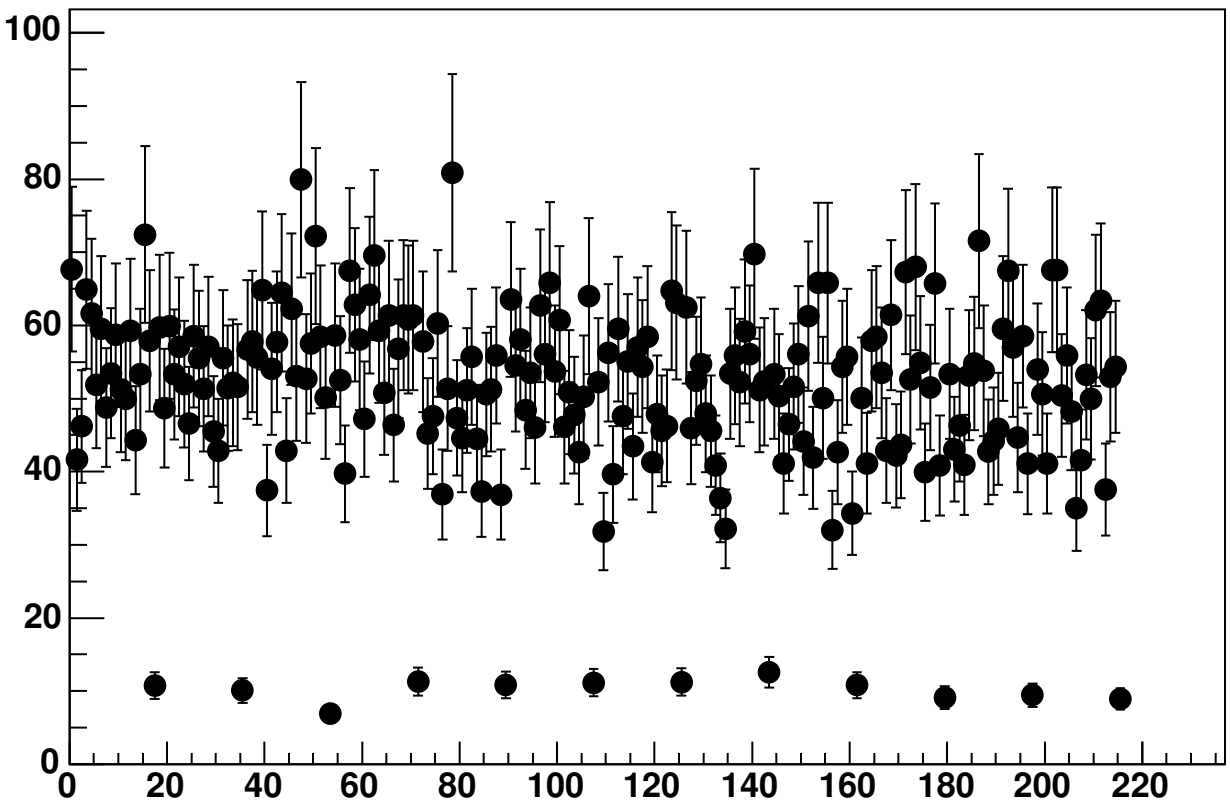
Enable 4, Hold=30, DAC=1100, ADC Noise vs 18*Chip+Chan



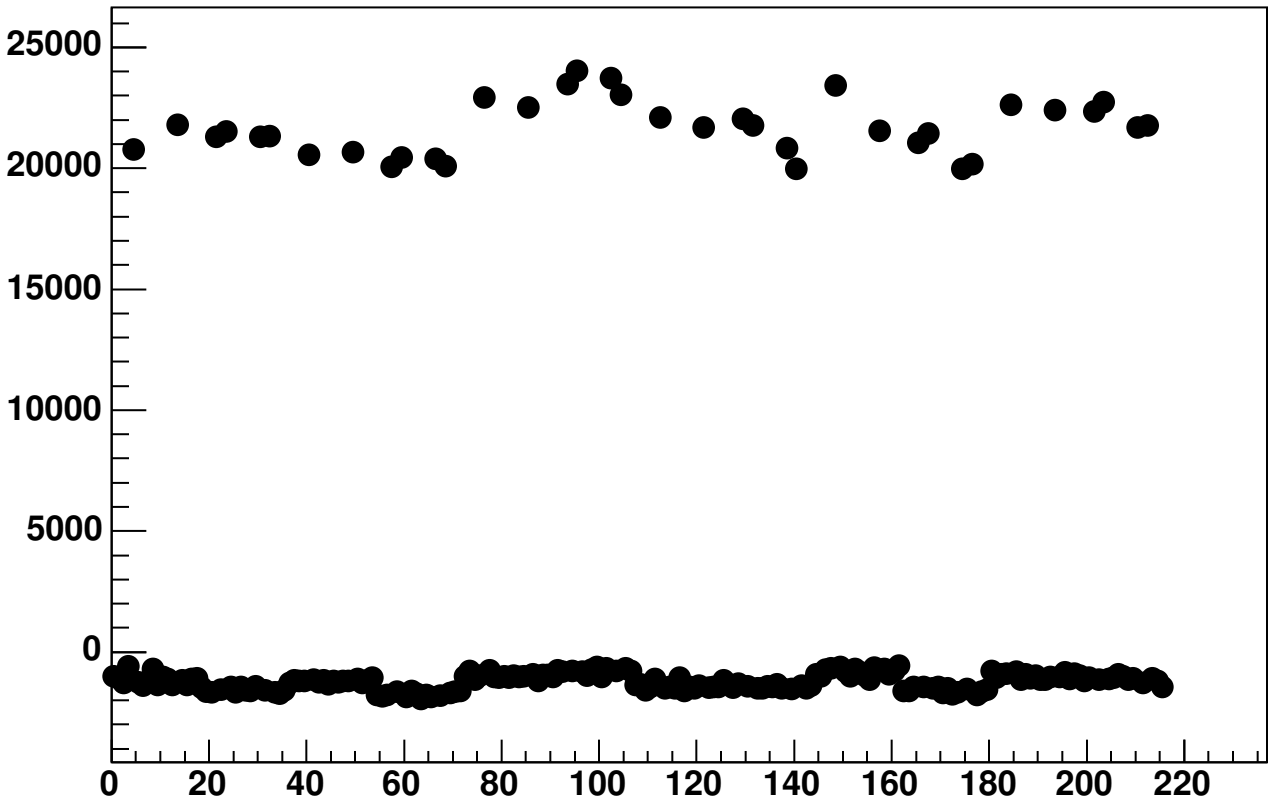
Enable 4, Hold=30, DAC=1150, ADC Mean vs 18*Chip+Chan



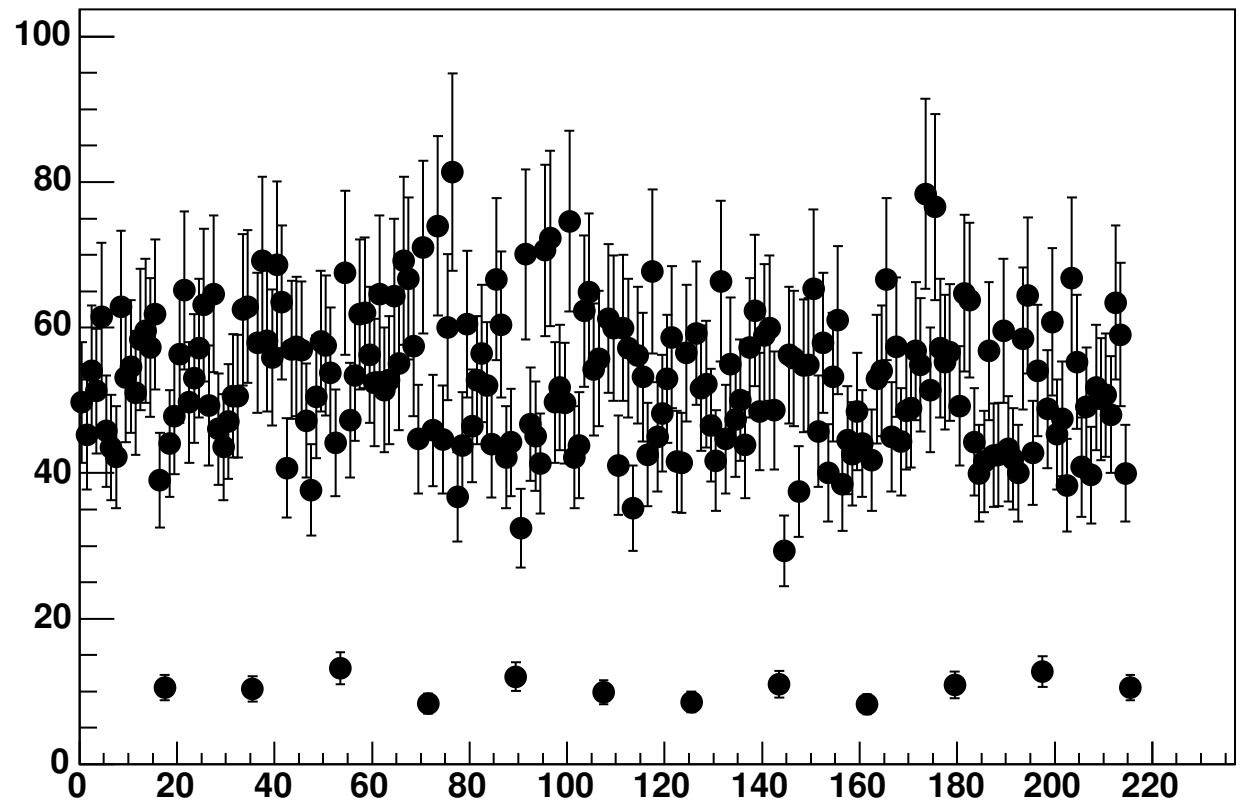
Enable 4, Hold=30, DAC=1150, ADC Noise vs 18*Chip+Chan



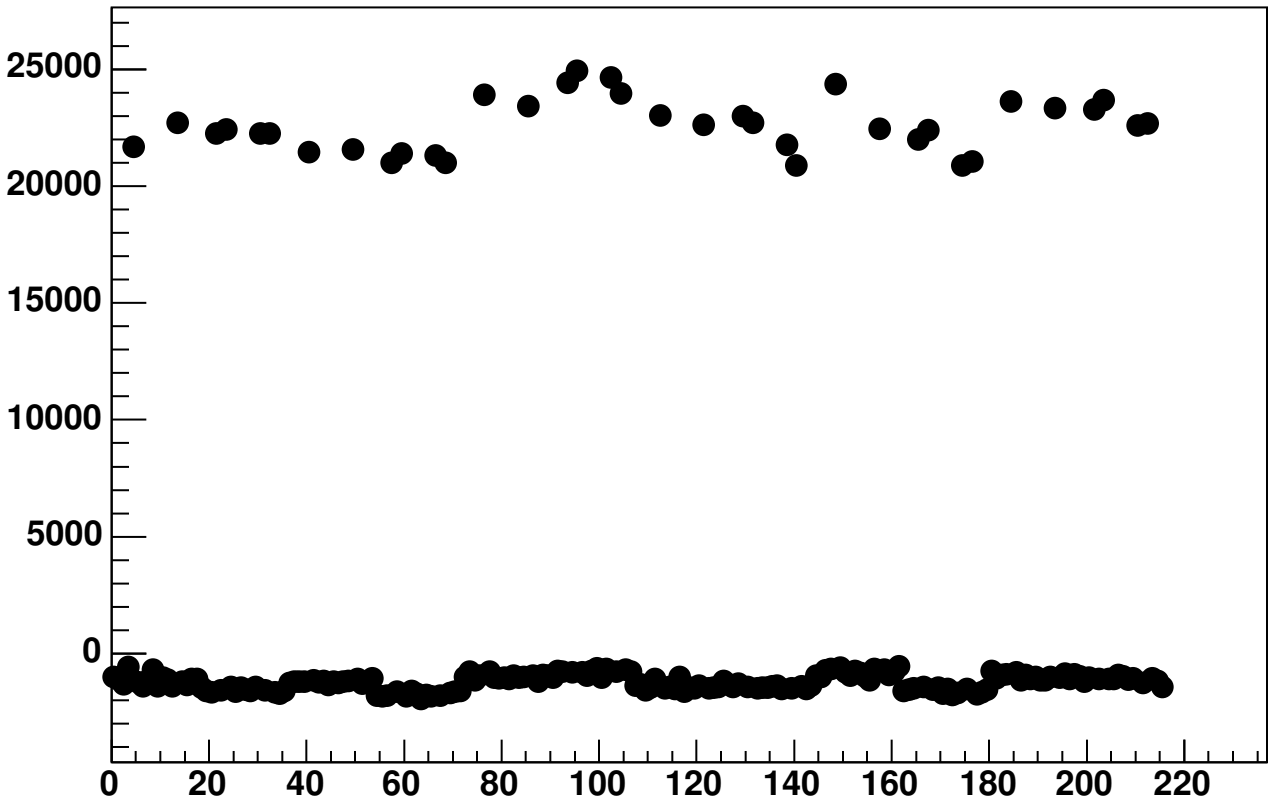
Enable 4, Hold=30, DAC=1200, ADC Mean vs 18*Chip+Chan



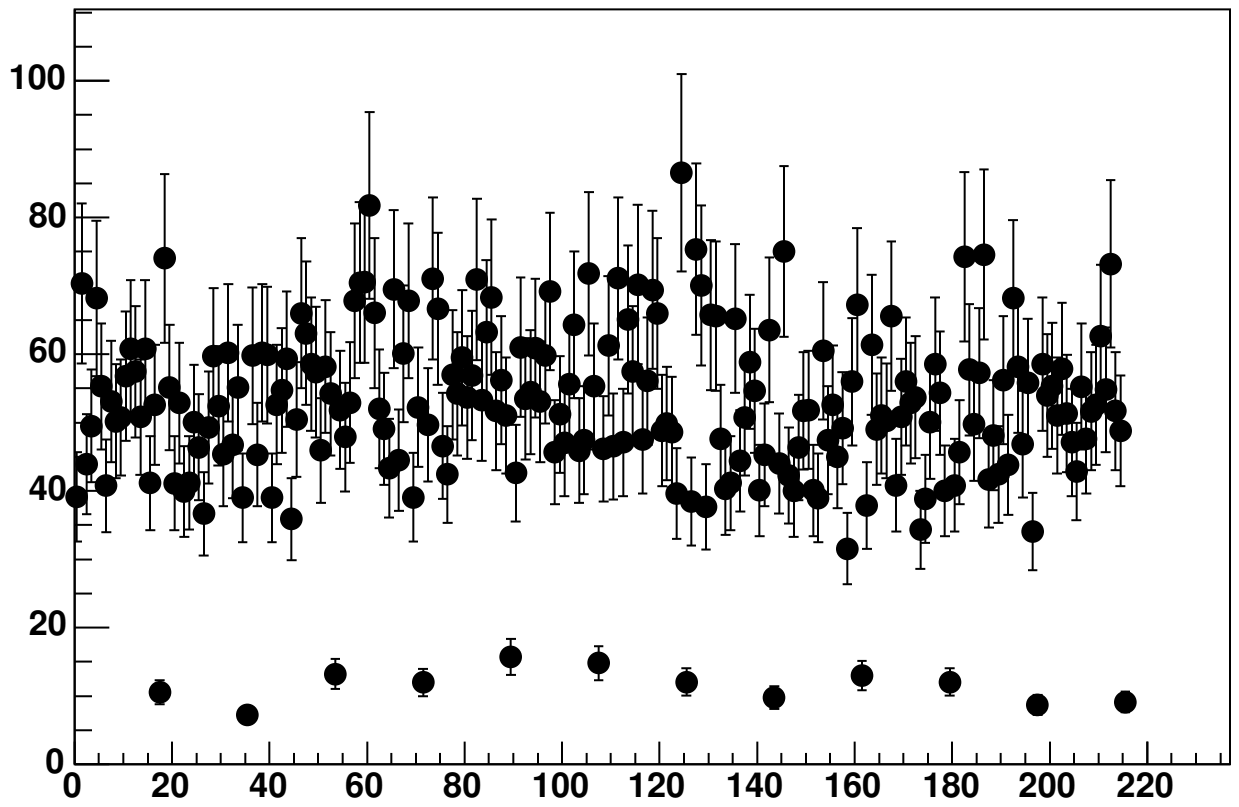
Enable 4, Hold=30, DAC=1200, ADC Noise vs 18*Chip+Chan



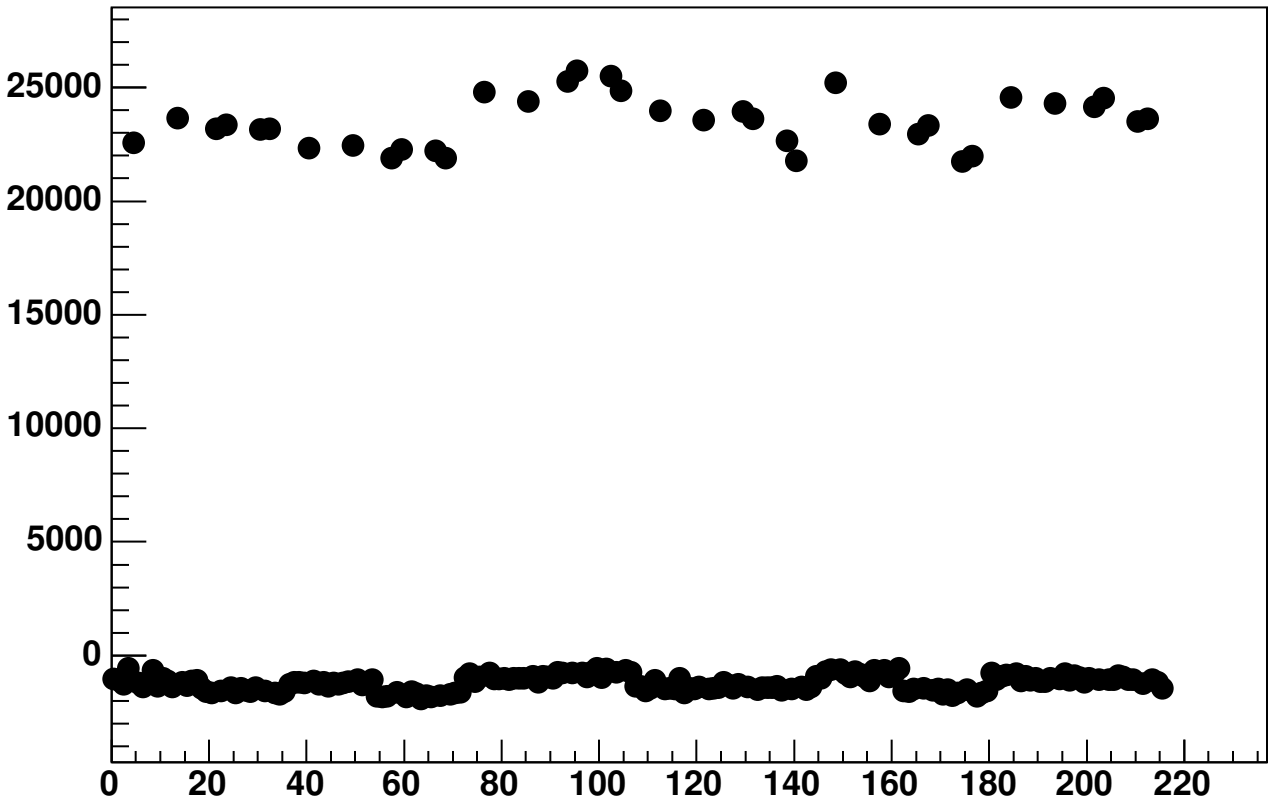
Enable 4, Hold=30, DAC=1250, ADC Mean vs 18*Chip+Chan



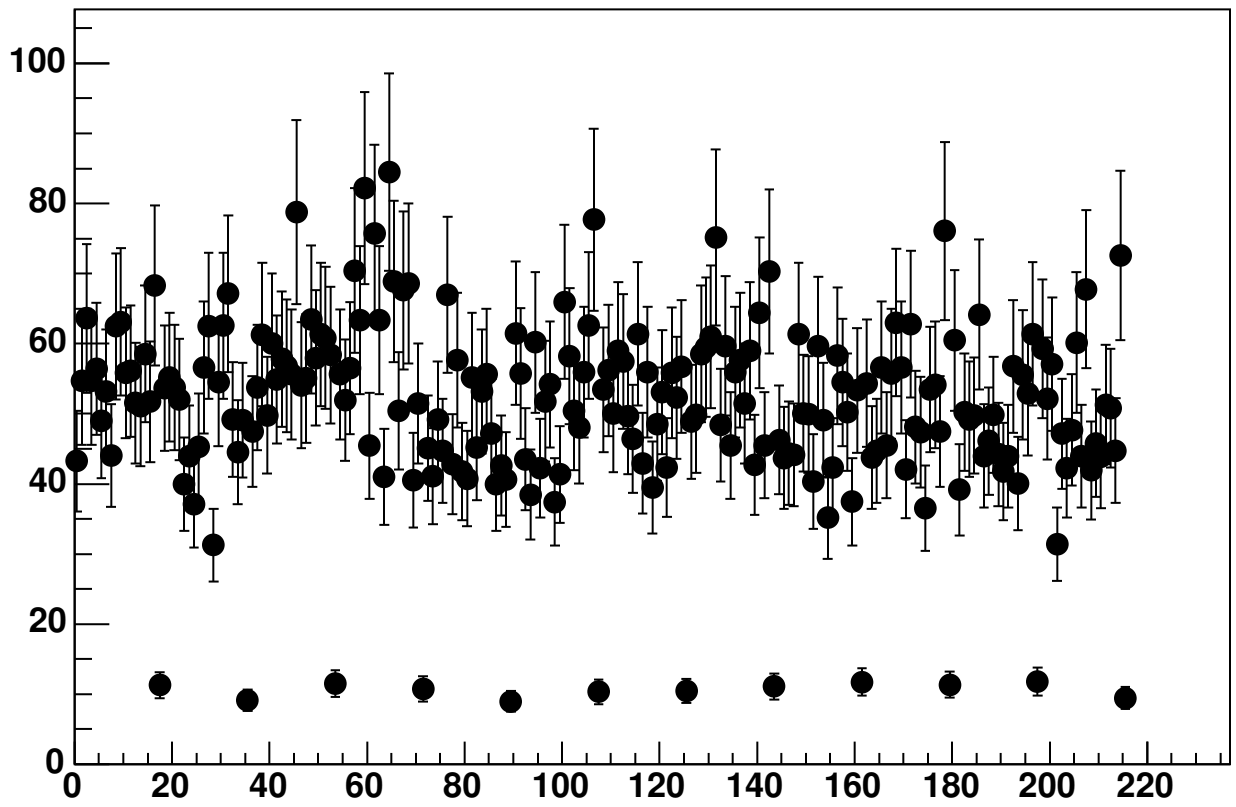
Enable 4, Hold=30, DAC=1250, ADC Noise vs 18*Chip+Chan



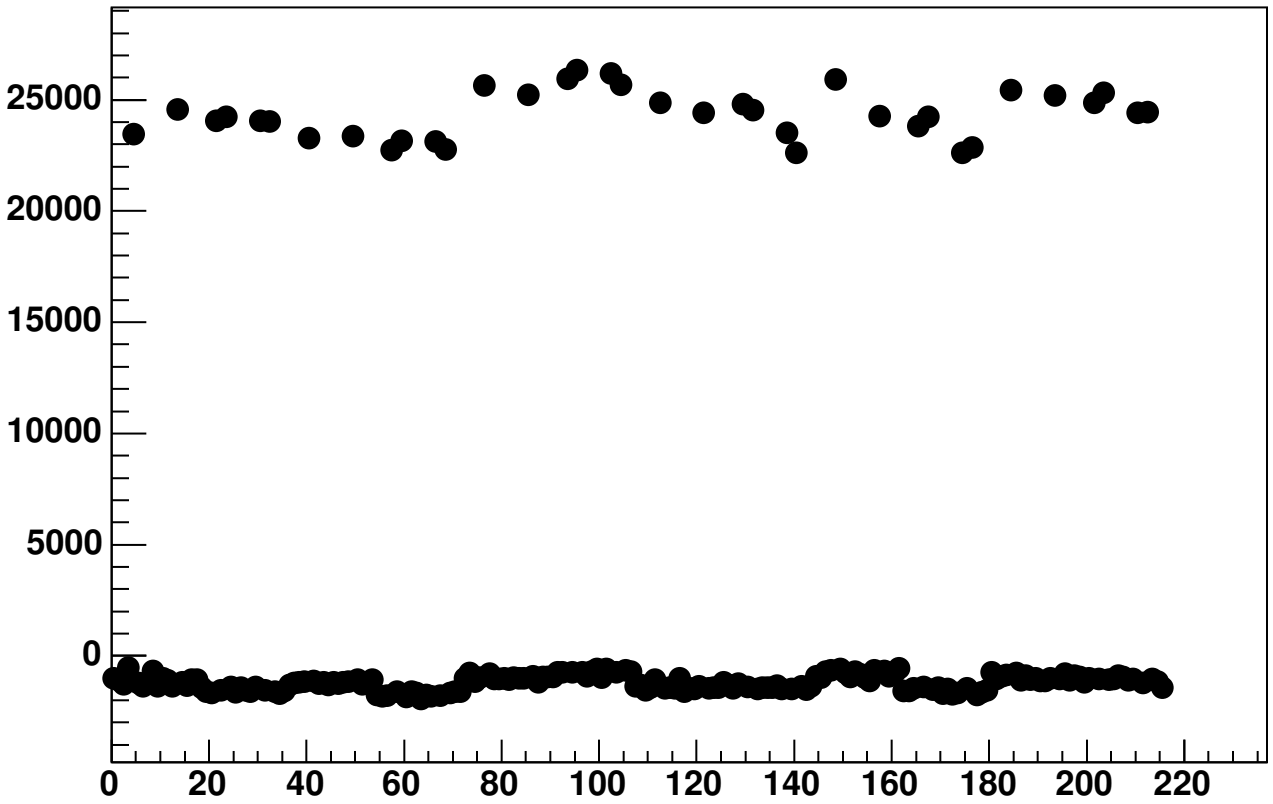
Enable 4, Hold=30, DAC=1300, ADC Mean vs 18*Chip+Chan



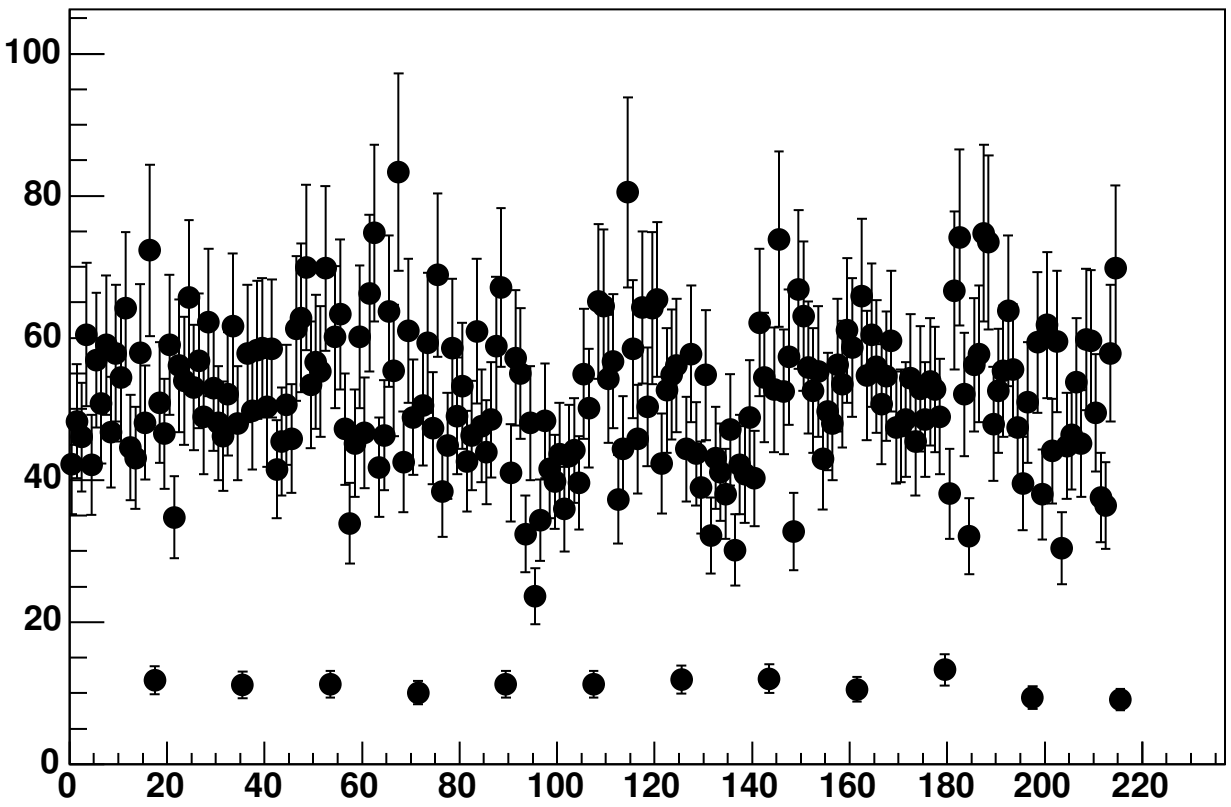
Enable 4, Hold=30, DAC=1300, ADC Noise vs 18*Chip+Chan



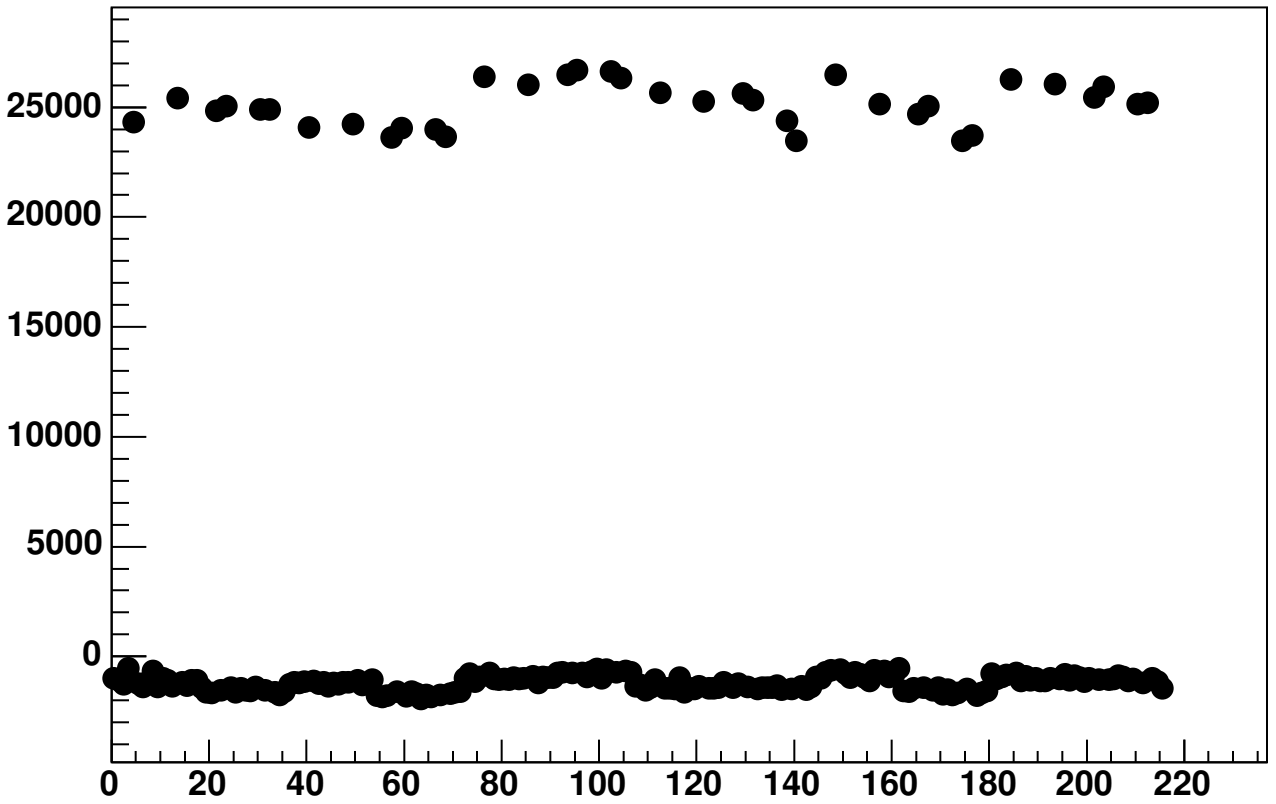
Enable 4, Hold=30, DAC=1350, ADC Mean vs 18*Chip+Chan



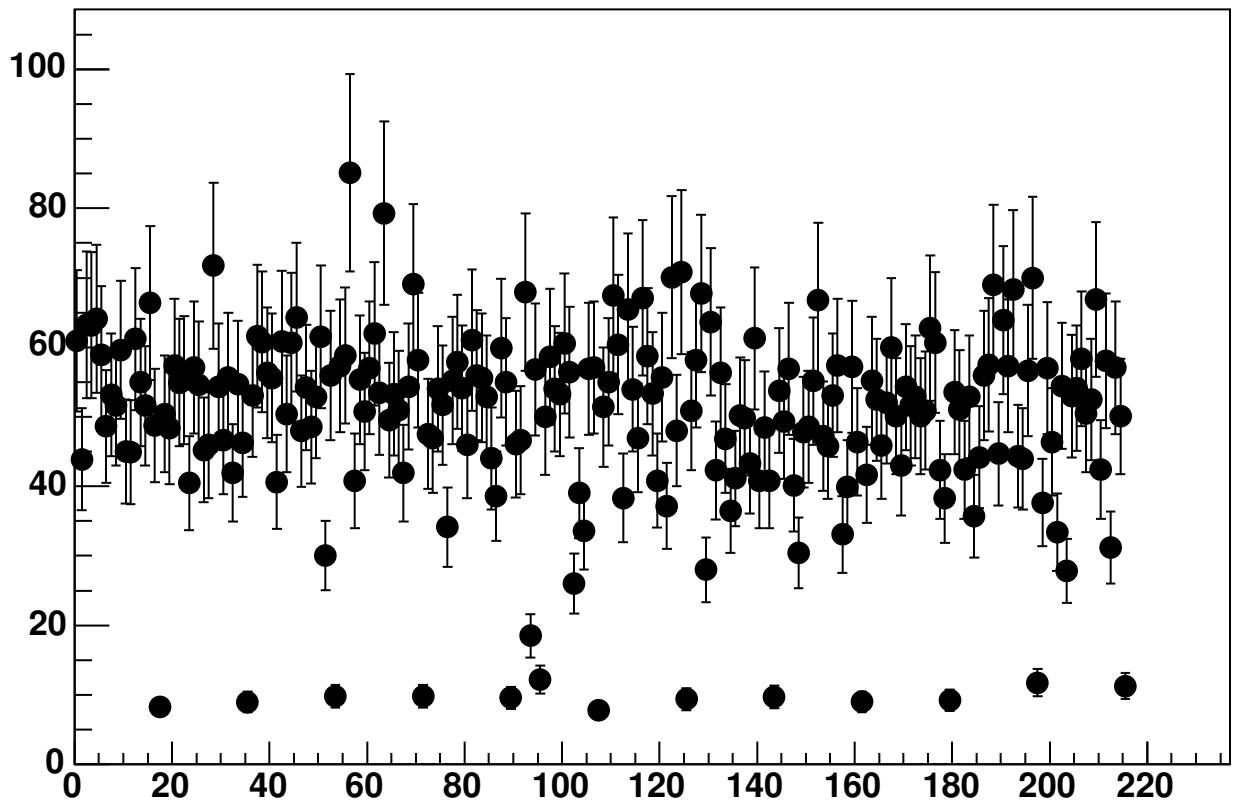
Enable 4, Hold=30, DAC=1350, ADC Noise vs 18*Chip+Chan



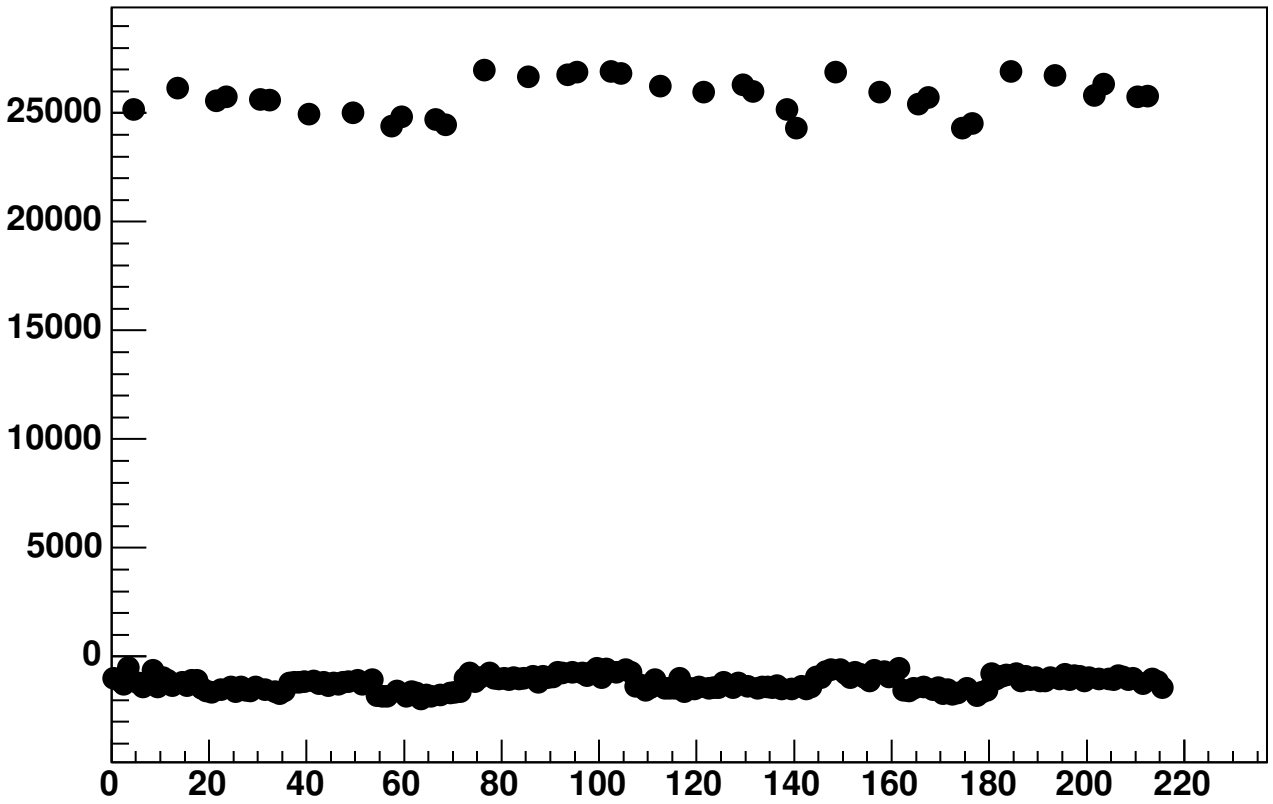
Enable 4, Hold=30, DAC=1400, ADC Mean vs 18*Chip+Chan



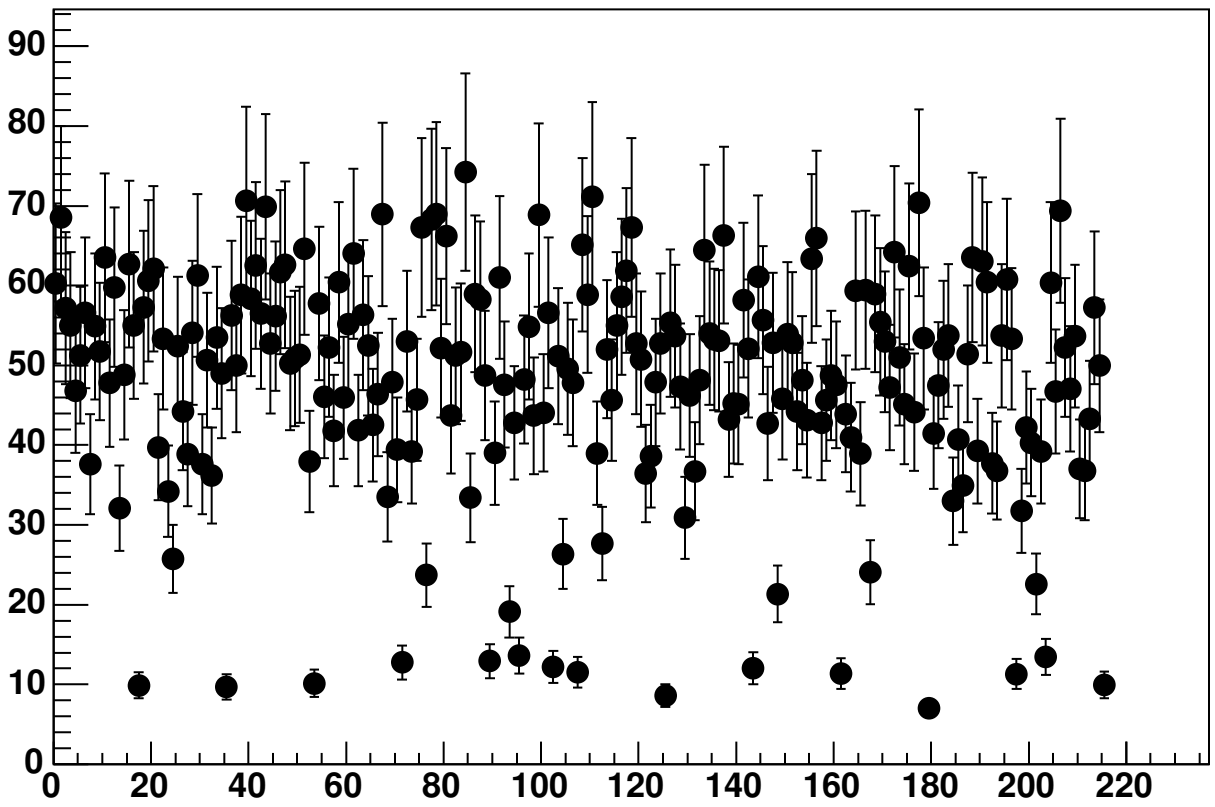
Enable 4, Hold=30, DAC=1400, ADC Noise vs 18*Chip+Chan



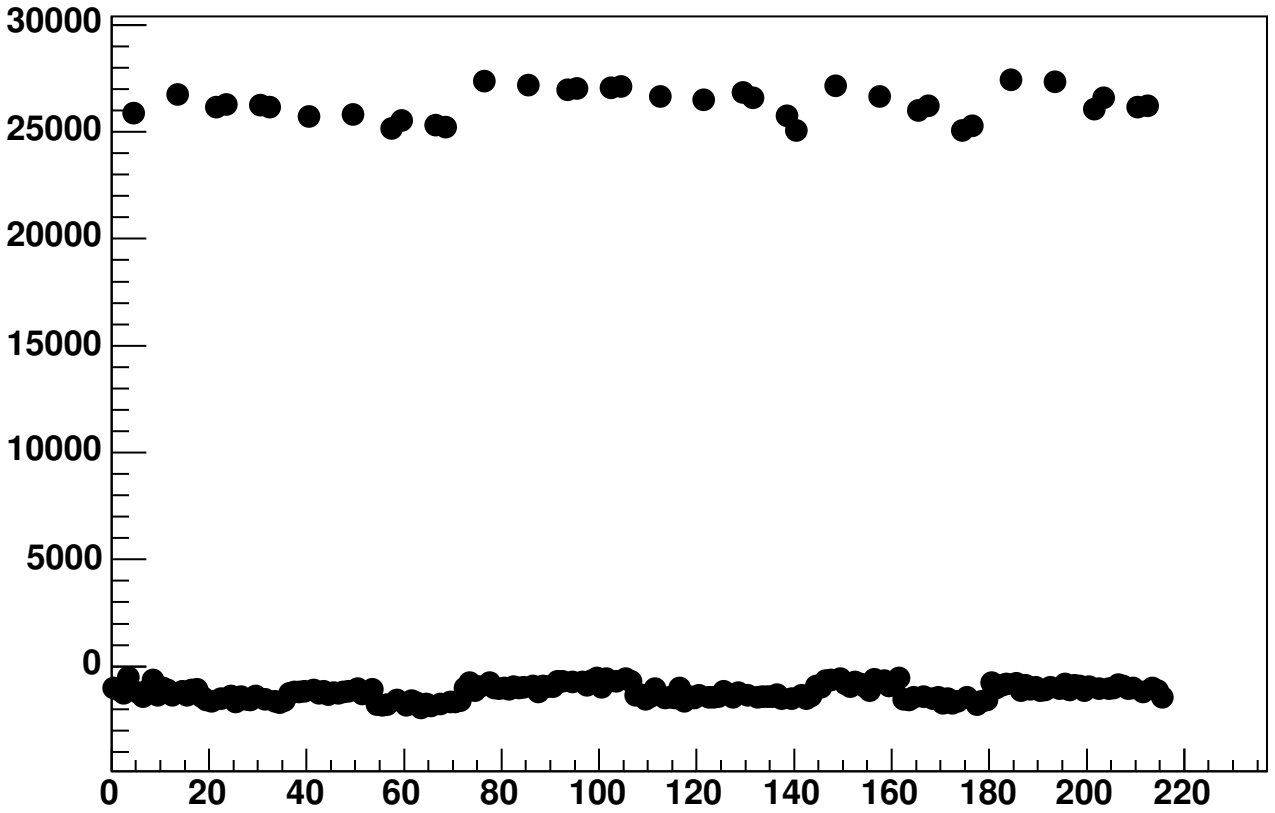
Enable 4, Hold=30, DAC=1450, ADC Mean vs 18*Chip+Chan



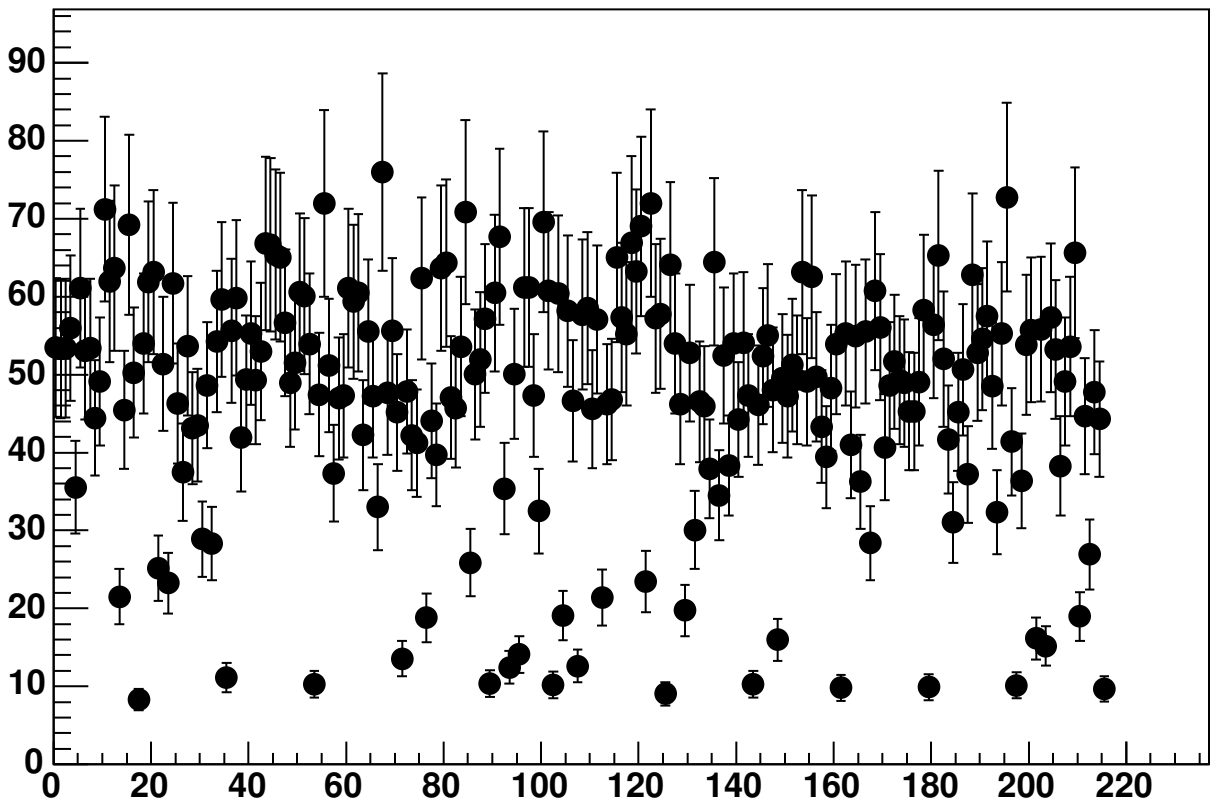
Enable 4, Hold=30, DAC=1450, ADC Noise vs 18*Chip+Chan



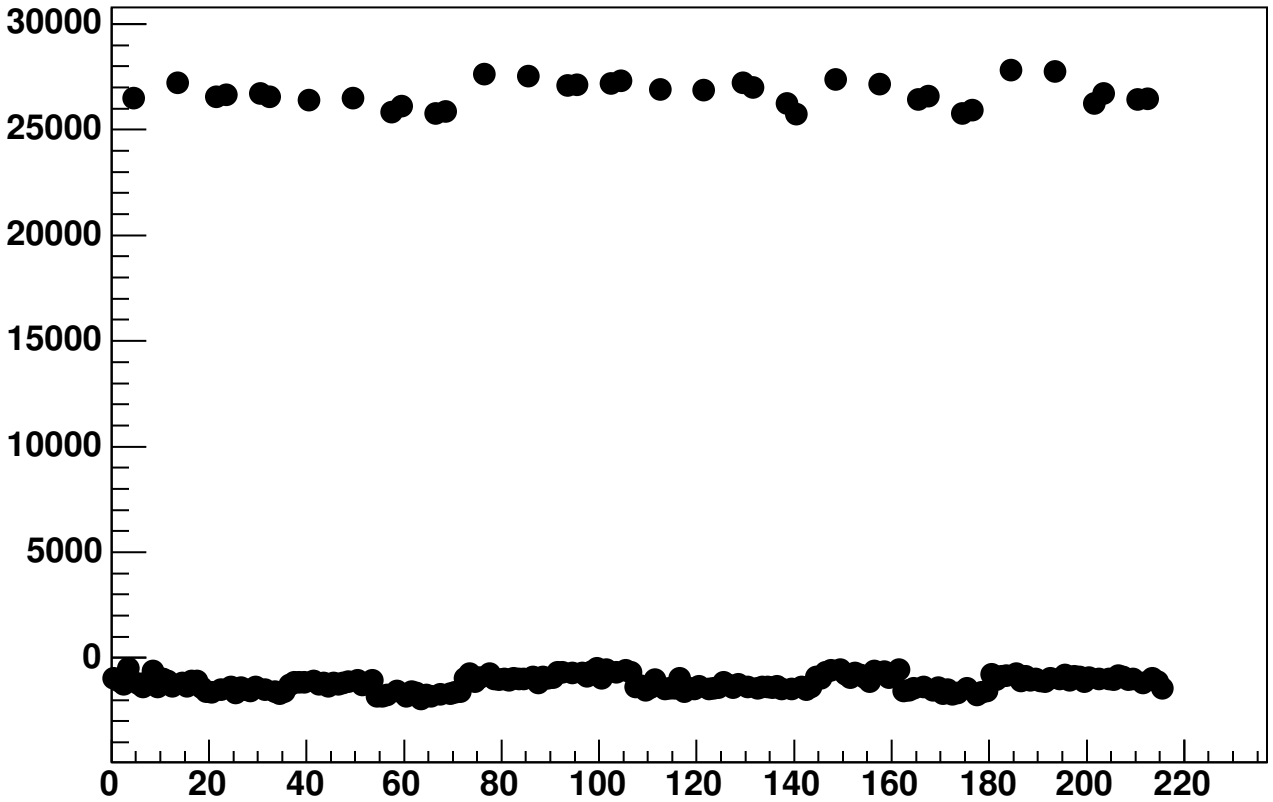
Enable 4, Hold=30, DAC=1500, ADC Mean vs 18*Chip+Chan



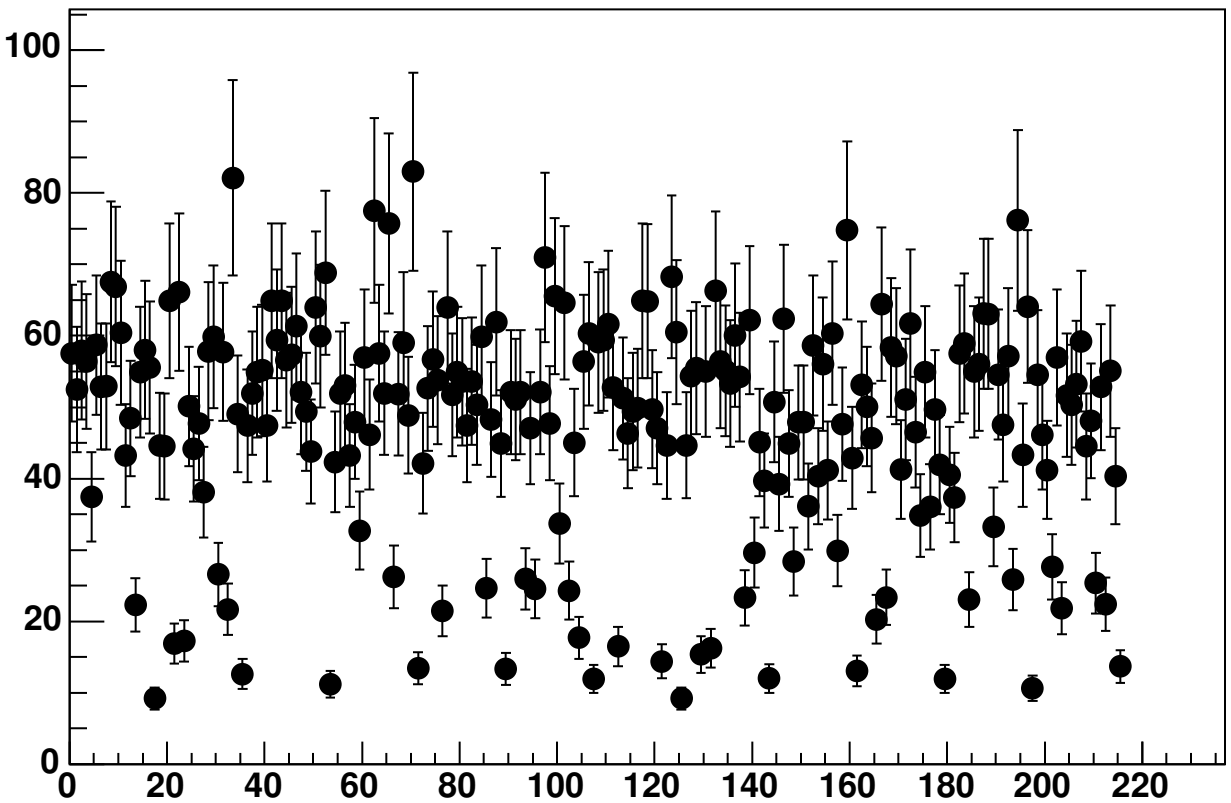
Enable 4, Hold=30, DAC=1500, ADC Noise vs 18*Chip+Chan



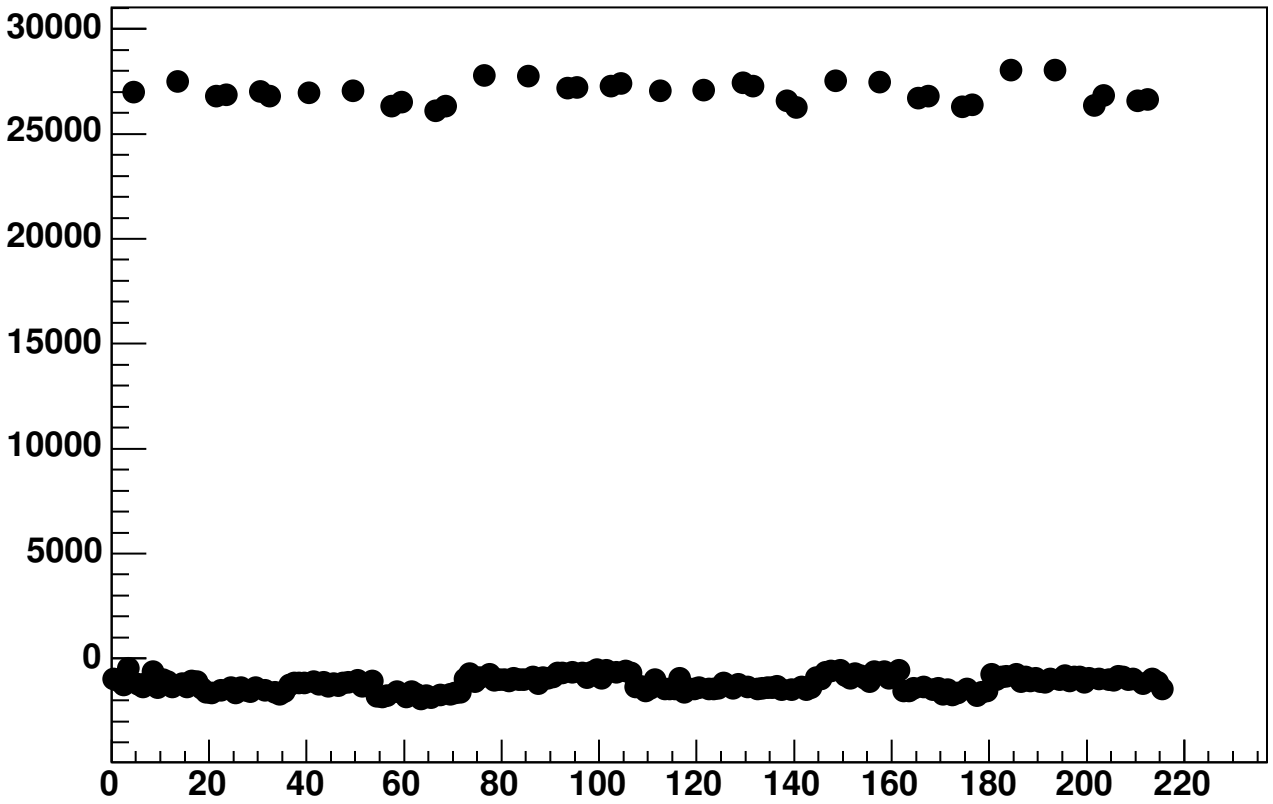
Enable 4, Hold=30, DAC=1550, ADC Mean vs 18*Chip+Chan



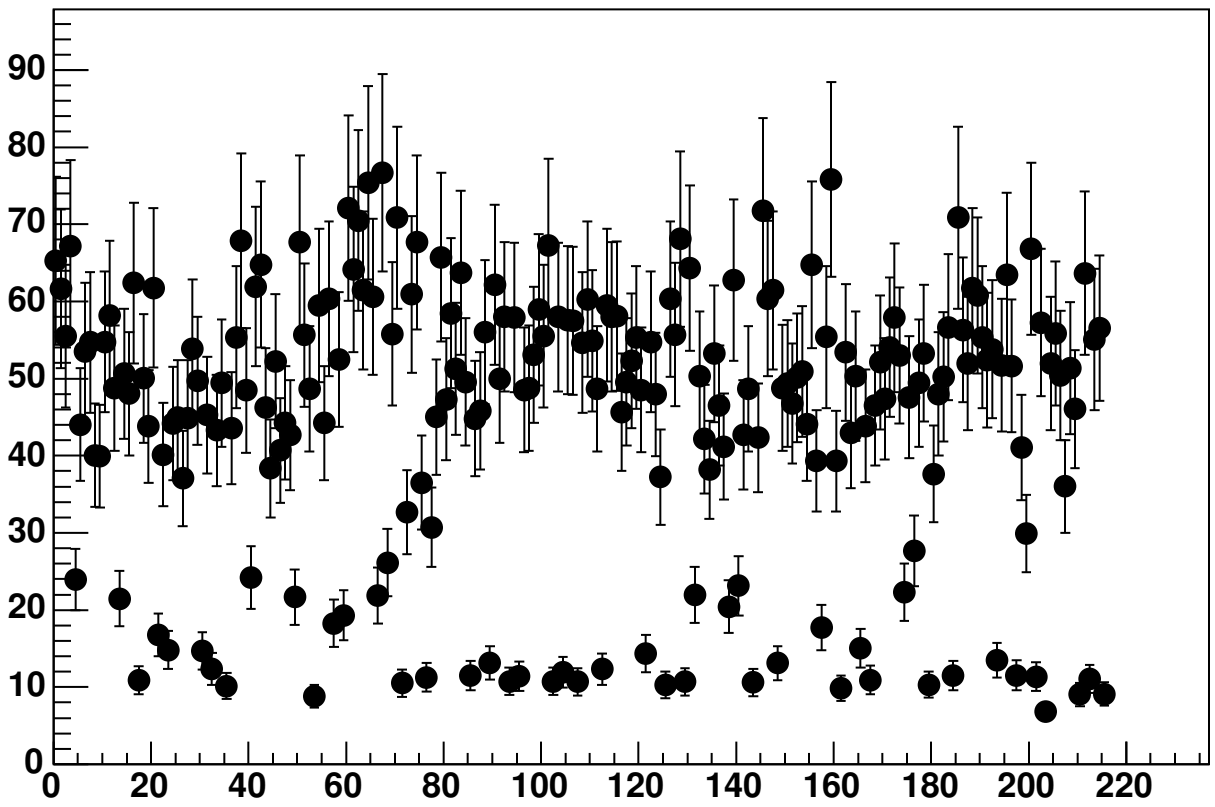
Enable 4, Hold=30, DAC=1550, ADC Noise vs 18*Chip+Chan



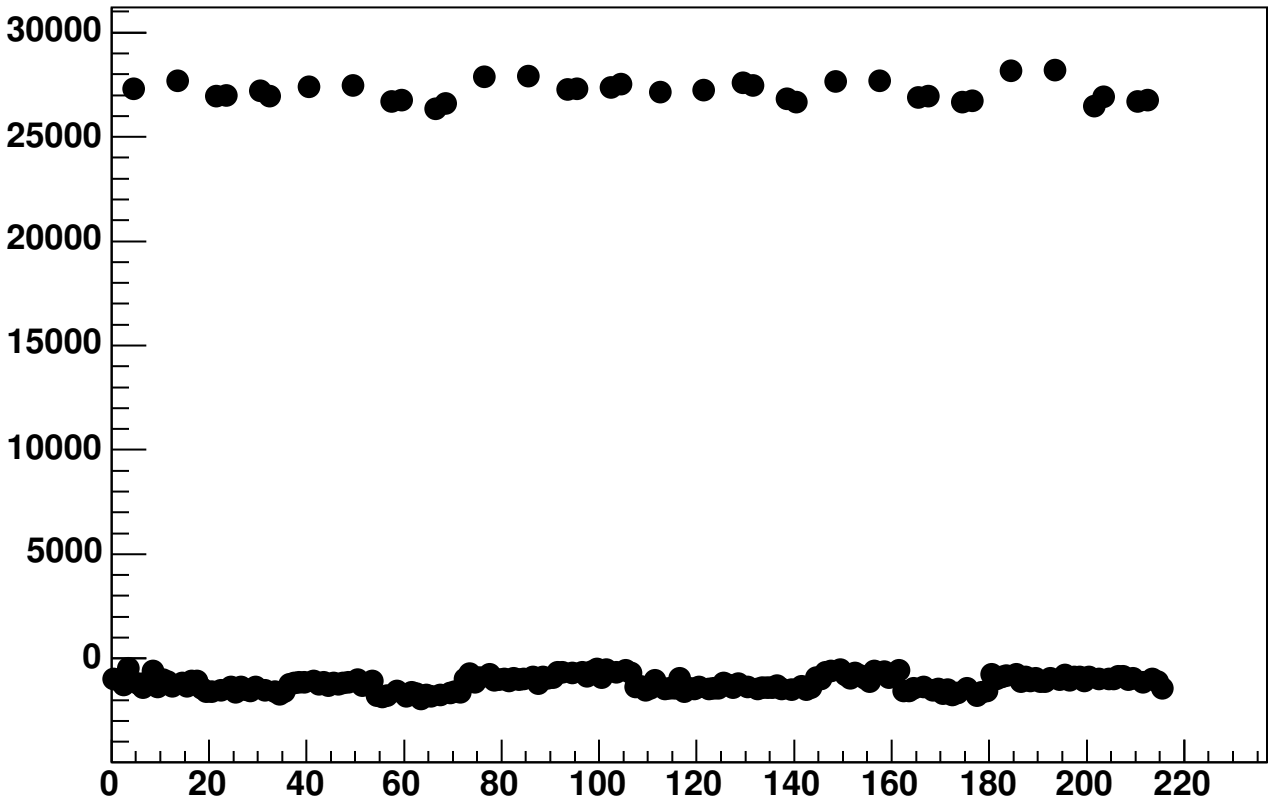
Enable 4, Hold=30, DAC=1600, ADC Mean vs 18*Chip+Chan



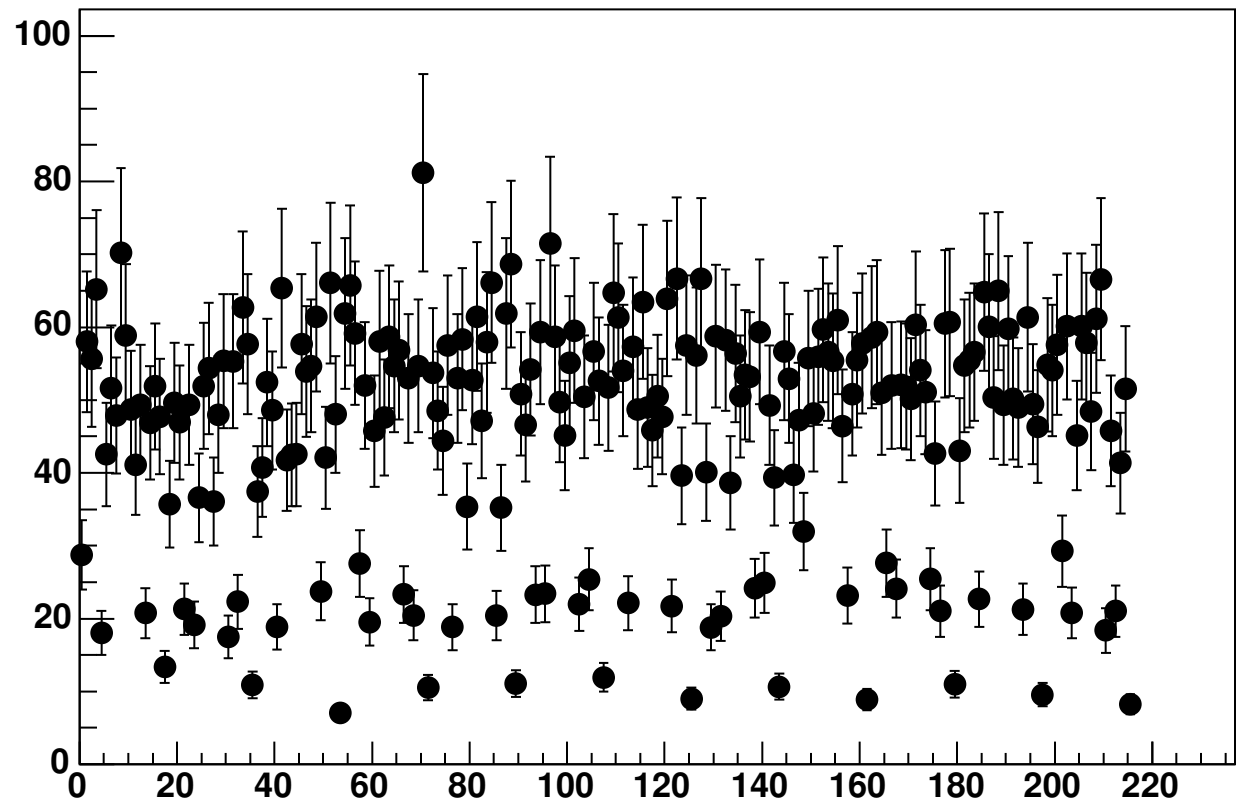
Enable 4, Hold=30, DAC=1600, ADC Noise vs 18*Chip+Chan



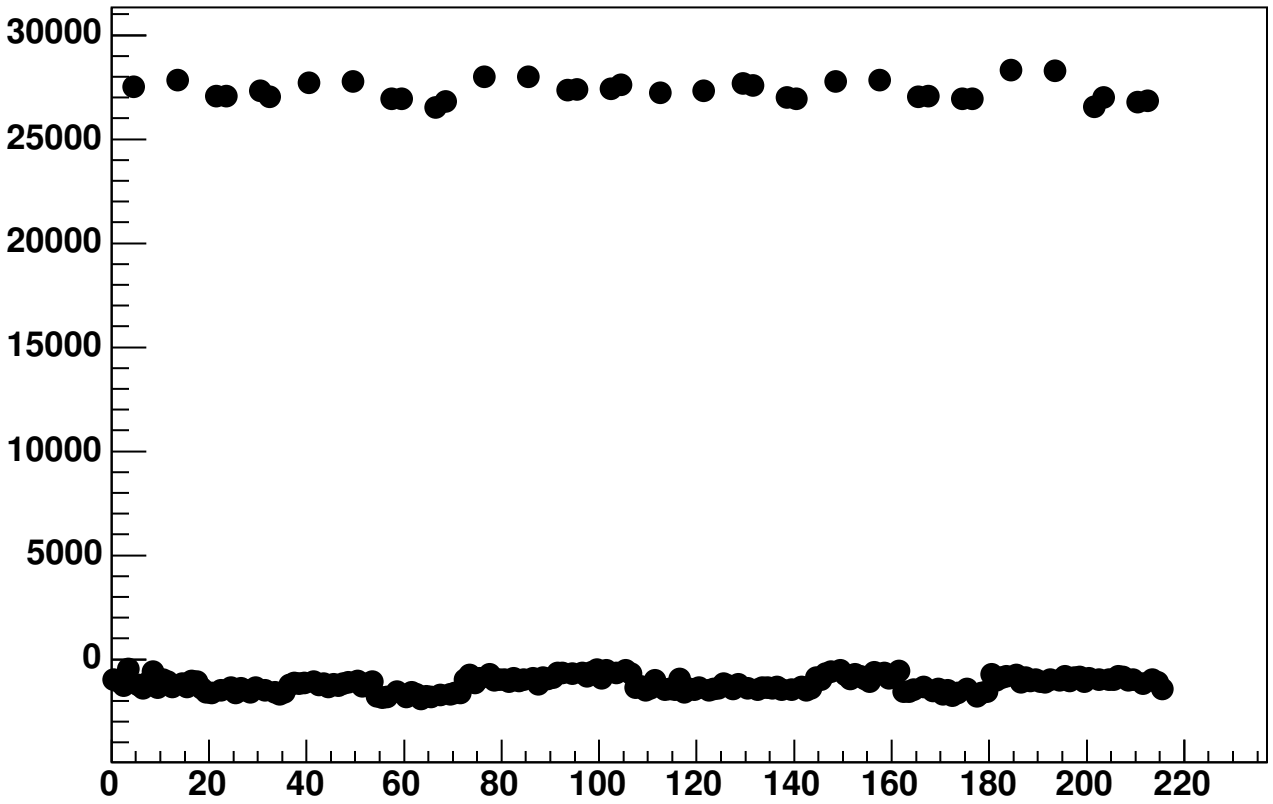
Enable 4, Hold=30, DAC=1650, ADC Mean vs 18*Chip+Chan



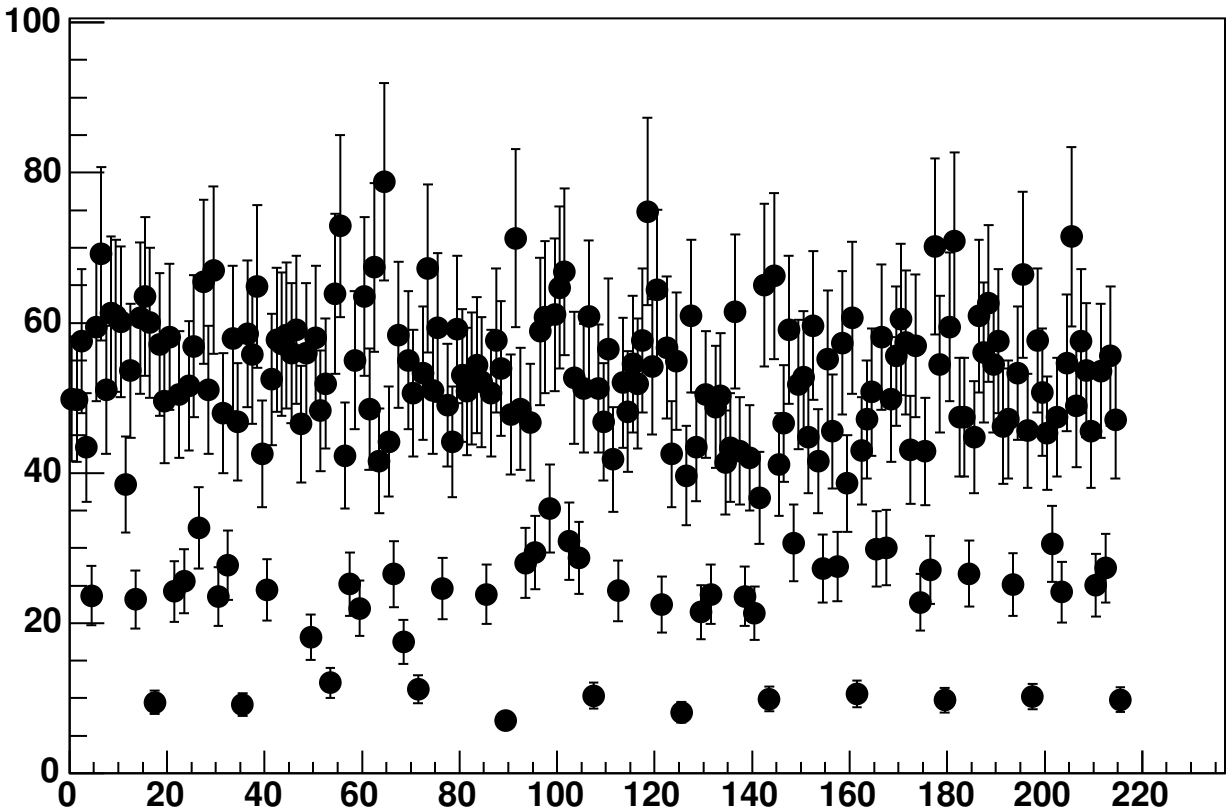
Enable 4, Hold=30, DAC=1650, ADC Noise vs 18*Chip+Chan



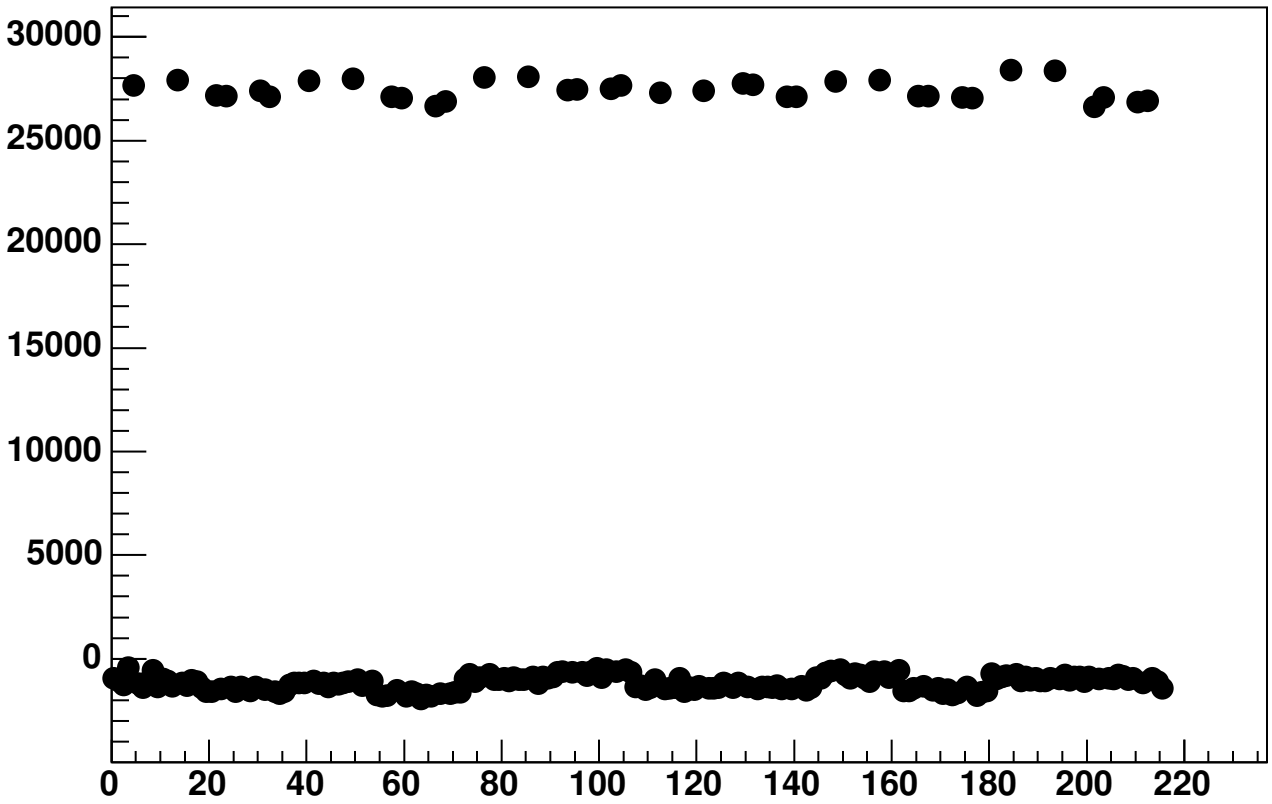
Enable 4, Hold=30, DAC=1700, ADC Mean vs 18*Chip+Chan



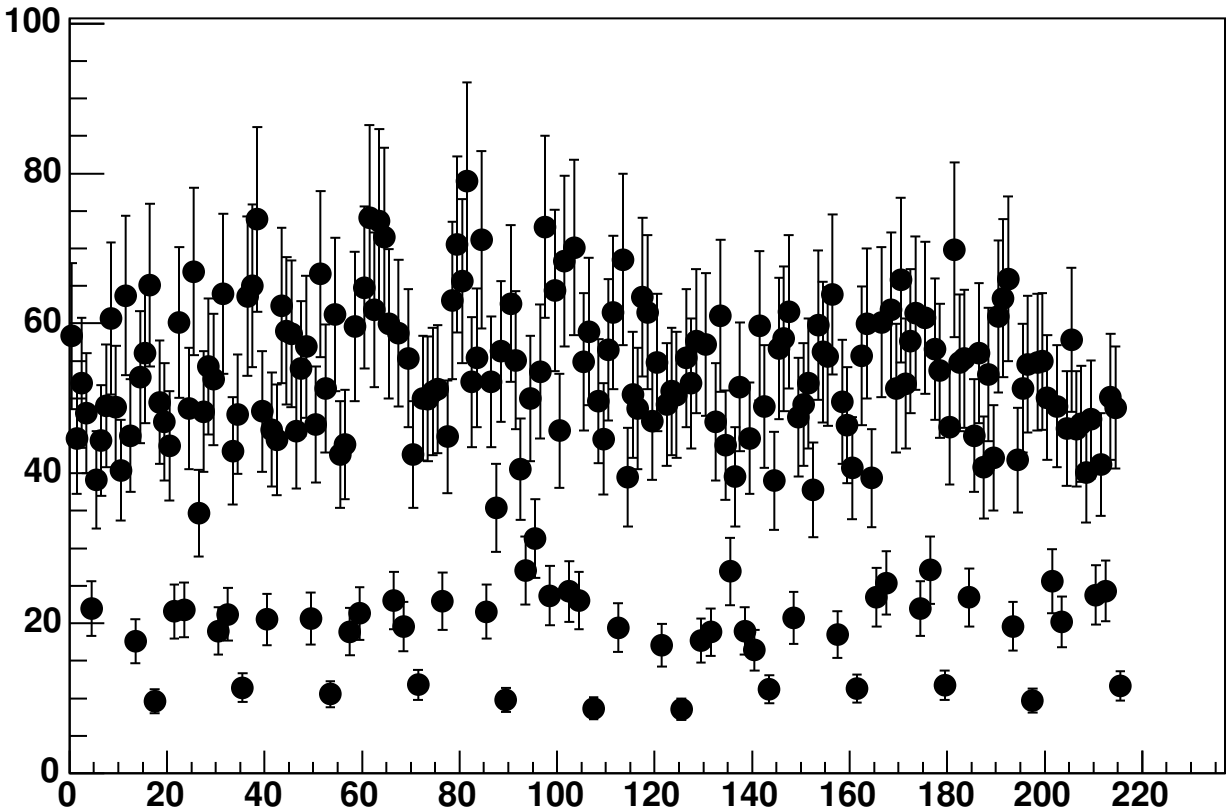
Enable 4, Hold=30, DAC=1700, ADC Noise vs 18*Chip+Chan



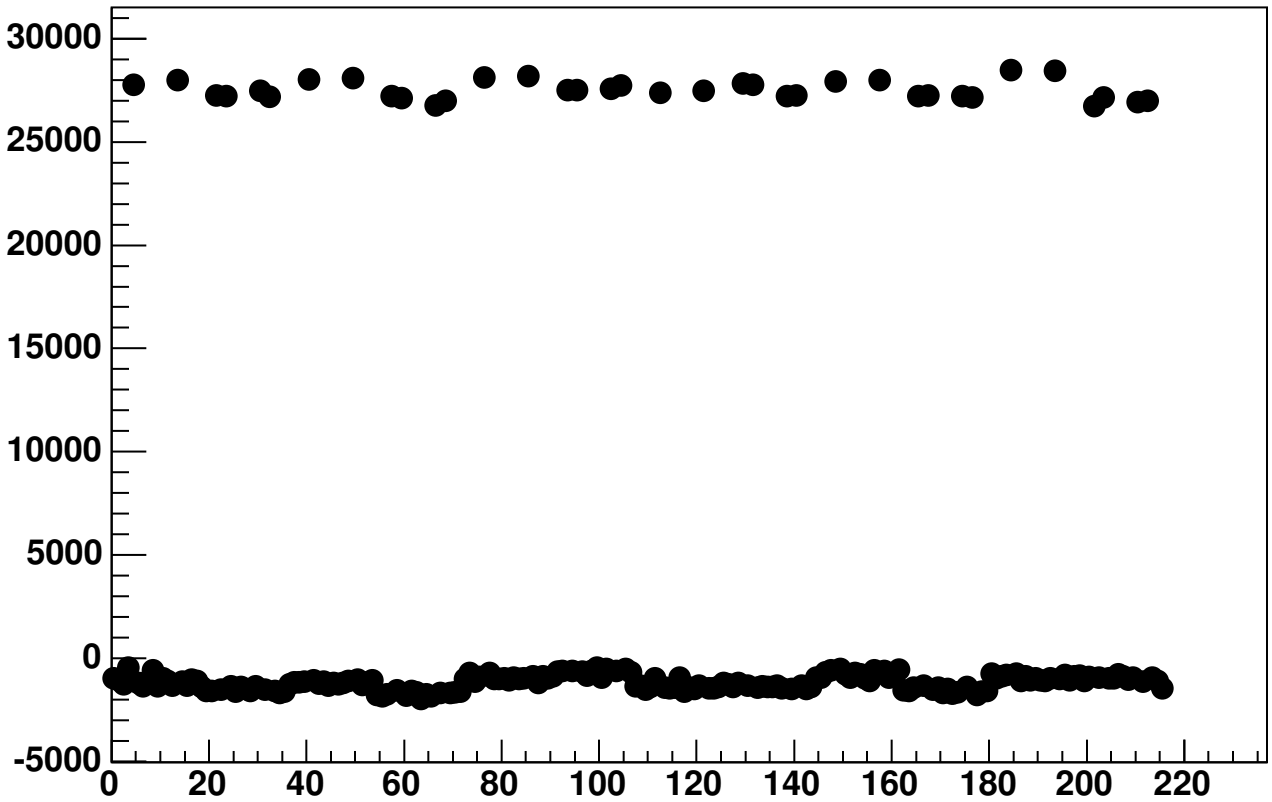
Enable 4, Hold=30, DAC=1750, ADC Mean vs 18*Chip+Chan



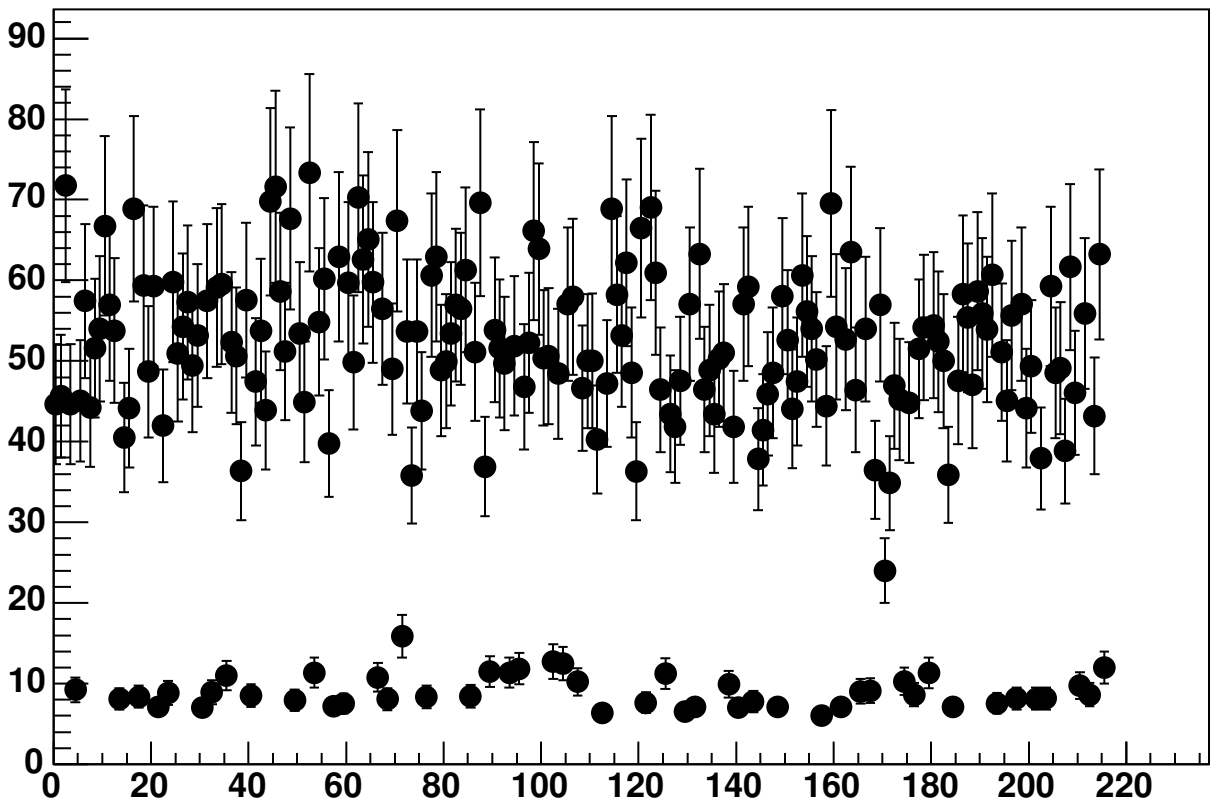
Enable 4, Hold=30, DAC=1750, ADC Noise vs 18*Chip+Chan



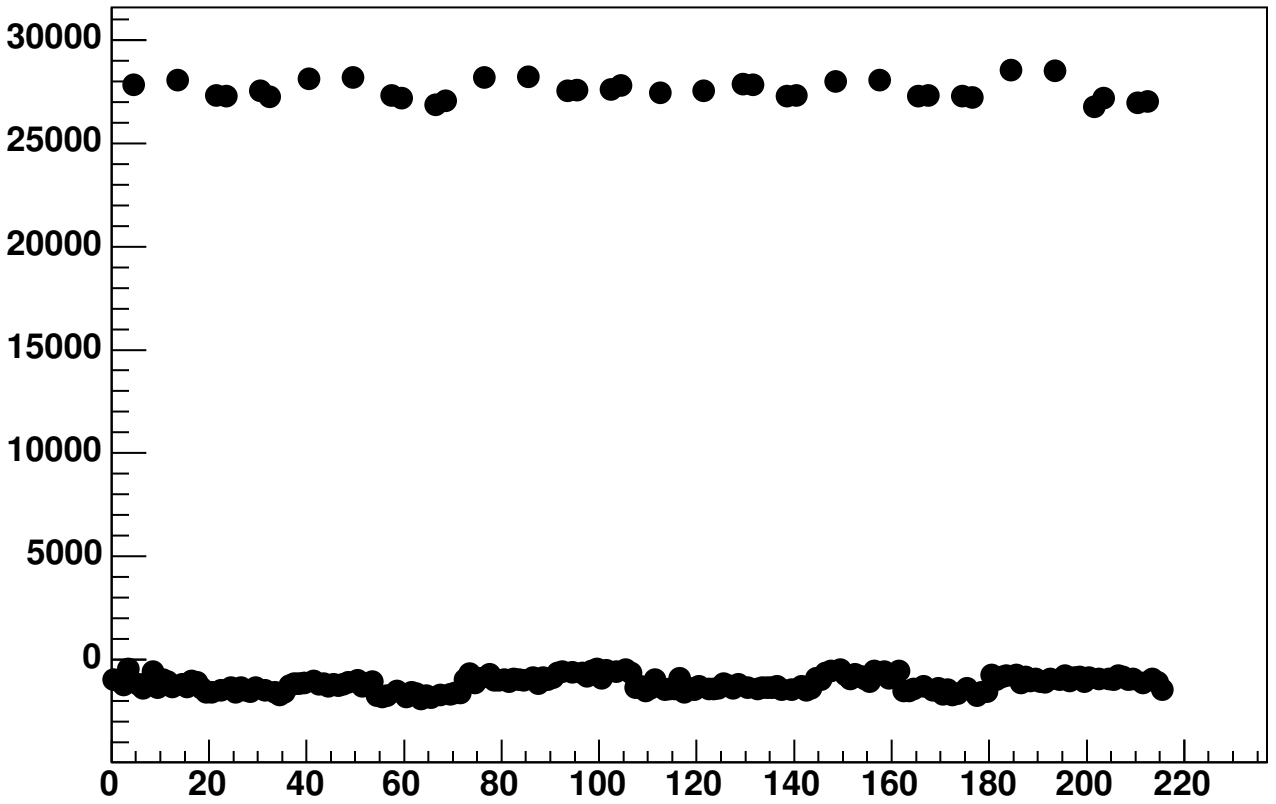
Enable 4, Hold=30, DAC=1800, ADC Mean vs 18*Chip+Chan



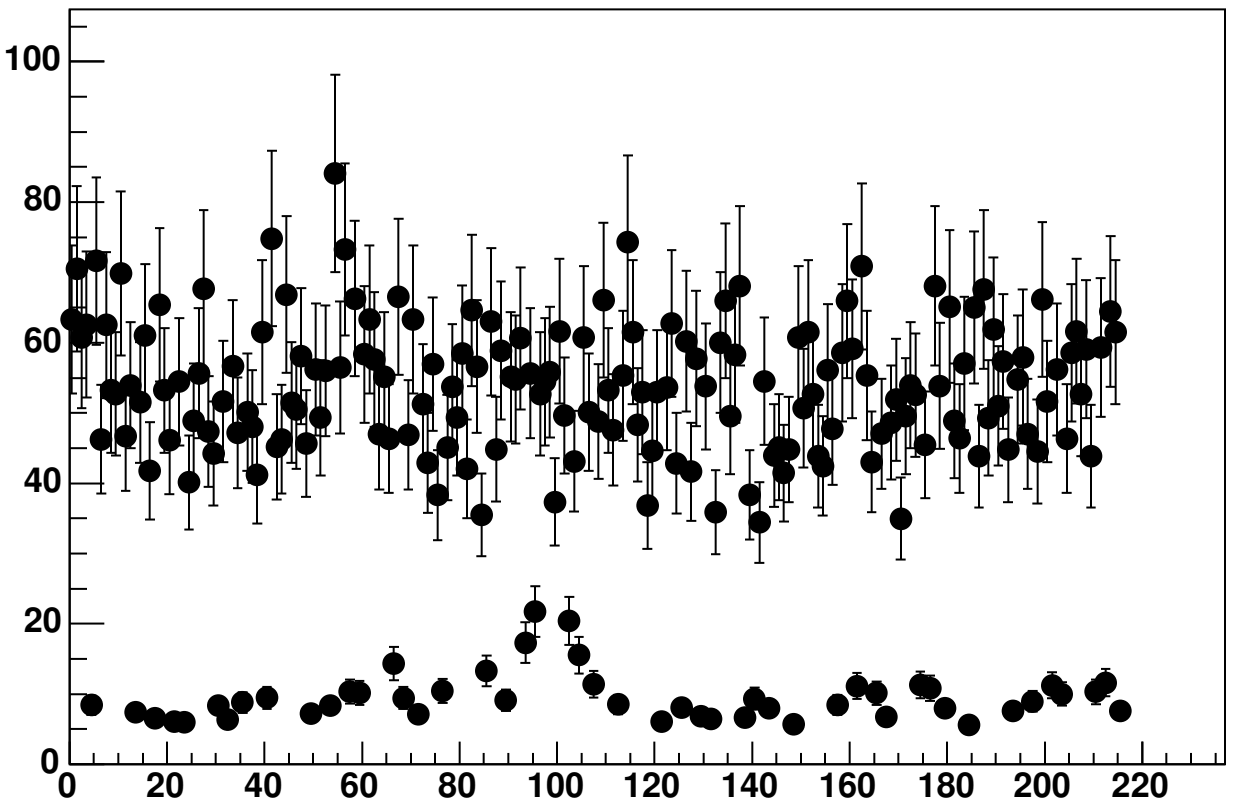
Enable 4, Hold=30, DAC=1800, ADC Noise vs 18*Chip+Chan



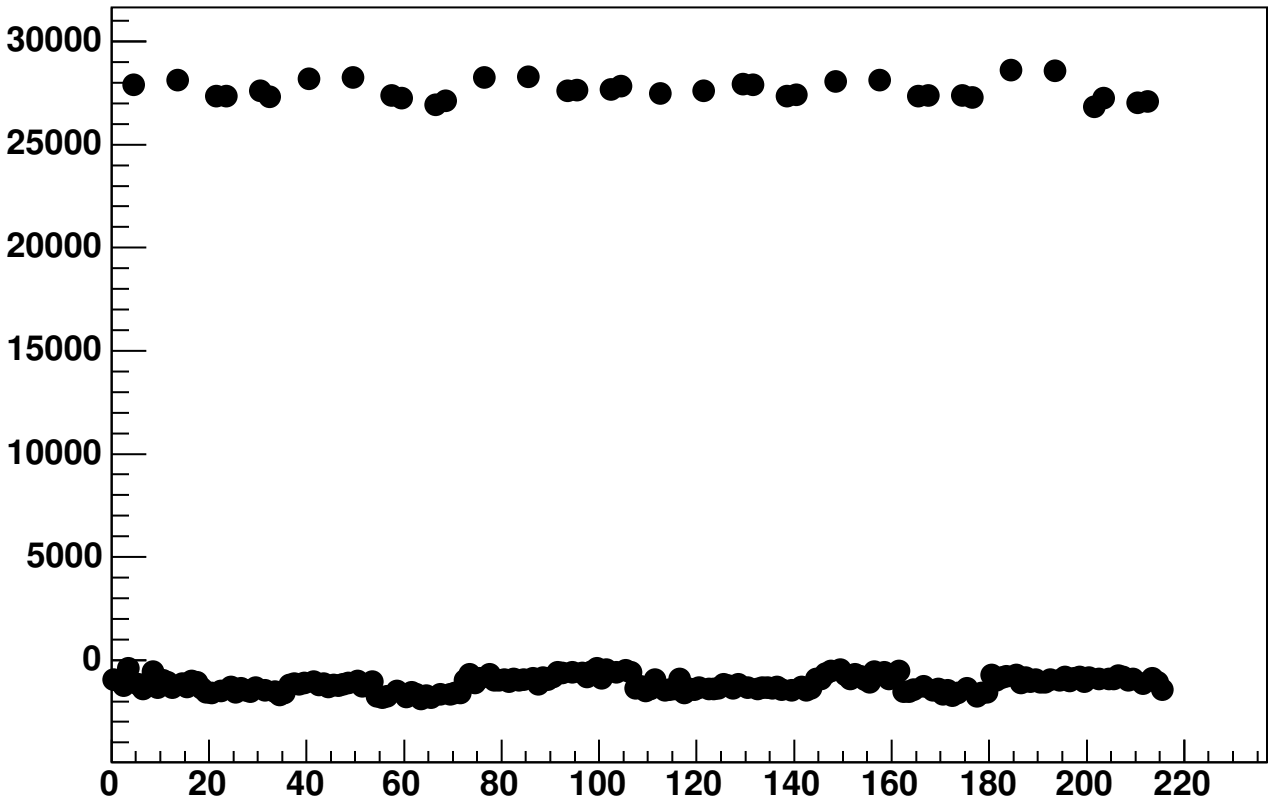
Enable 4, Hold=30, DAC=1850, ADC Mean vs 18*Chip+Chan



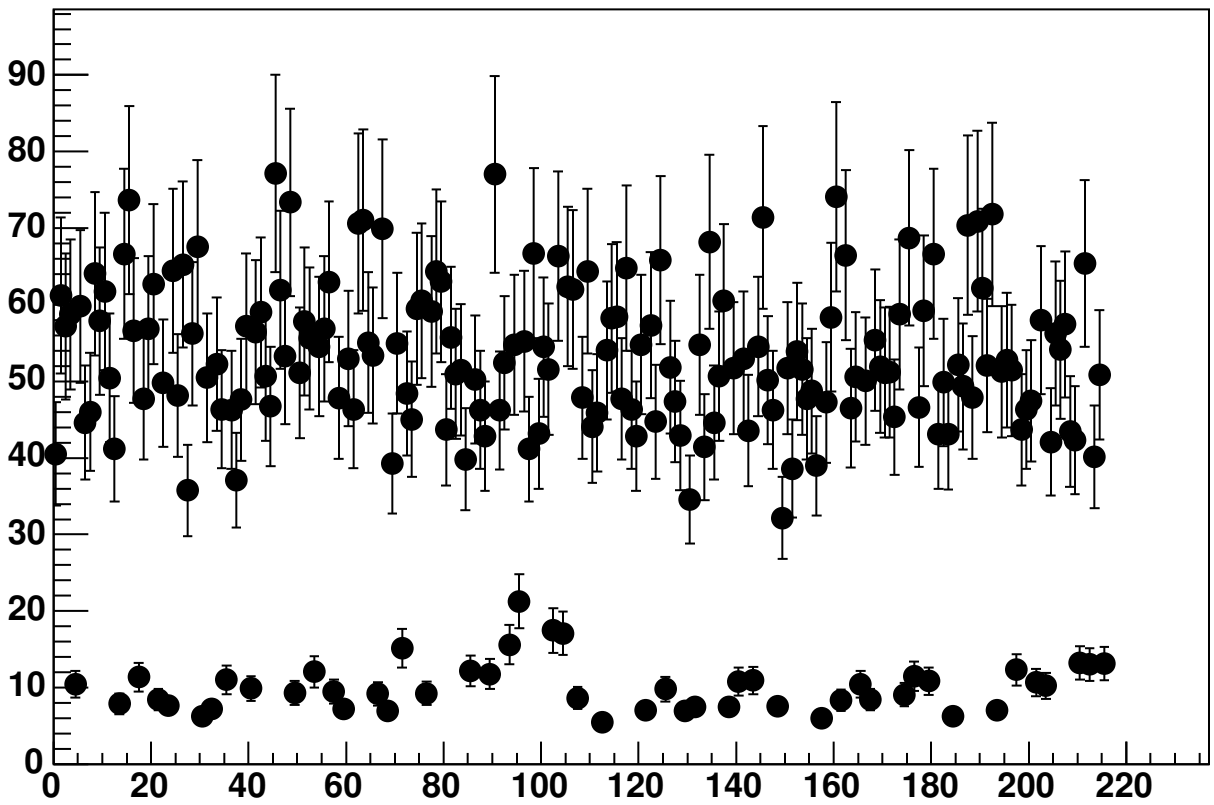
Enable 4, Hold=30, DAC=1850, ADC Noise vs 18*Chip+Chan



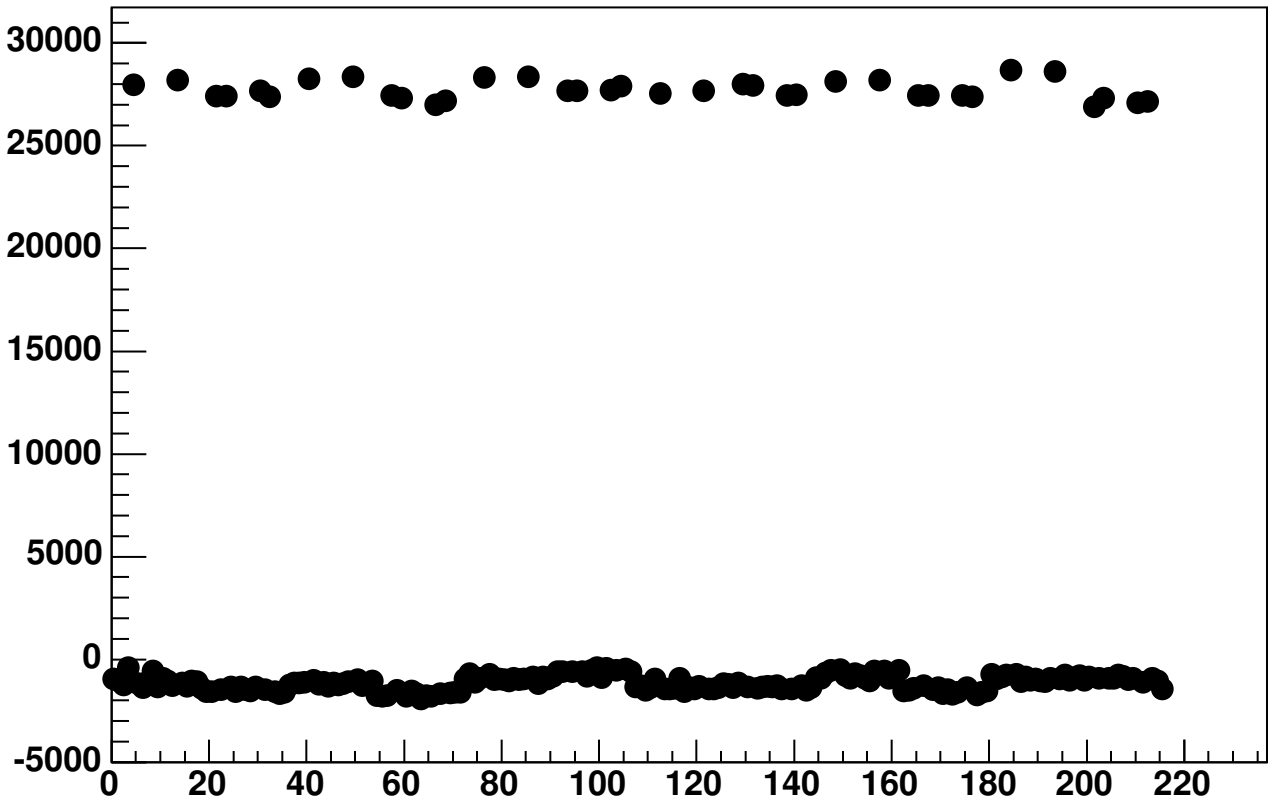
Enable 4, Hold=30, DAC=1900, ADC Mean vs 18*Chip+Chan



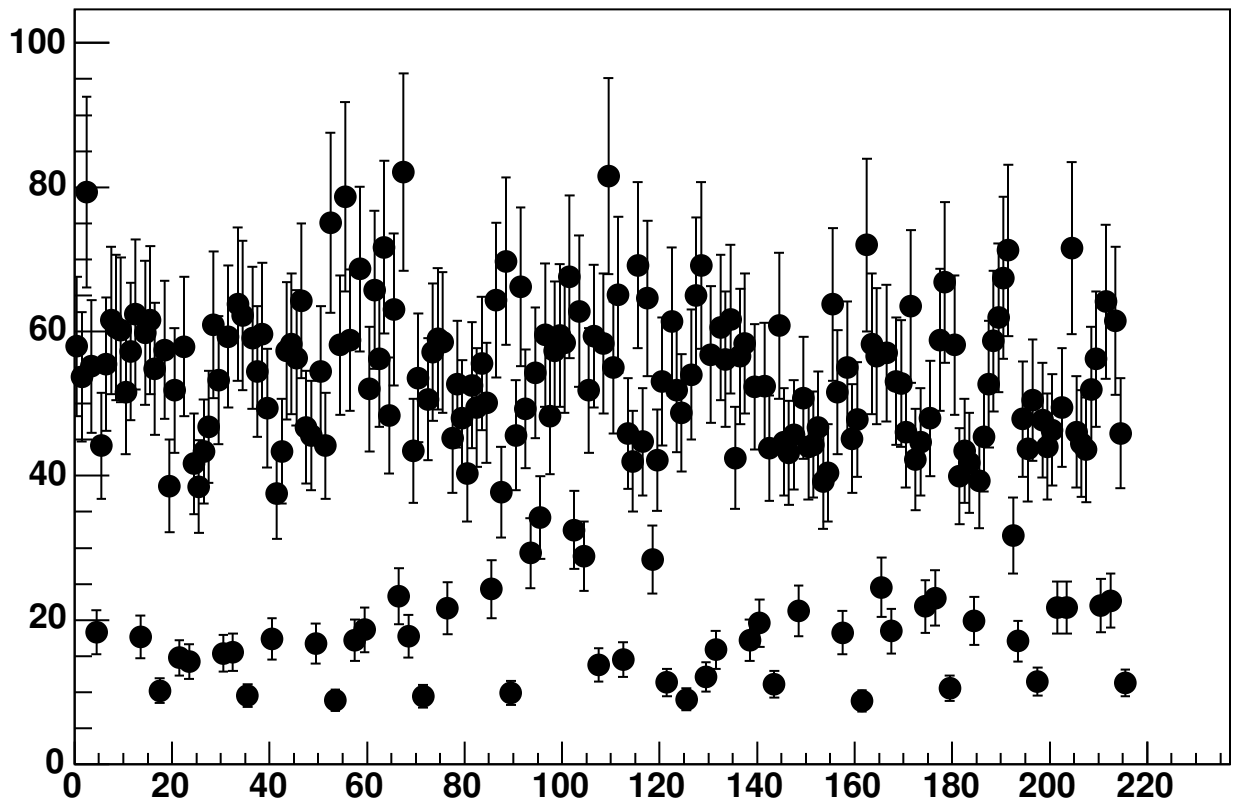
Enable 4, Hold=30, DAC=1900, ADC Noise vs 18*Chip+Chan



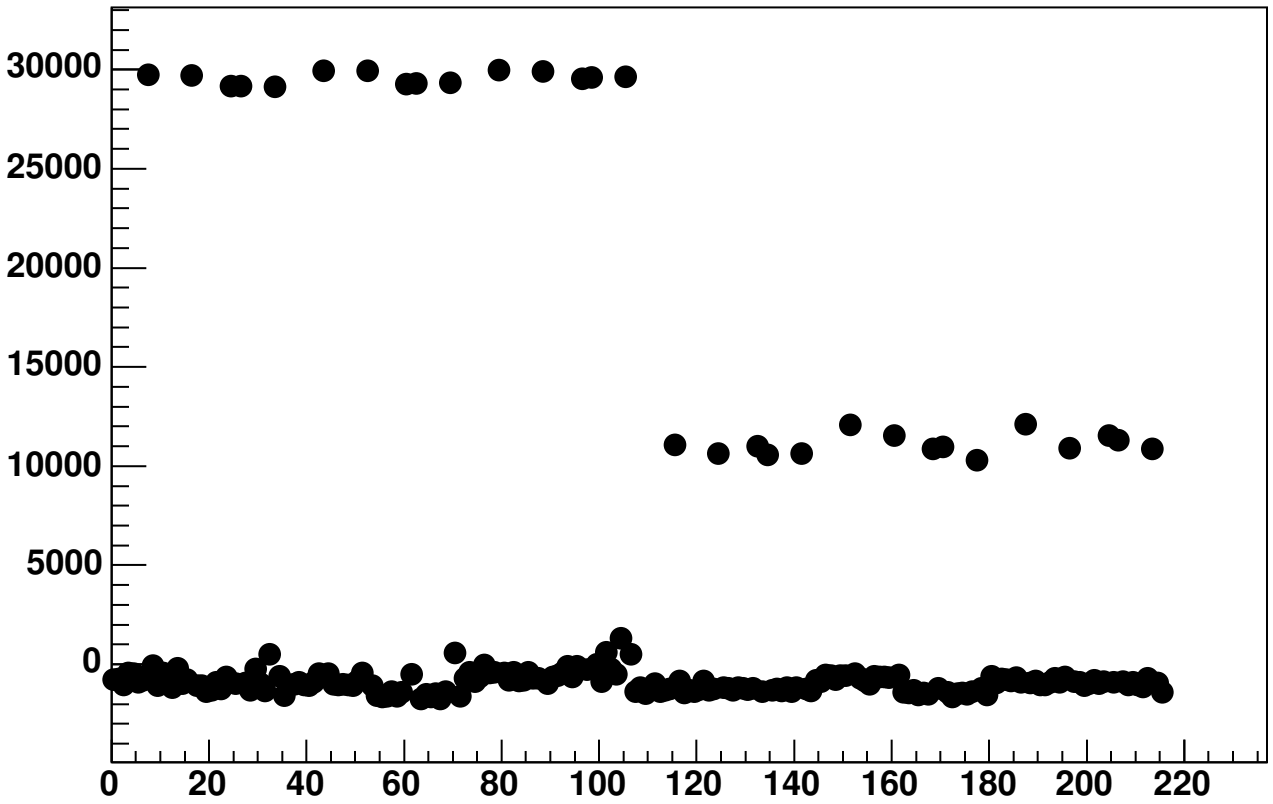
Enable 4, Hold=30, DAC=1950, ADC Mean vs 18*Chip+Chan



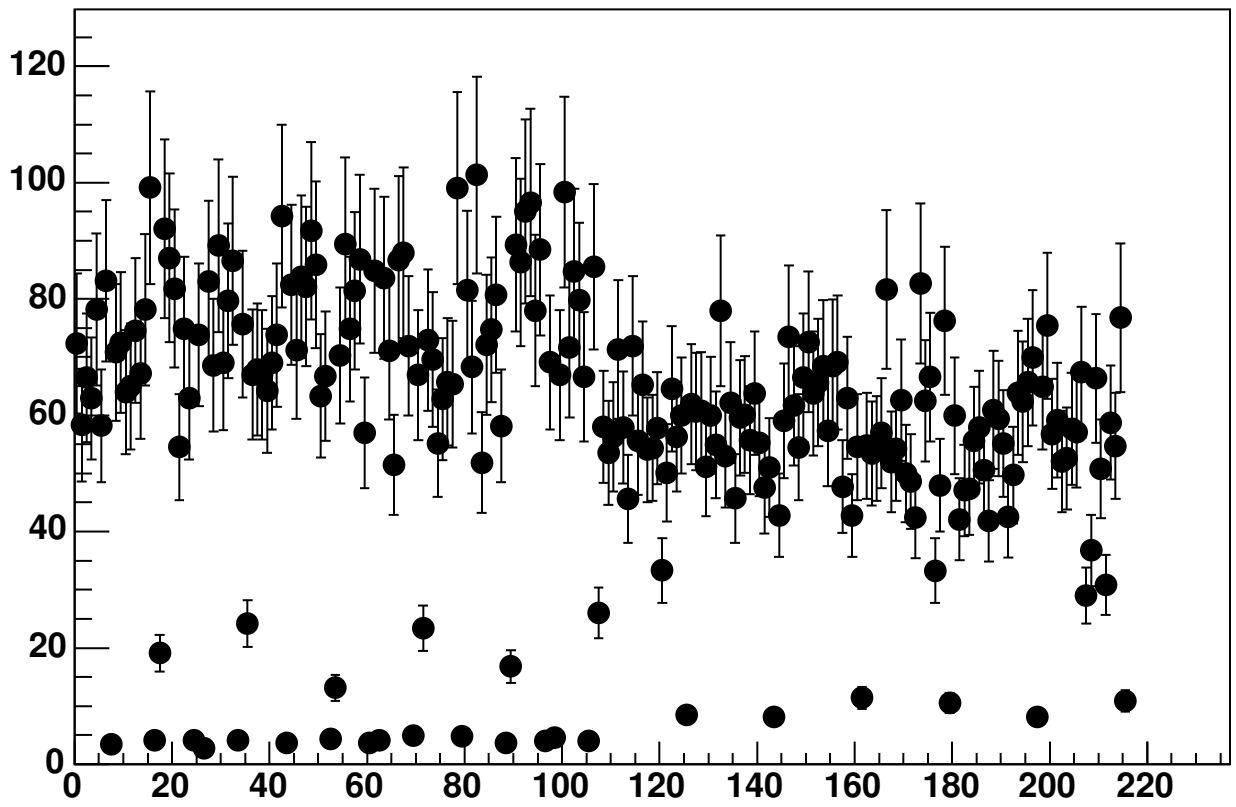
Enable 4, Hold=30, DAC=1950, ADC Noise vs 18*Chip+Chan



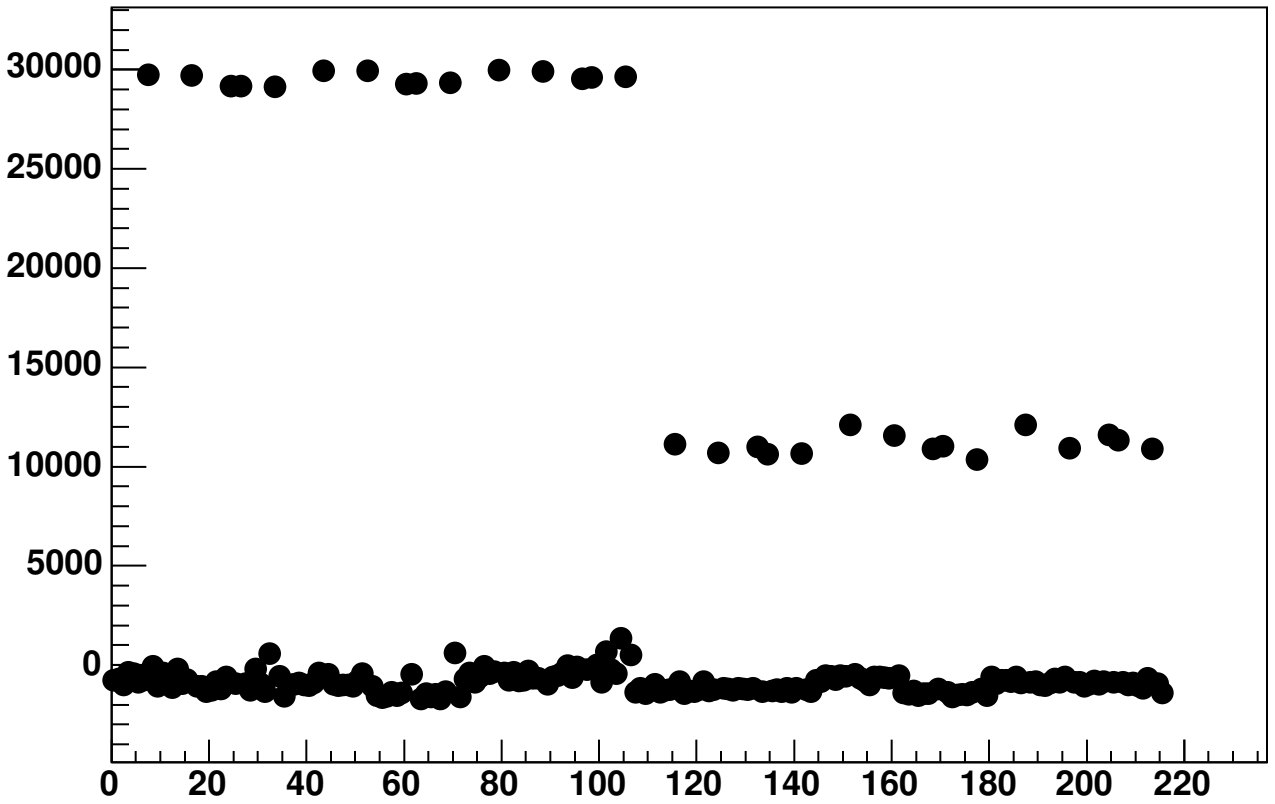
Enable 5, Hold=30, DAC=0, ADC Mean vs 18*Chip+Chan



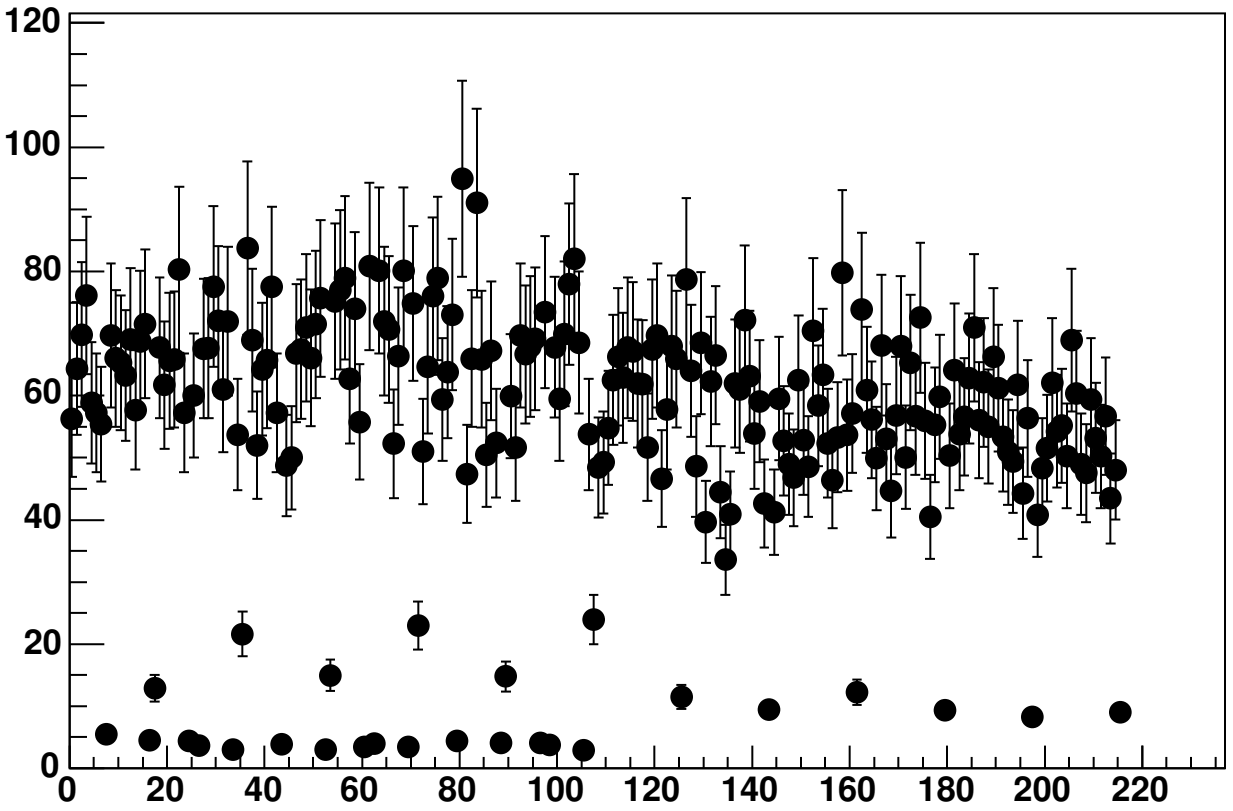
Enable 5, Hold=30, DAC=0, ADC Noise vs 18*Chip+Chan



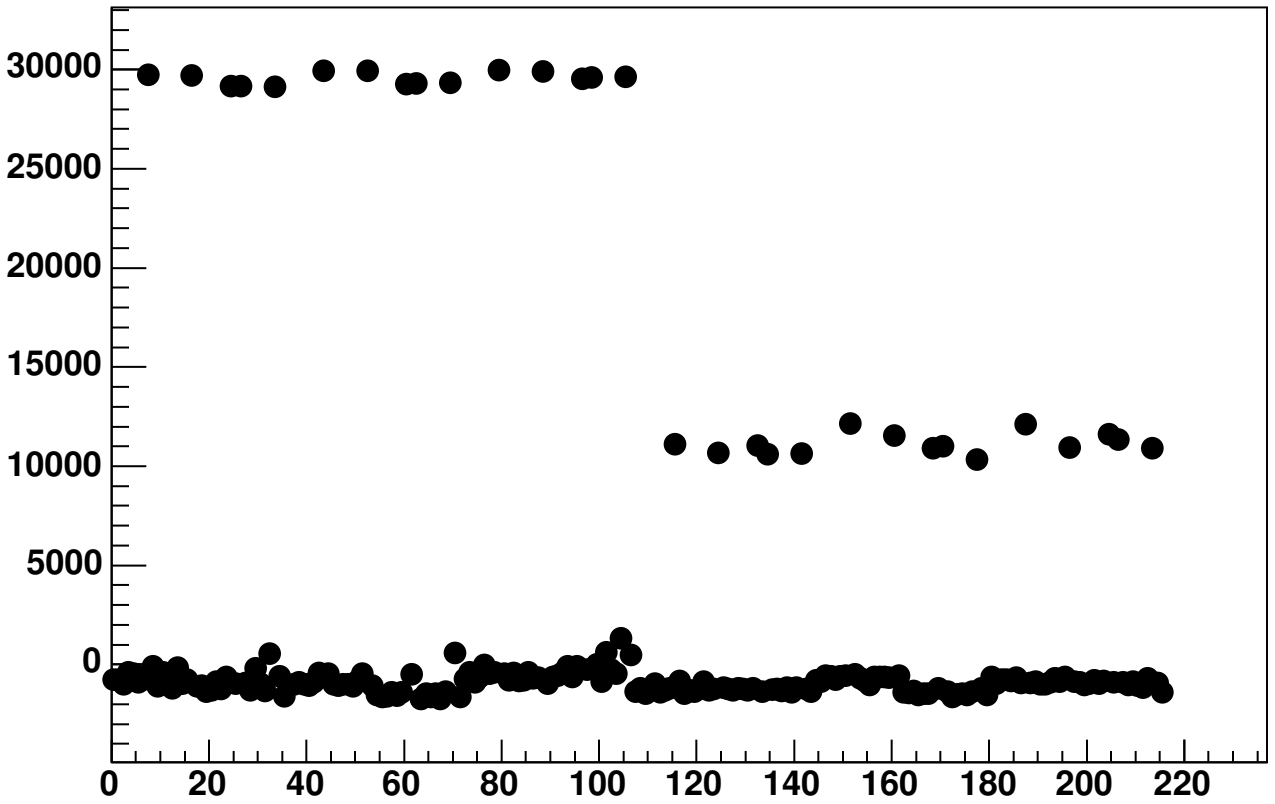
Enable 5, Hold=30, DAC=50, ADC Mean vs 18*Chip+Chan



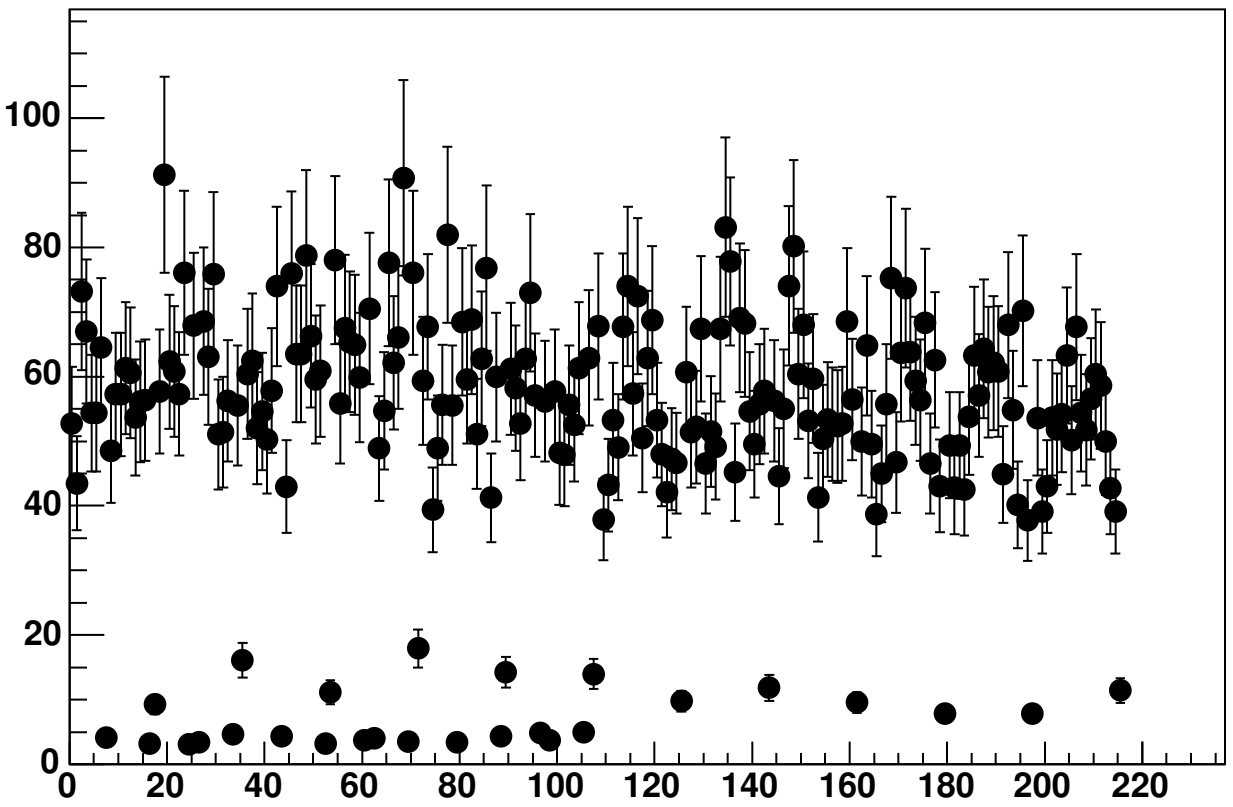
Enable 5, Hold=30, DAC=50, ADC Noise vs 18*Chip+Chan



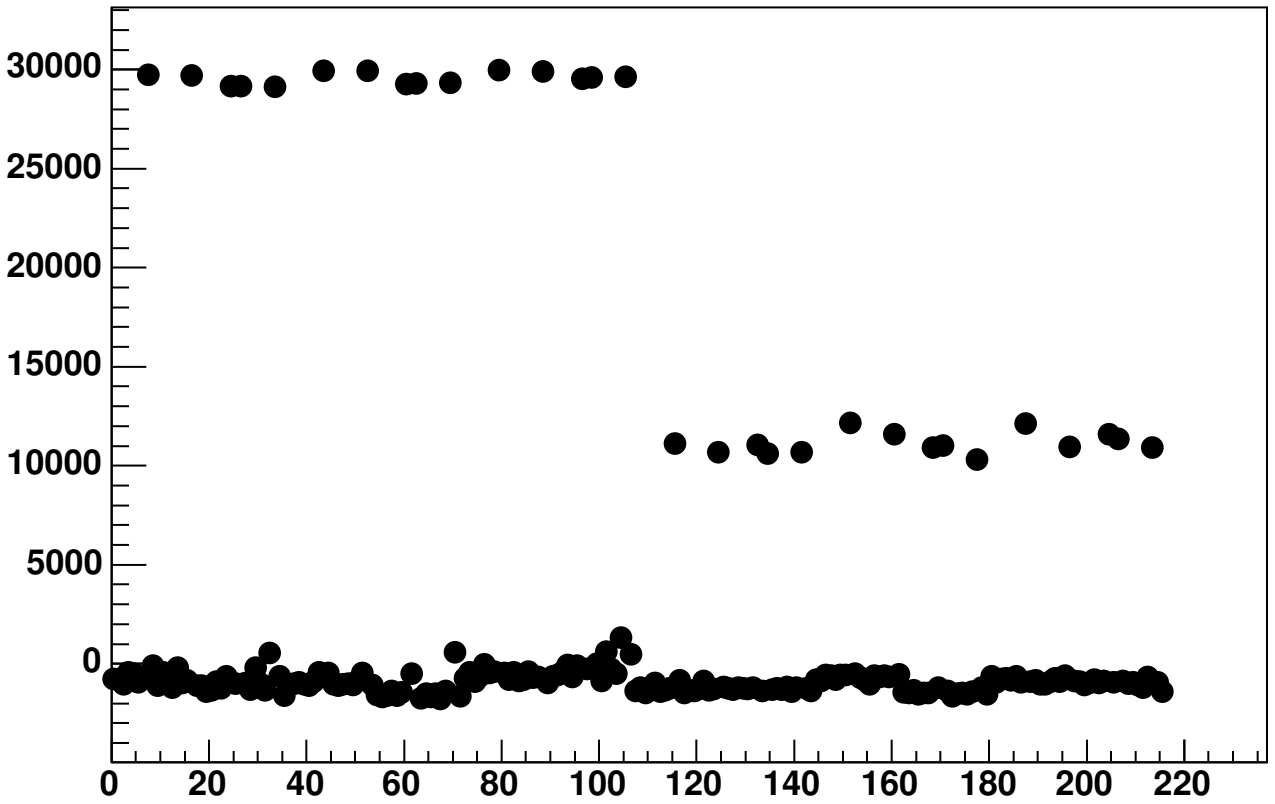
Enable 5, Hold=30, DAC=100, ADC Mean vs 18*Chip+Chan



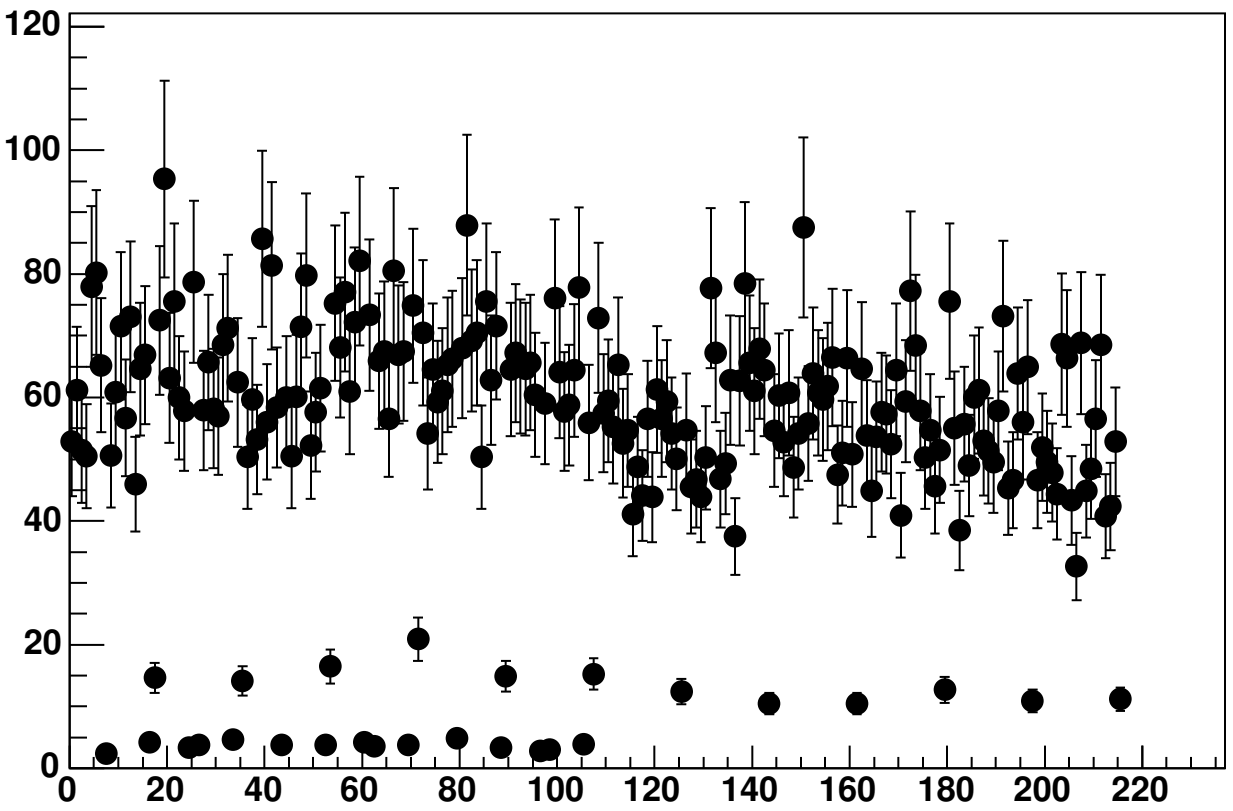
Enable 5, Hold=30, DAC=100, ADC Noise vs 18*Chip+Chan



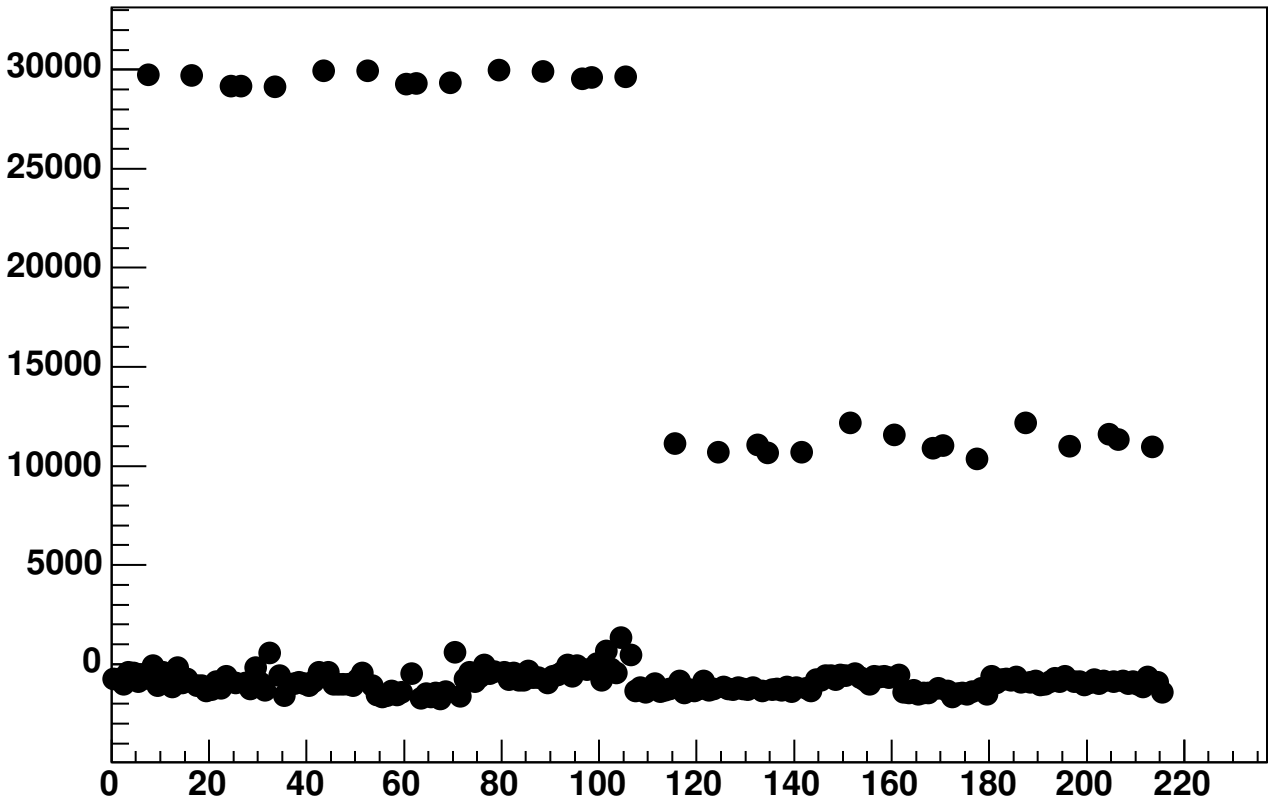
Enable 5, Hold=30, DAC=150, ADC Mean vs 18*Chip+Chan



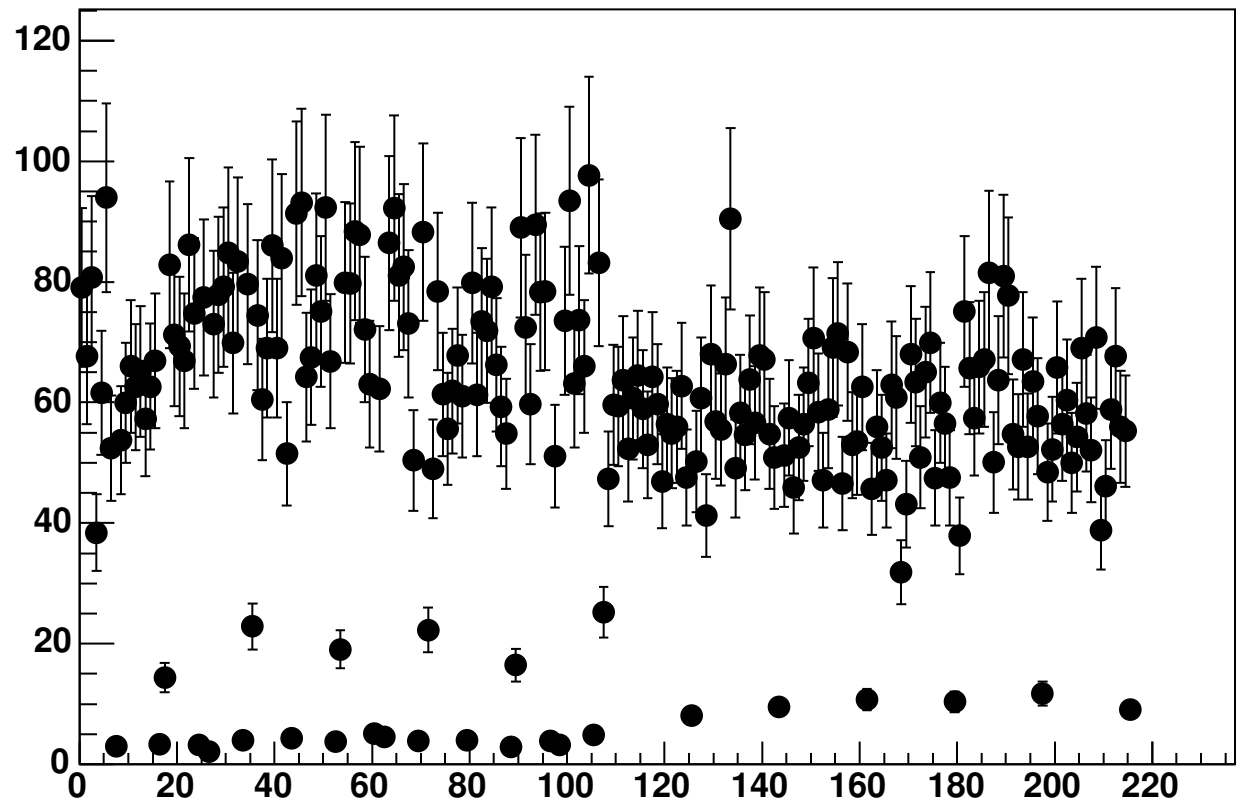
Enable 5, Hold=30, DAC=150, ADC Noise vs 18*Chip+Chan



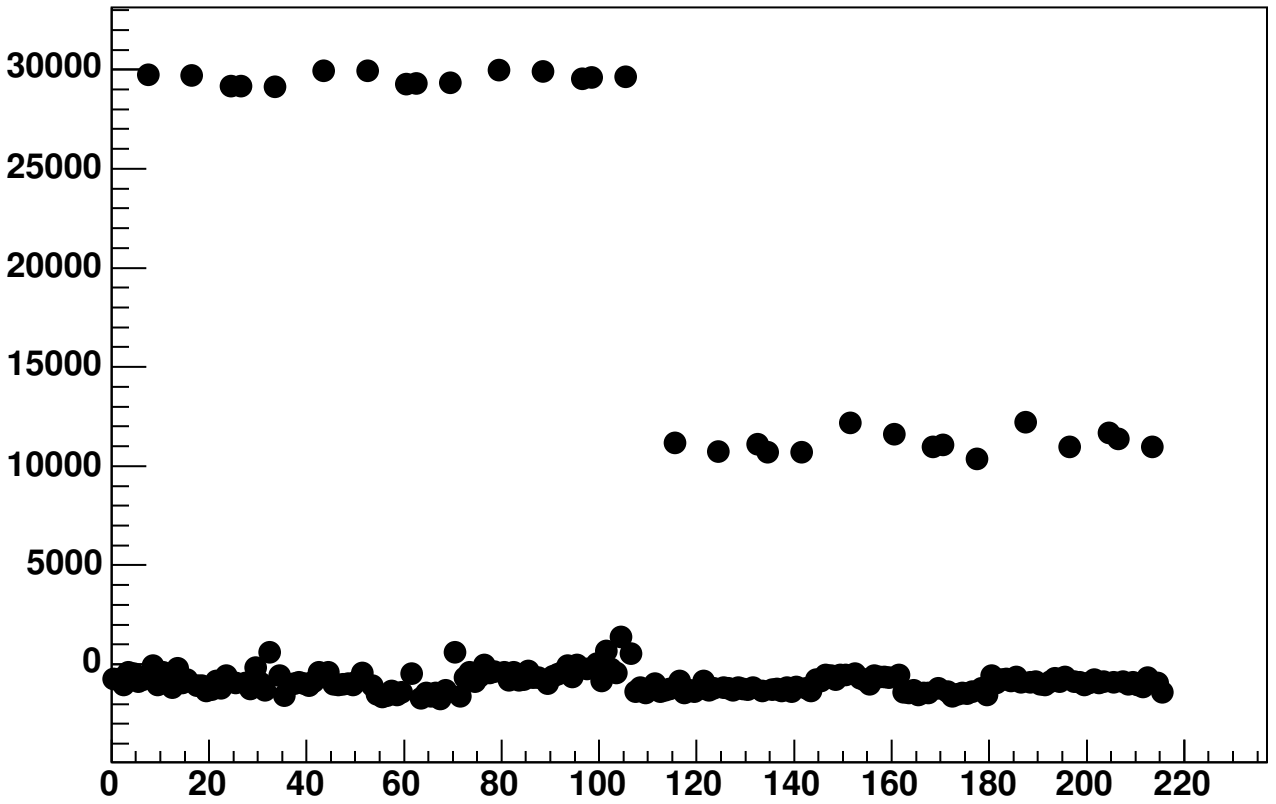
Enable 5, Hold=30, DAC=200, ADC Mean vs 18*Chip+Chan



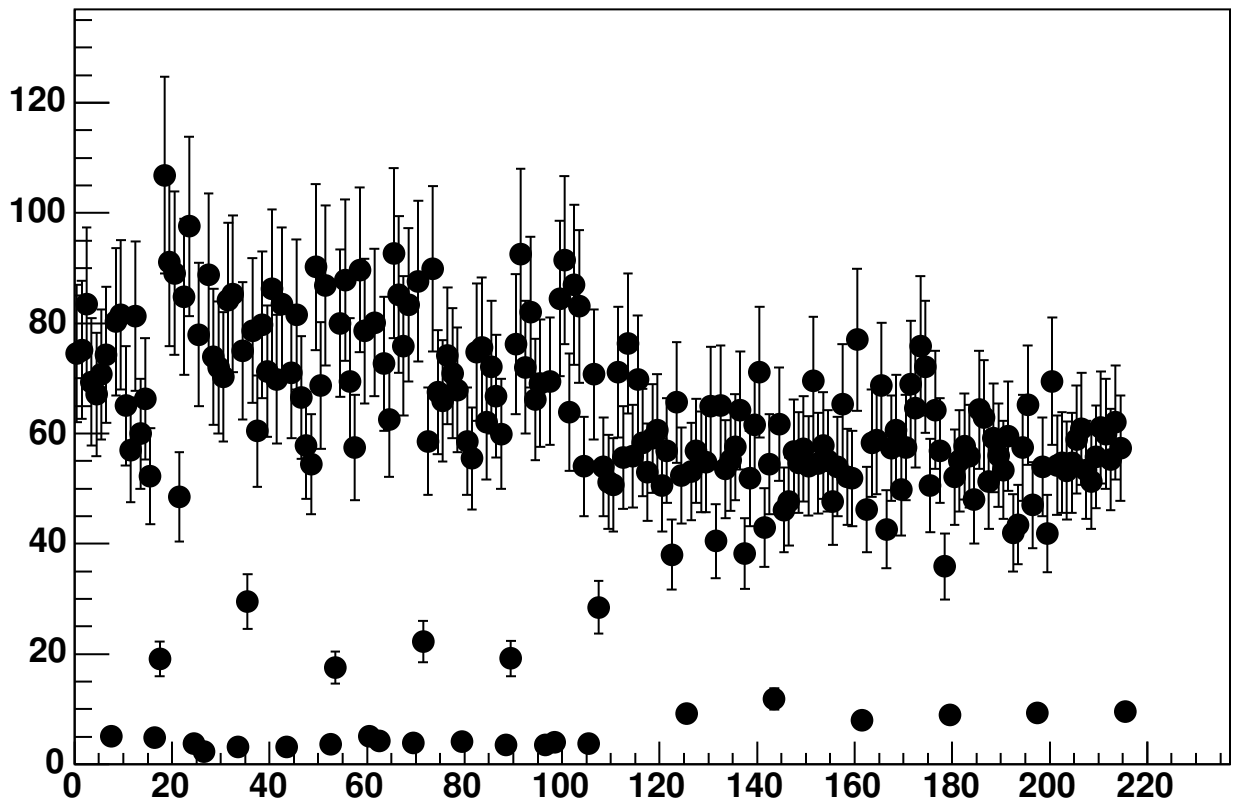
Enable 5, Hold=30, DAC=200, ADC Noise vs 18*Chip+Chan



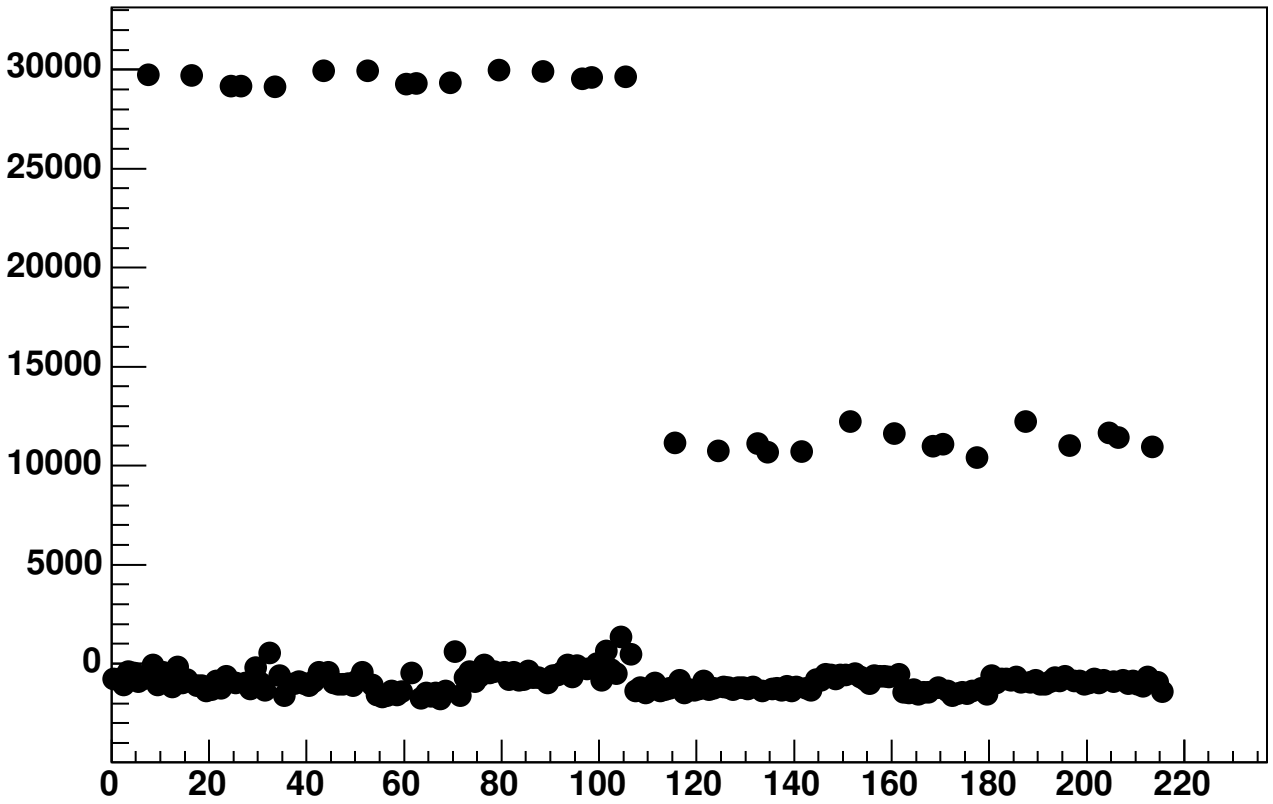
Enable 5, Hold=30, DAC=250, ADC Mean vs 18*Chip+Chan



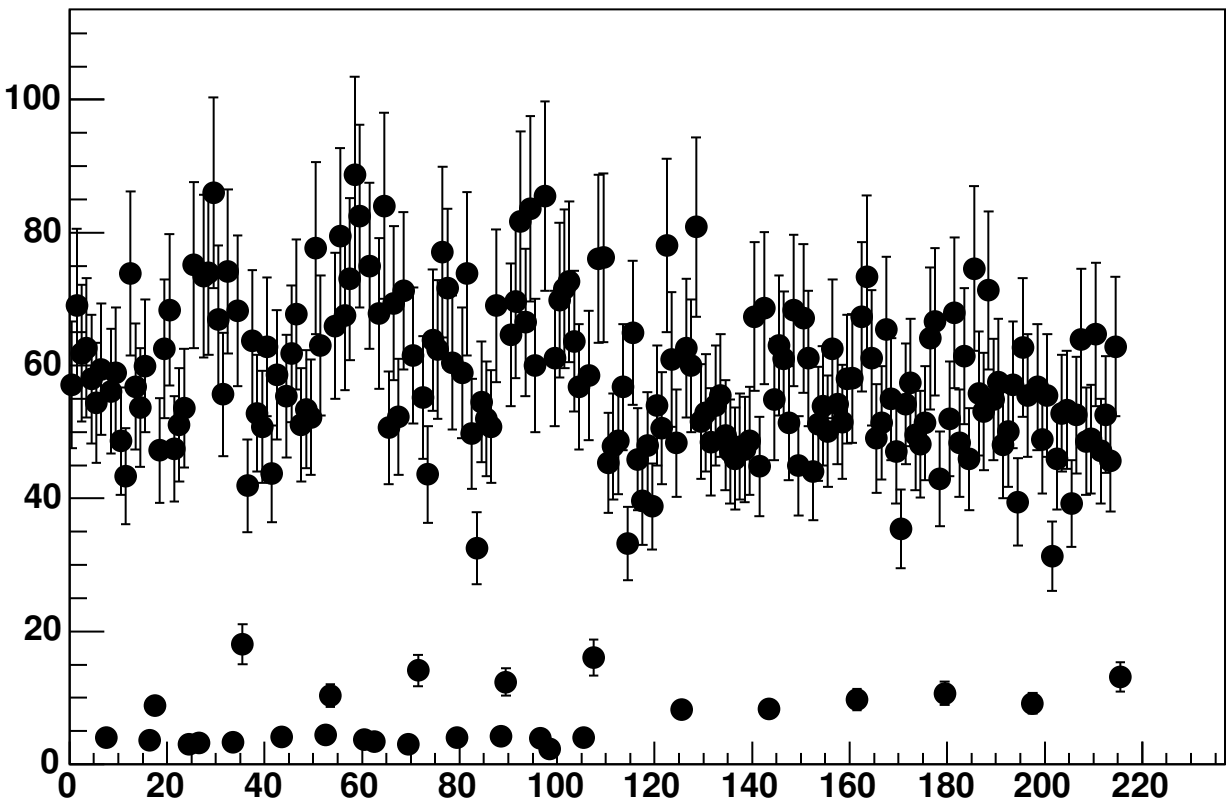
Enable 5, Hold=30, DAC=250, ADC Noise vs 18*Chip+Chan



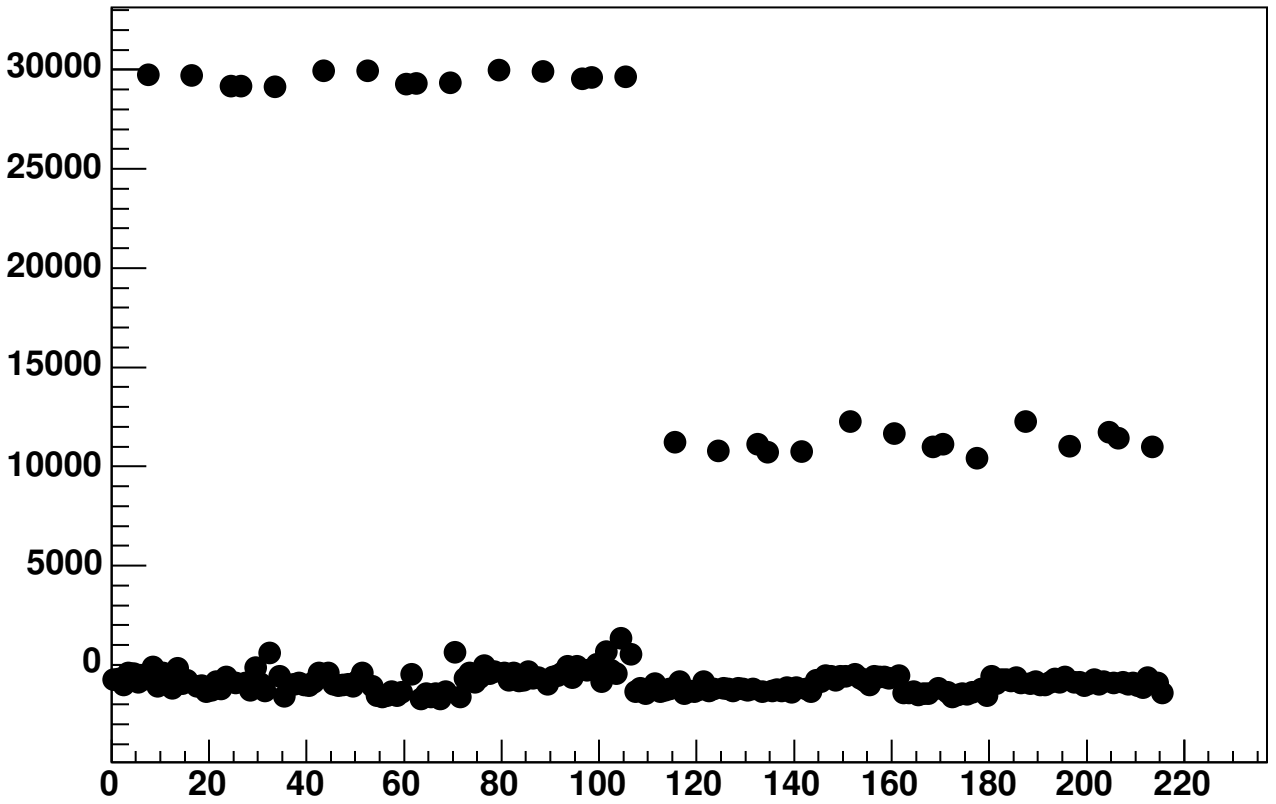
Enable 5, Hold=30, DAC=300, ADC Mean vs 18*Chip+Chan



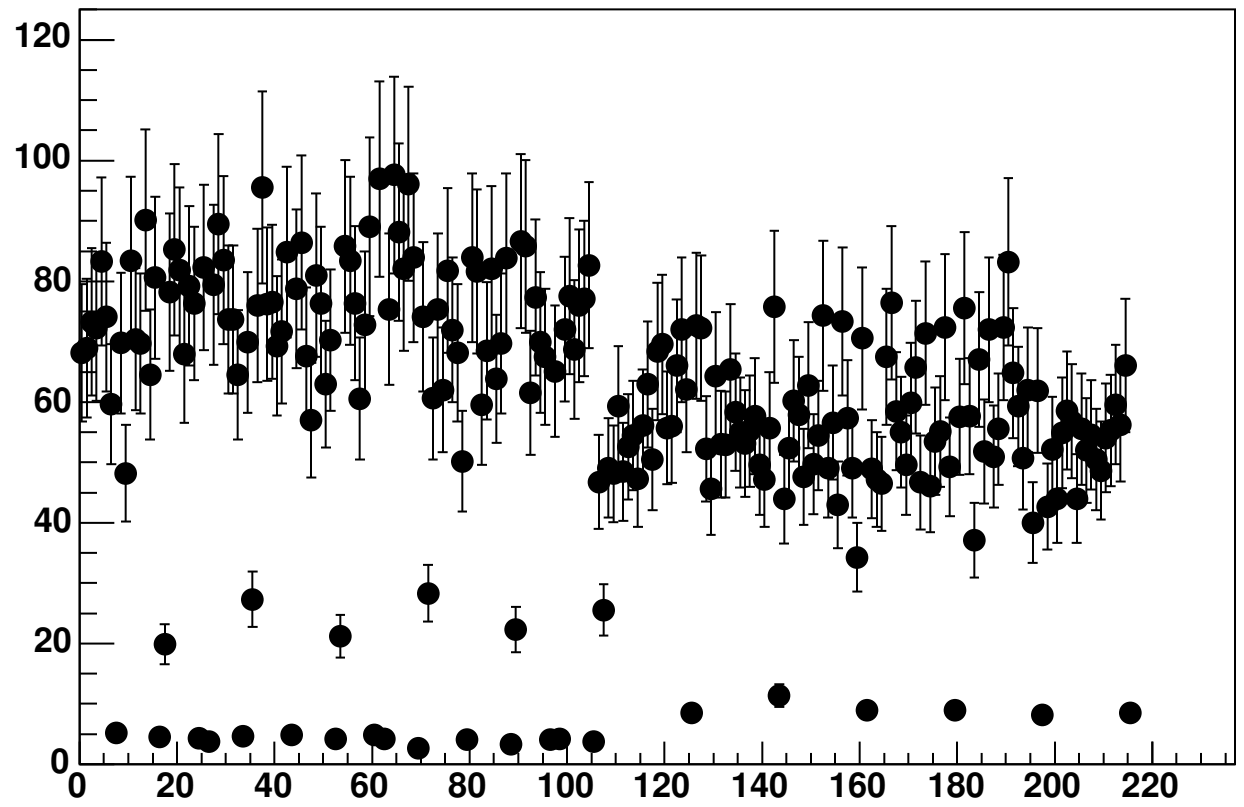
Enable 5, Hold=30, DAC=300, ADC Noise vs 18*Chip+Chan



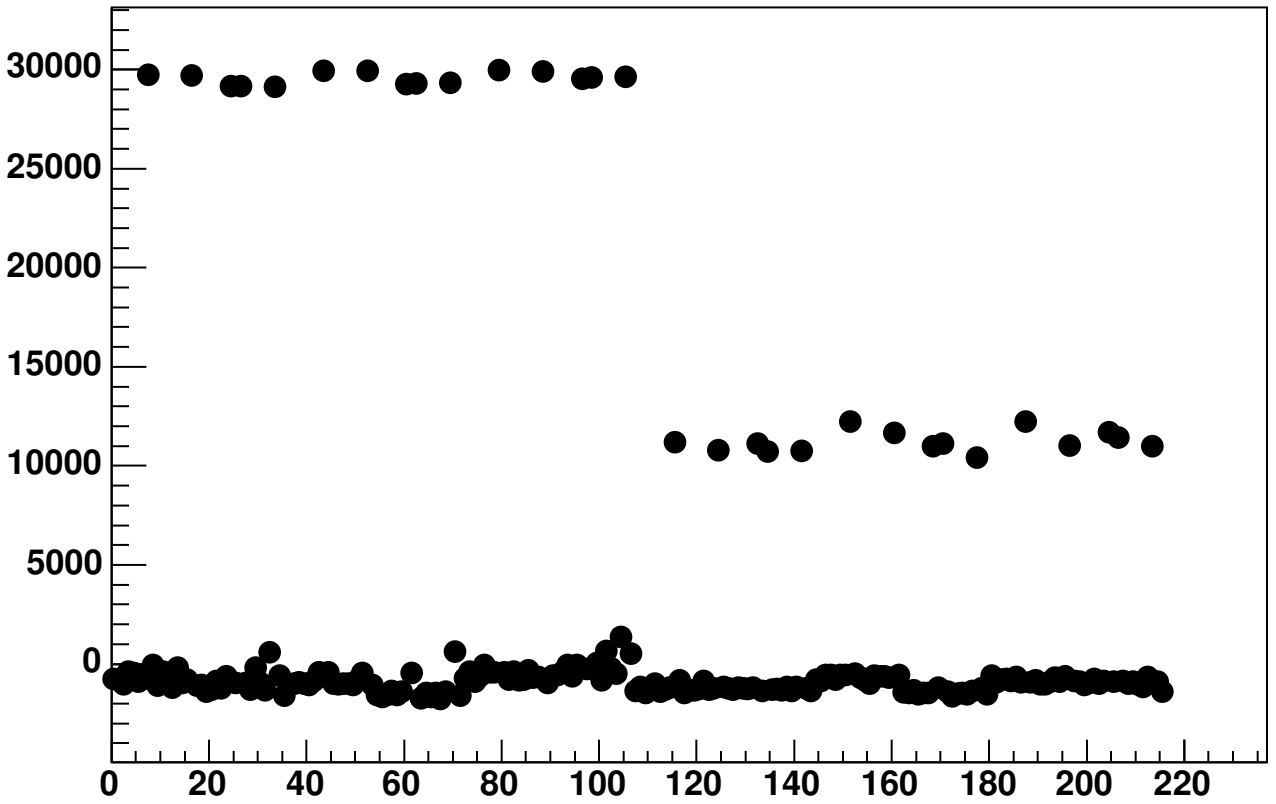
Enable 5, Hold=30, DAC=350, ADC Mean vs 18*Chip+Chan



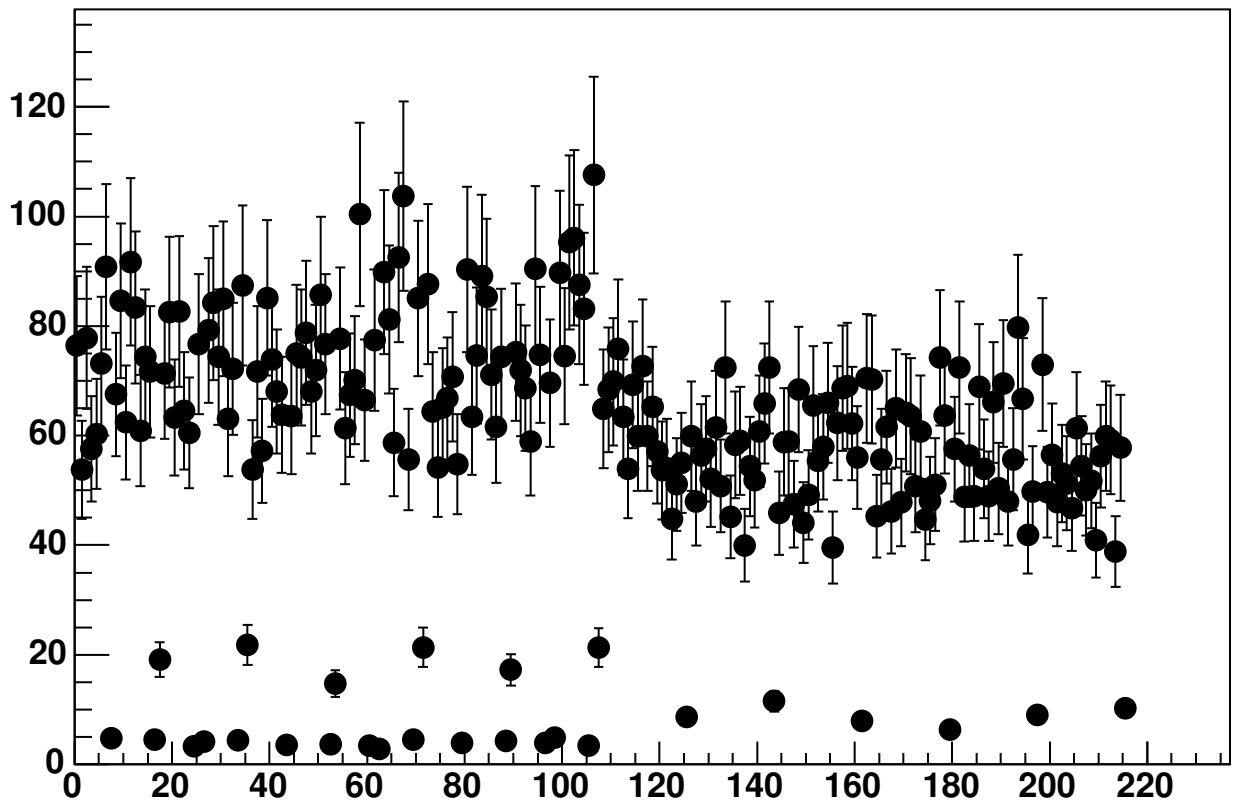
Enable 5, Hold=30, DAC=350, ADC Noise vs 18*Chip+Chan



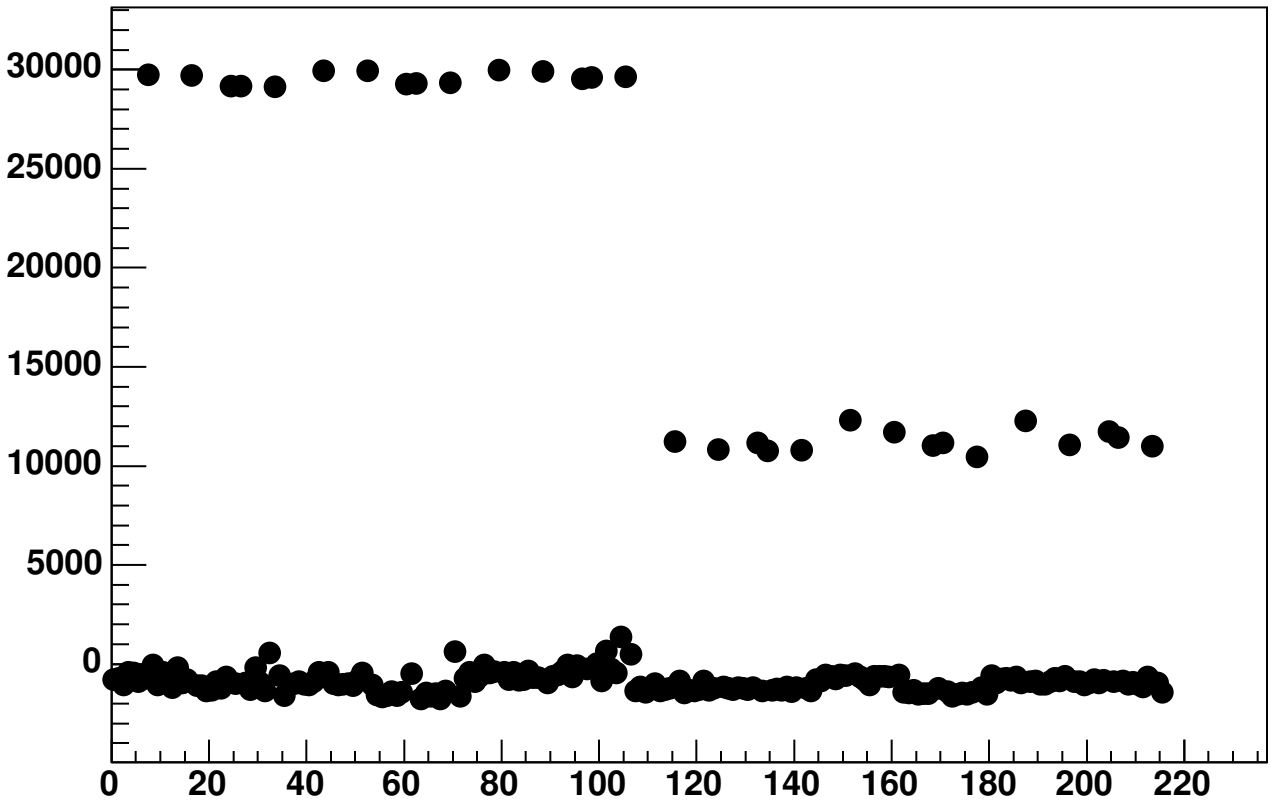
Enable 5, Hold=30, DAC=400, ADC Mean vs 18*Chip+Chan



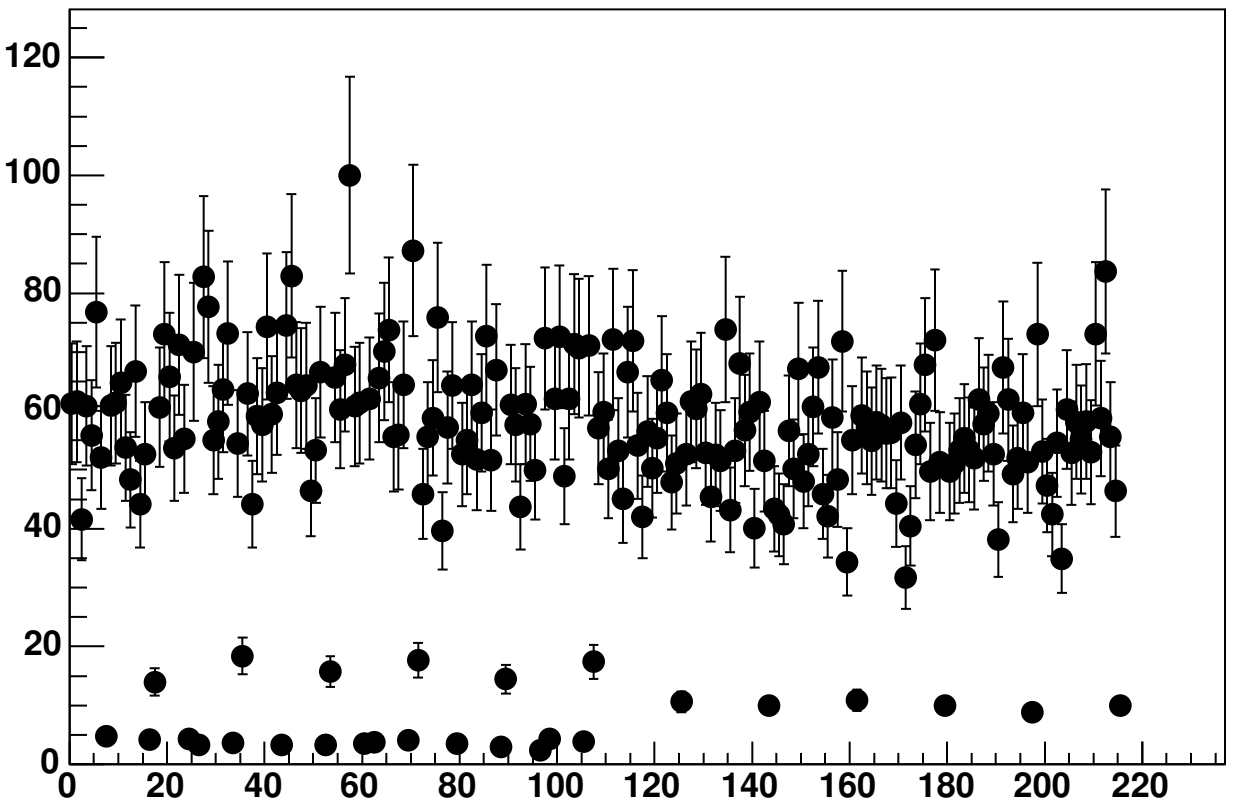
Enable 5, Hold=30, DAC=400, ADC Noise vs 18*Chip+Chan



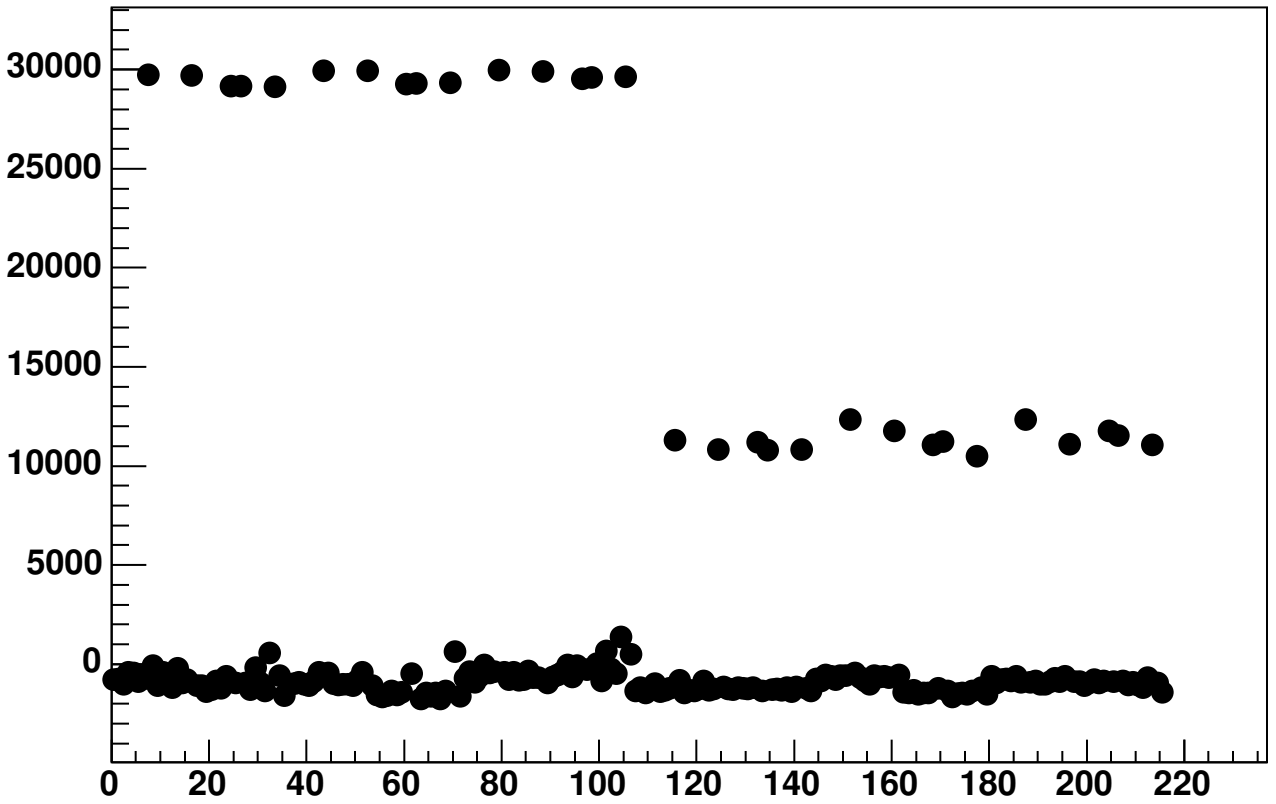
Enable 5, Hold=30, DAC=450, ADC Mean vs 18*Chip+Chan



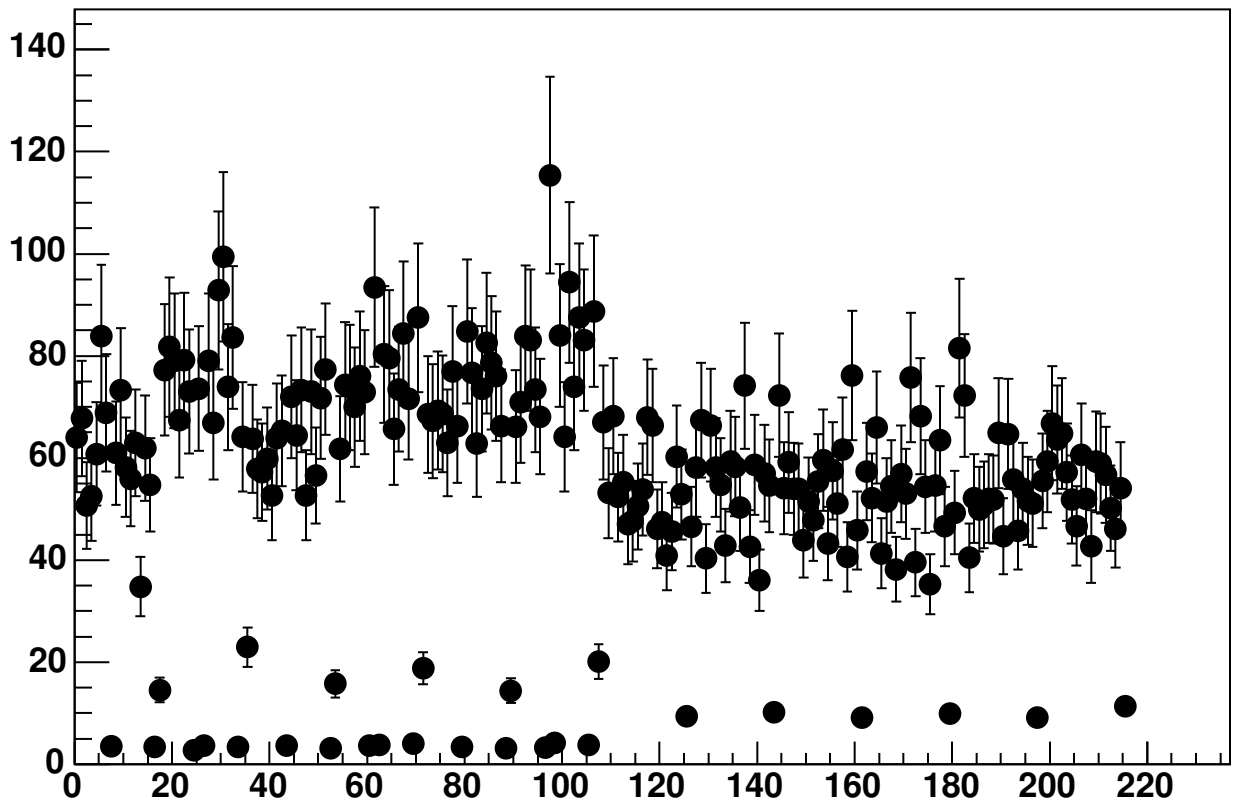
Enable 5, Hold=30, DAC=450, ADC Noise vs 18*Chip+Chan



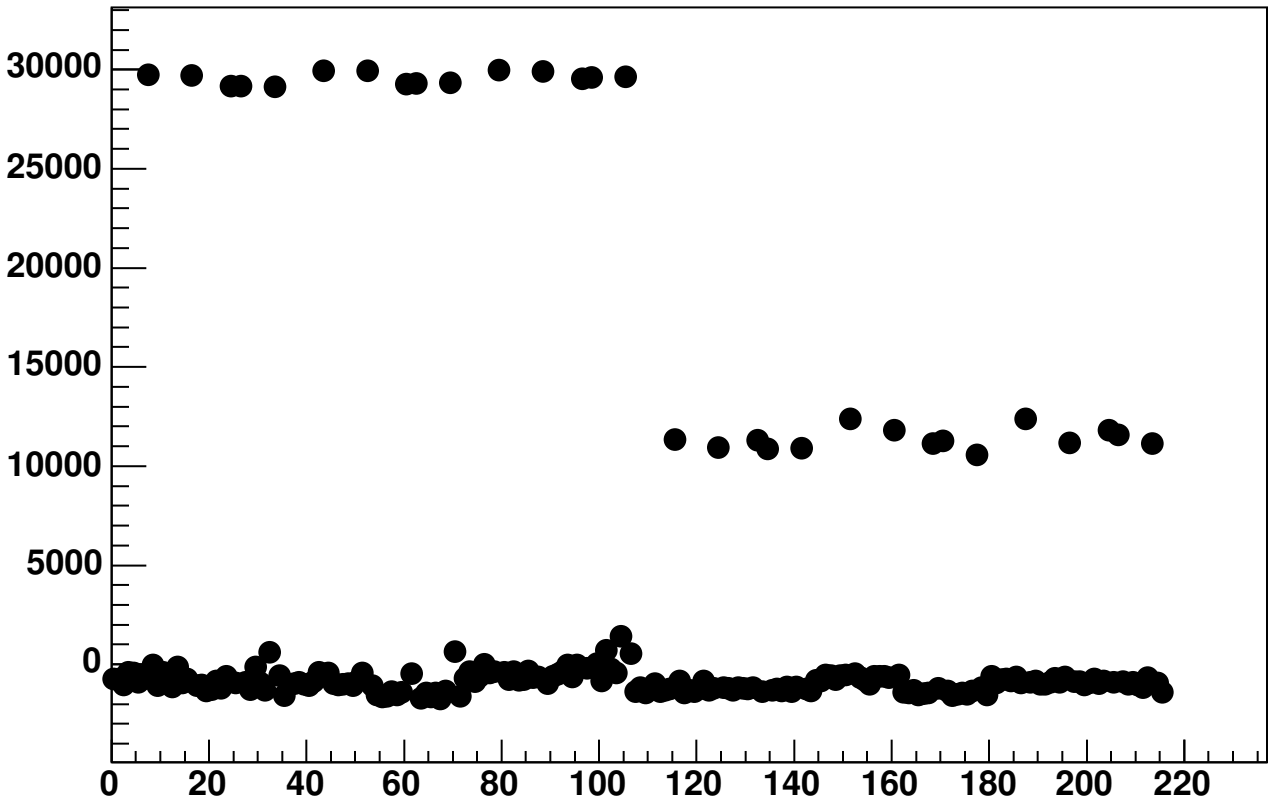
Enable 5, Hold=30, DAC=500, ADC Mean vs 18*Chip+Chan



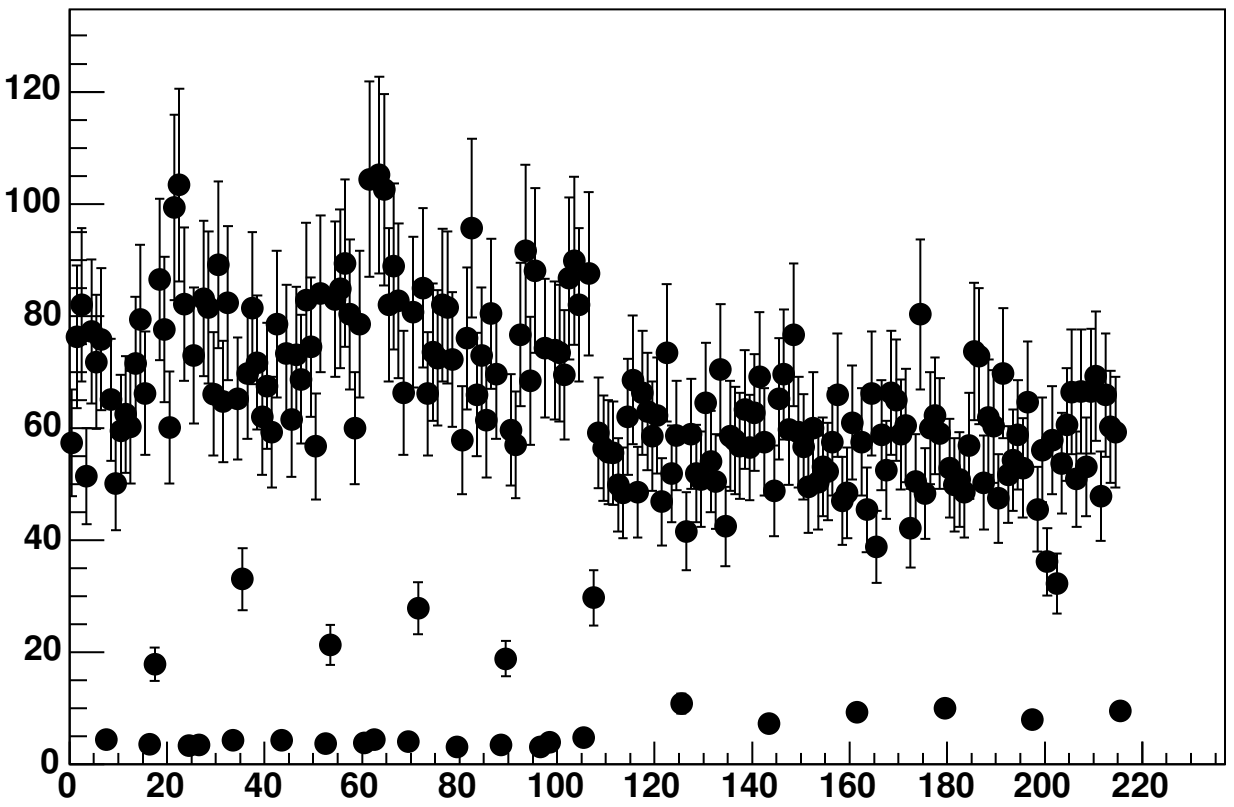
Enable 5, Hold=30, DAC=500, ADC Noise vs 18*Chip+Chan



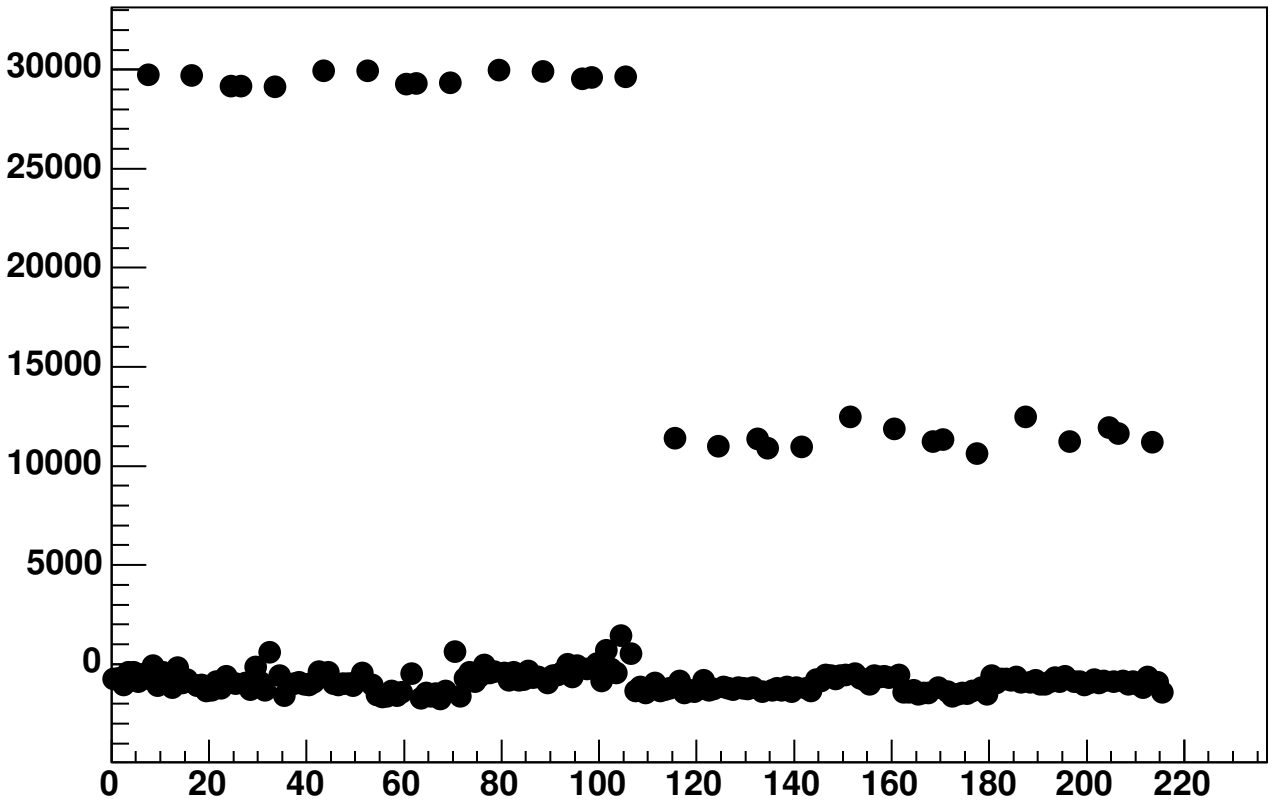
Enable 5, Hold=30, DAC=550, ADC Mean vs 18*Chip+Chan



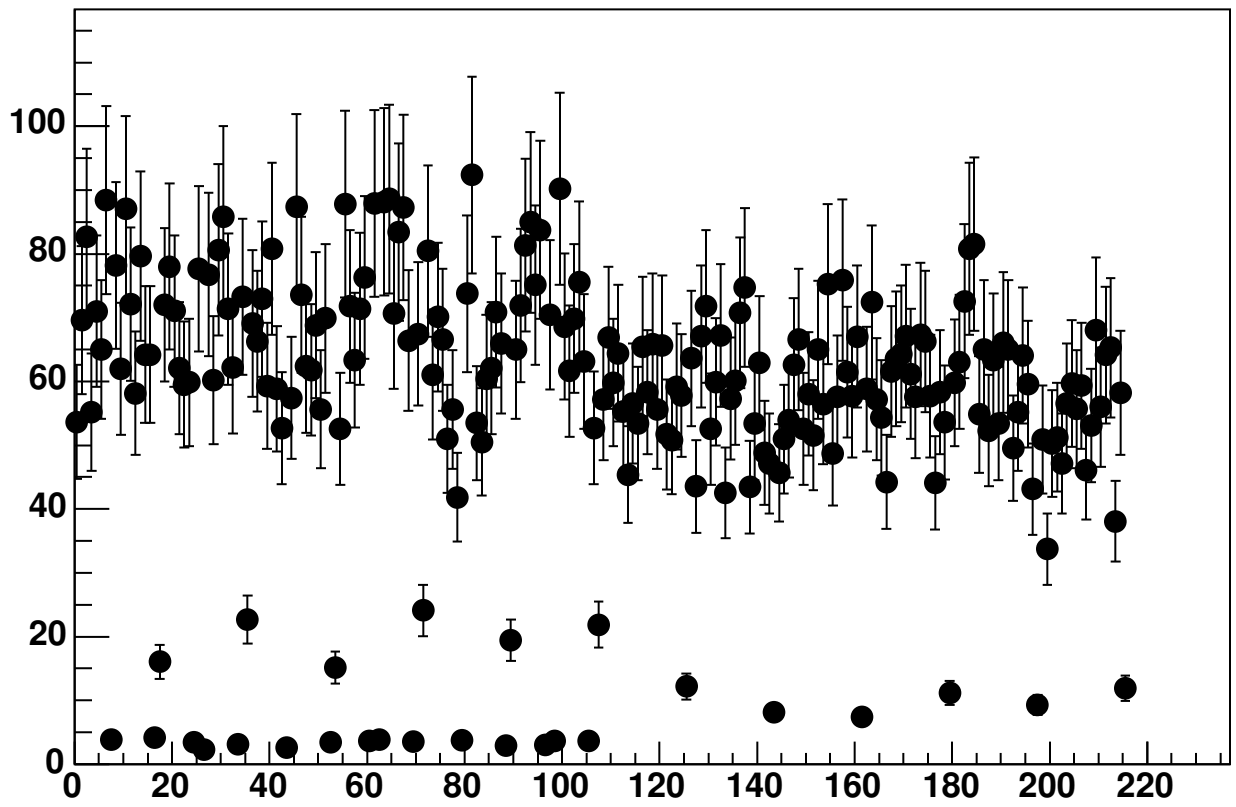
Enable 5, Hold=30, DAC=550, ADC Noise vs 18*Chip+Chan



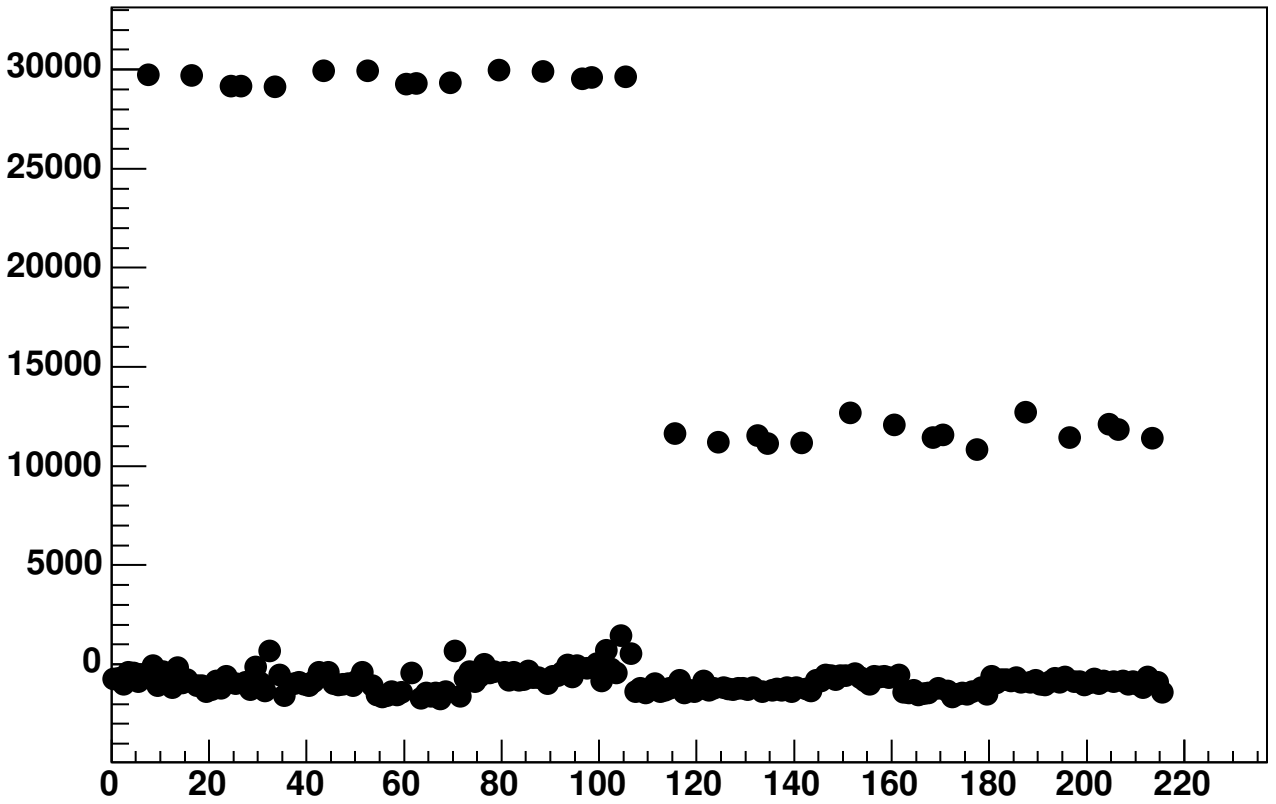
Enable 5, Hold=30, DAC=600, ADC Mean vs 18*Chip+Chan



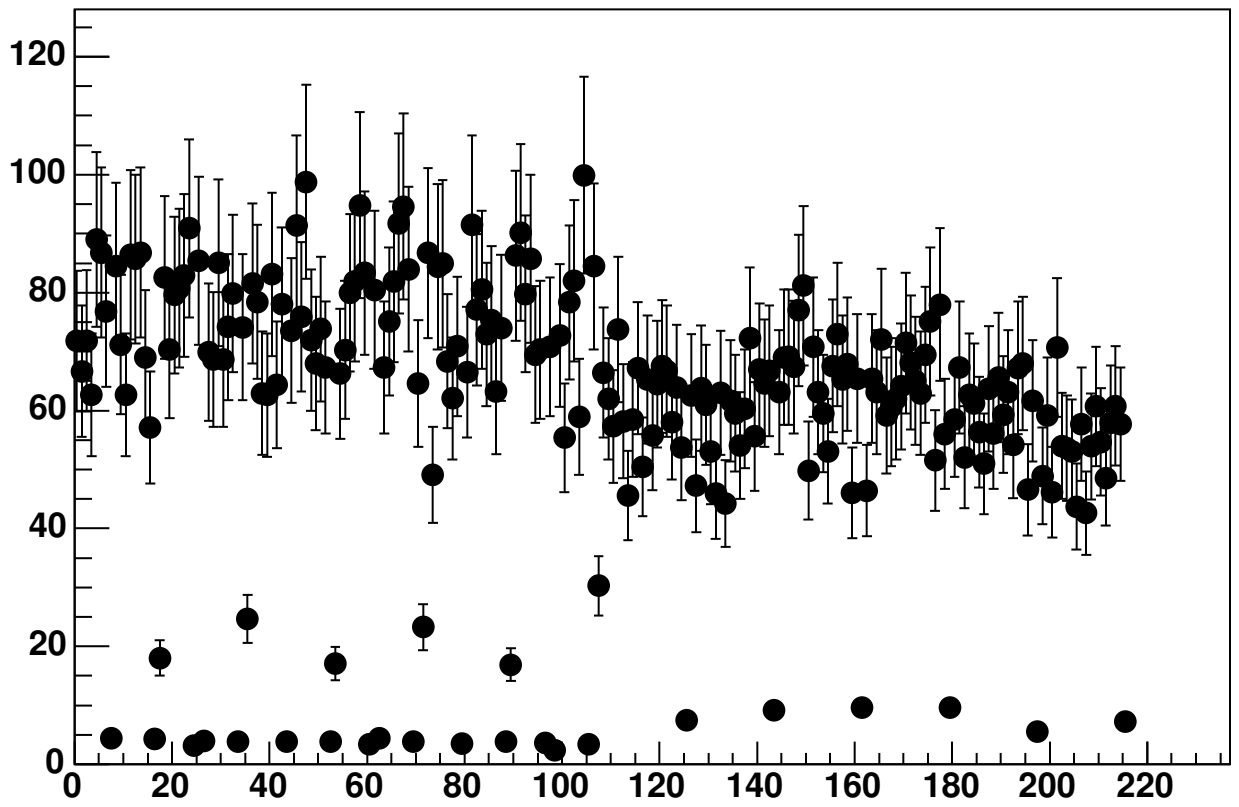
Enable 5, Hold=30, DAC=600, ADC Noise vs 18*Chip+Chan



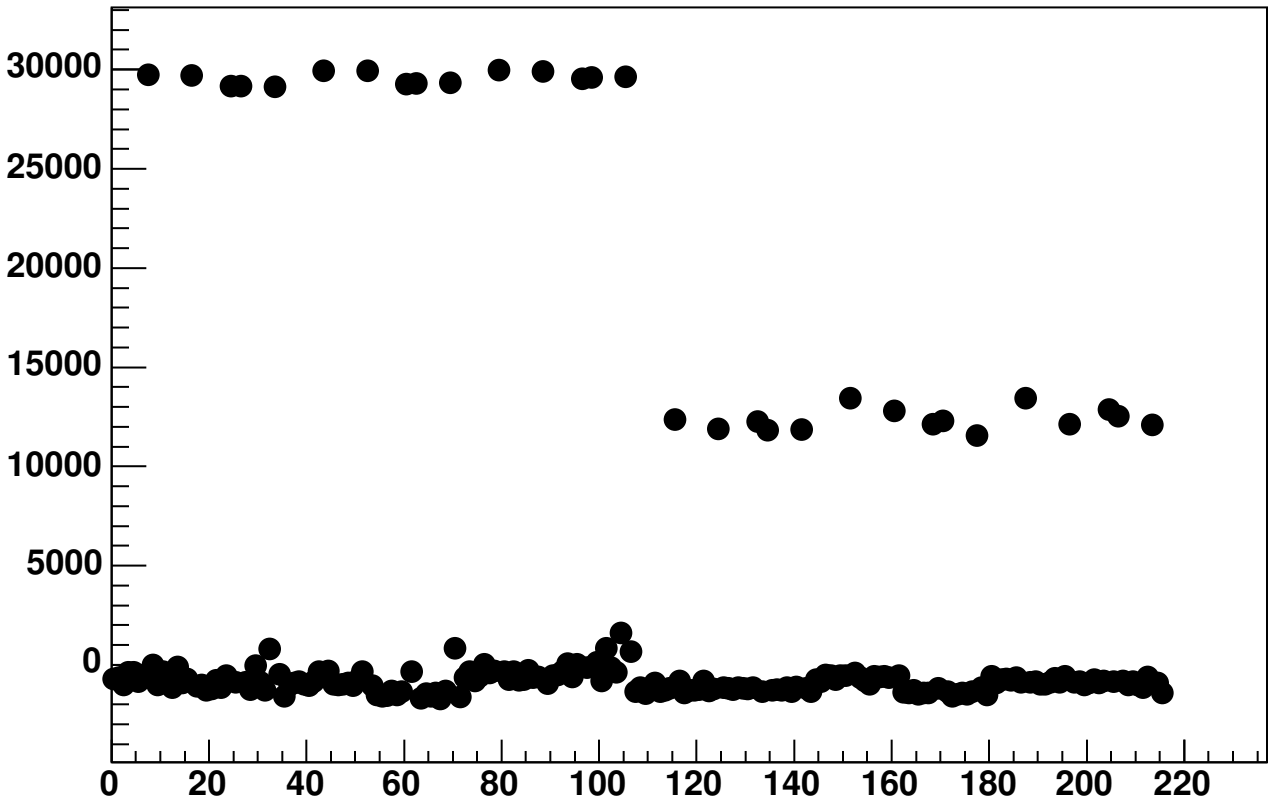
Enable 5, Hold=30, DAC=650, ADC Mean vs 18*Chip+Chan



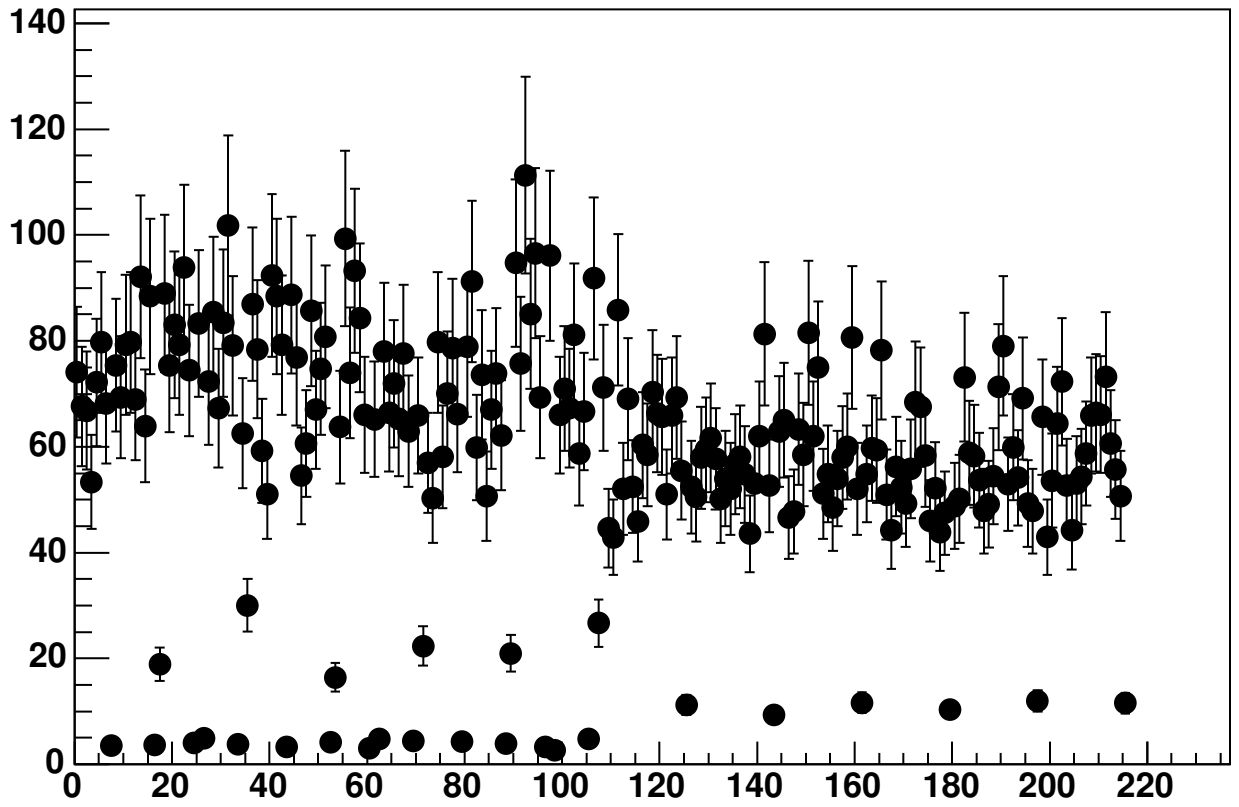
Enable 5, Hold=30, DAC=650, ADC Noise vs 18*Chip+Chan



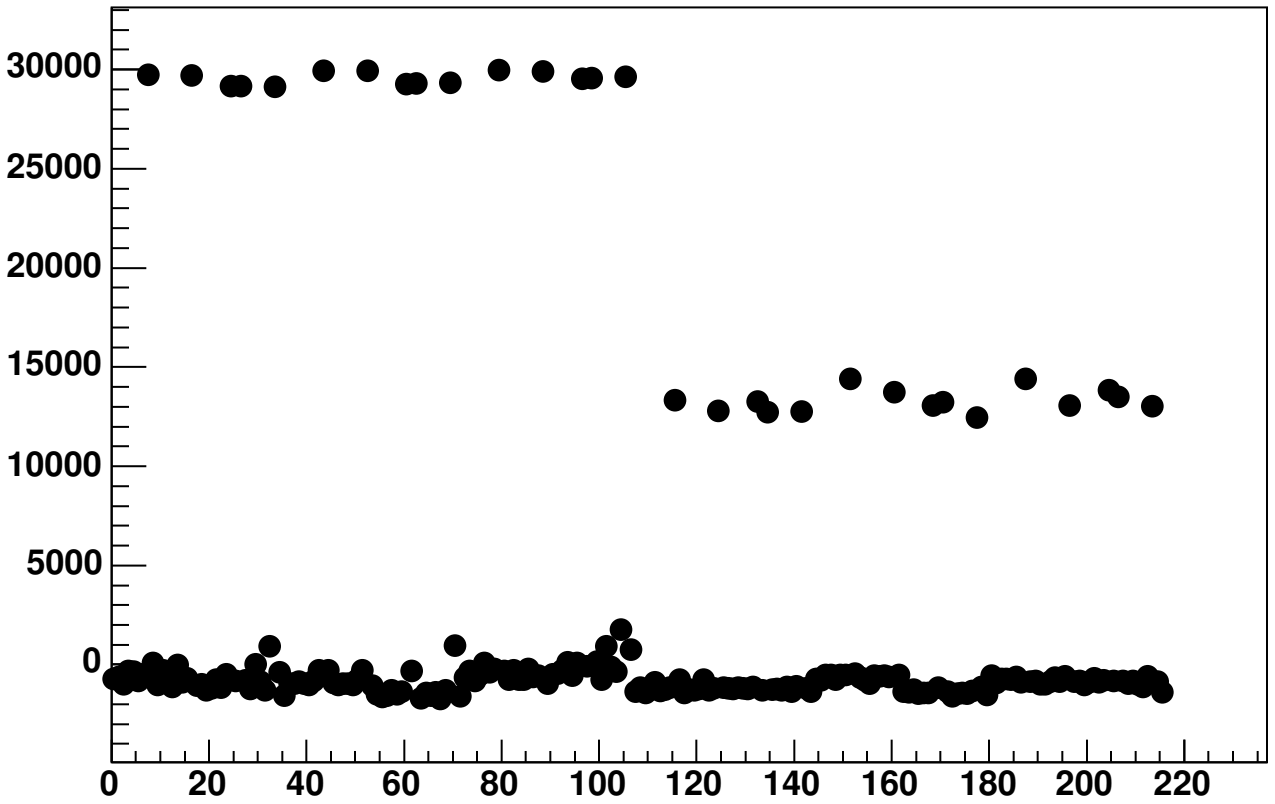
Enable 5, Hold=30, DAC=700, ADC Mean vs 18*Chip+Chan



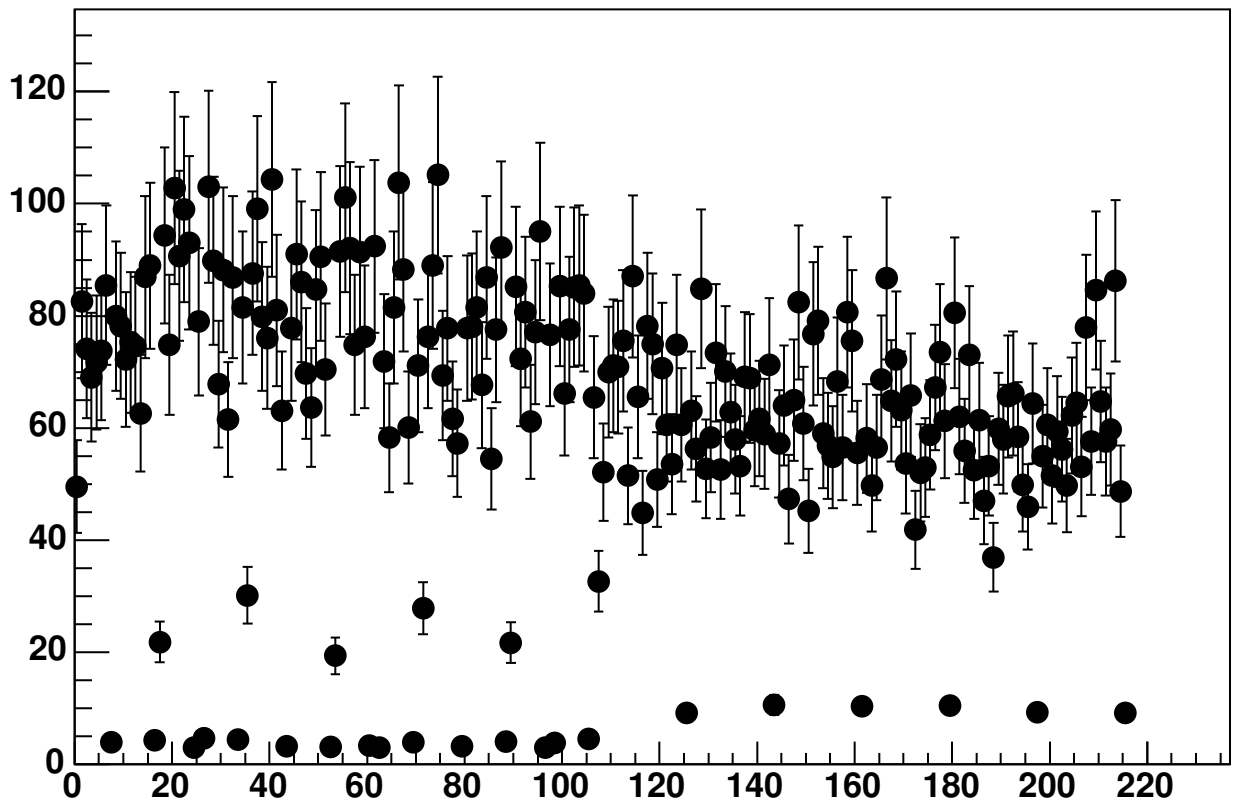
Enable 5, Hold=30, DAC=700, ADC Noise vs 18*Chip+Chan



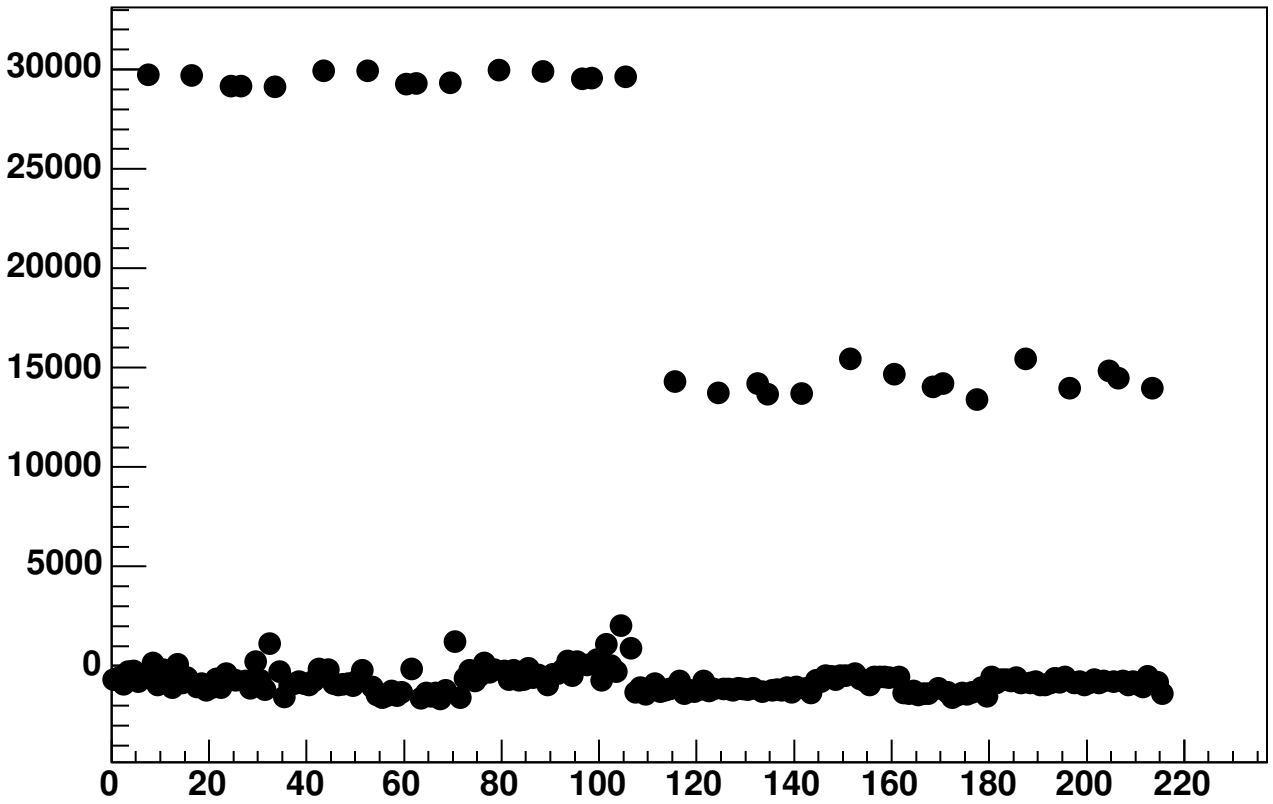
Enable 5, Hold=30, DAC=750, ADC Mean vs 18*Chip+Chan



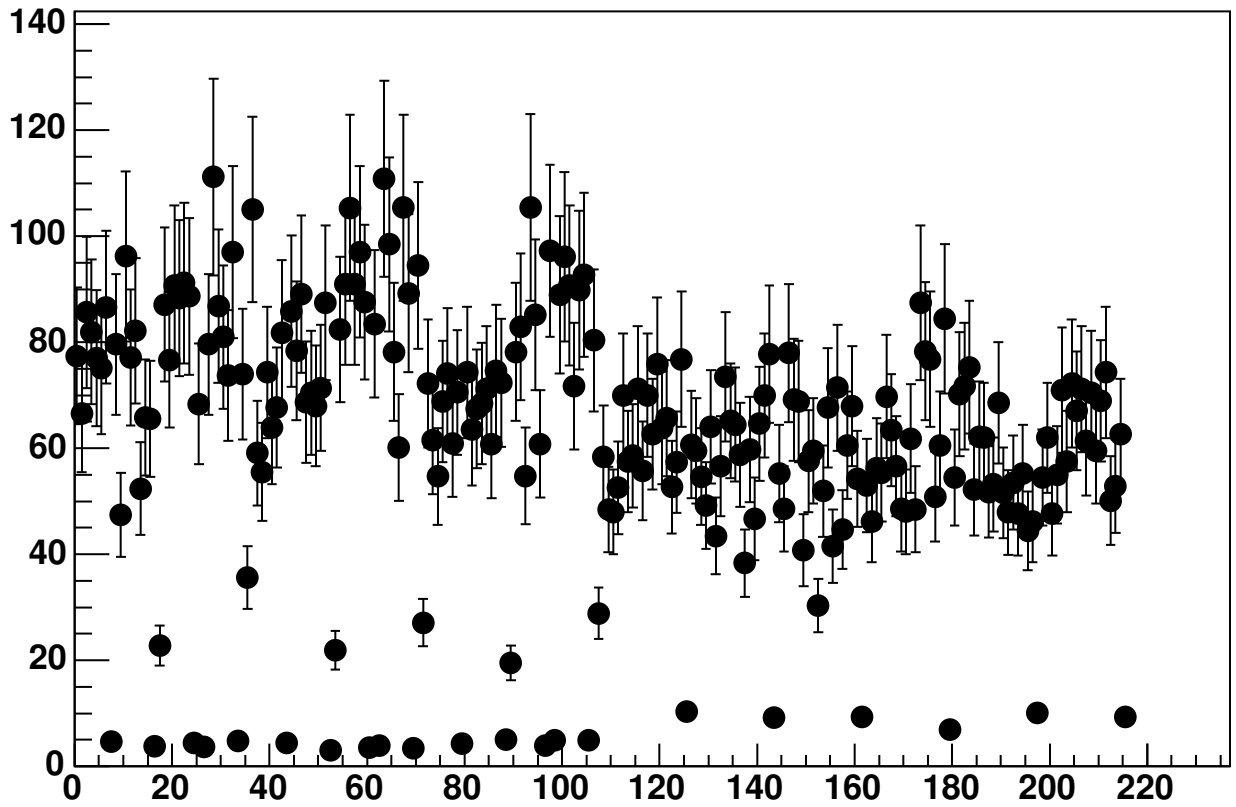
Enable 5, Hold=30, DAC=750, ADC Noise vs 18*Chip+Chan



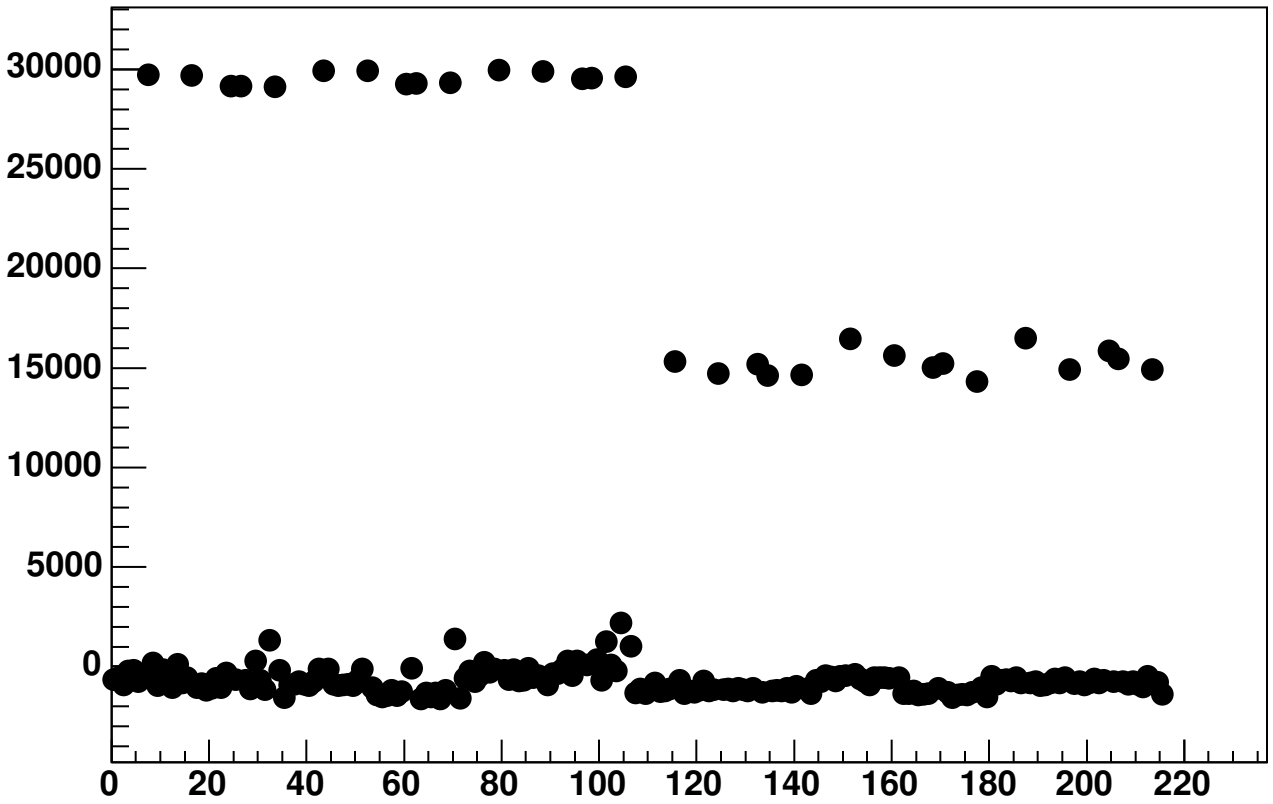
Enable 5, Hold=30, DAC=800, ADC Mean vs 18*Chip+Chan



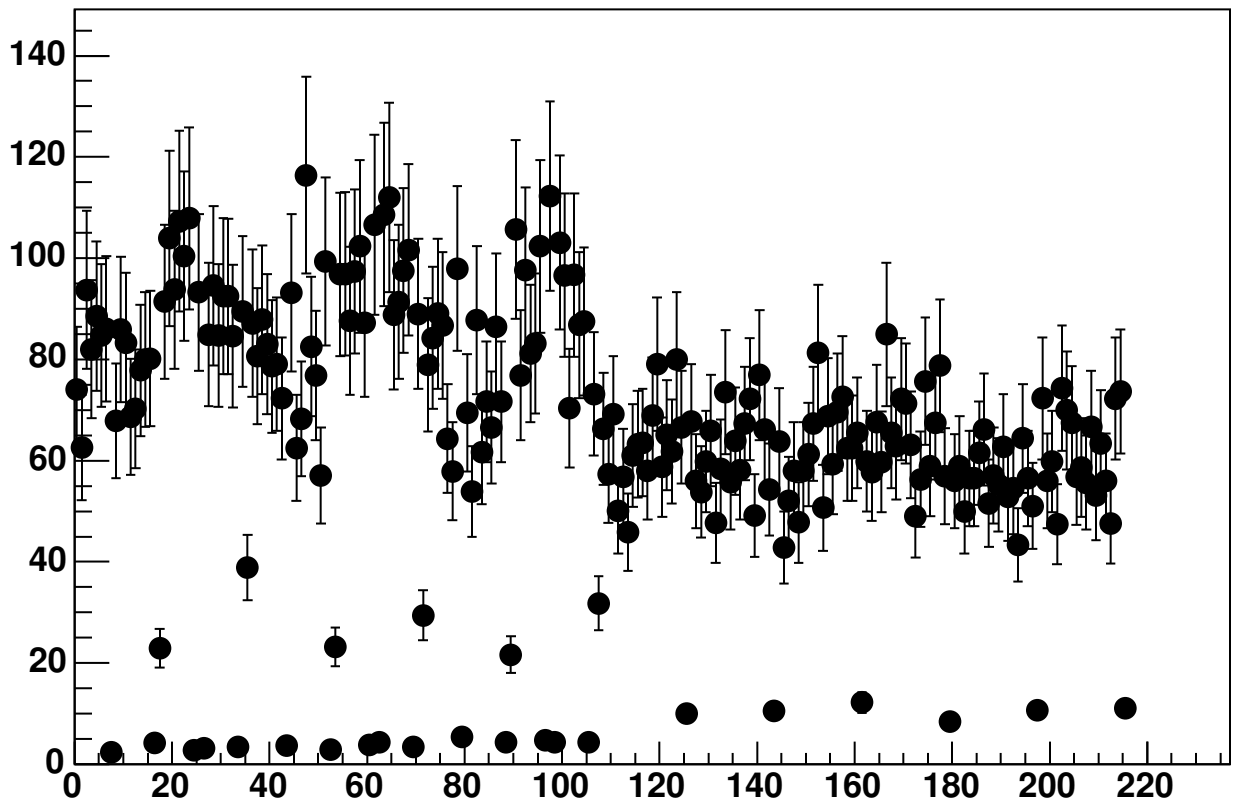
Enable 5, Hold=30, DAC=800, ADC Noise vs 18*Chip+Chan



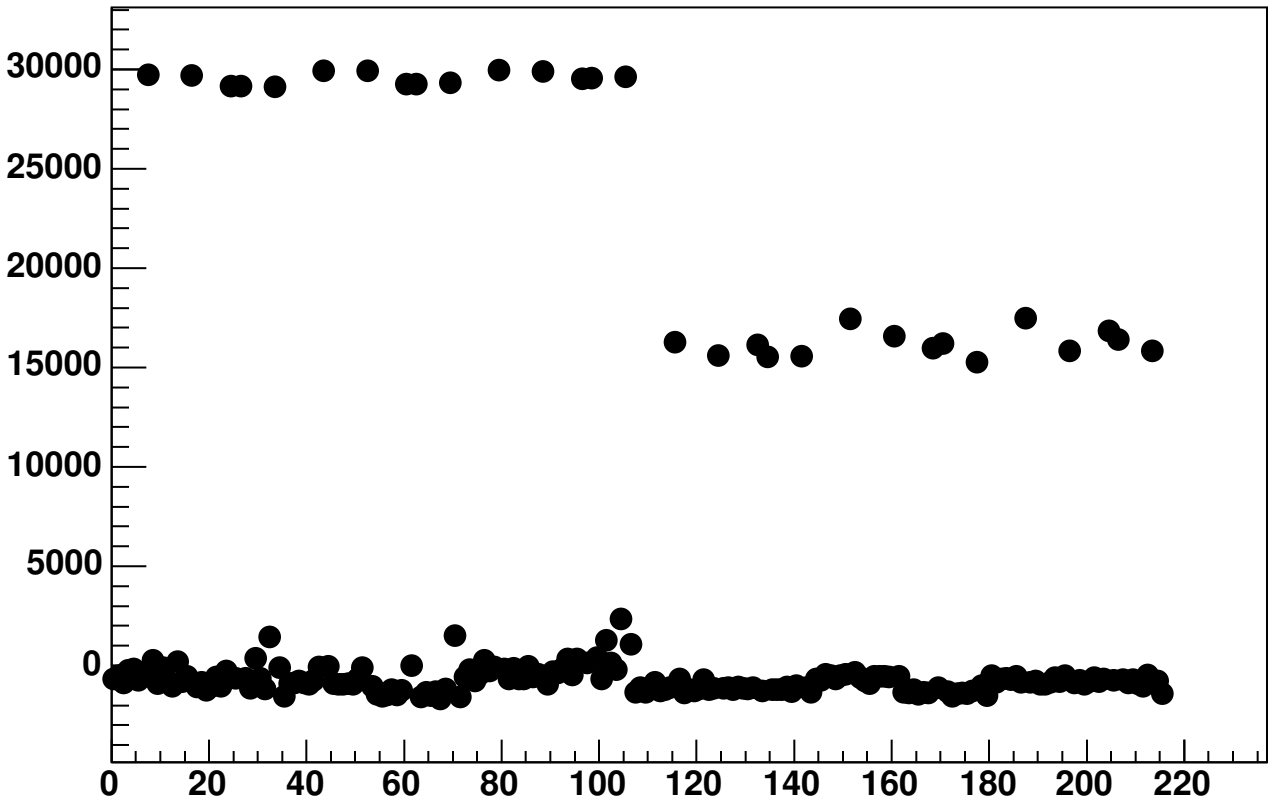
Enable 5, Hold=30, DAC=850, ADC Mean vs 18*Chip+Chan



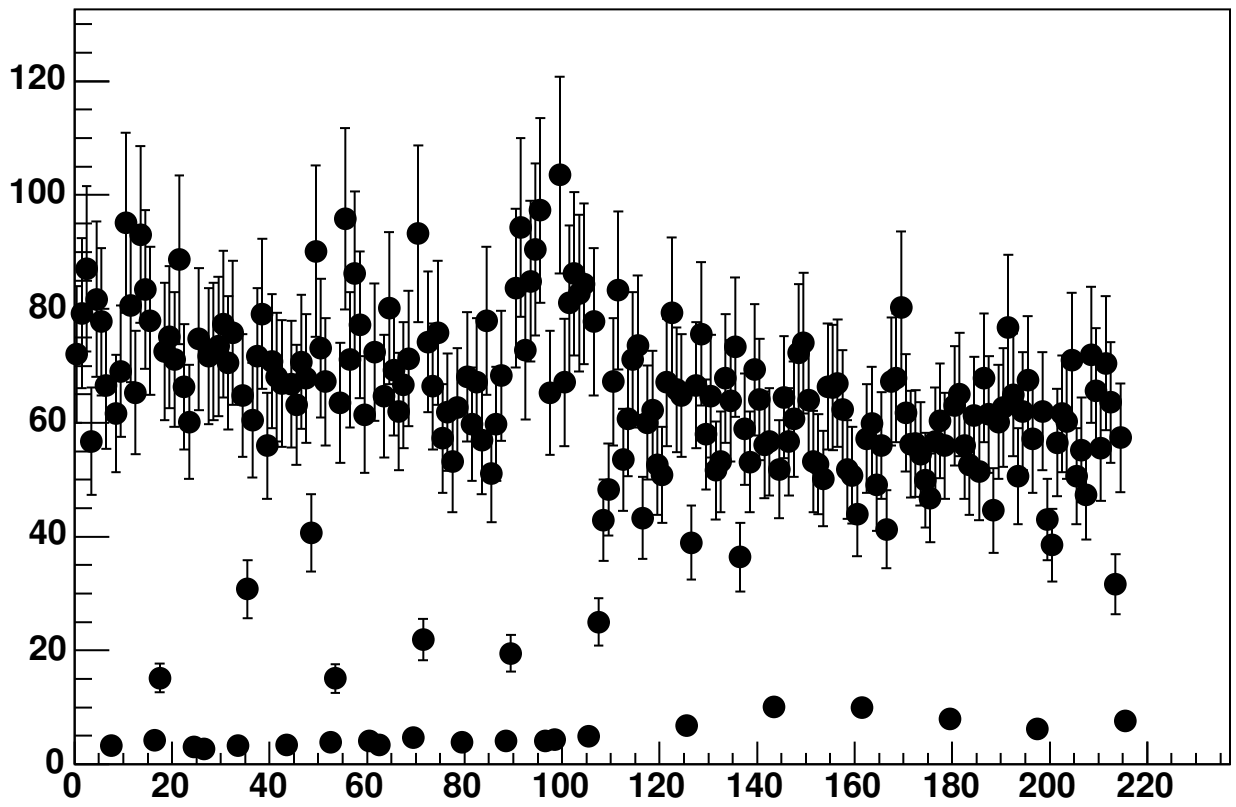
Enable 5, Hold=30, DAC=850, ADC Noise vs 18*Chip+Chan



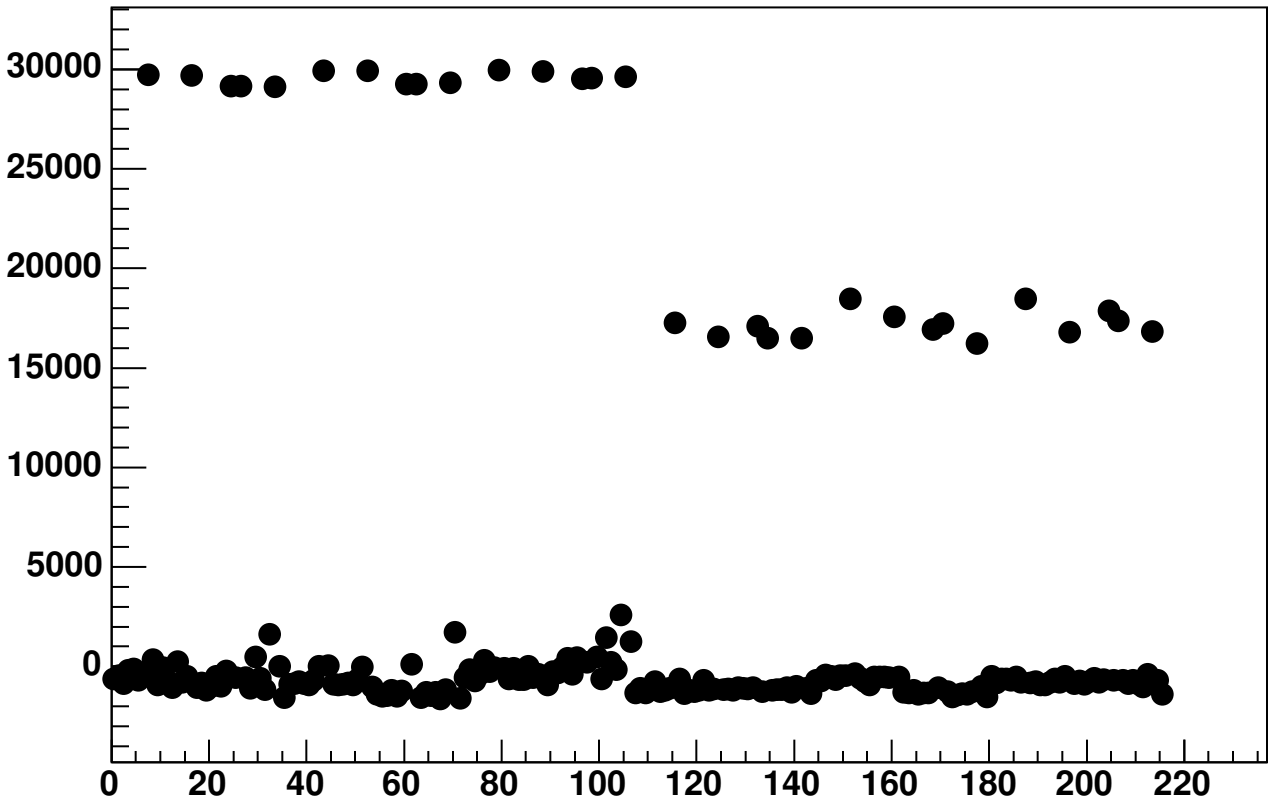
Enable 5, Hold=30, DAC=900, ADC Mean vs 18*Chip+Chan



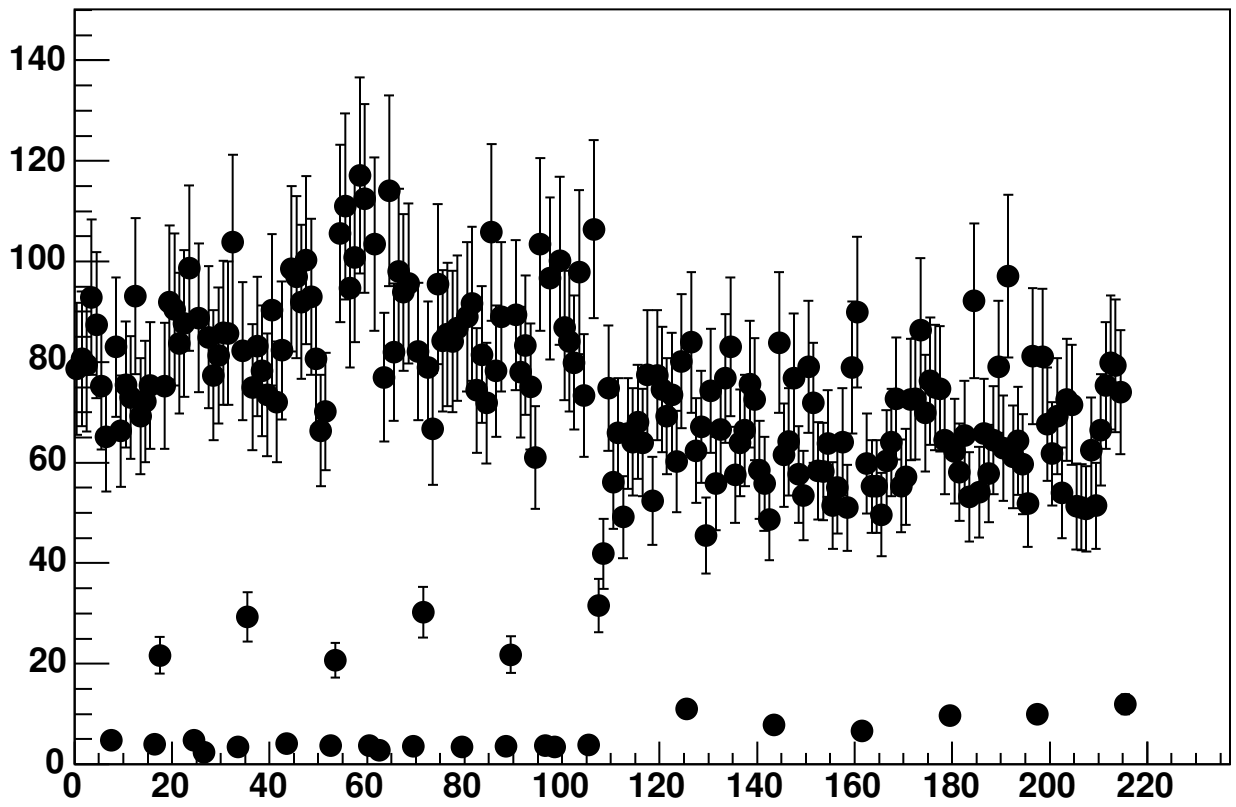
Enable 5, Hold=30, DAC=900, ADC Noise vs 18*Chip+Chan



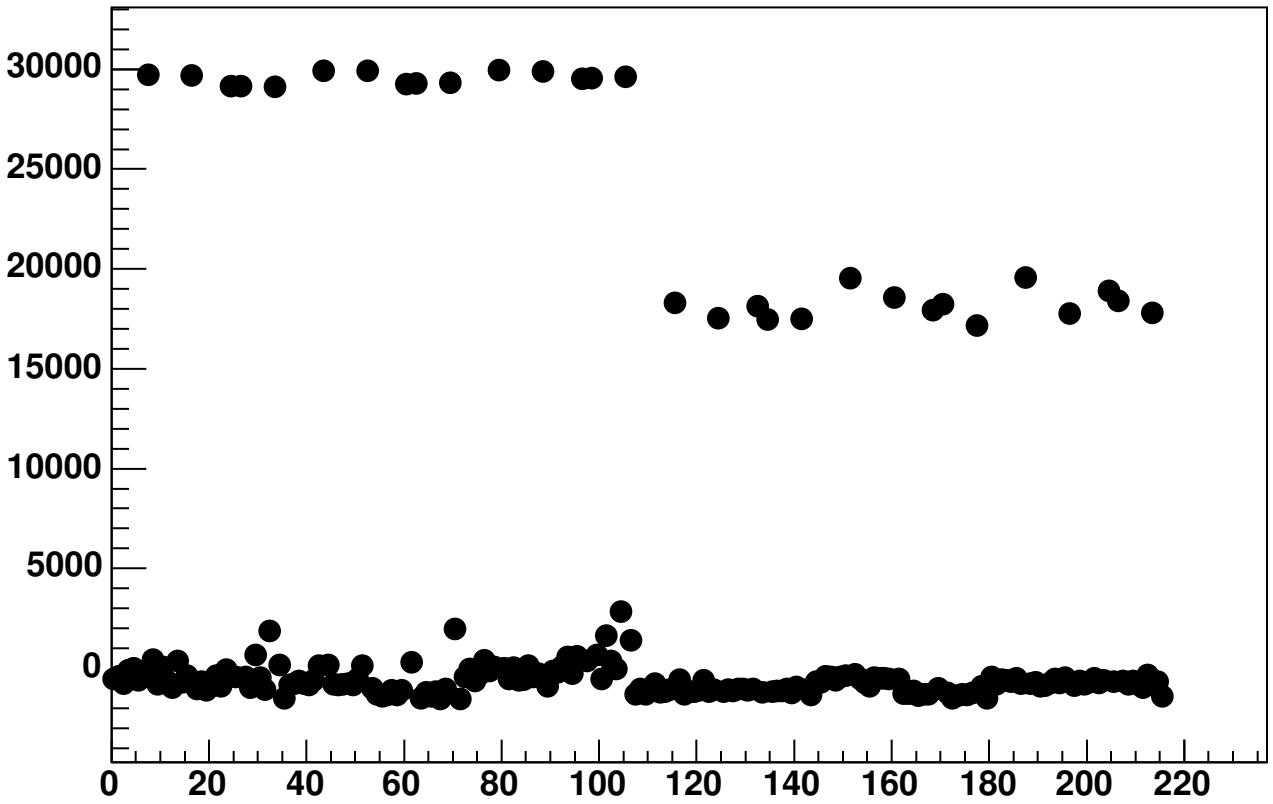
Enable 5, Hold=30, DAC=950, ADC Mean vs 18*Chip+Chan



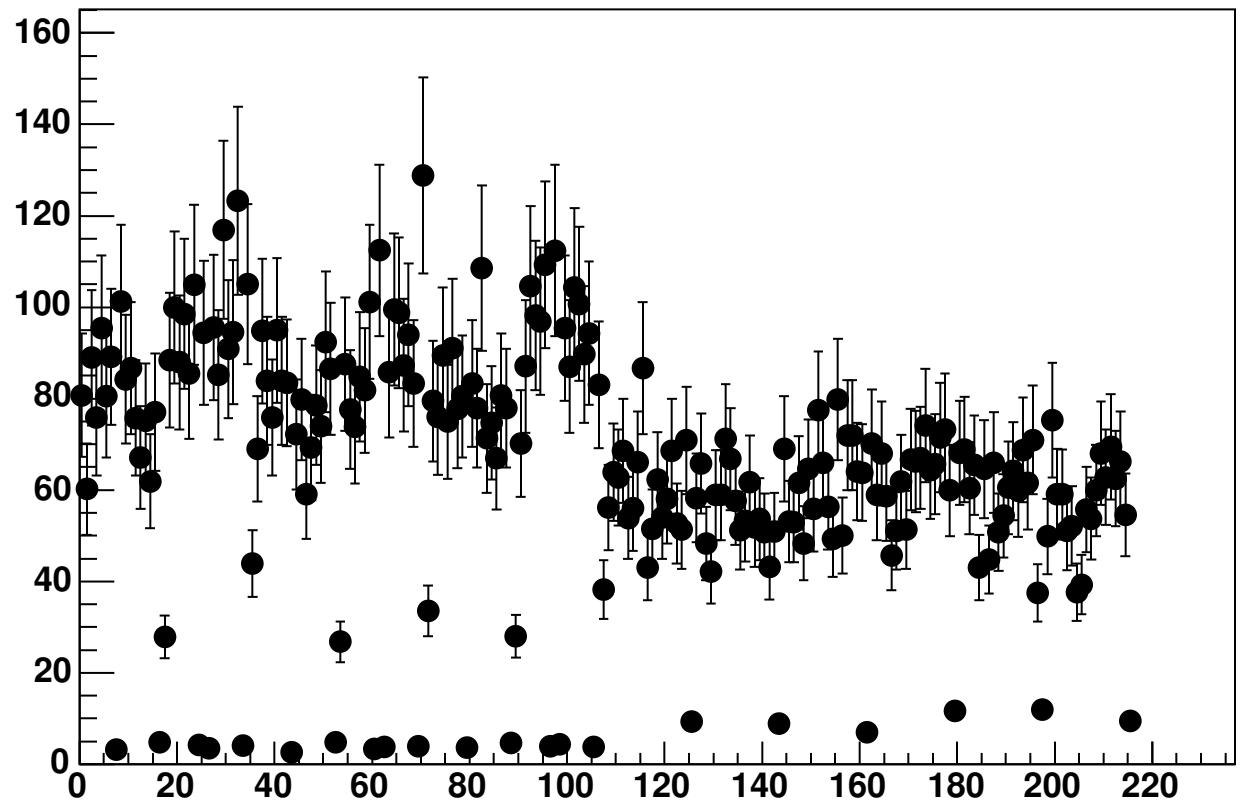
Enable 5, Hold=30, DAC=950, ADC Noise vs 18*Chip+Chan



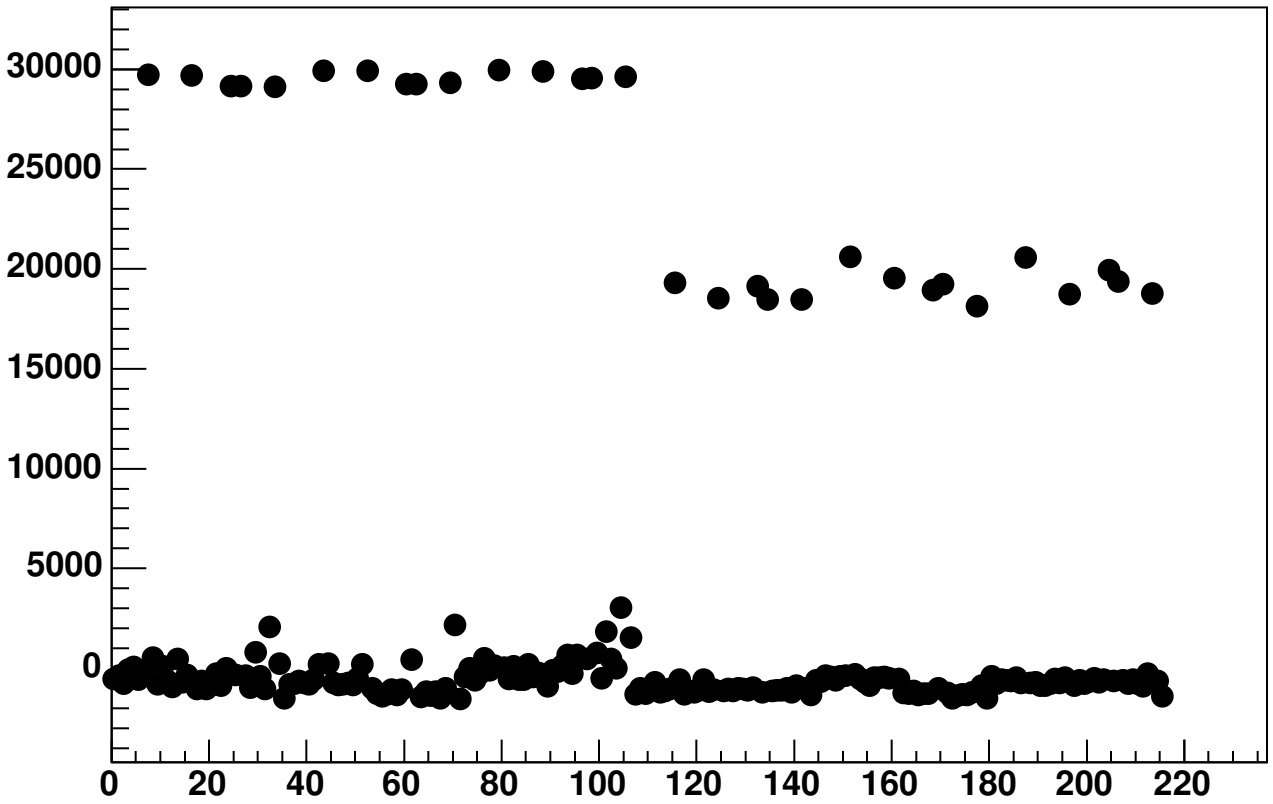
Enable 5, Hold=30, DAC=1000, ADC Mean vs 18*Chip+Chan



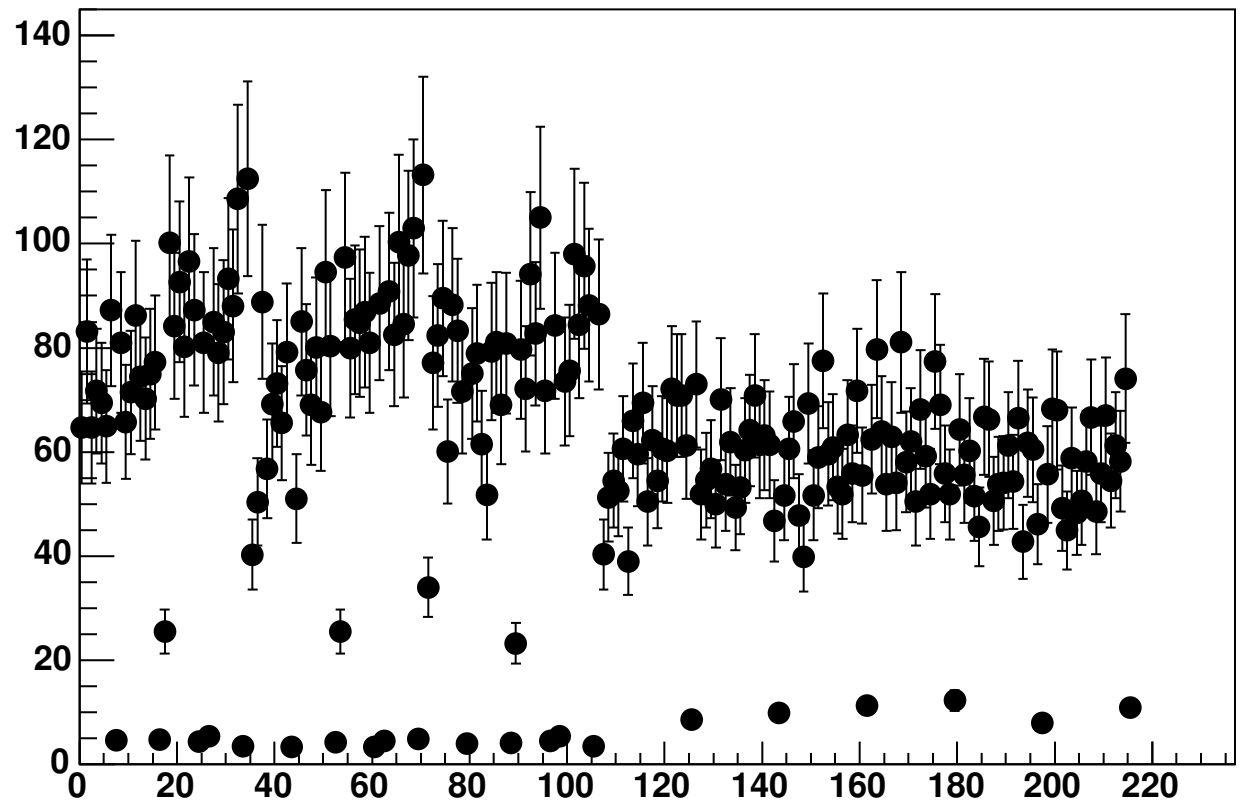
Enable 5, Hold=30, DAC=1000, ADC Noise vs 18*Chip+Chan



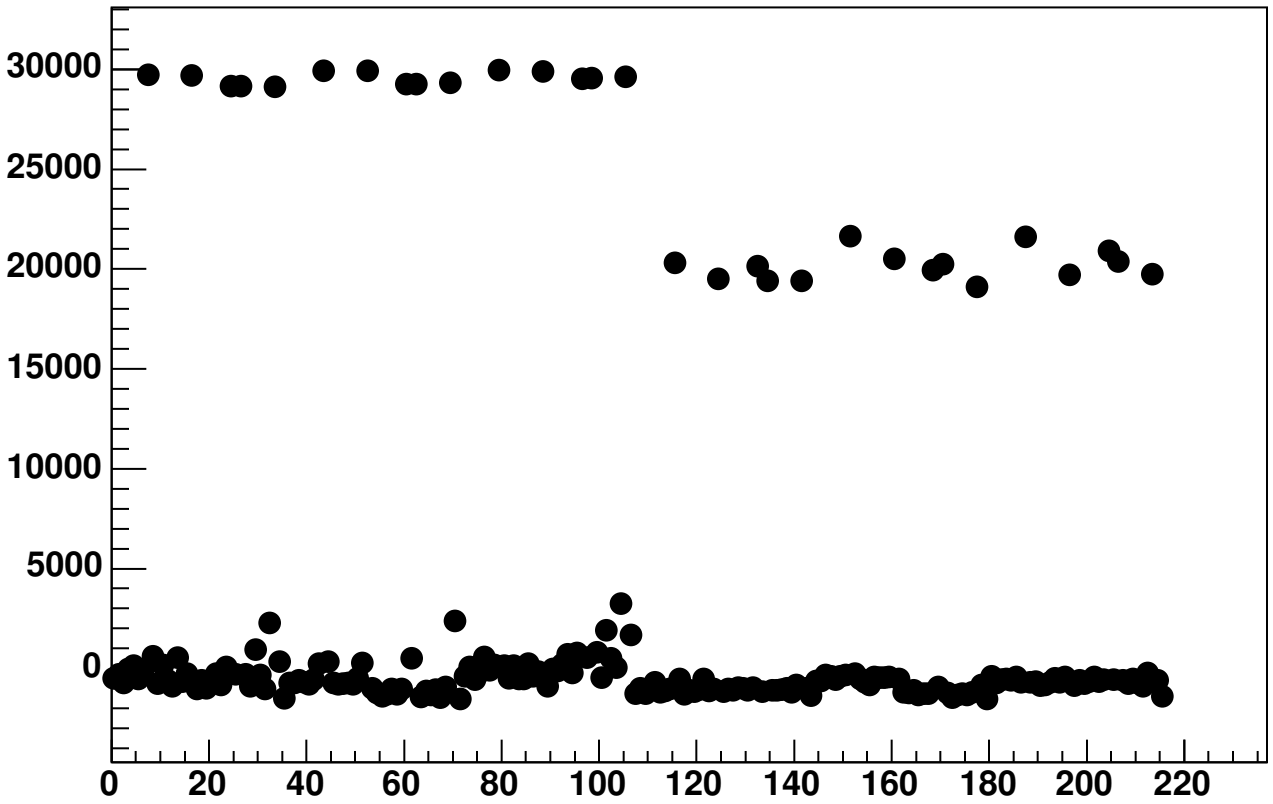
Enable 5, Hold=30, DAC=1050, ADC Mean vs 18*Chip+Chan



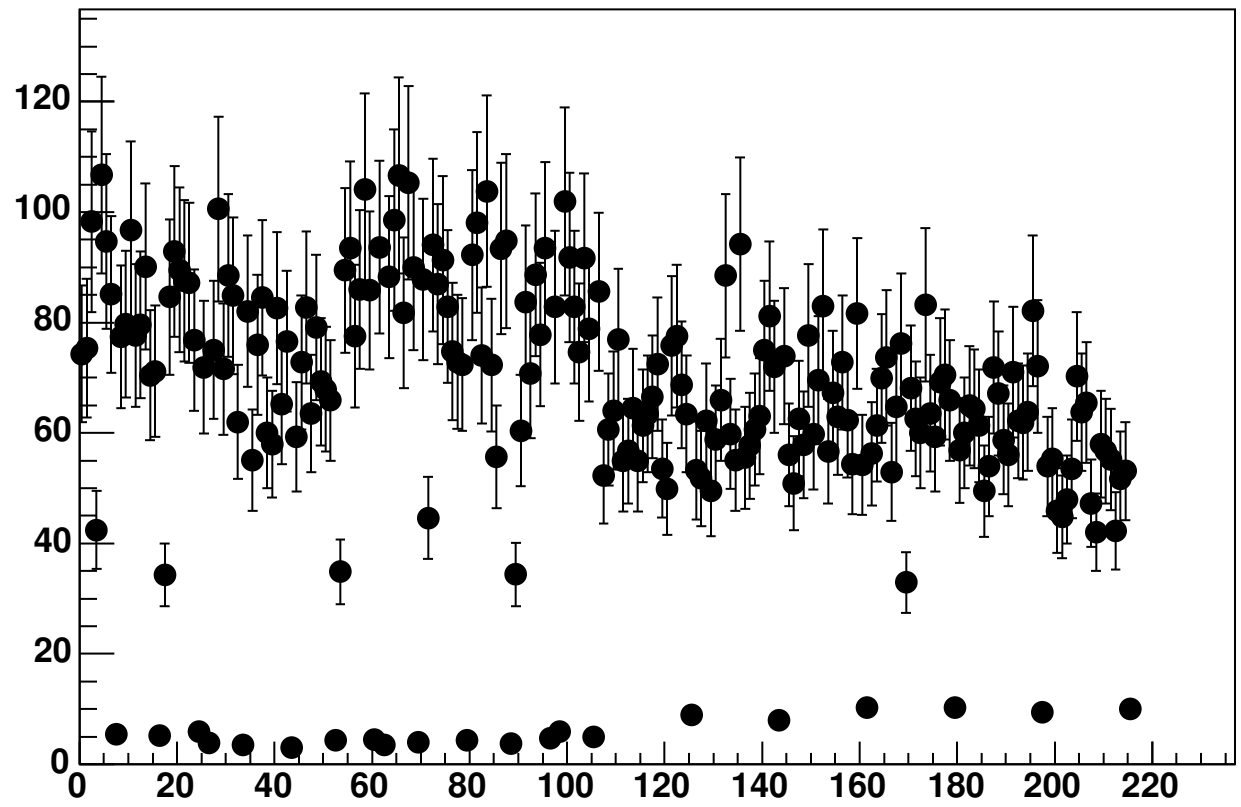
Enable 5, Hold=30, DAC=1050, ADC Noise vs 18*Chip+Chan



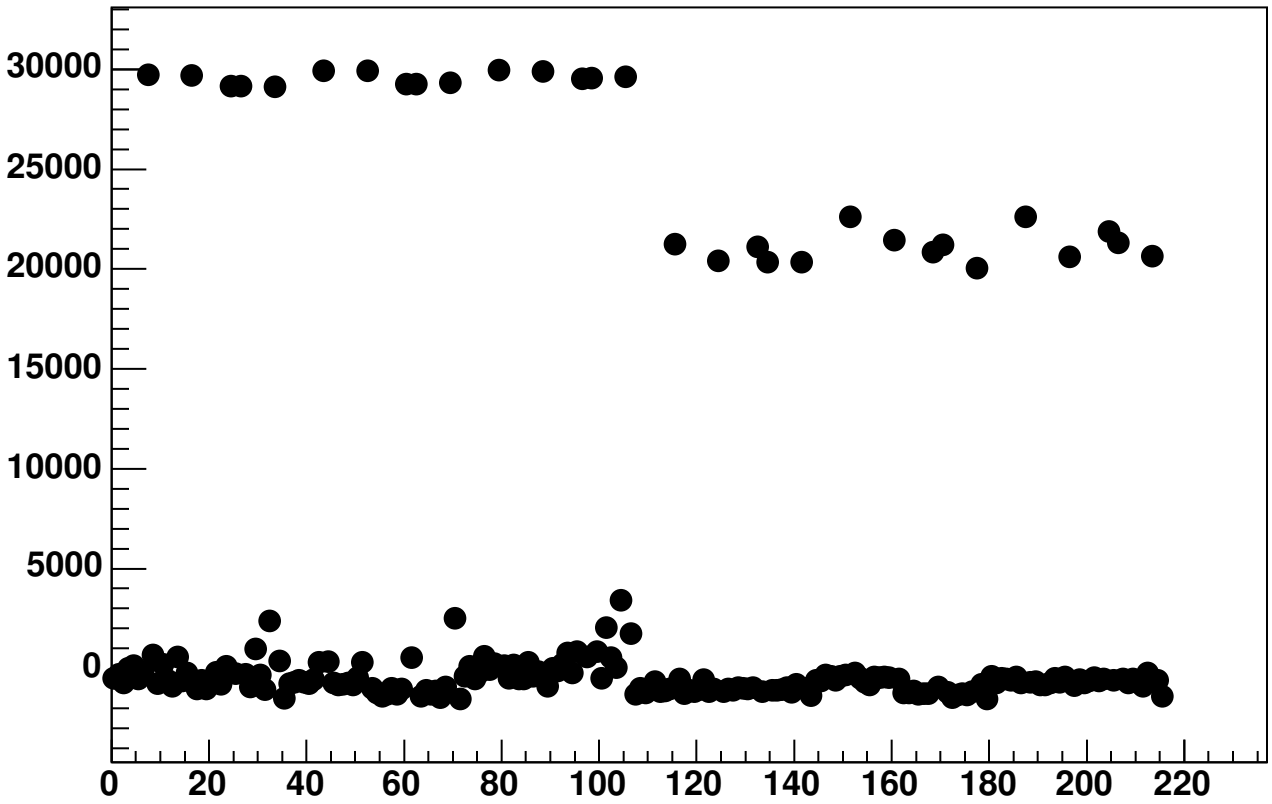
Enable 5, Hold=30, DAC=1100, ADC Mean vs 18*Chip+Chan



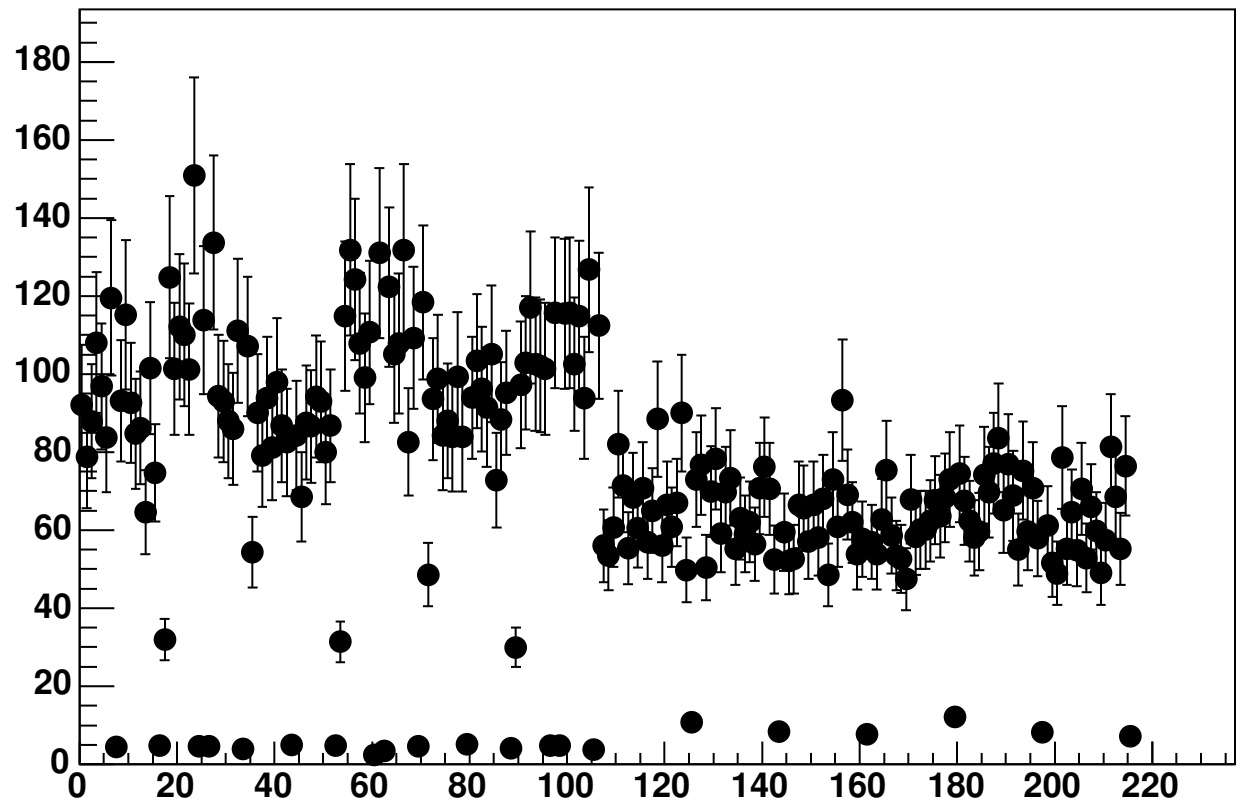
Enable 5, Hold=30, DAC=1100, ADC Noise vs 18*Chip+Chan



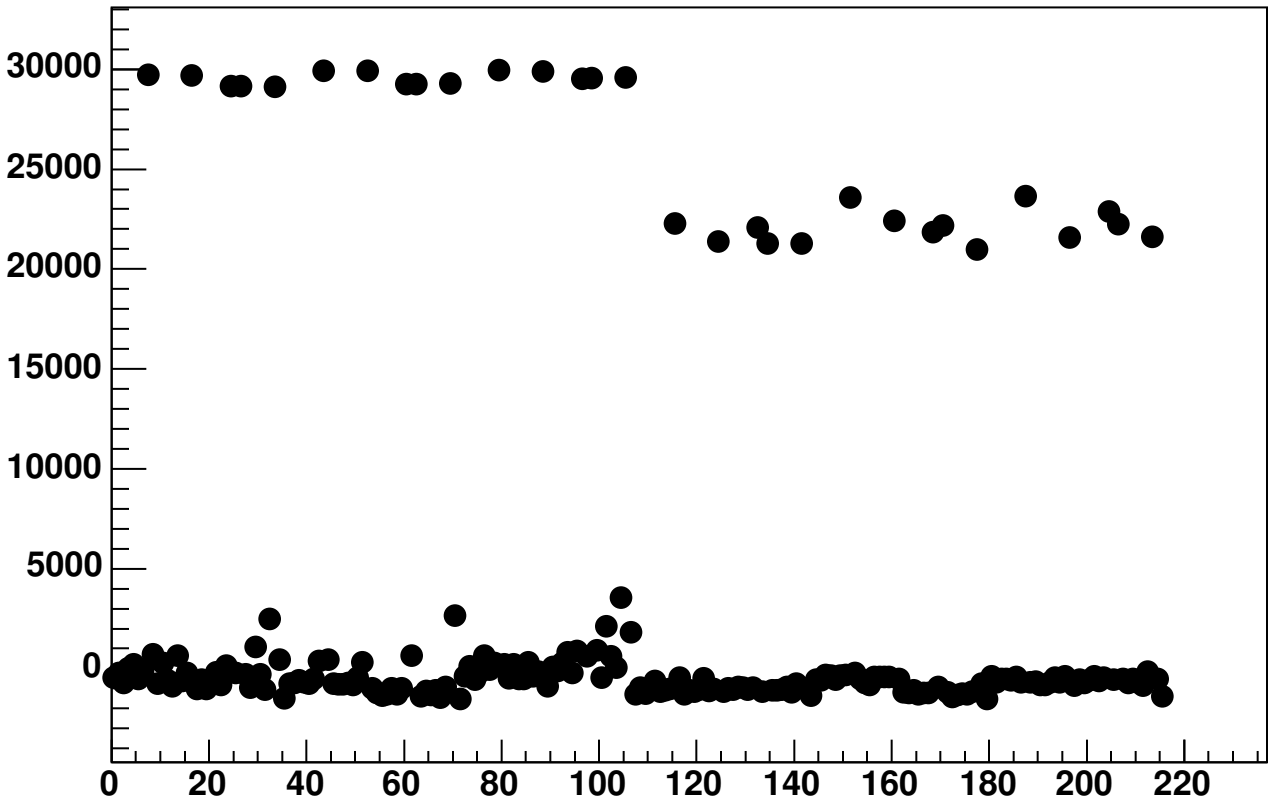
Enable 5, Hold=30, DAC=1150, ADC Mean vs 18*Chip+Chan



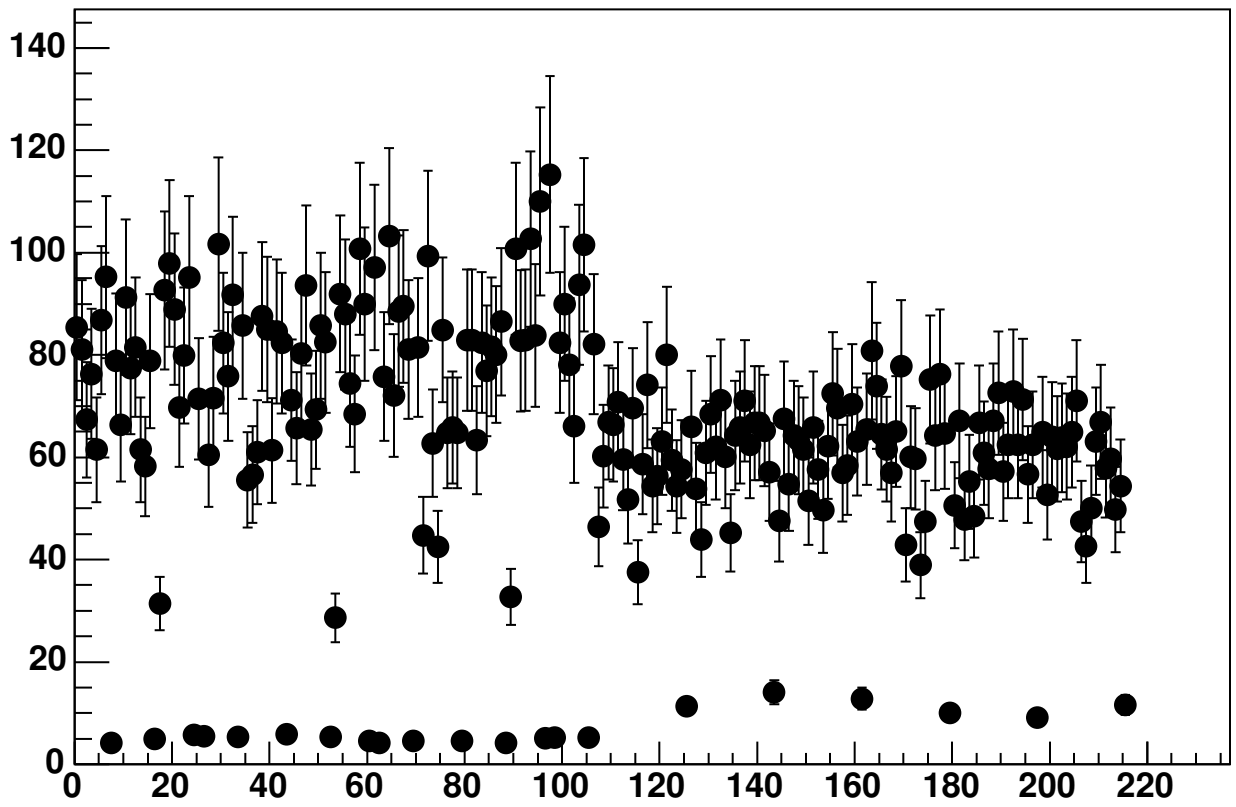
Enable 5, Hold=30, DAC=1150, ADC Noise vs 18*Chip+Chan



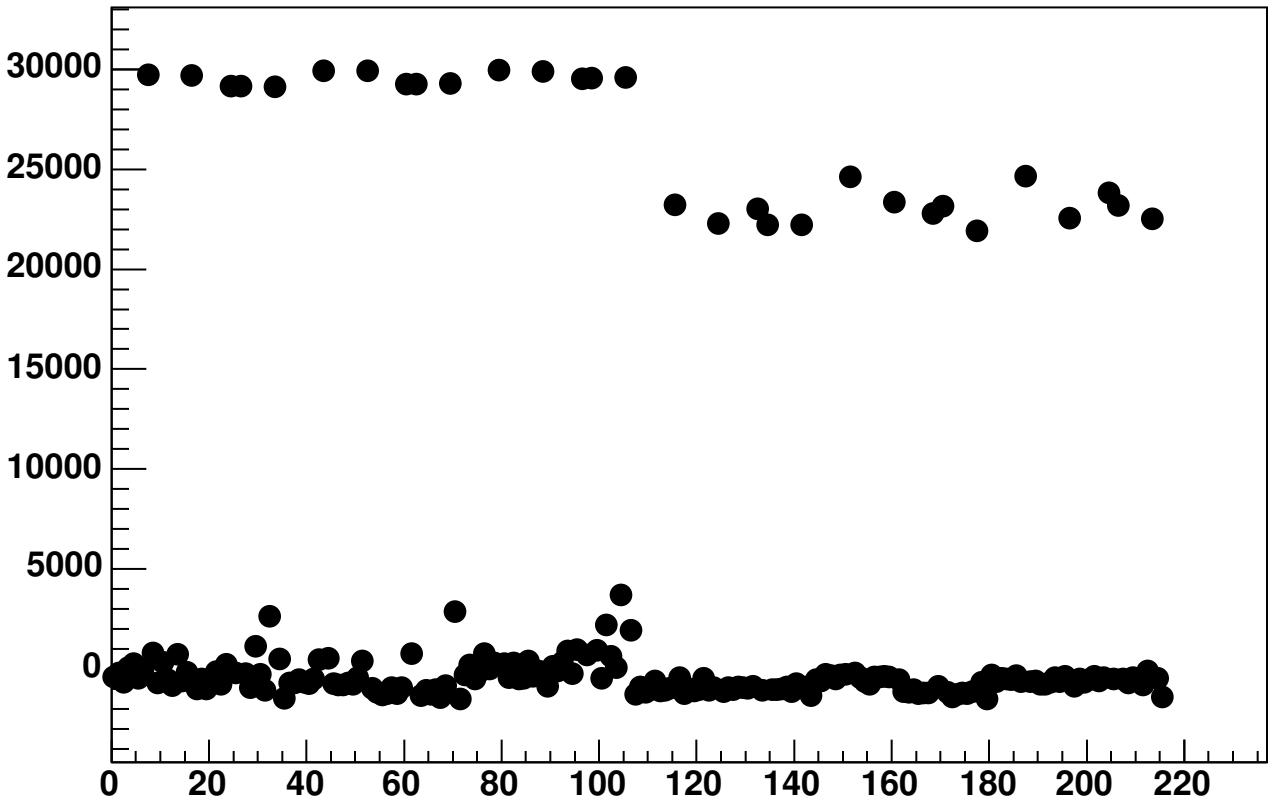
Enable 5, Hold=30, DAC=1200, ADC Mean vs 18*Chip+Chan



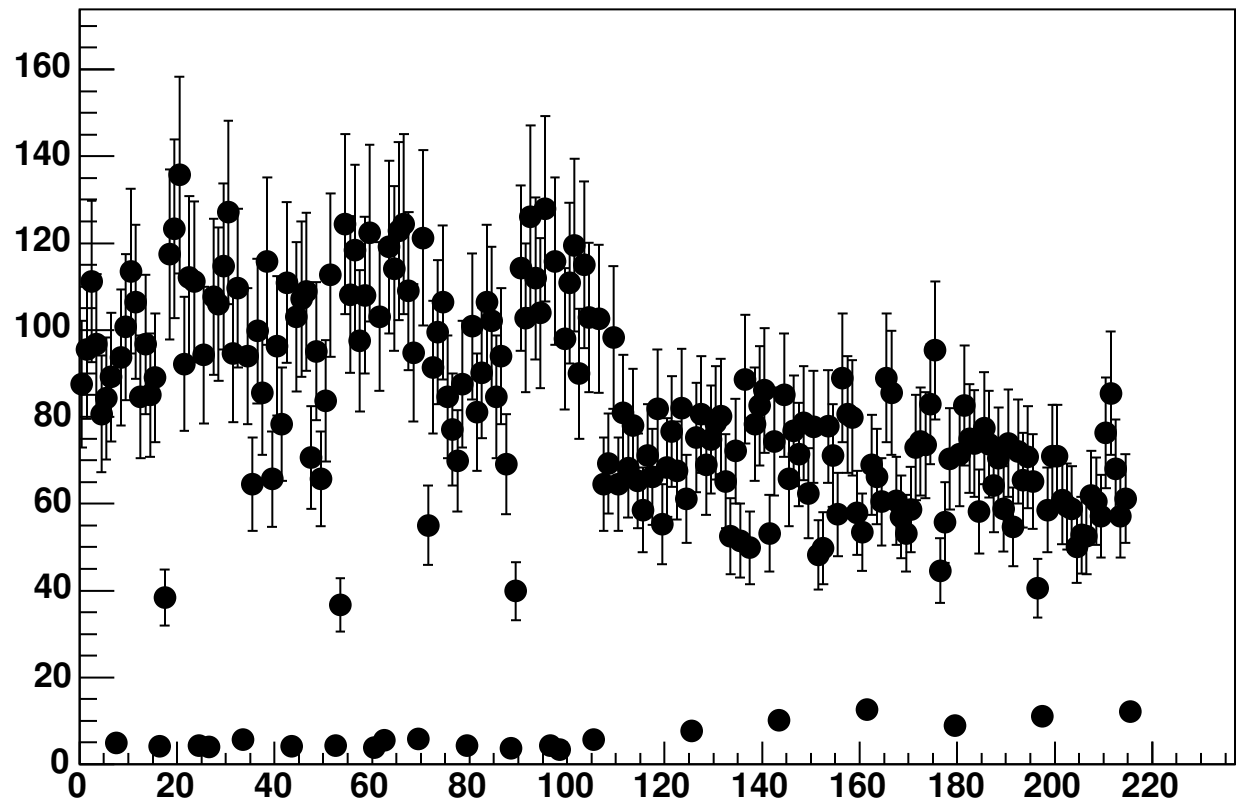
Enable 5, Hold=30, DAC=1200, ADC Noise vs 18*Chip+Chan



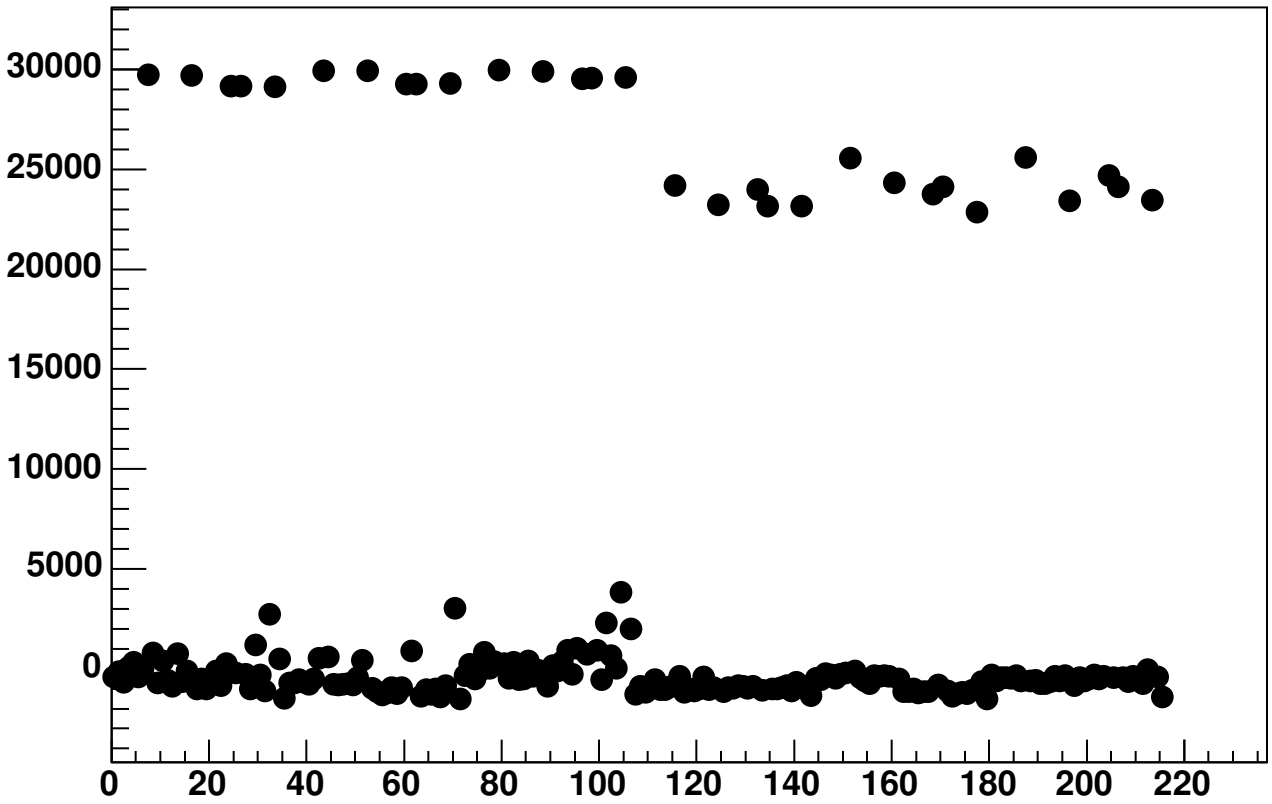
Enable 5, Hold=30, DAC=1250, ADC Mean vs 18*Chip+Chan



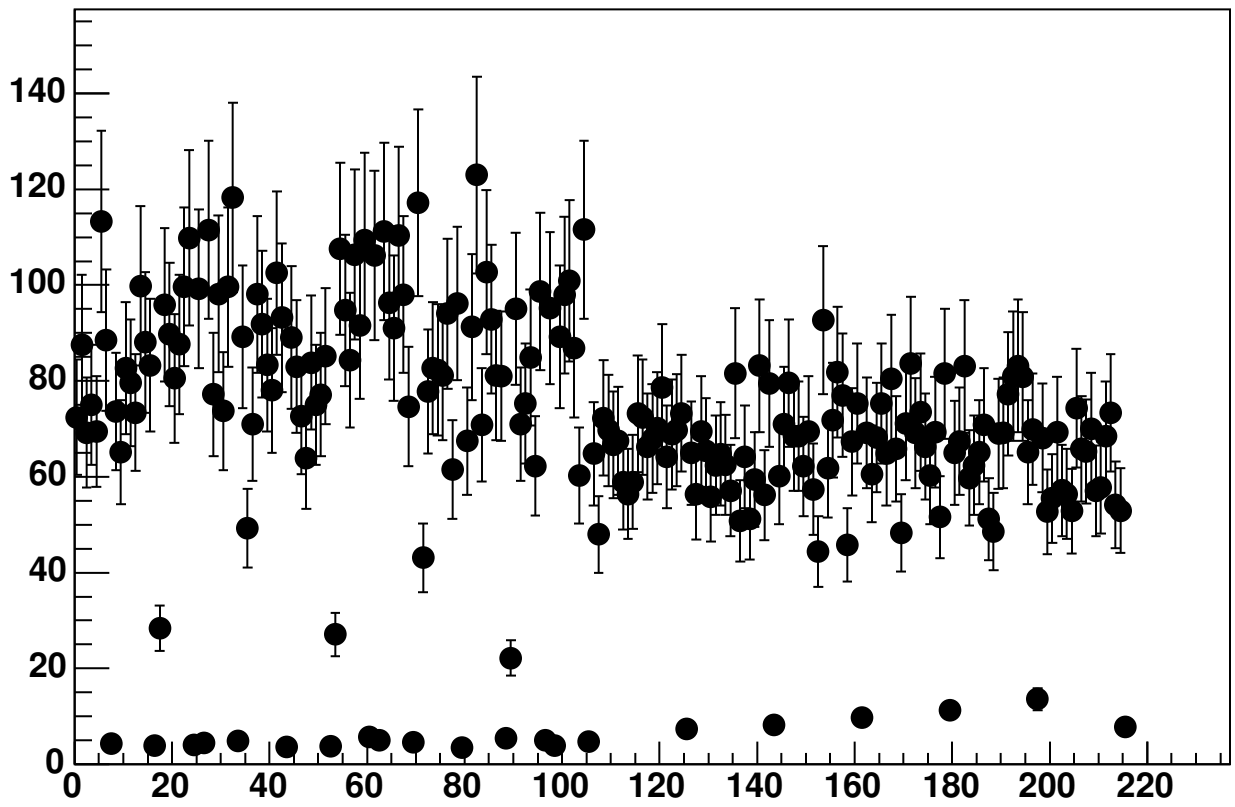
Enable 5, Hold=30, DAC=1250, ADC Noise vs 18*Chip+Chan



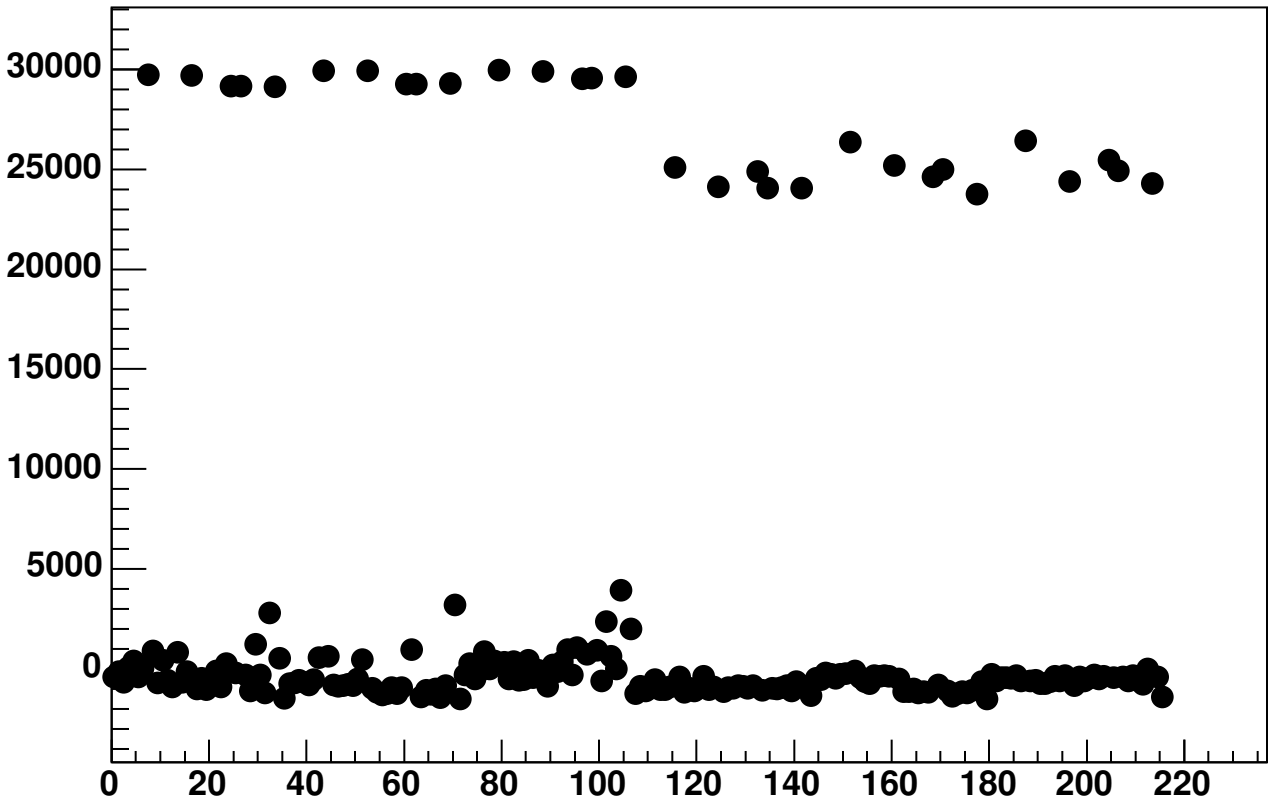
Enable 5, Hold=30, DAC=1300, ADC Mean vs 18*Chip+Chan



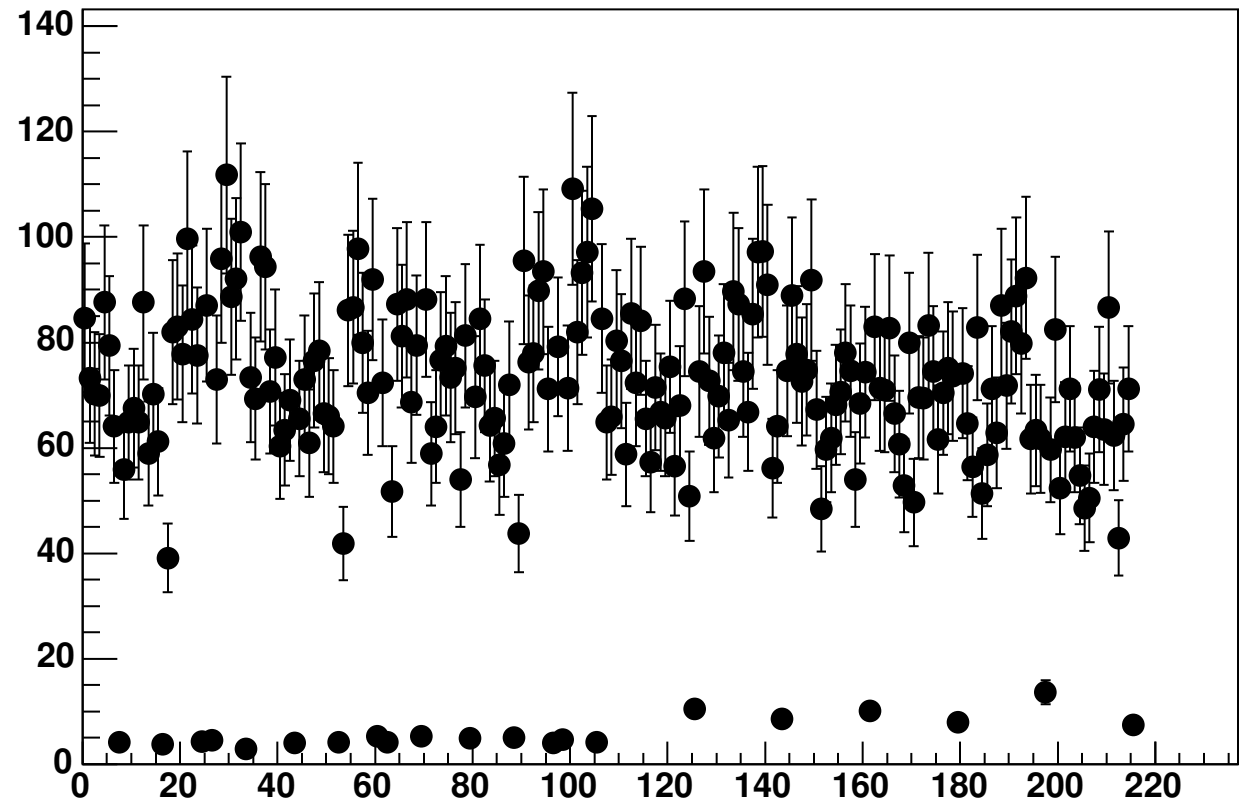
Enable 5, Hold=30, DAC=1300, ADC Noise vs 18*Chip+Chan



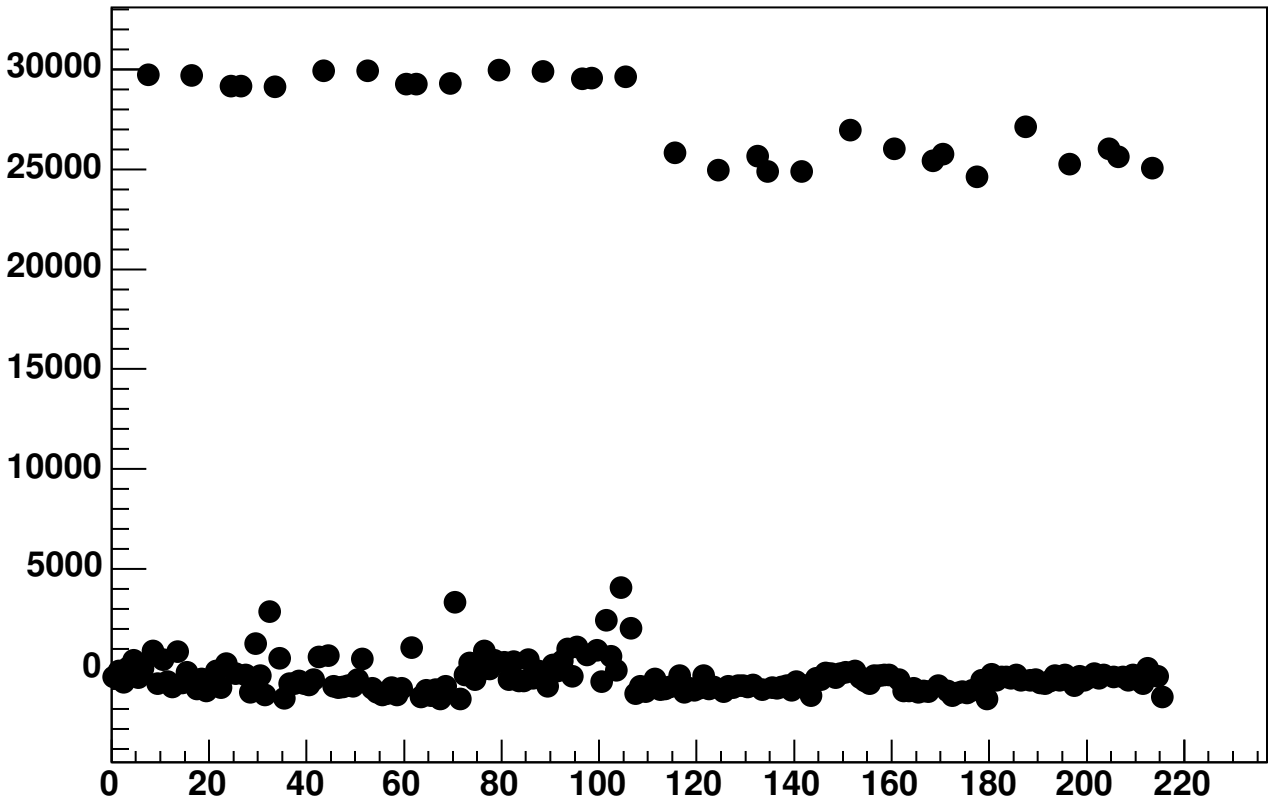
Enable 5, Hold=30, DAC=1350, ADC Mean vs 18*Chip+Chan



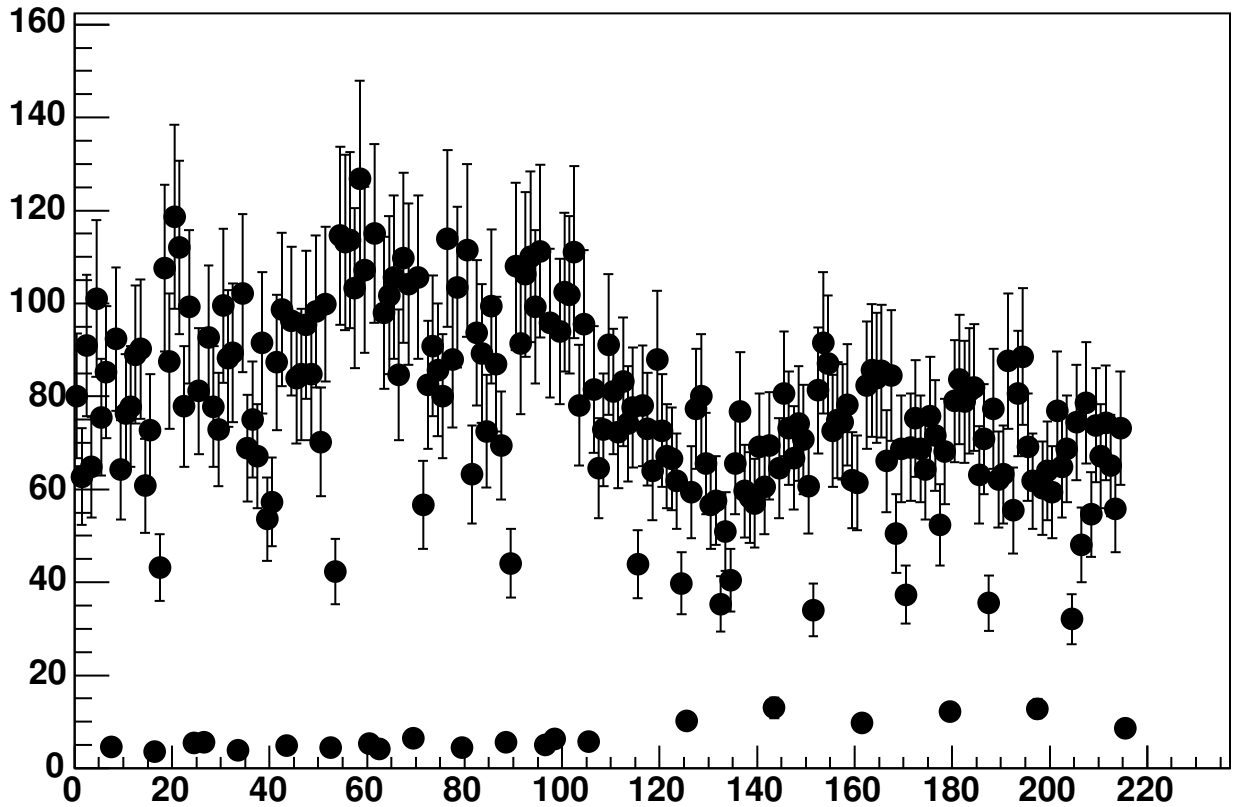
Enable 5, Hold=30, DAC=1350, ADC Noise vs 18*Chip+Chan



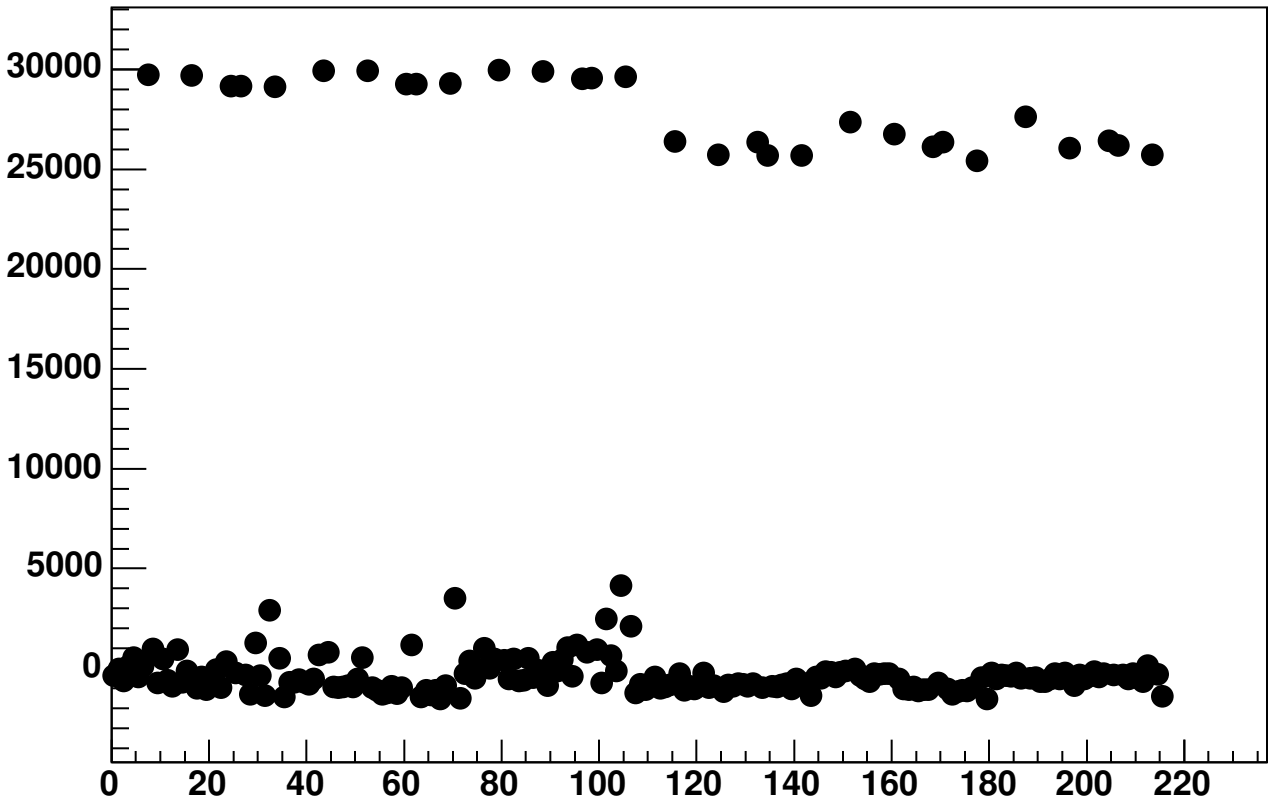
Enable 5, Hold=30, DAC=1400, ADC Mean vs 18*Chip+Chan



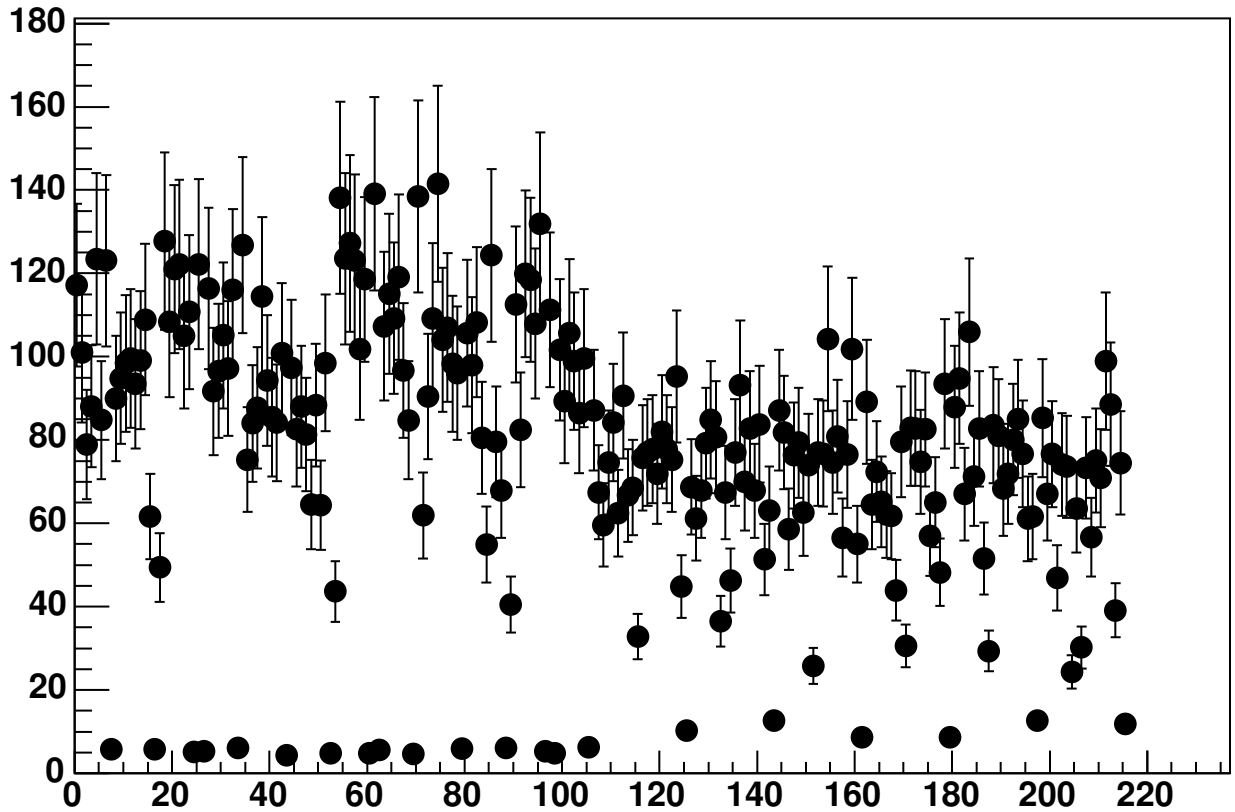
Enable 5, Hold=30, DAC=1400, ADC Noise vs 18*Chip+Chan



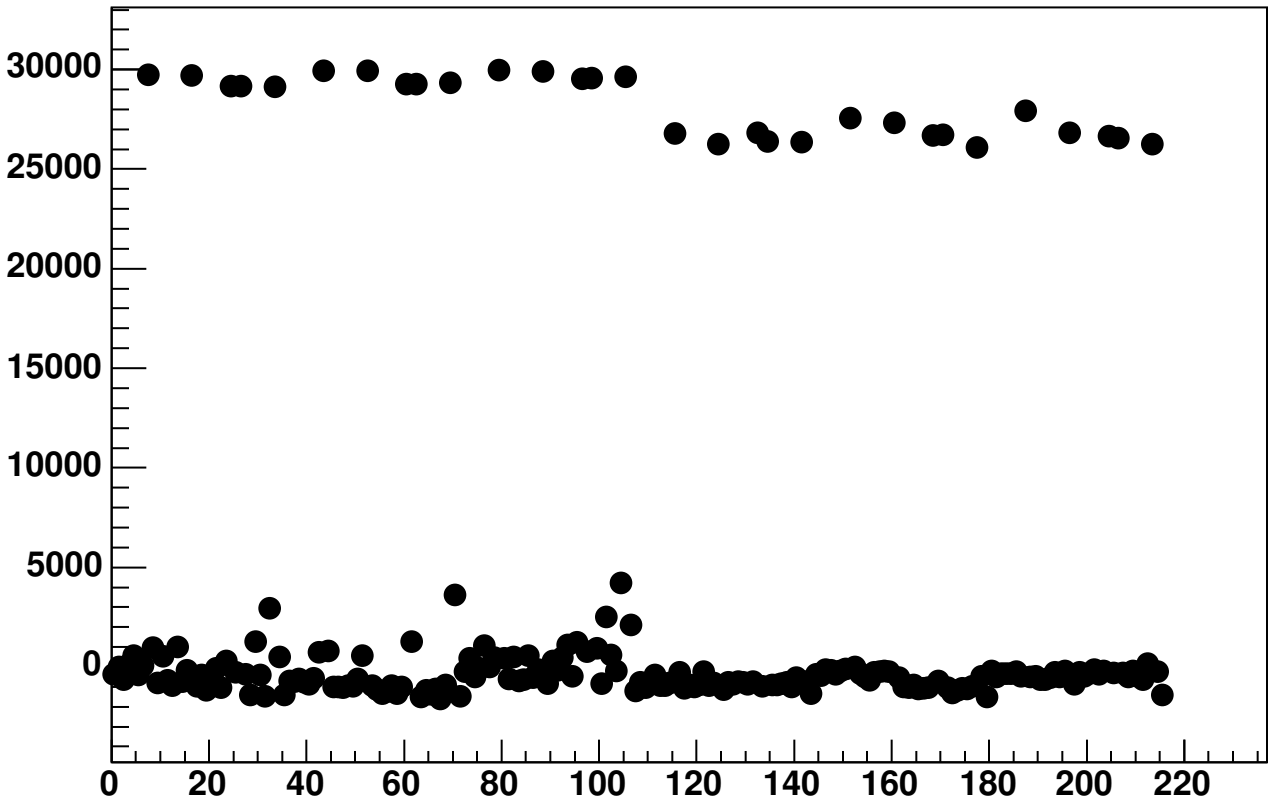
Enable 5, Hold=30, DAC=1450, ADC Mean vs 18*Chip+Chan



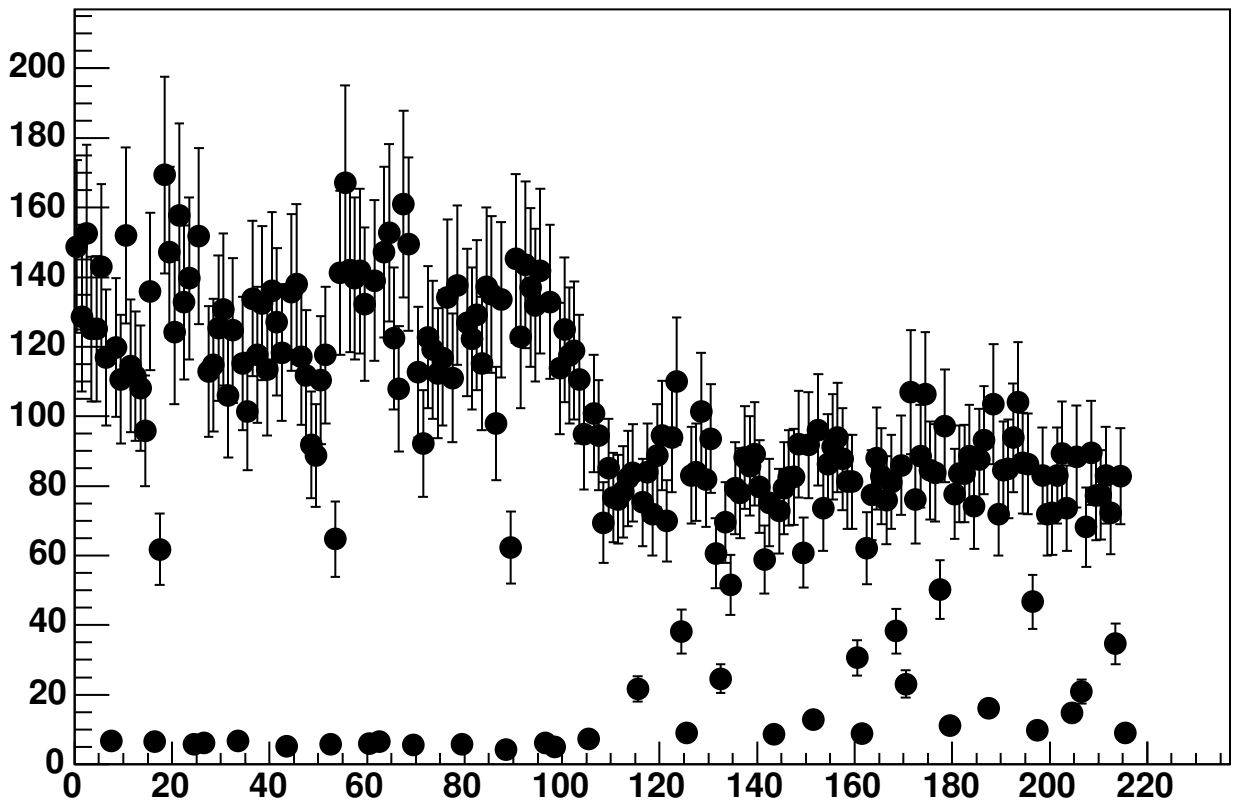
Enable 5, Hold=30, DAC=1450, ADC Noise vs 18*Chip+Chan



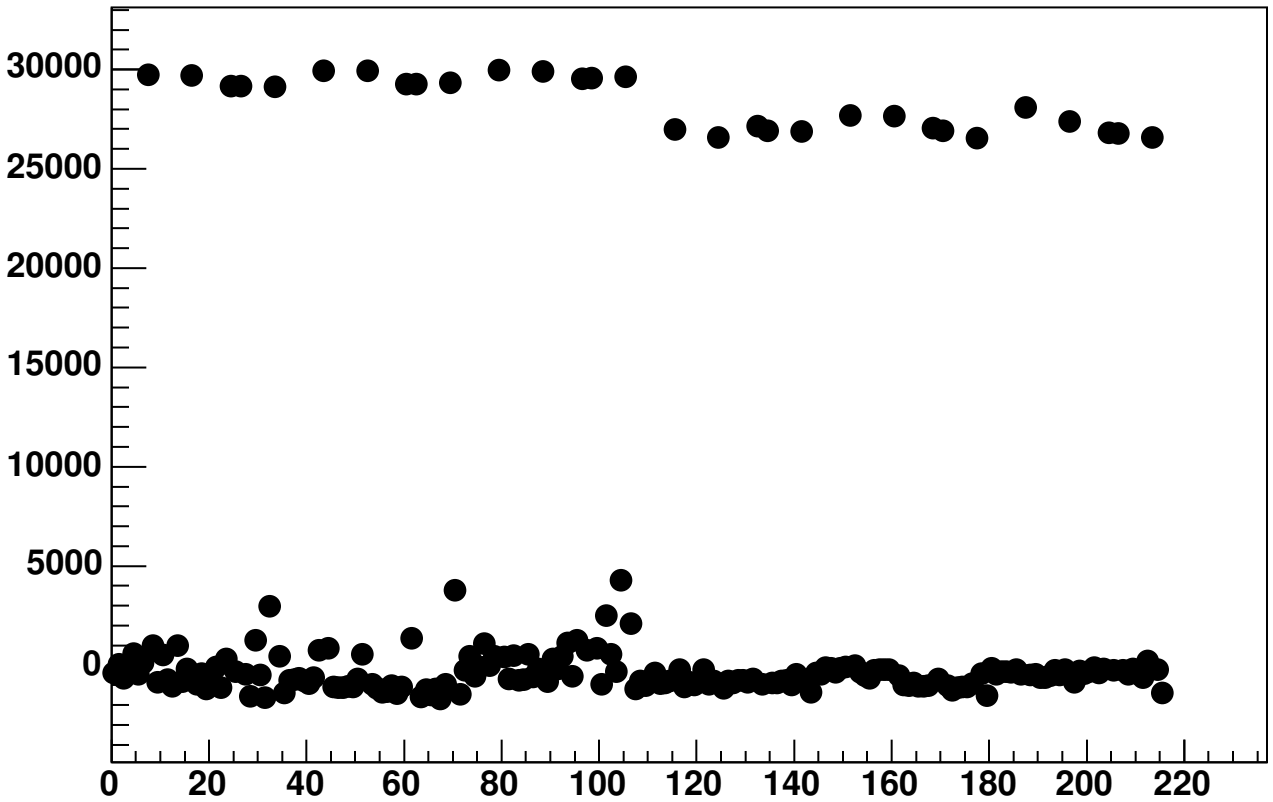
Enable 5, Hold=30, DAC=1500, ADC Mean vs 18*Chip+Chan



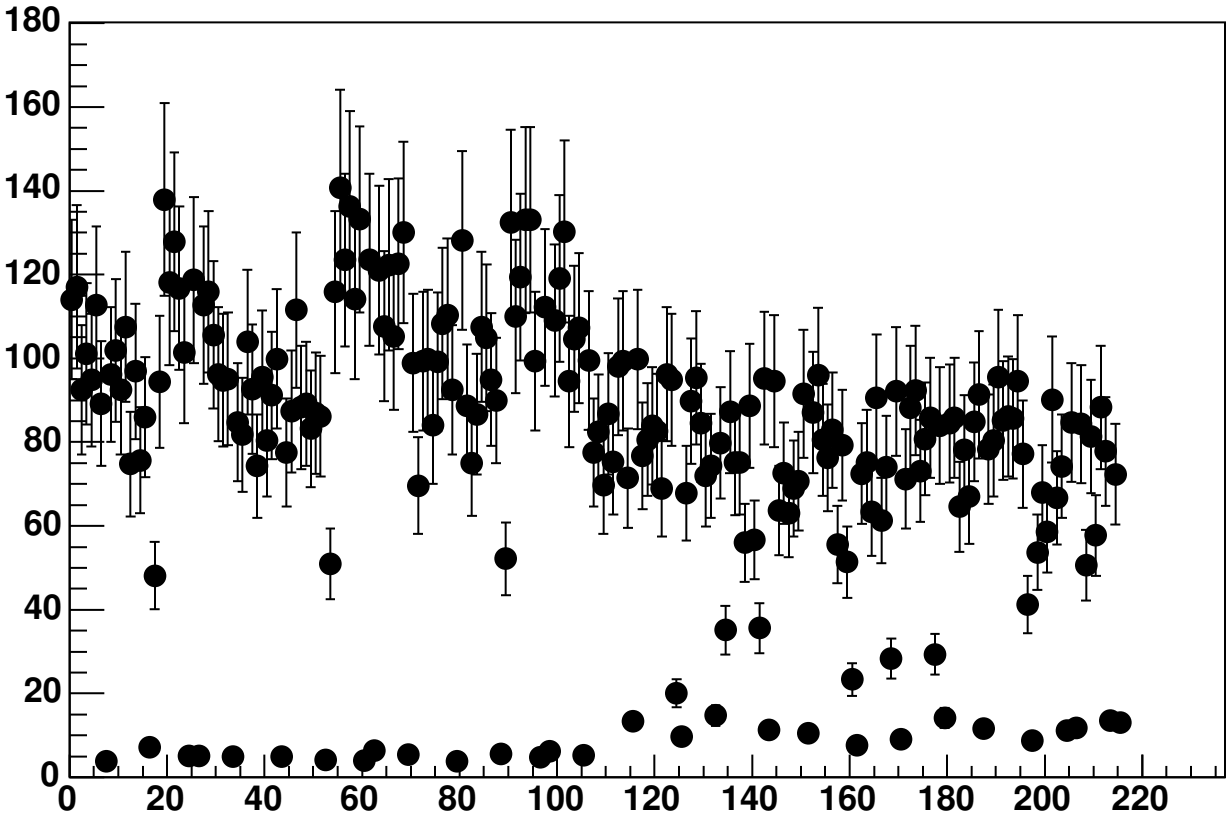
Enable 5, Hold=30, DAC=1500, ADC Noise vs 18*Chip+Chan



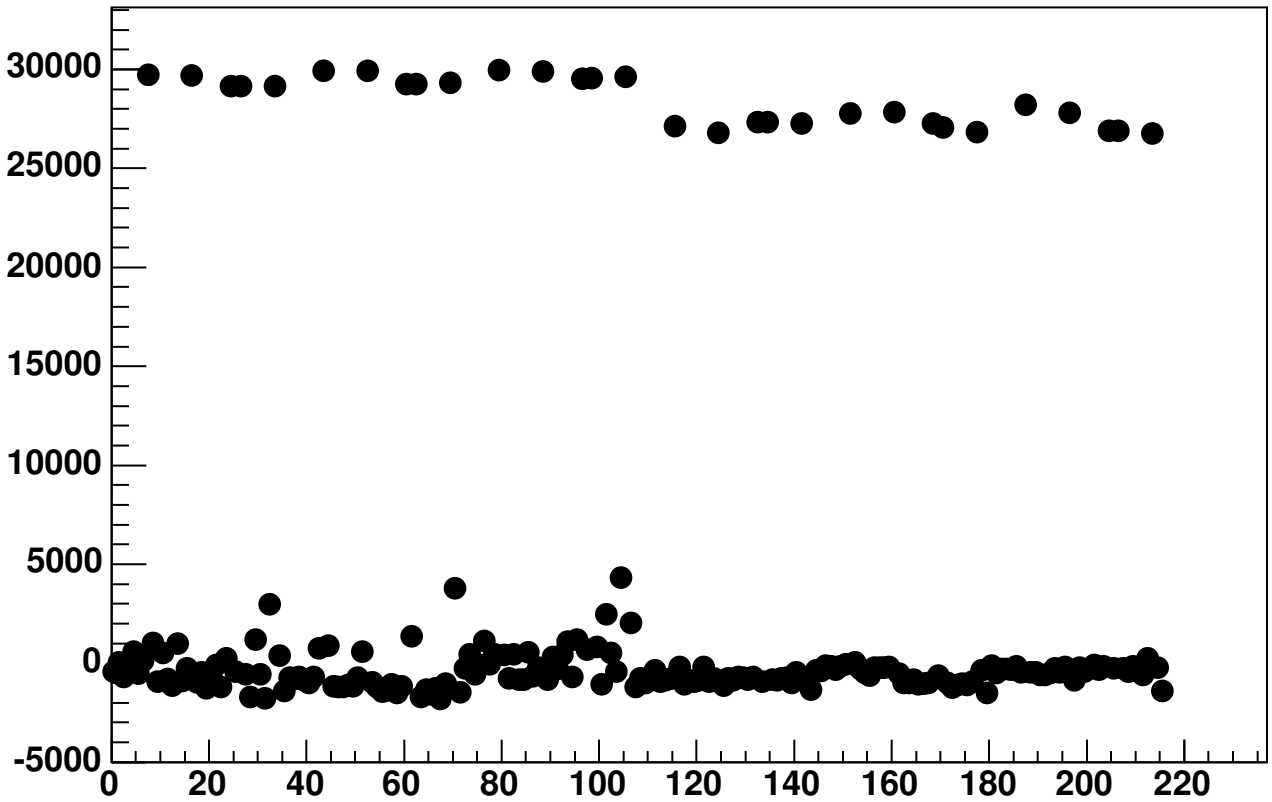
Enable 5, Hold=30, DAC=1550, ADC Mean vs 18*Chip+Chan



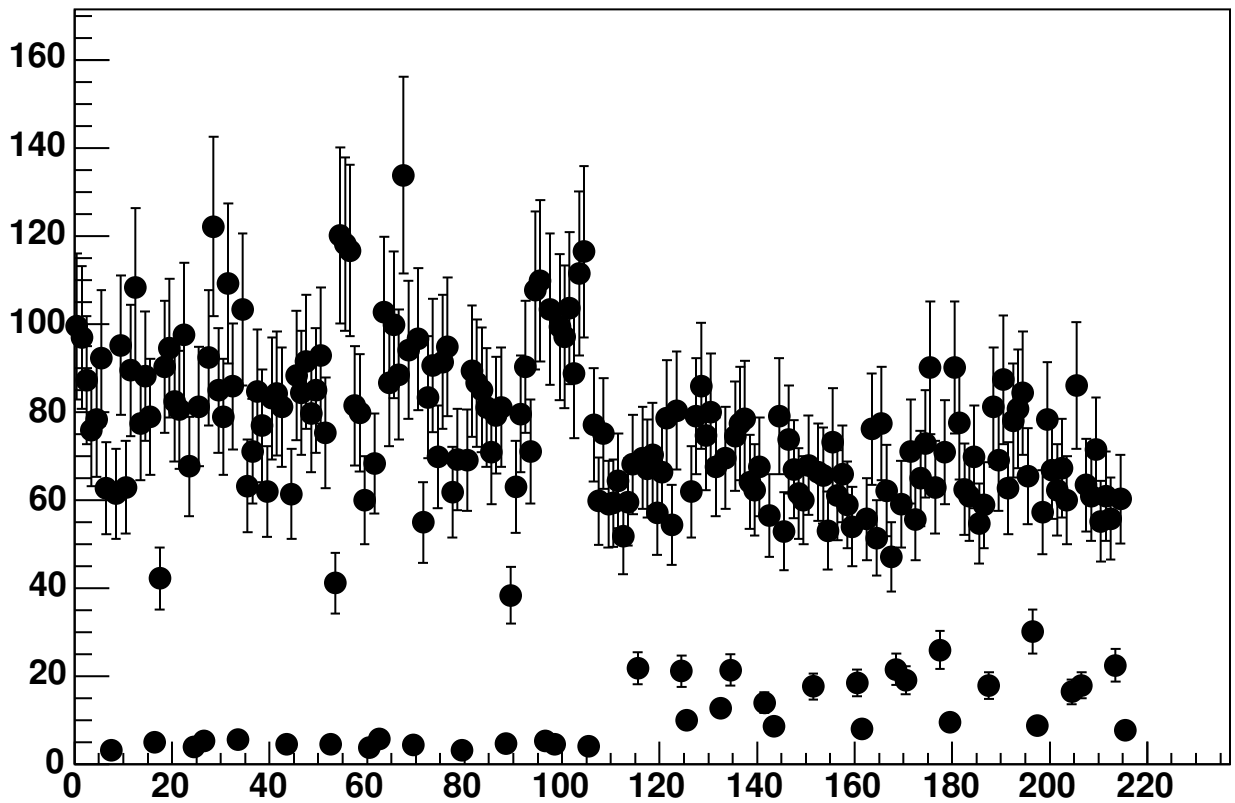
Enable 5, Hold=30, DAC=1550, ADC Noise vs 18*Chip+Chan



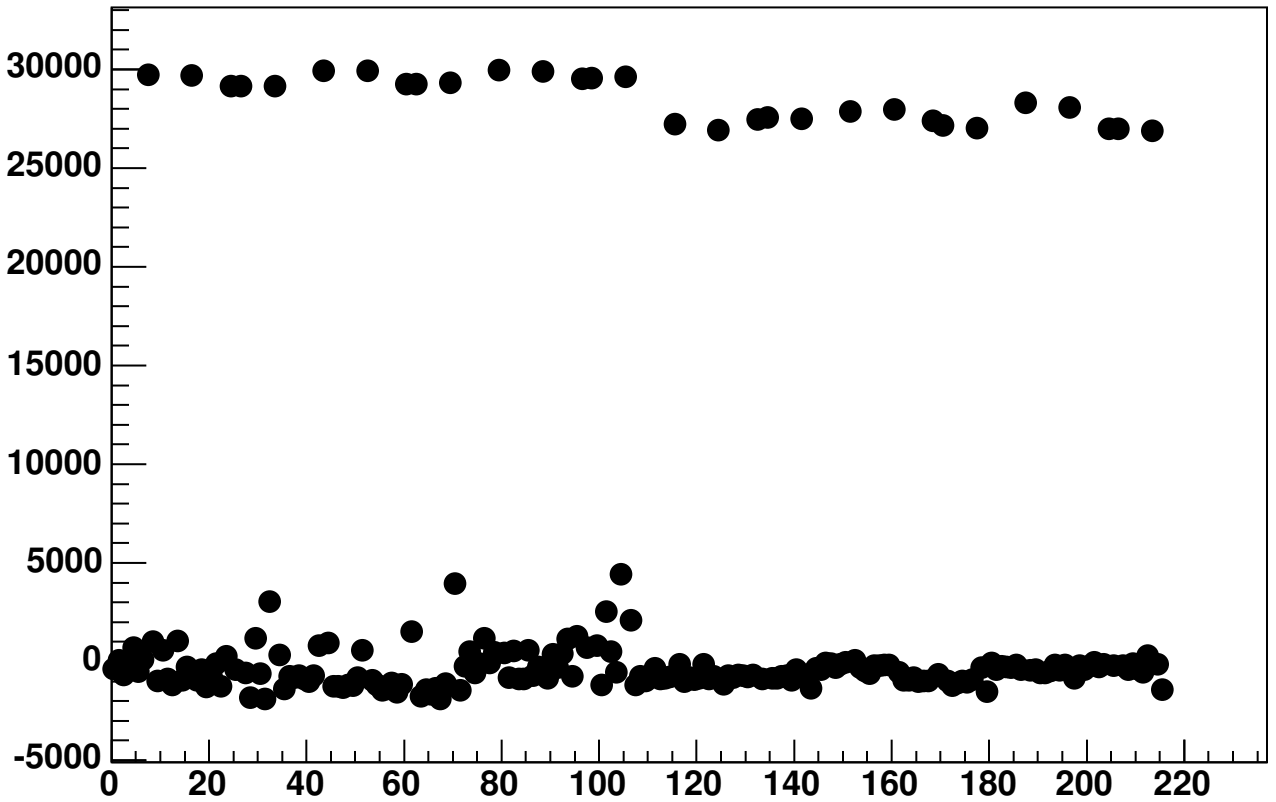
Enable 5, Hold=30, DAC=1600, ADC Mean vs 18*Chip+Chan



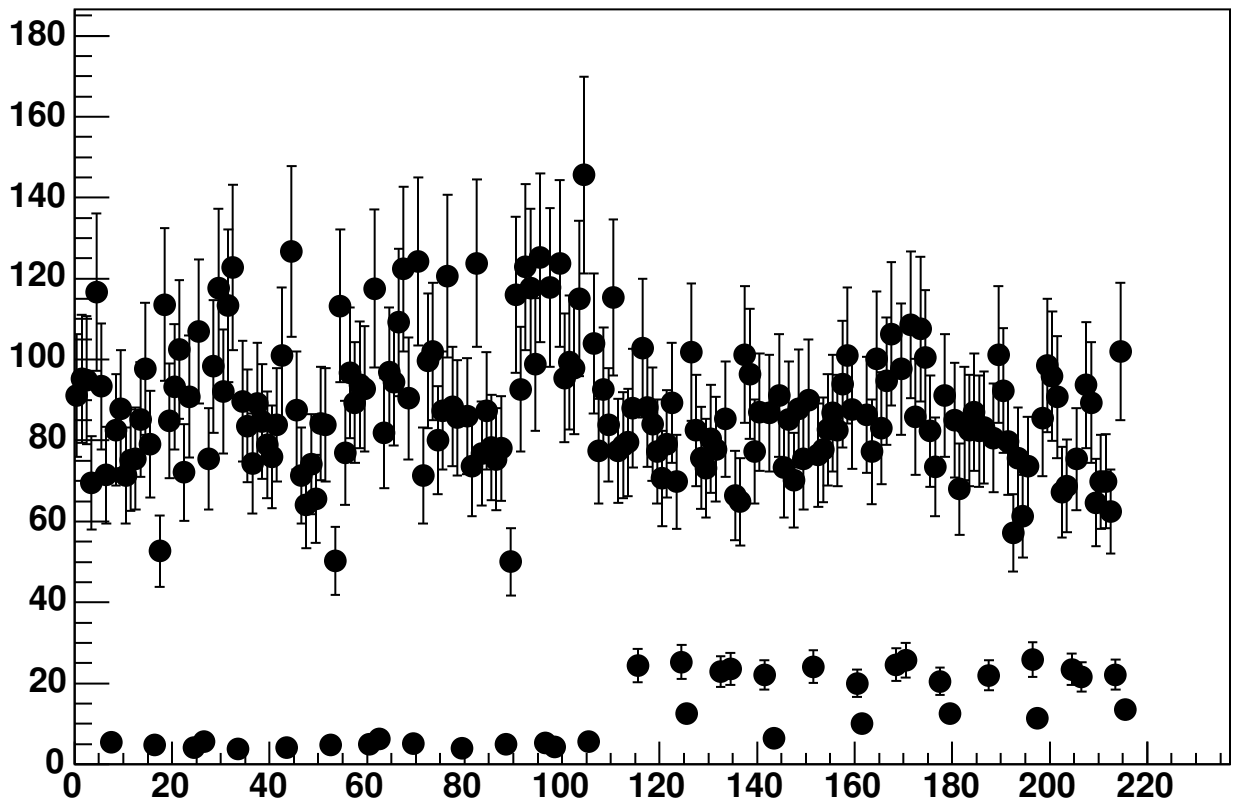
Enable 5, Hold=30, DAC=1600, ADC Noise vs 18*Chip+Chan



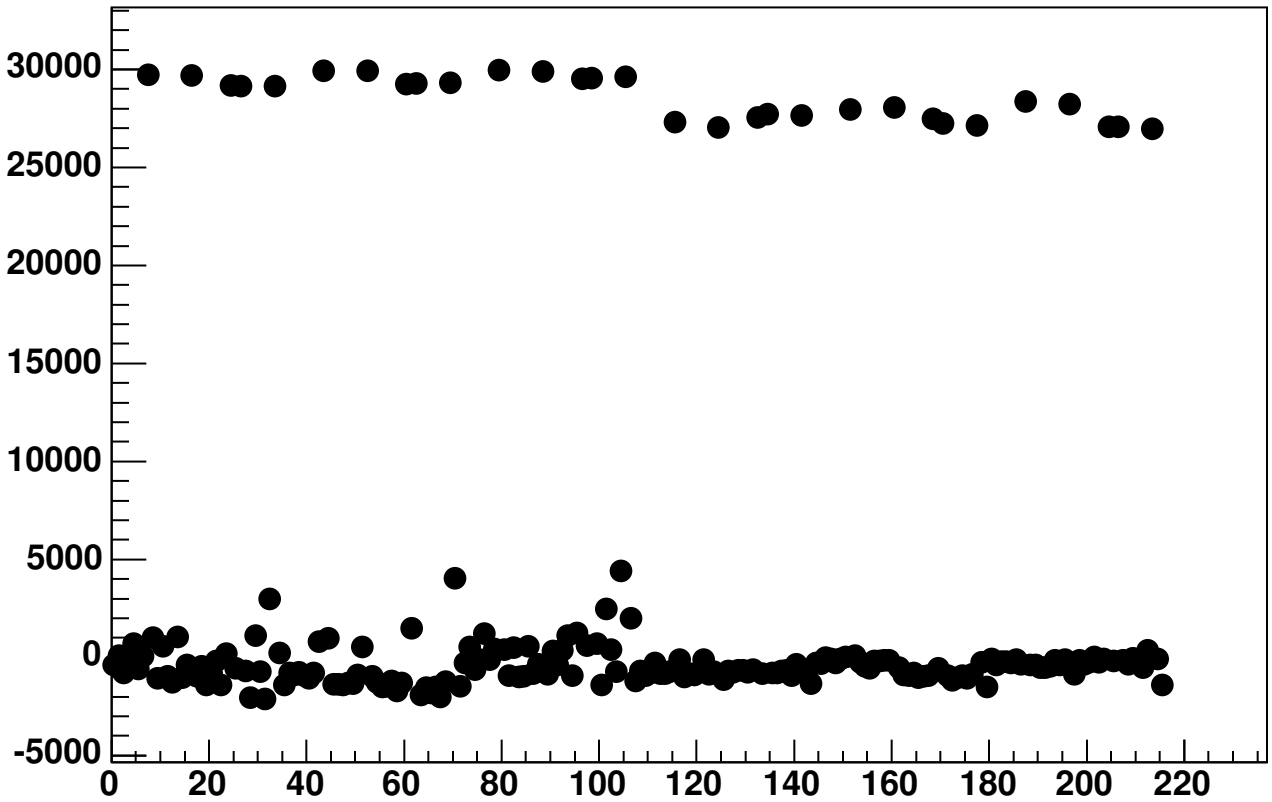
Enable 5, Hold=30, DAC=1650, ADC Mean vs 18*Chip+Chan



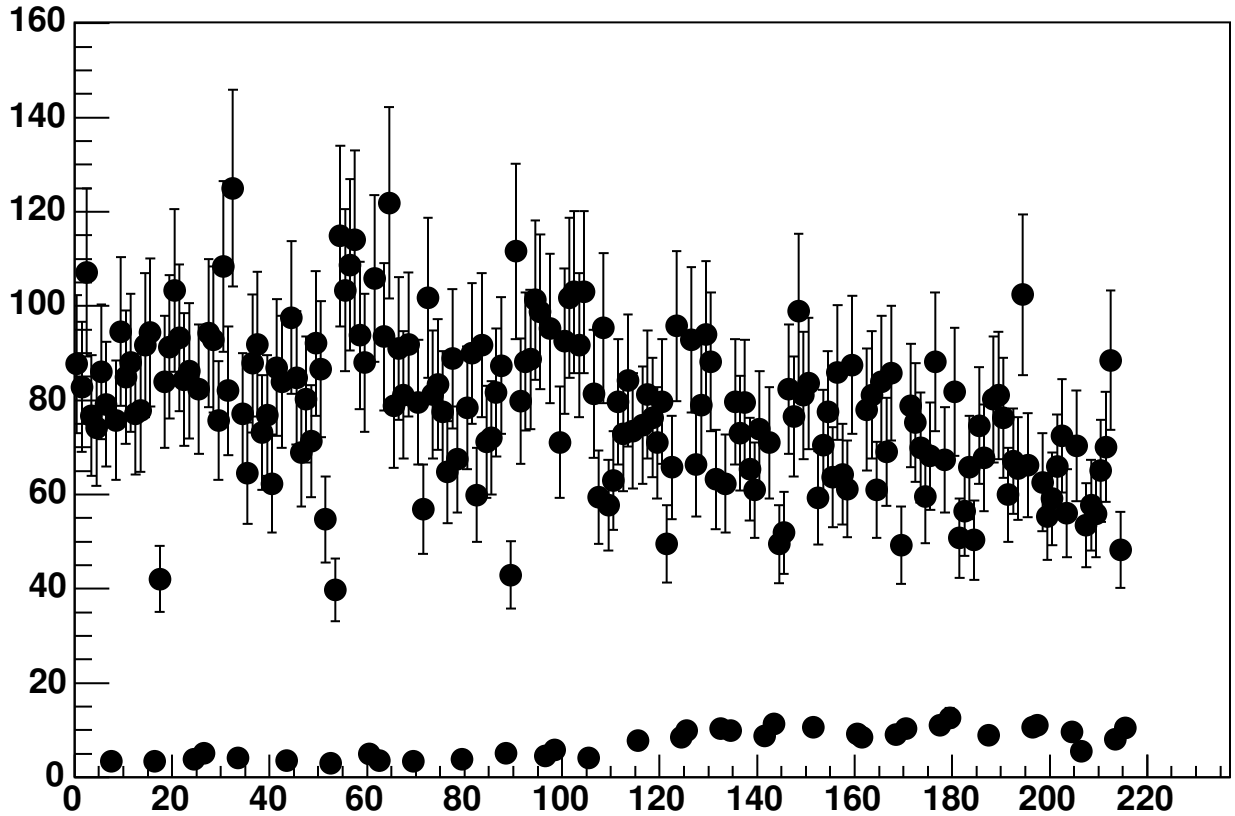
Enable 5, Hold=30, DAC=1650, ADC Noise vs 18*Chip+Chan



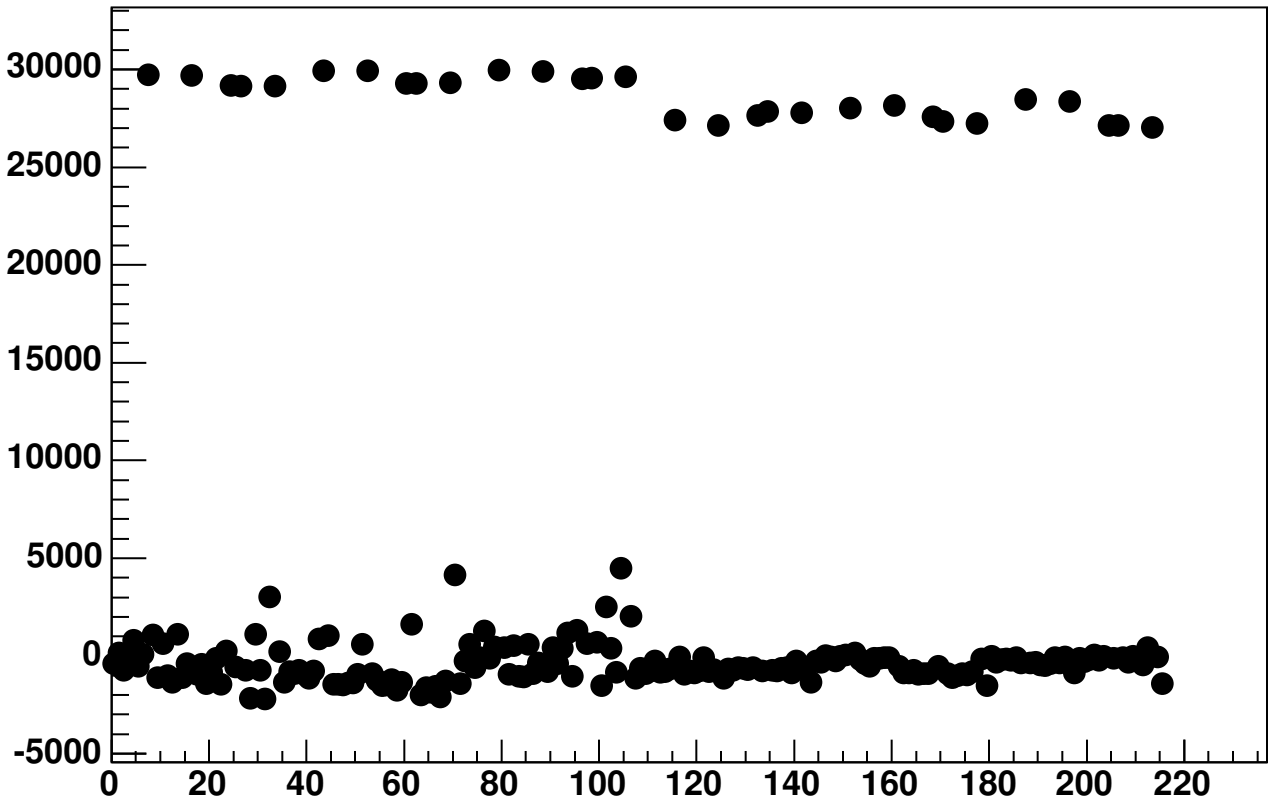
Enable 5, Hold=30, DAC=1700, ADC Mean vs 18*Chip+Chan



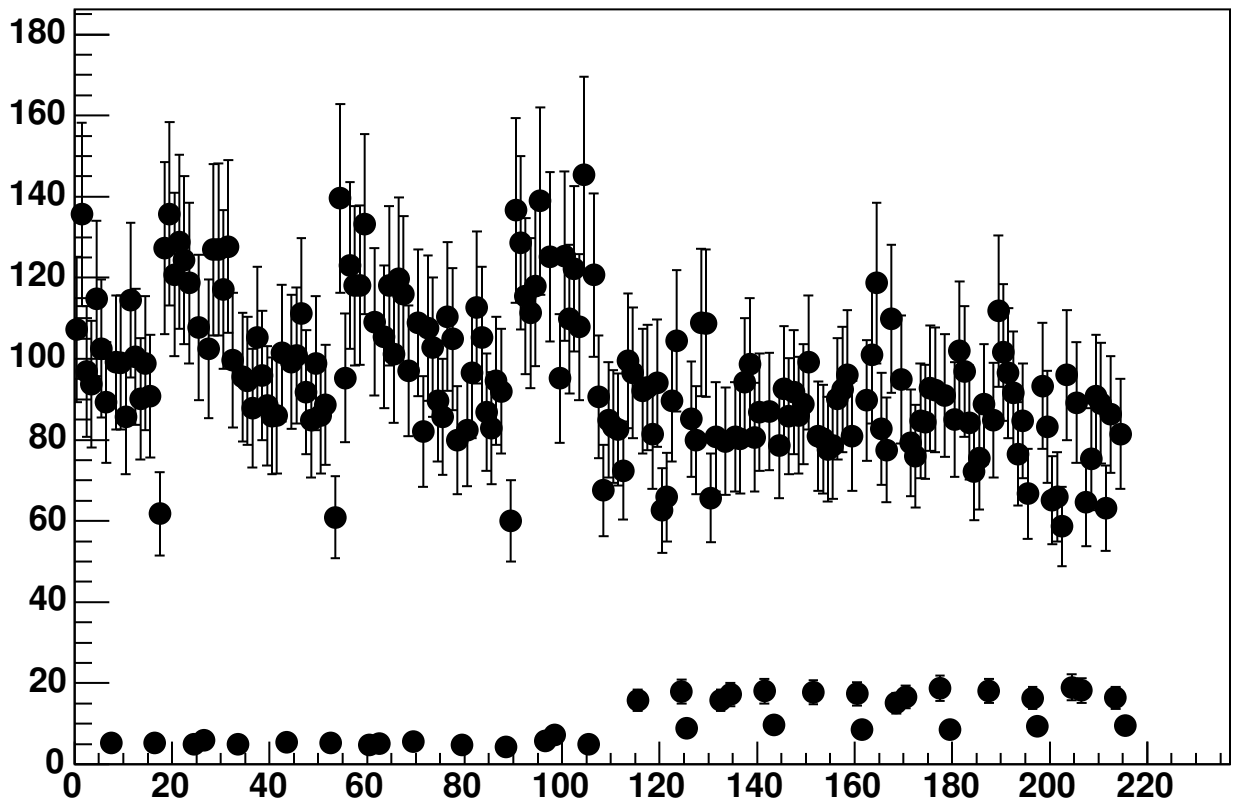
Enable 5, Hold=30, DAC=1700, ADC Noise vs 18*Chip+Chan



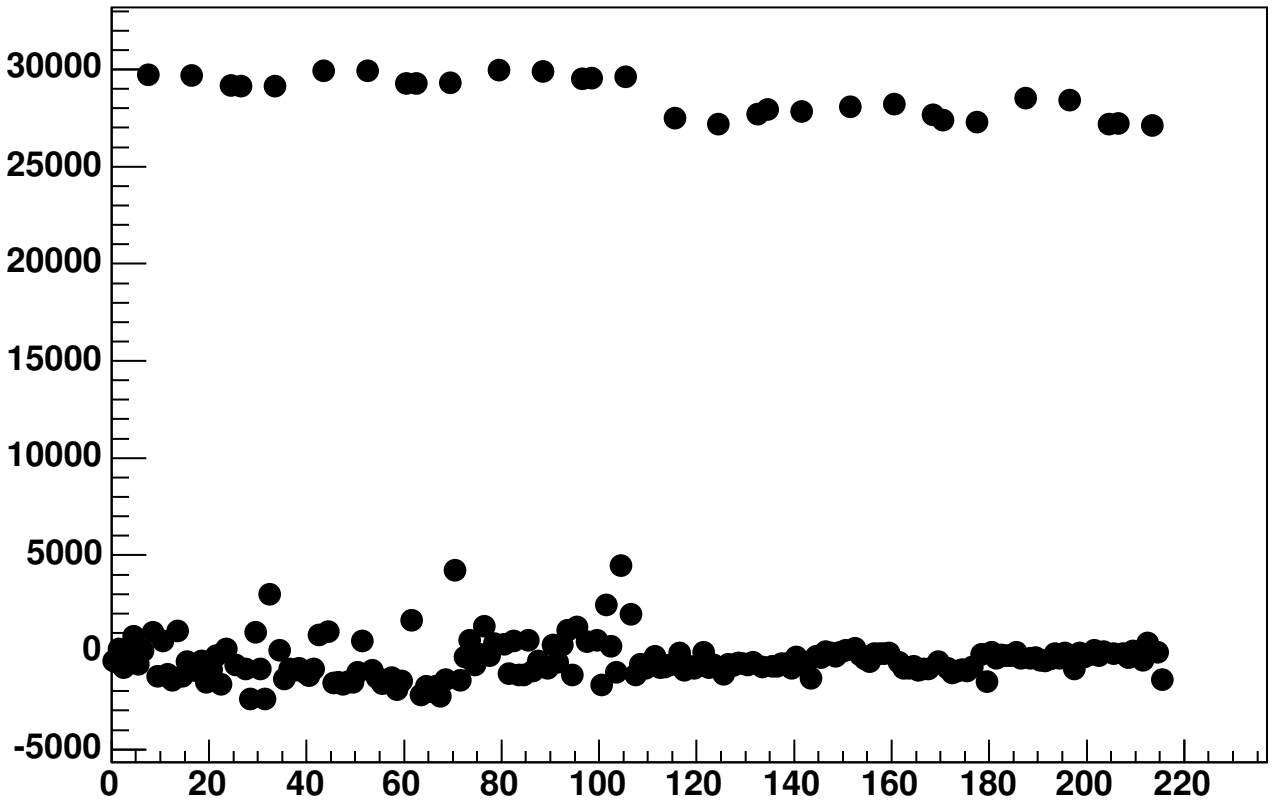
Enable 5, Hold=30, DAC=1750, ADC Mean vs 18*Chip+Chan



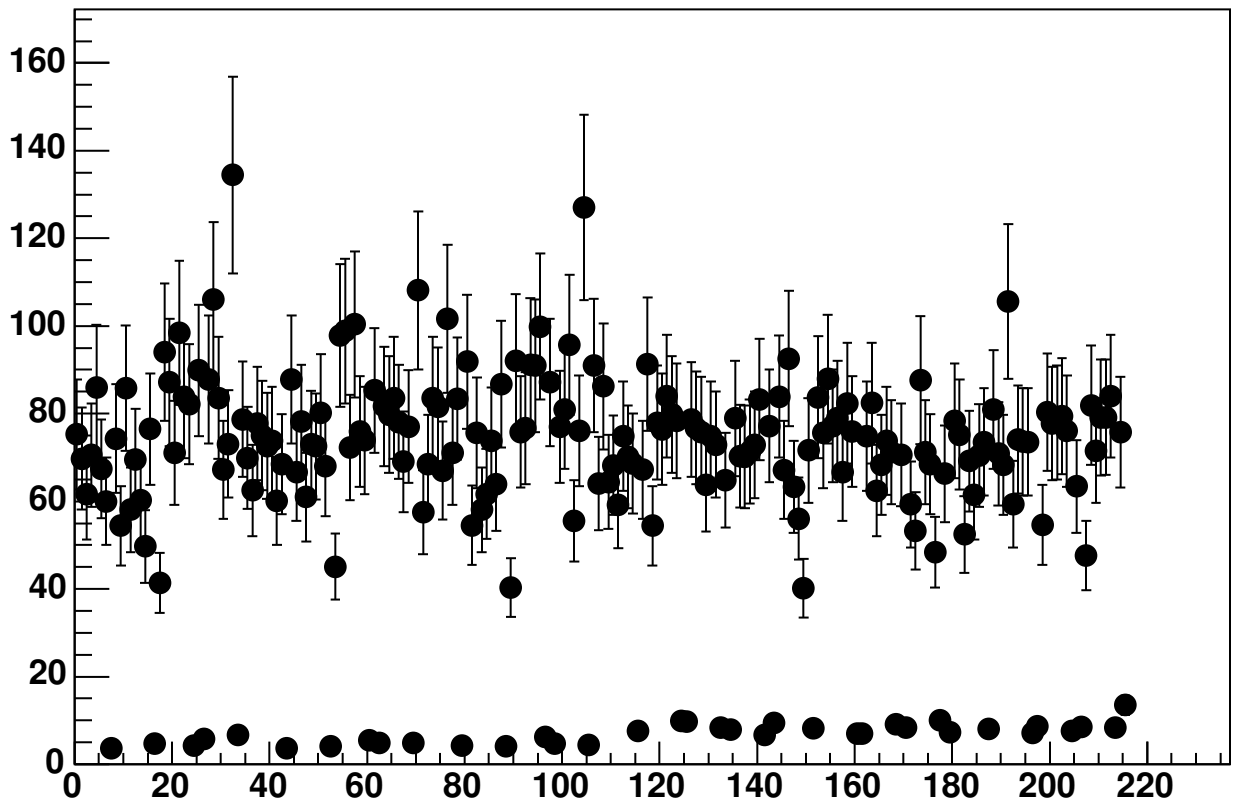
Enable 5, Hold=30, DAC=1750, ADC Noise vs 18*Chip+Chan



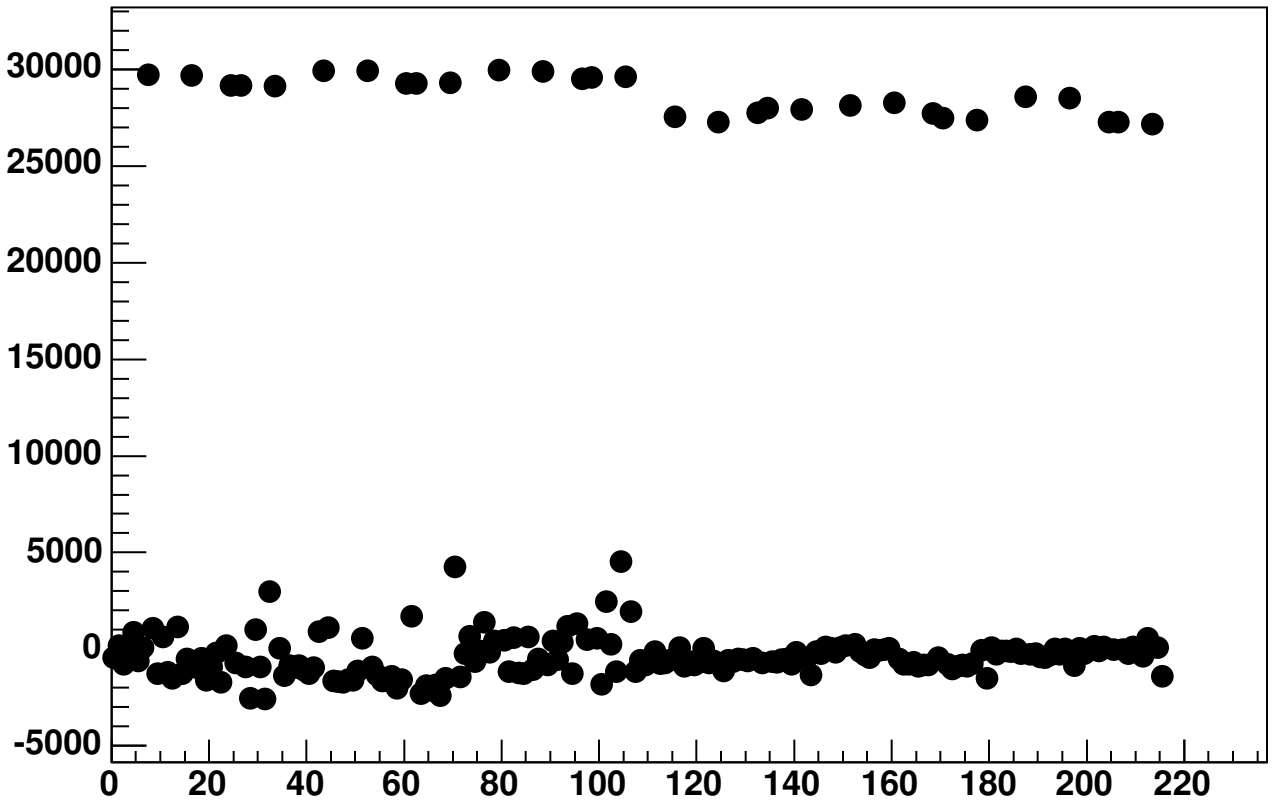
Enable 5, Hold=30, DAC=1800, ADC Mean vs 18*Chip+Chan



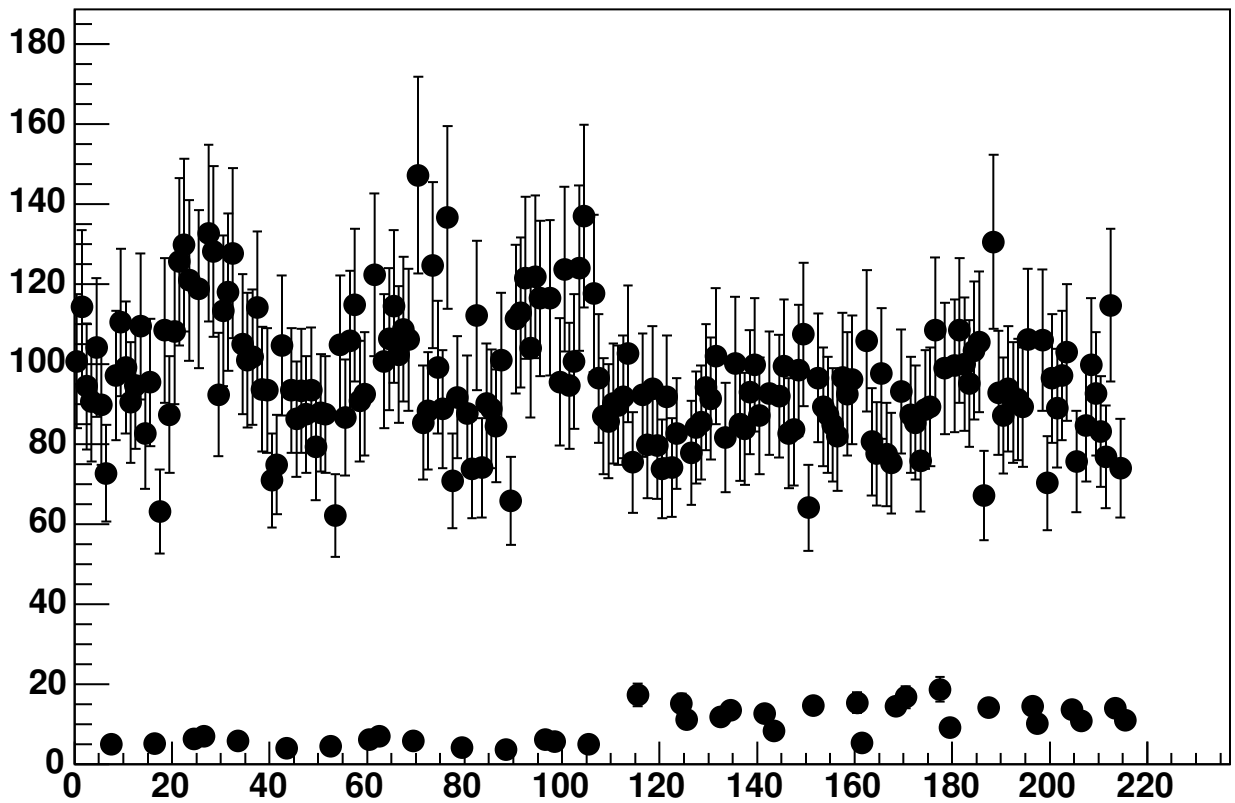
Enable 5, Hold=30, DAC=1800, ADC Noise vs 18*Chip+Chan



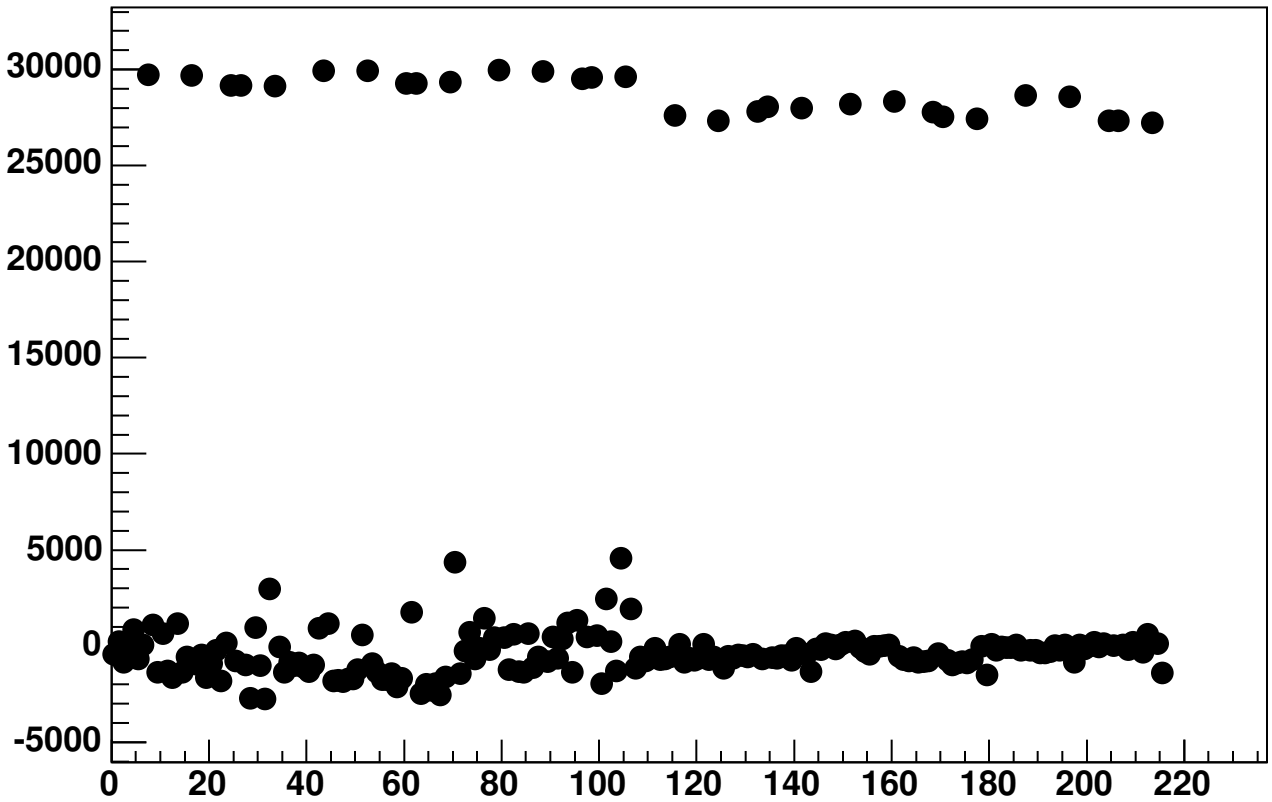
Enable 5, Hold=30, DAC=1850, ADC Mean vs 18*Chip+Chan



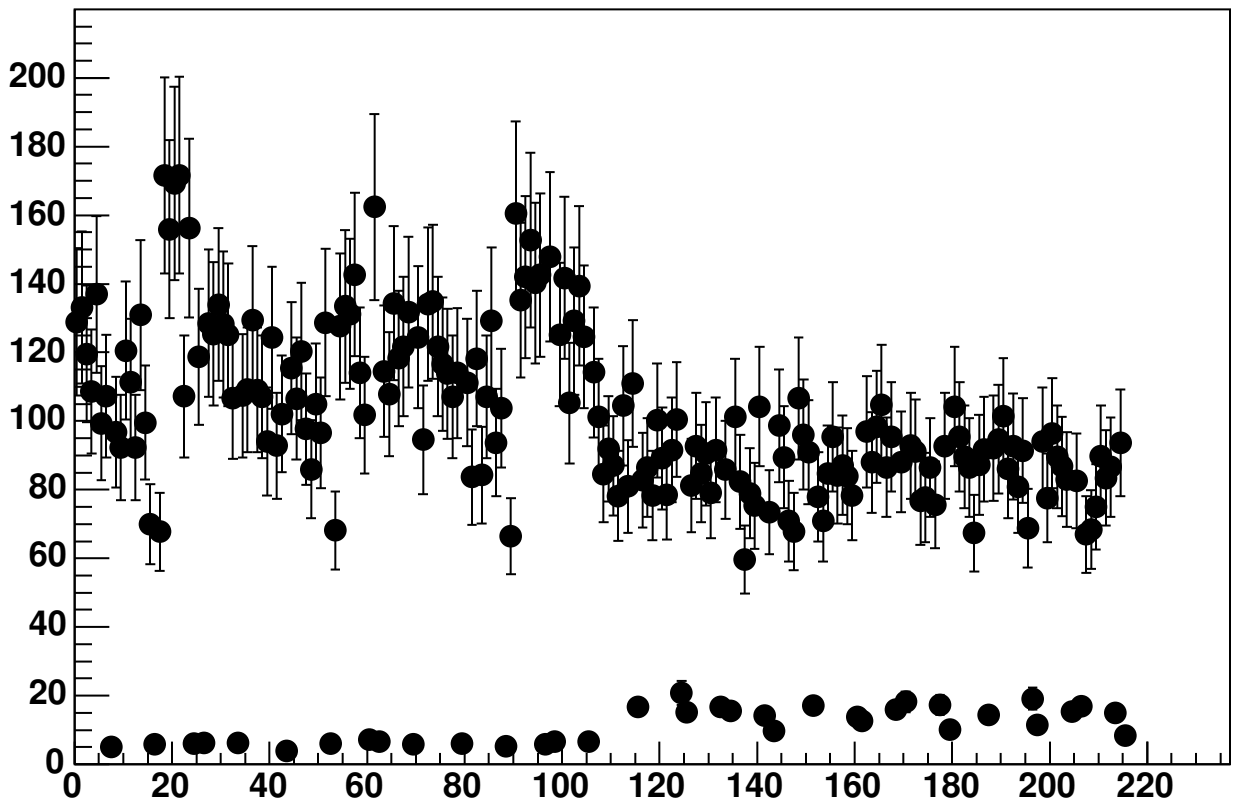
Enable 5, Hold=30, DAC=1850, ADC Noise vs 18*Chip+Chan

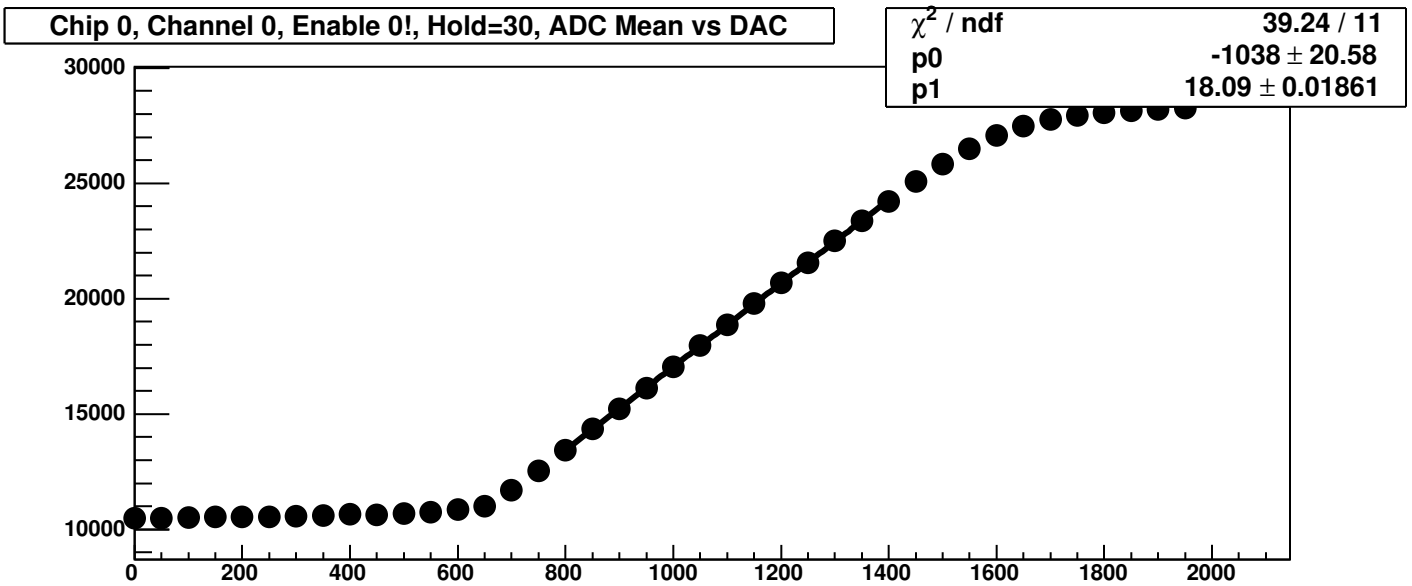


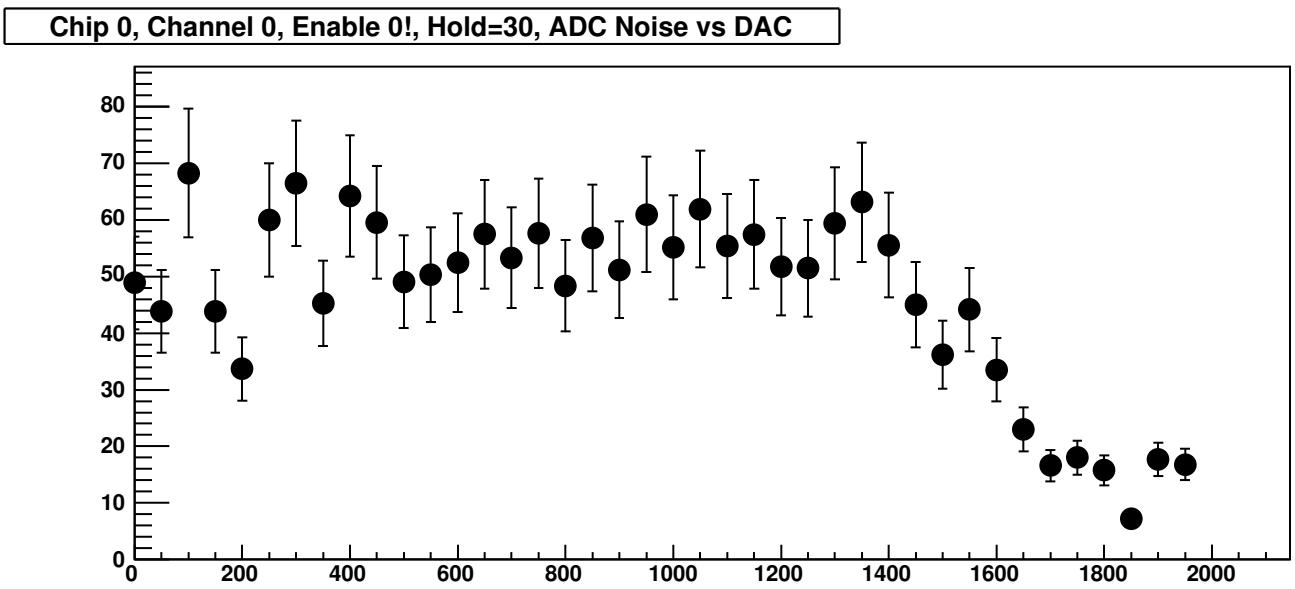
Enable 5, Hold=30, DAC=1900, ADC Mean vs 18*Chip+Chan

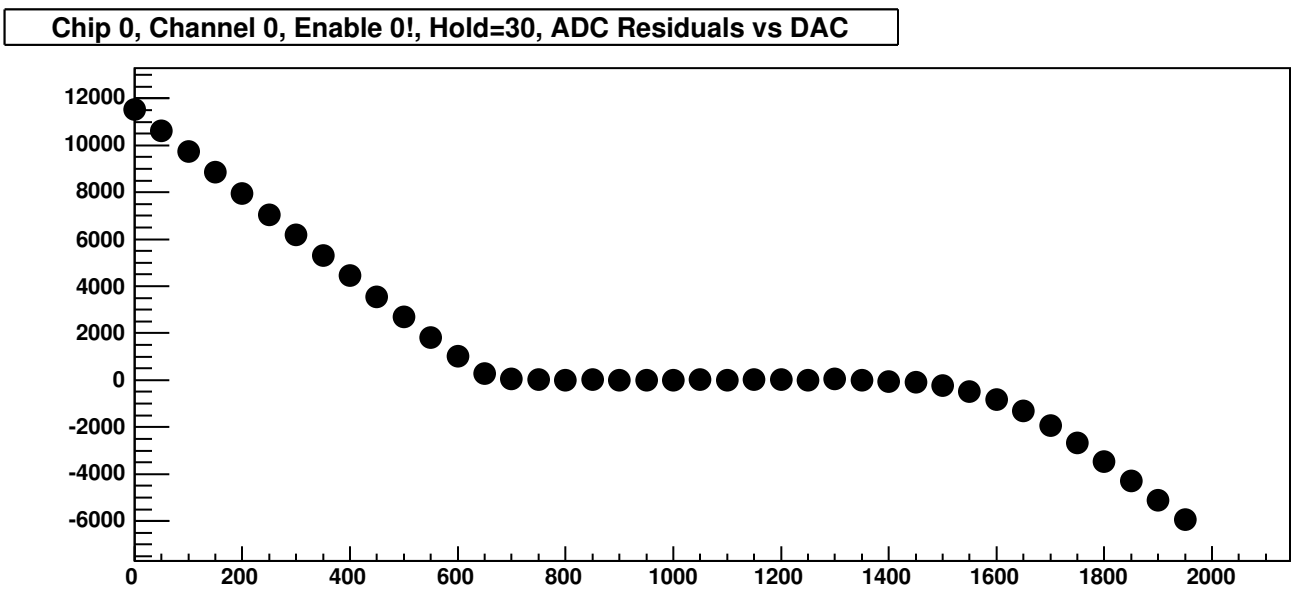


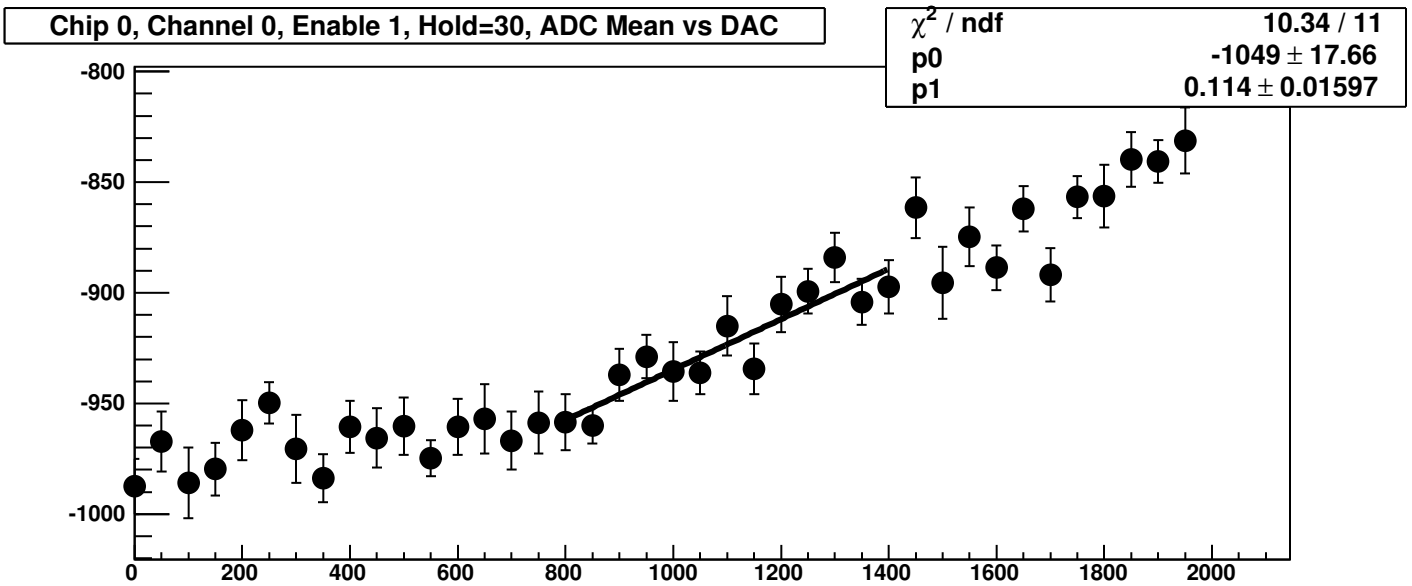
Enable 5, Hold=30, DAC=1900, ADC Noise vs 18*Chip+Chan

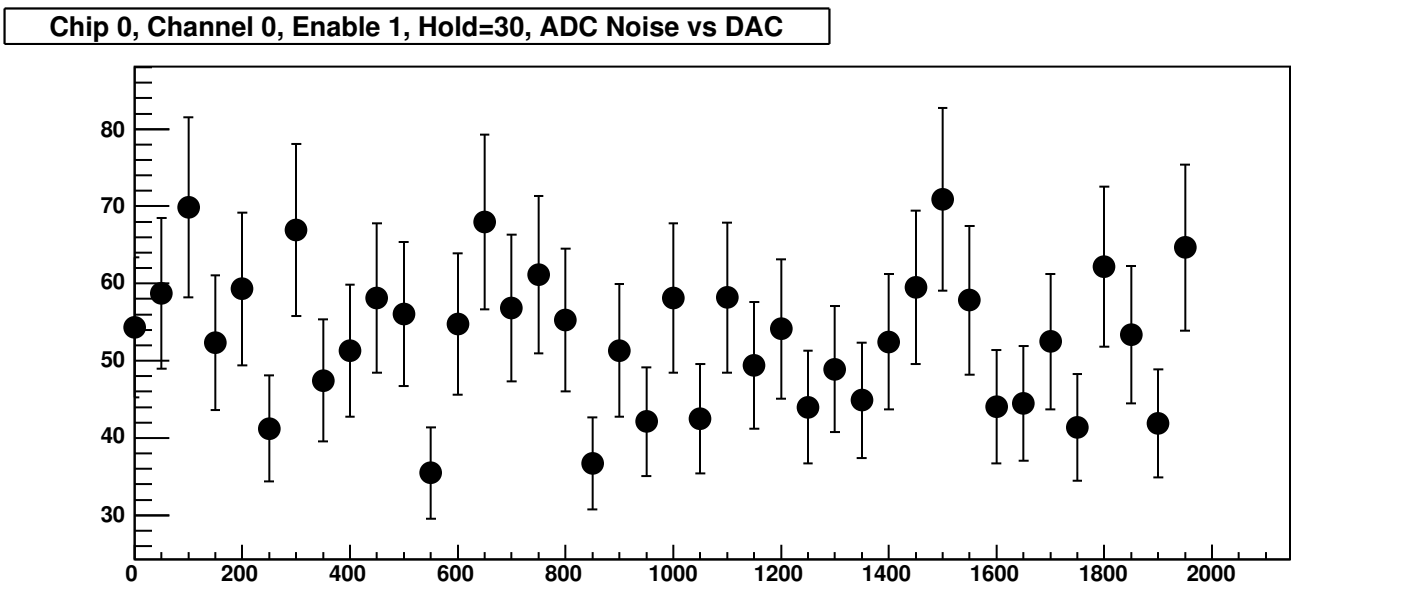


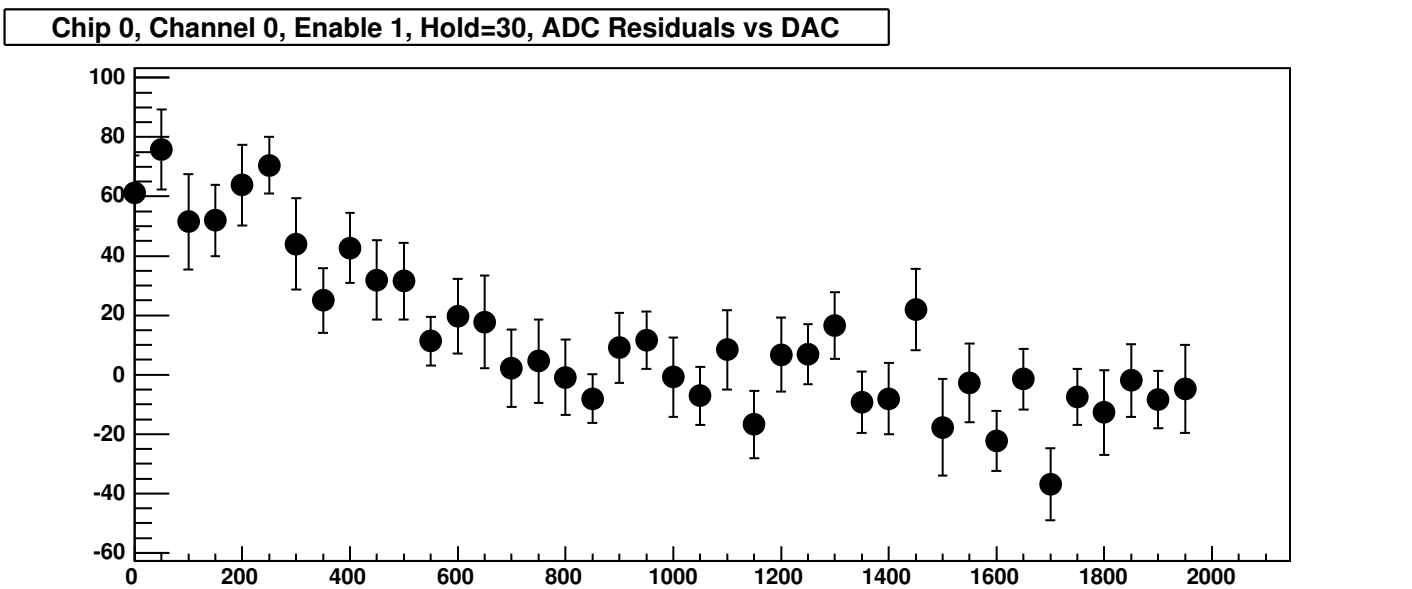

Chip 0, Channel 0, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 39.24 / 11
p0 -1038 ± 20.58
p1 18.09 ± 0.01861
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

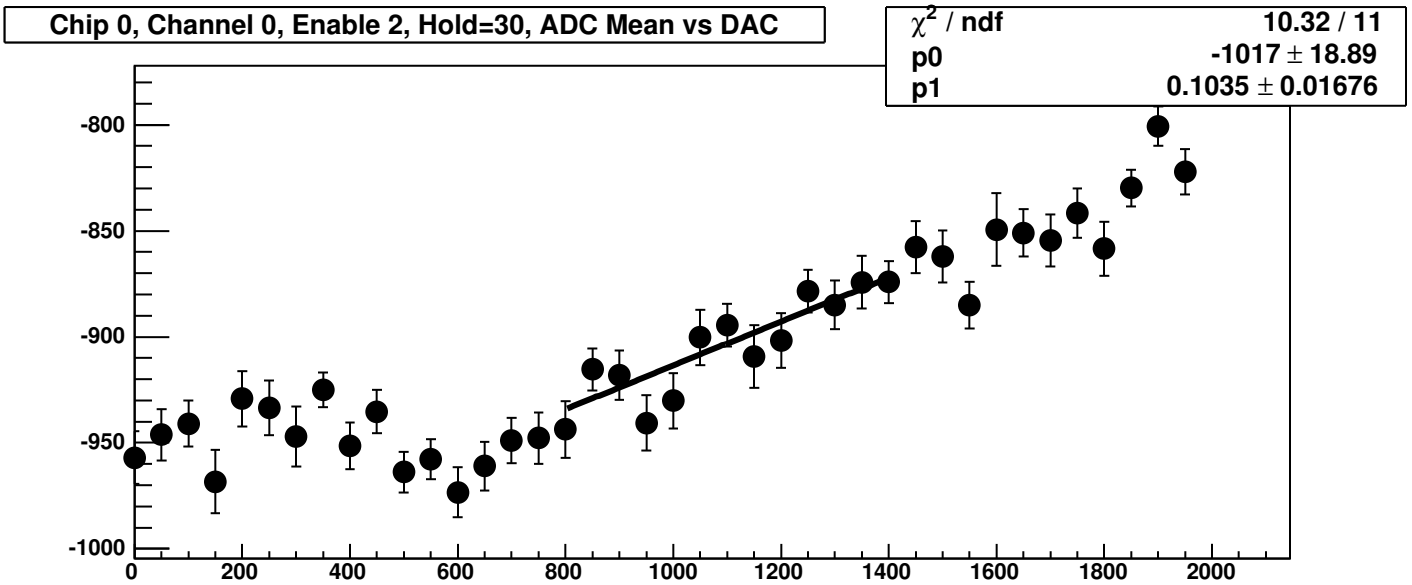

Chip 0, Channel 0, Enable 0!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

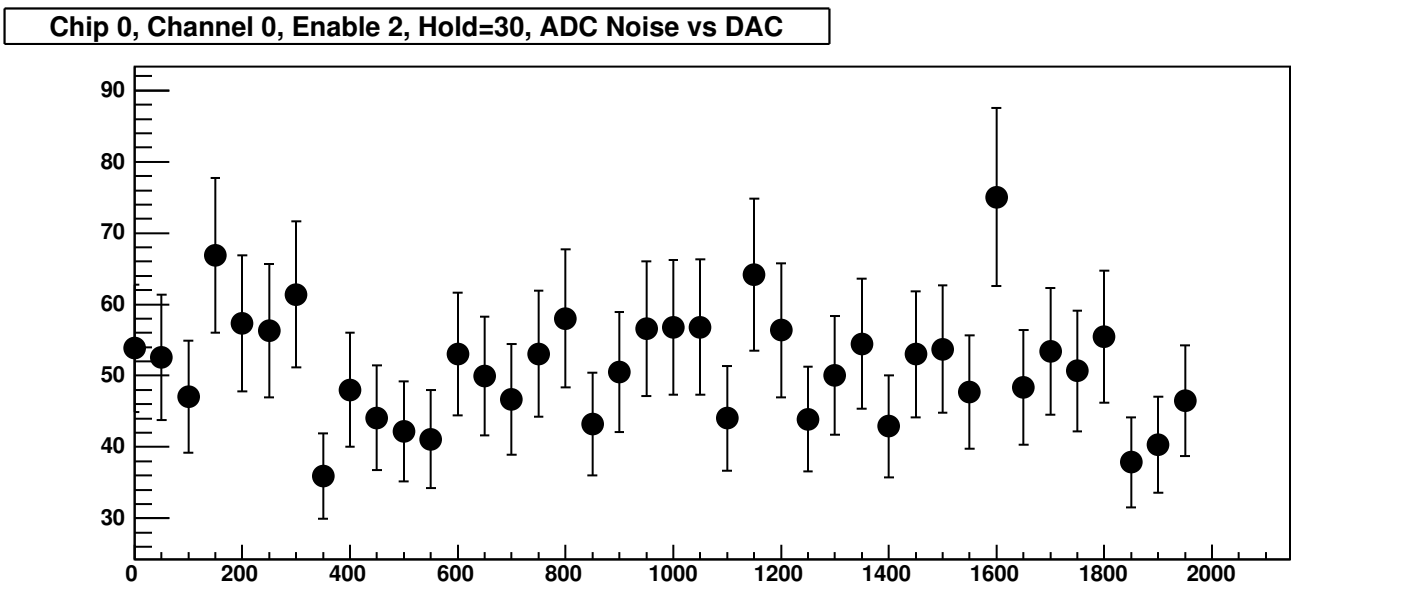

Chip 0, Channel 0, Enable 0!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

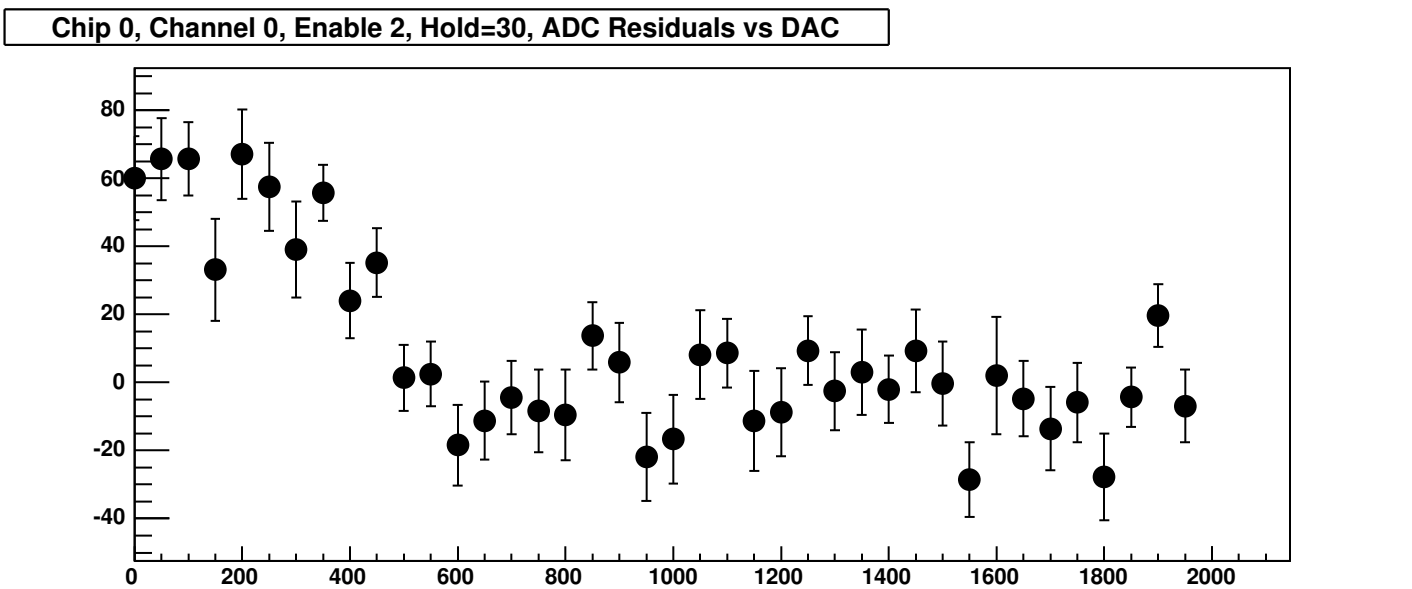

Chip 0, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 10.34 / 11
p0 -1049 ± 17.66
p1 0.114 ± 0.01597
-800
-850
-900
-950
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

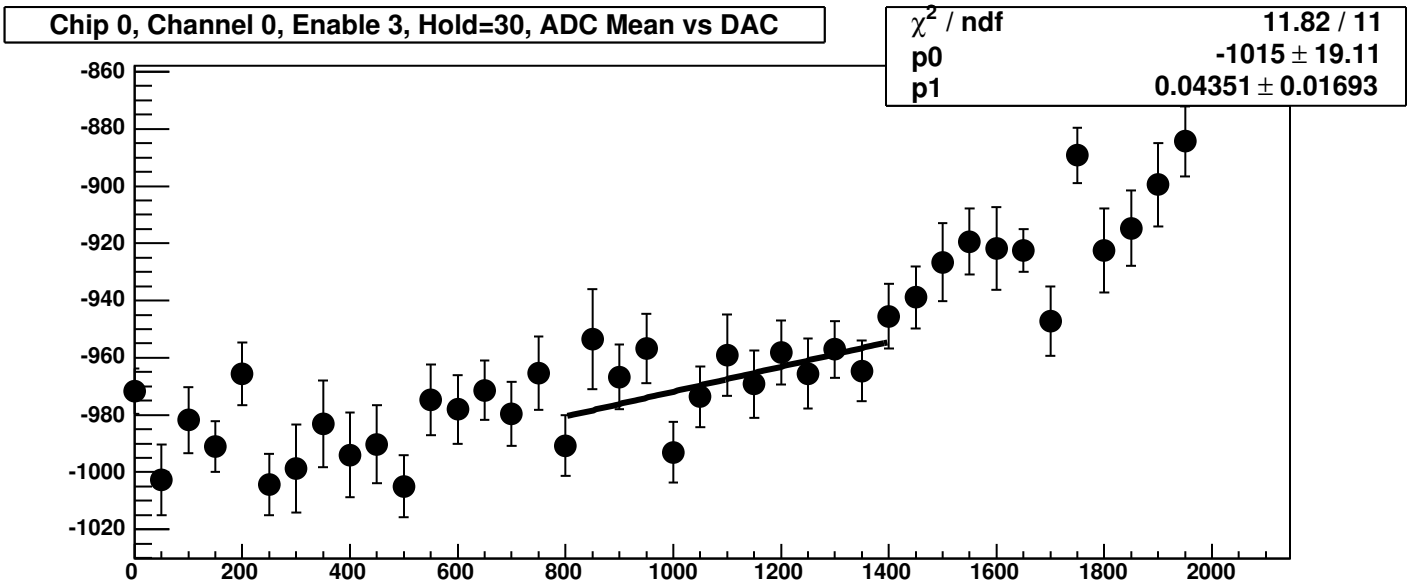

Chip 0, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

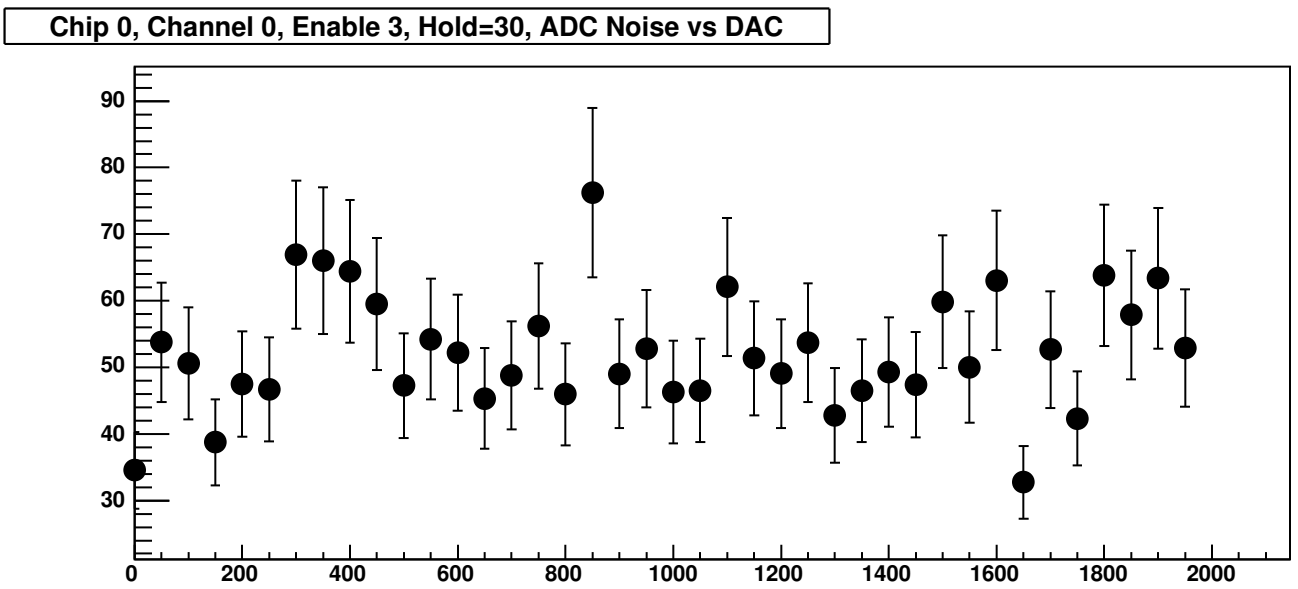

Chip 0, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

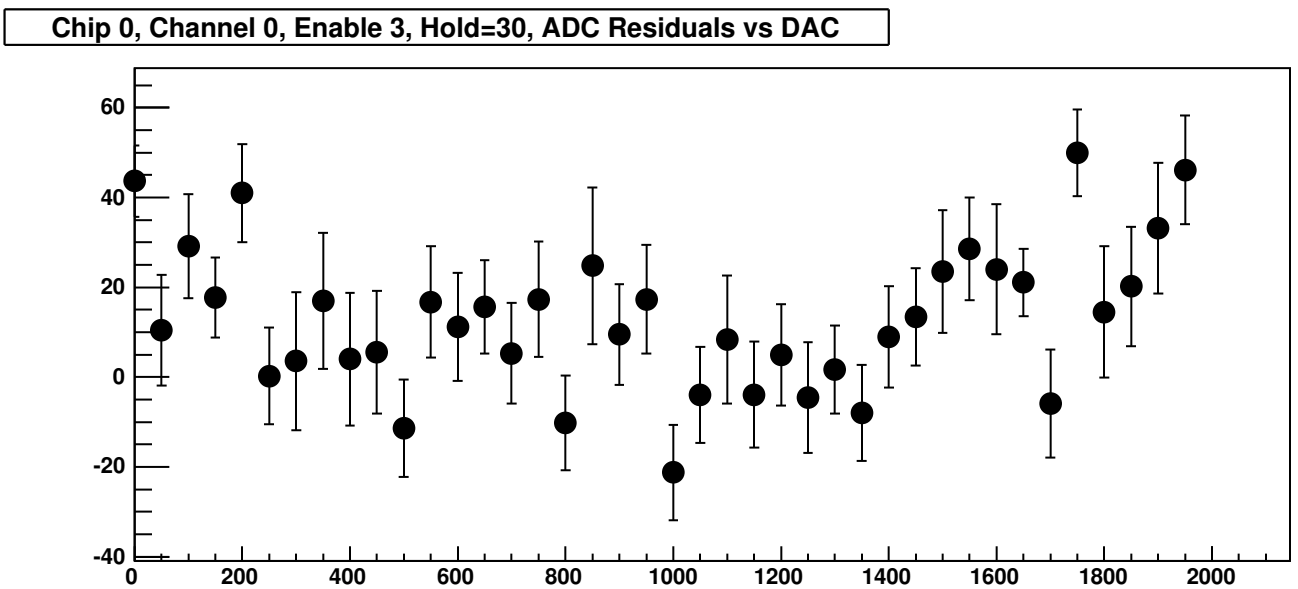

Chip 0, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 10.32 / 11
p0 -1017 ± 18.89
p1 0.1035 ± 0.01676
-800
-850
-900
-950
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

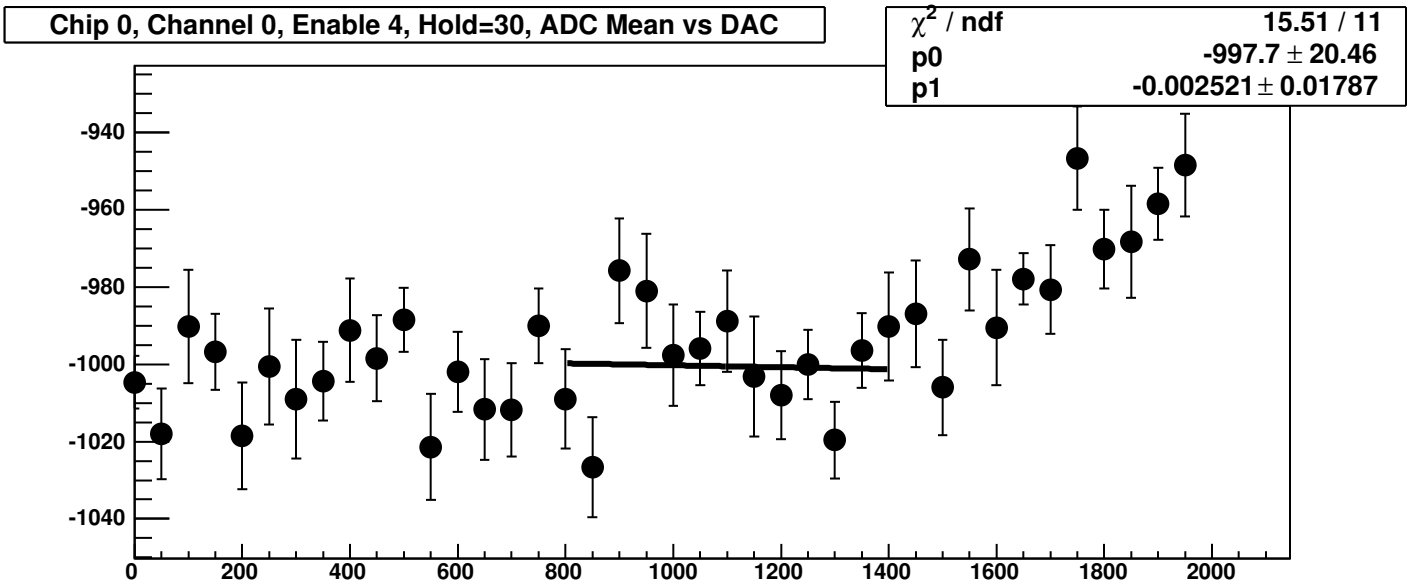

Chip 0, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

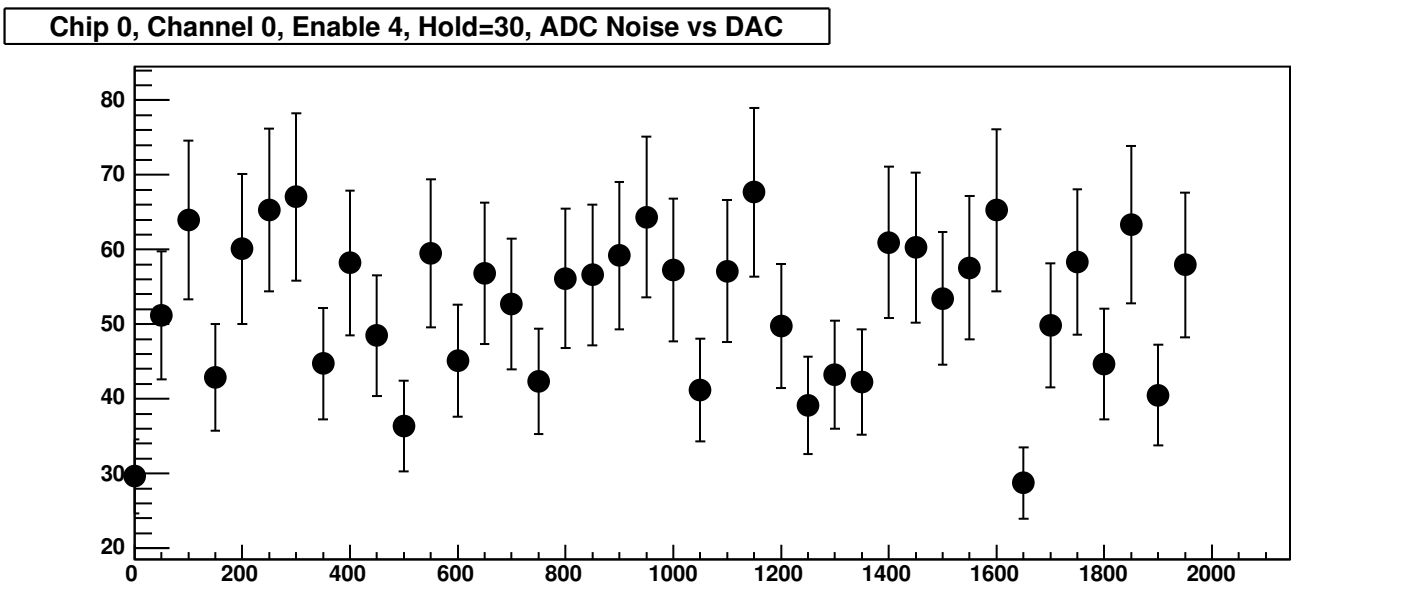

Chip 0, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

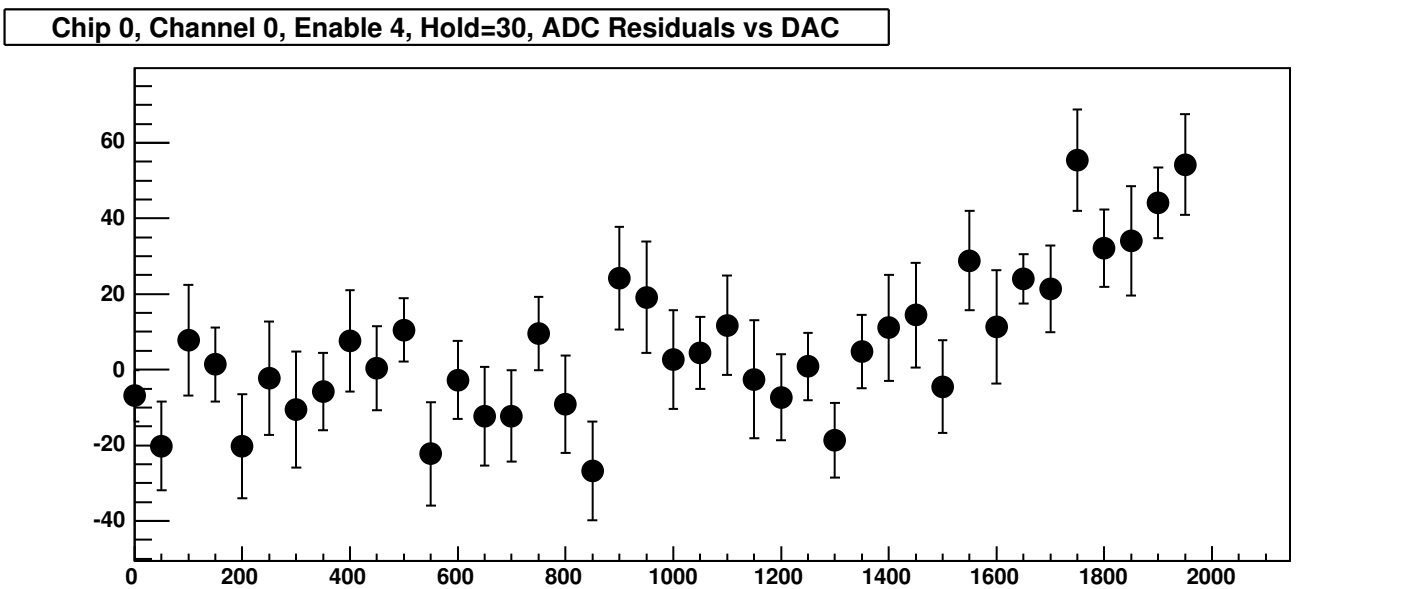

Chip 0, Channel 0, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 11.82 / 11
p0 -1015 ± 19.11
p1 0.04351 ± 0.01693
-860
-880
-900
-920
-940
-960
-980
-1000
-1020
0
200
400
600
800
1000
1200
1400
1600
1800
2000

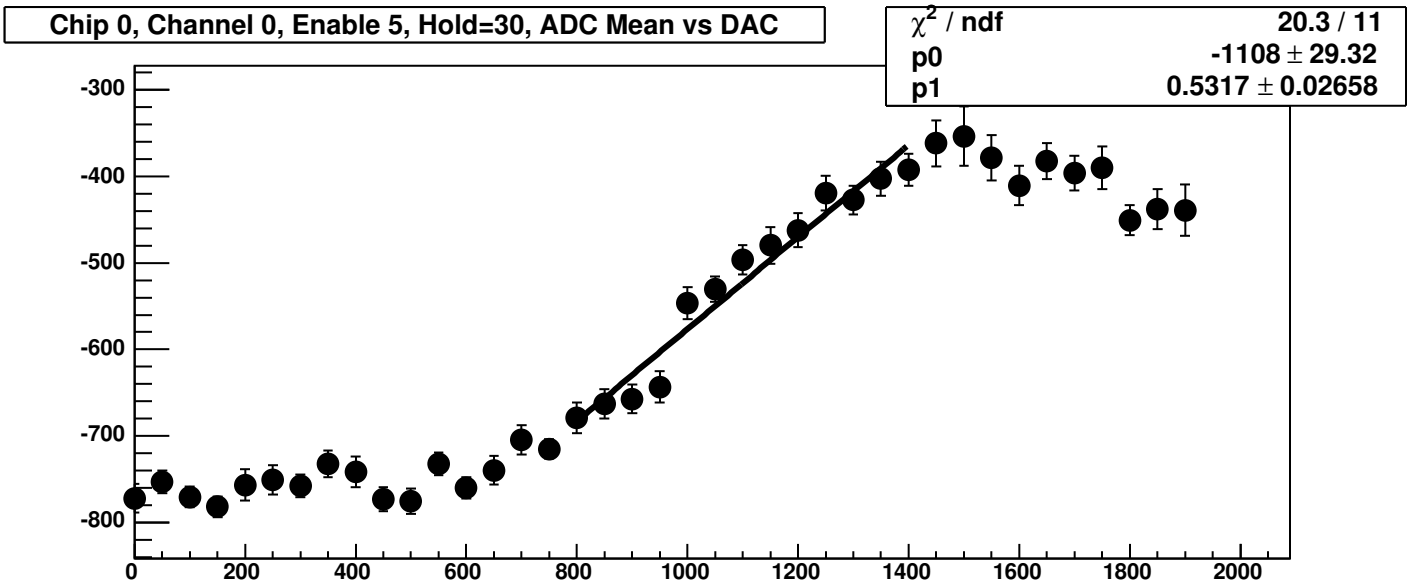

Chip 0, Channel 0, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

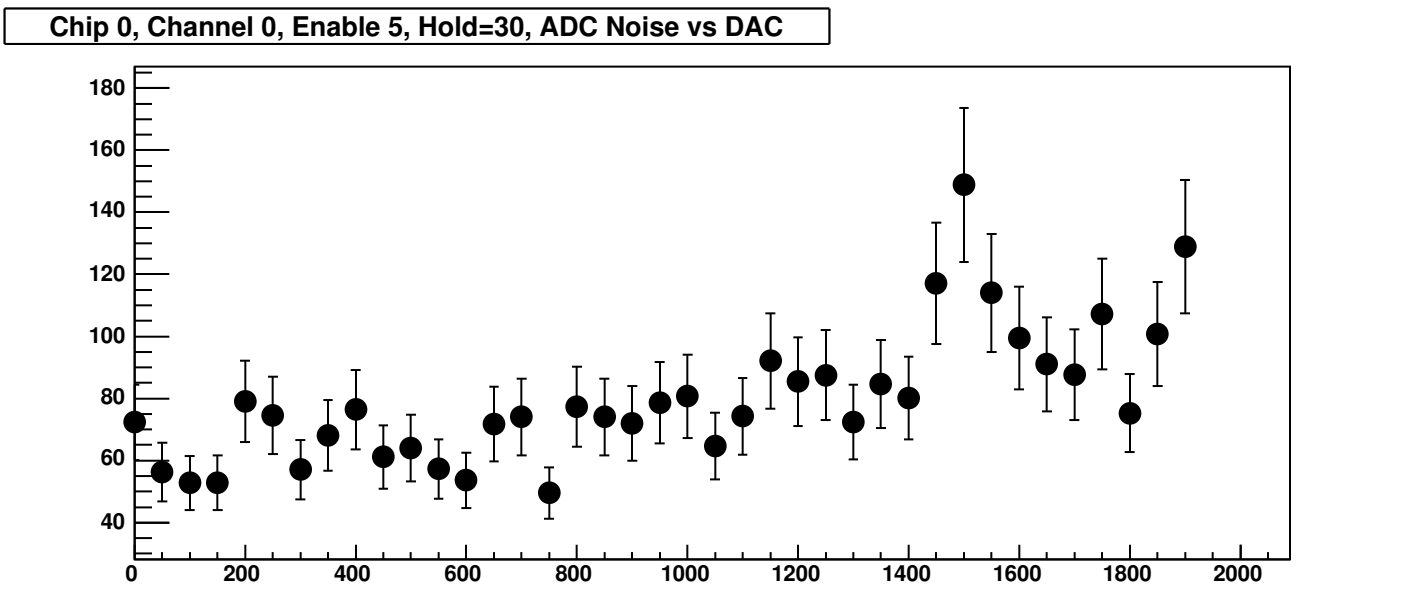

Chip 0, Channel 0, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

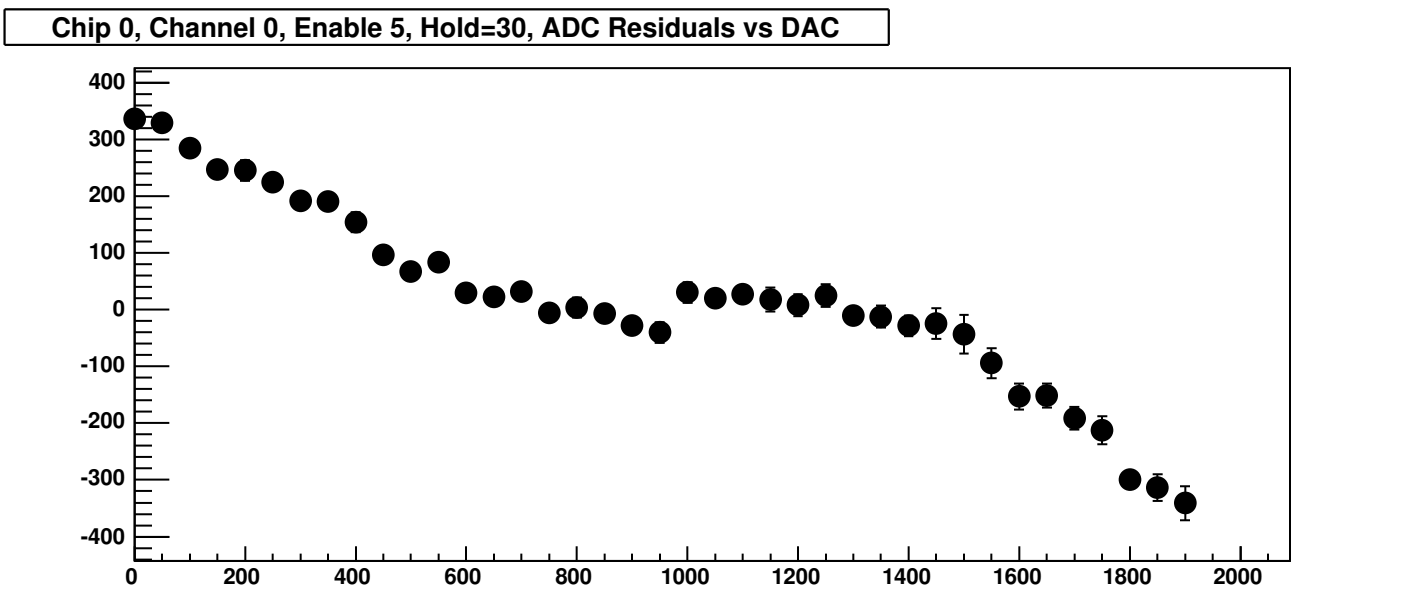

Chip 0, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 15.51 / 11
p0 -997.7 ± 20.46
p1 -0.002521 ± 0.01787
-940
-960
-980
-1000
-1020
-1040
0
200
400
600
800
1000
1200
1400
1600
1800
2000

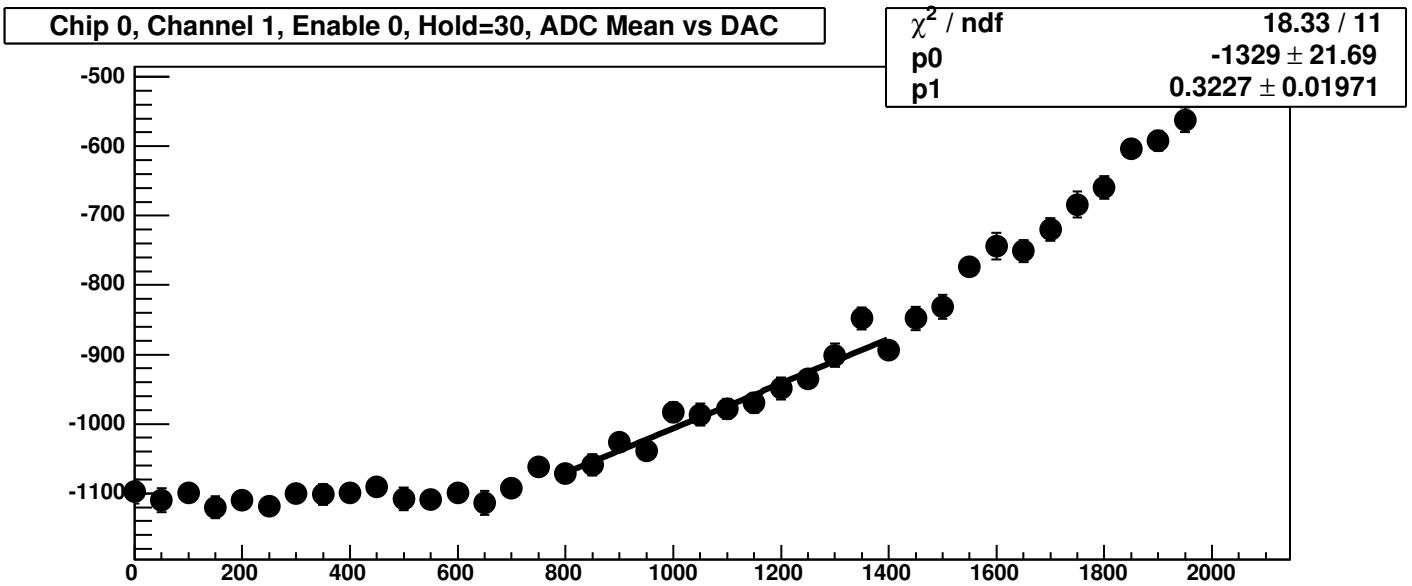

Chip 0, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

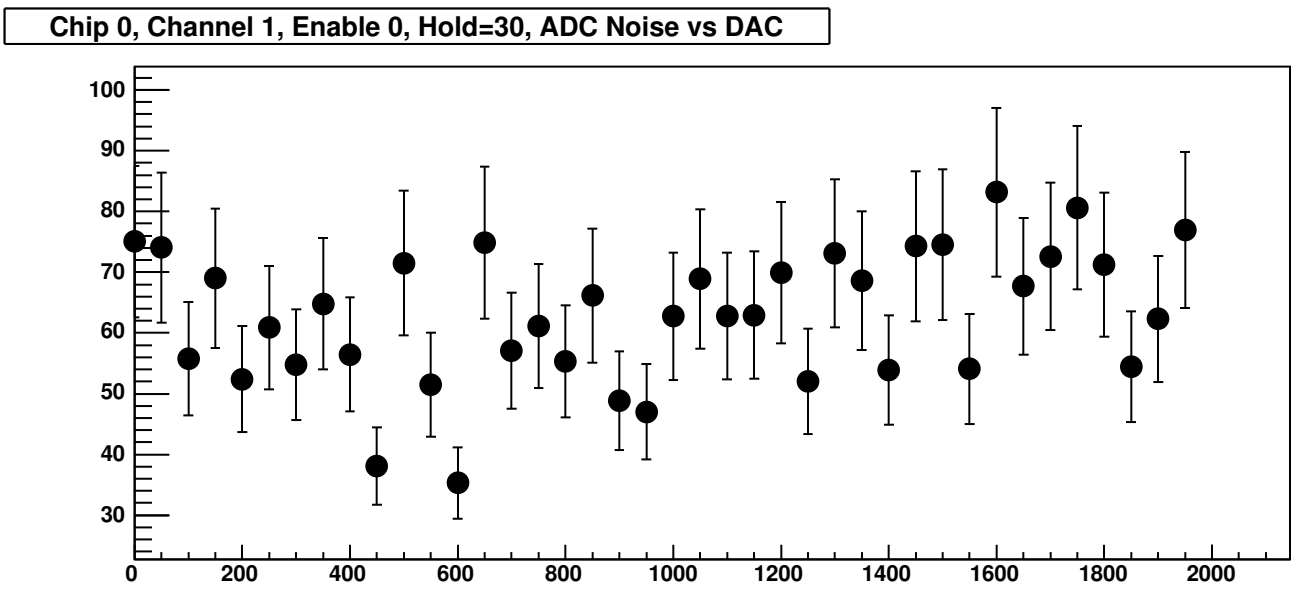

Chip 0, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

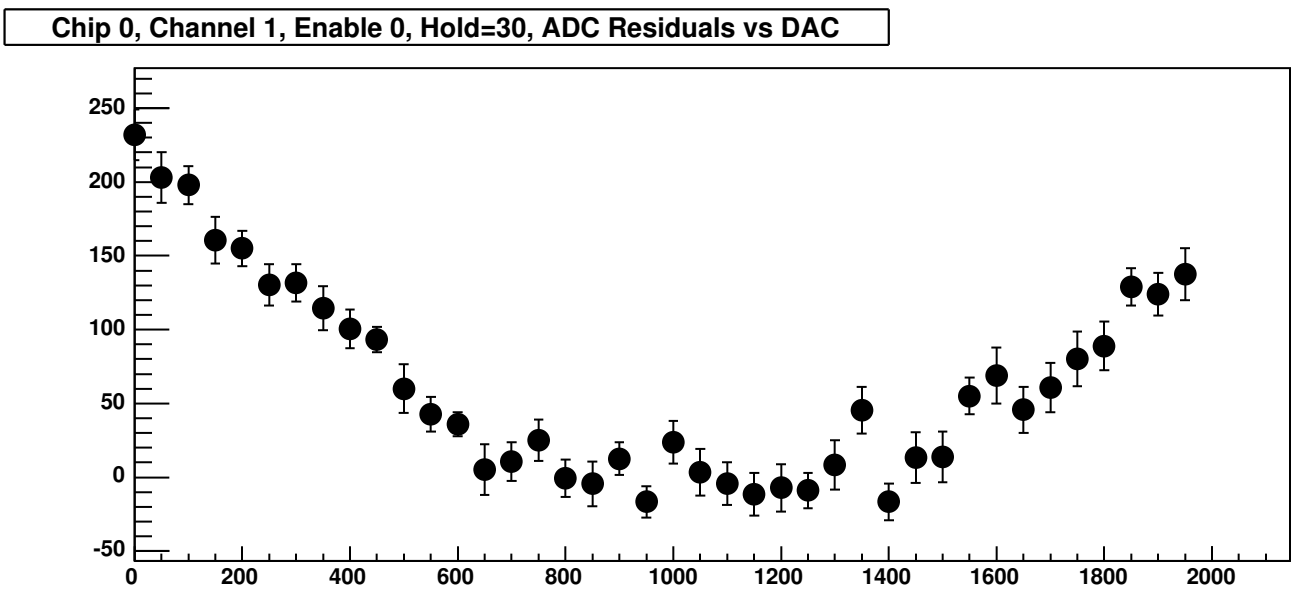

Chip 0, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 20.3 / 11
p0 -1108 ± 29.32
p1 0.5317 ± 0.02658
-300
-400
-500
-600
-700
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

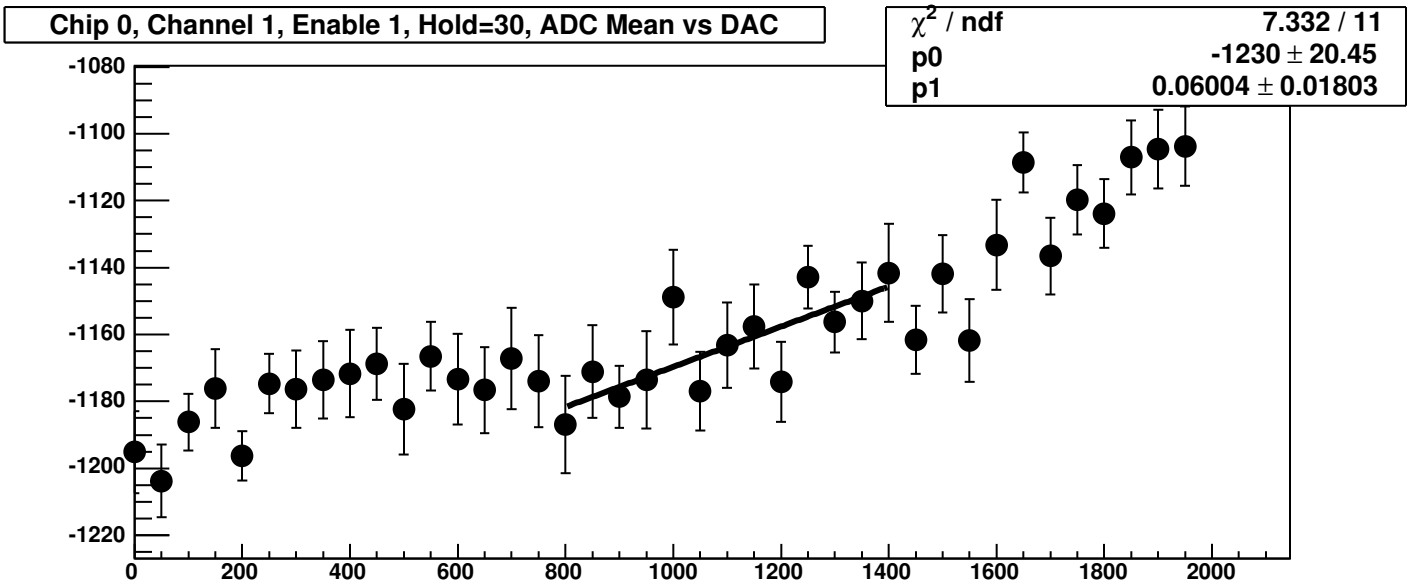

Chip 0, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

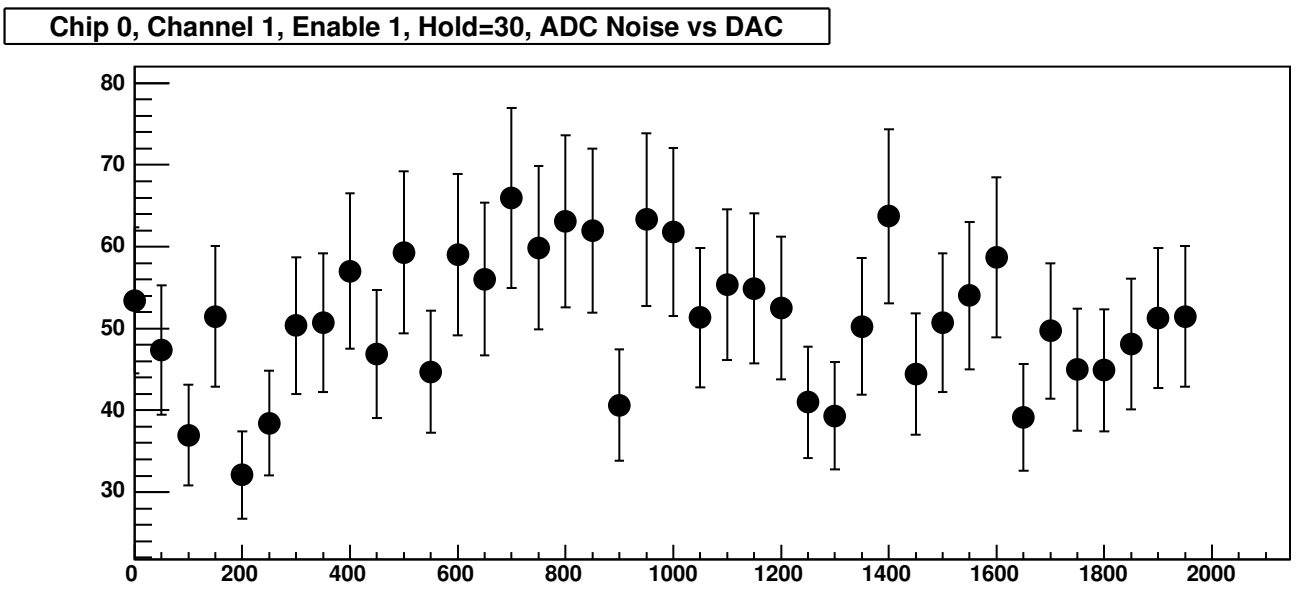

Chip 0, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
-300
-400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

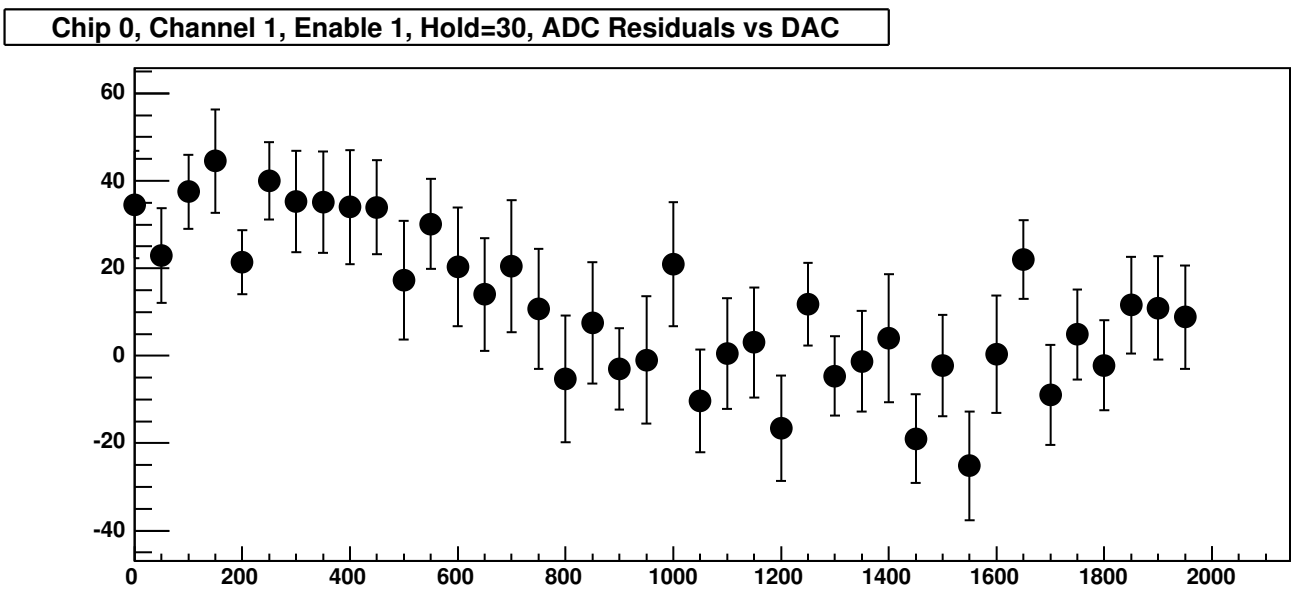

Chip 0, Channel 1, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 18.33 / 11
p0 -1329 ± 21.69
p1 0.3227 ± 0.01971
-500
-600
-700
-800
-900
-1000
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

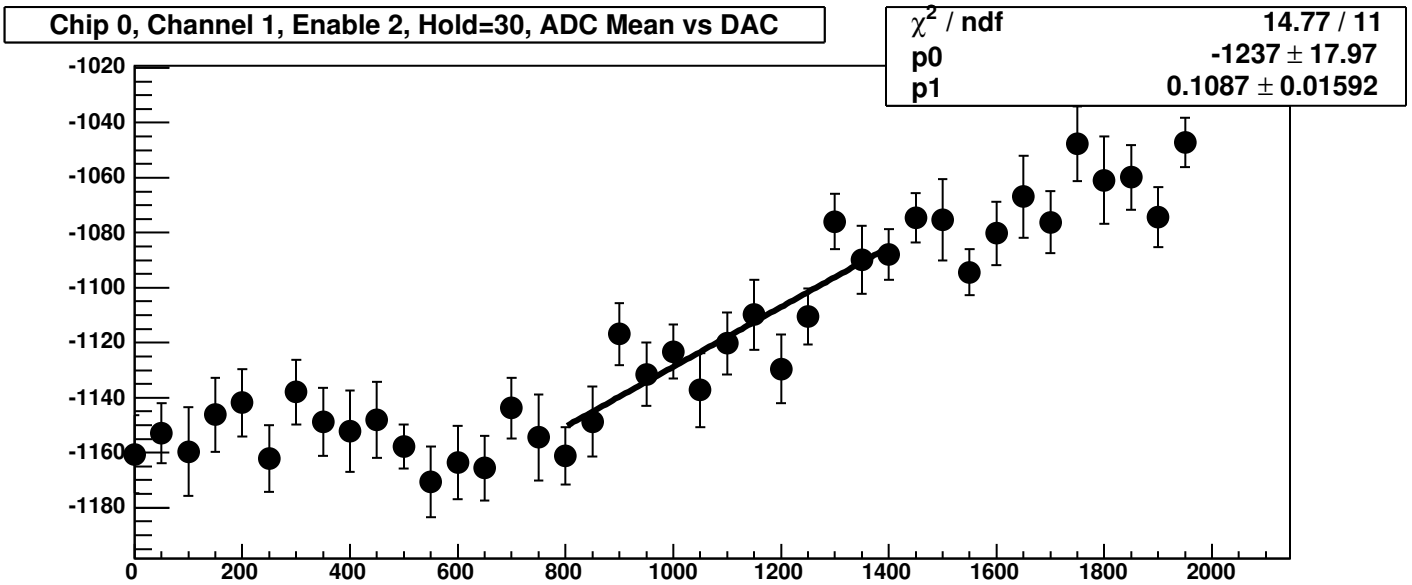

Chip 0, Channel 1, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

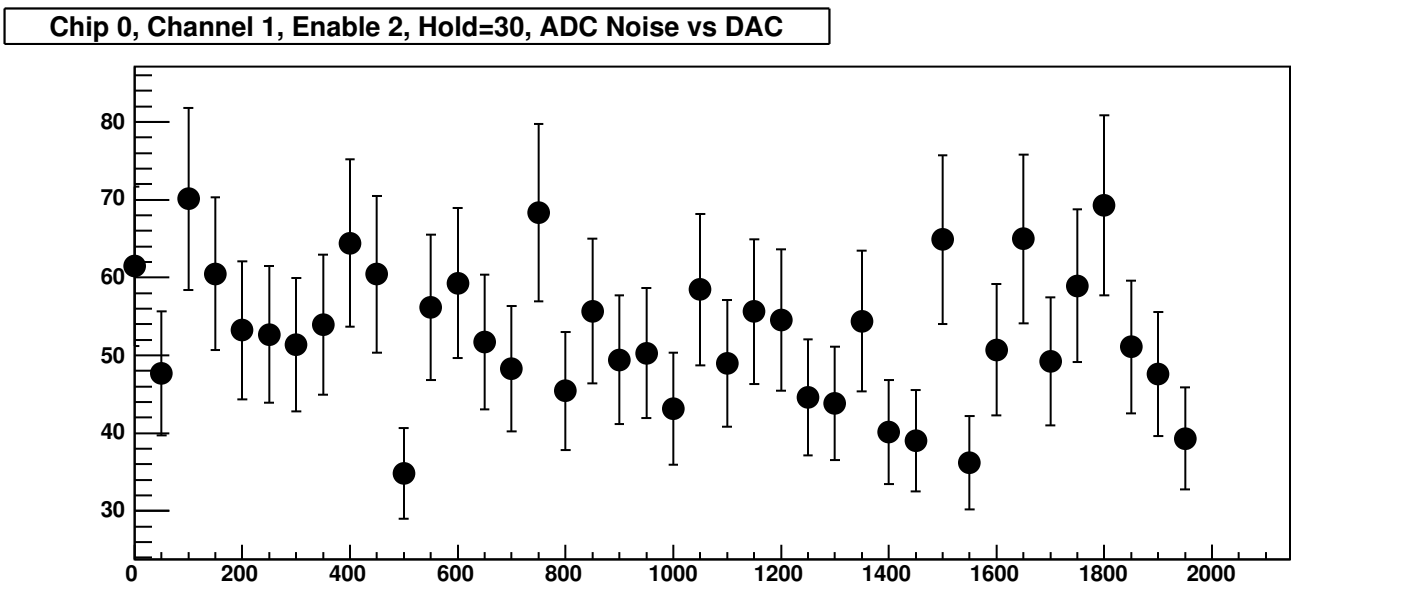

Chip 0, Channel 1, Enable 0, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

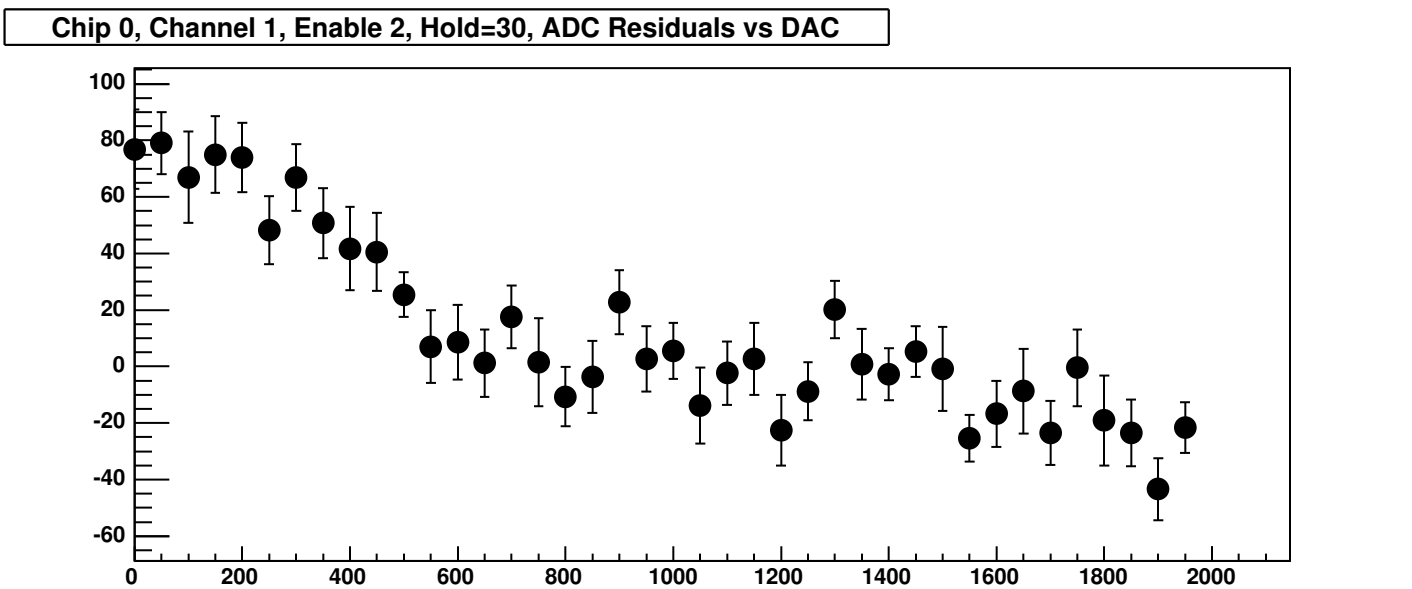

Chip 0, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7.332 / 11
p0 -1230 ± 20.45
p1 0.06004 ± 0.01803
-1080
-1100
-1120
-1140
-1160
-1180
-1200
-1220
0
200
400
600
800
1000
1200
1400
1600
1800
2000

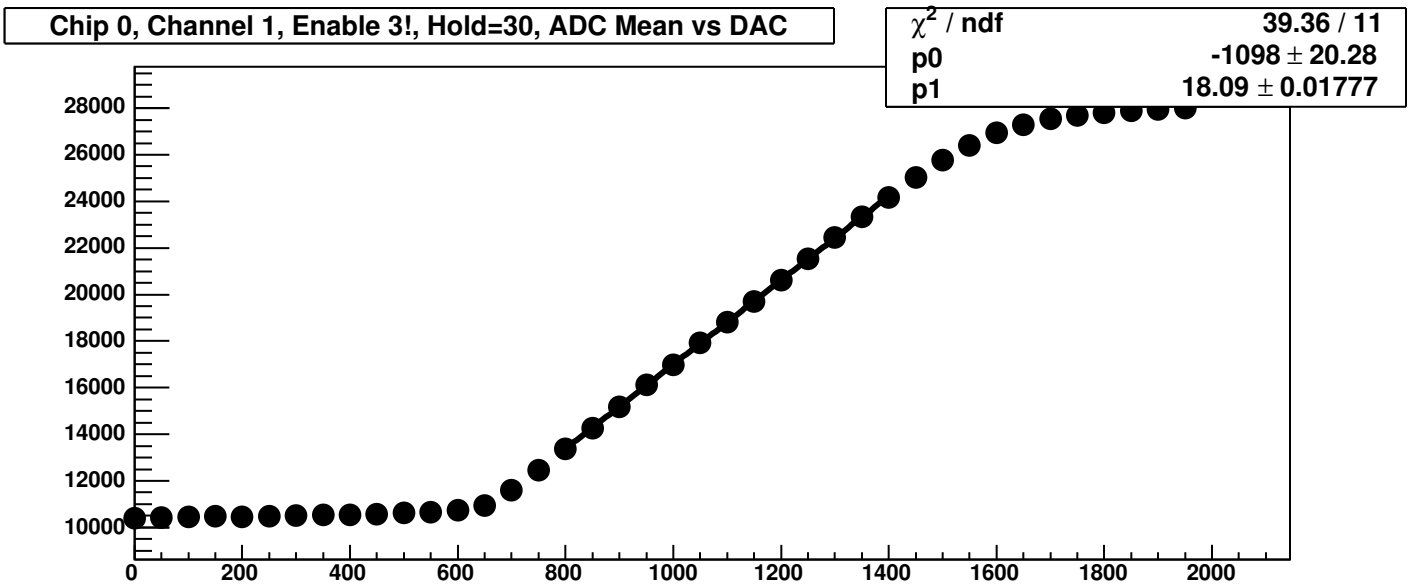

Chip 0, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

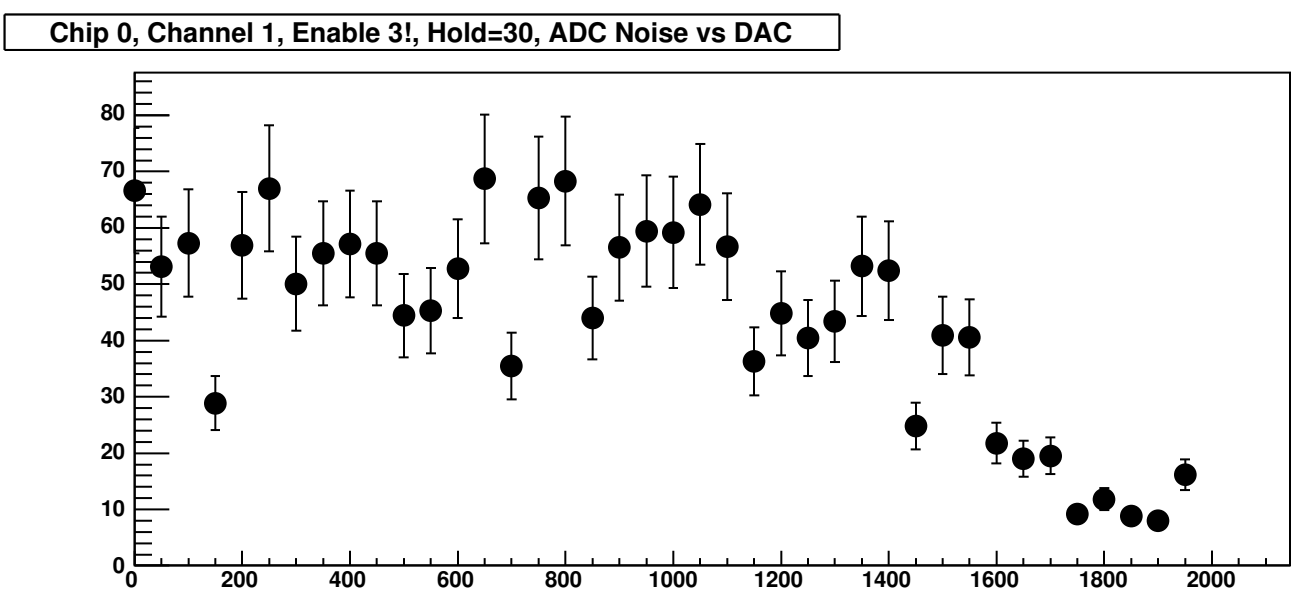

Chip 0, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

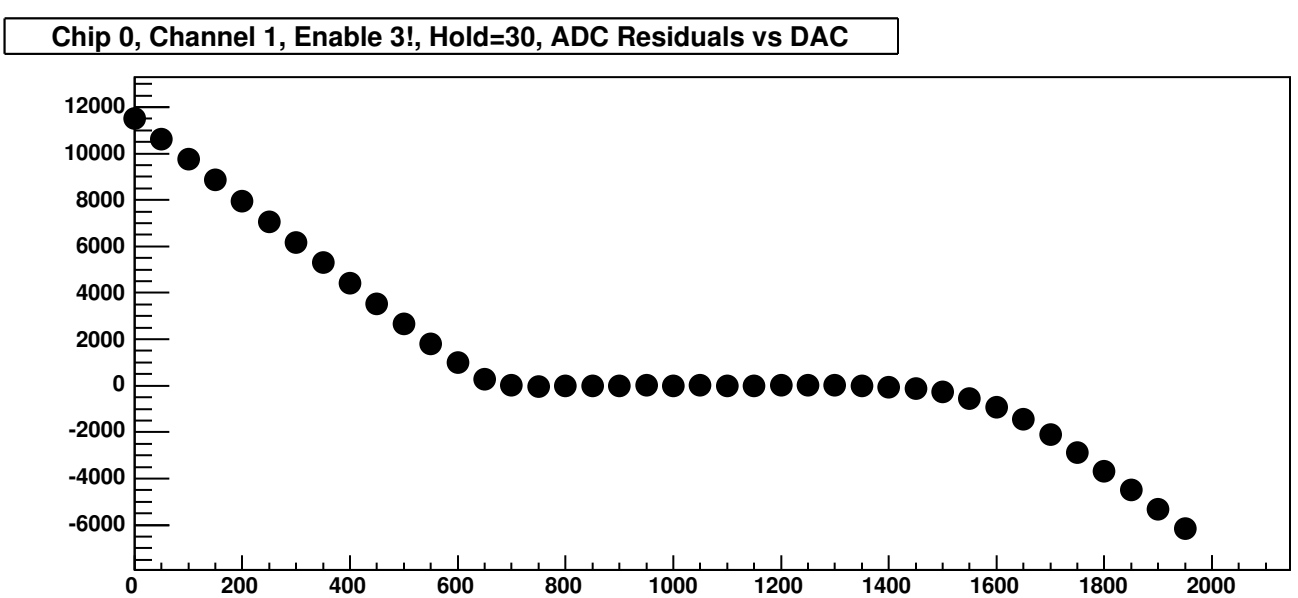

Chip 0, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 14.77 / 11
p0 -1237 ± 17.97
p1 0.1087 ± 0.01592
-1020
-1040
-1060
-1080
-1100
-1120
-1140
-1160
-1180
0
200
400
600
800
1000
1200
1400
1600
1800
2000

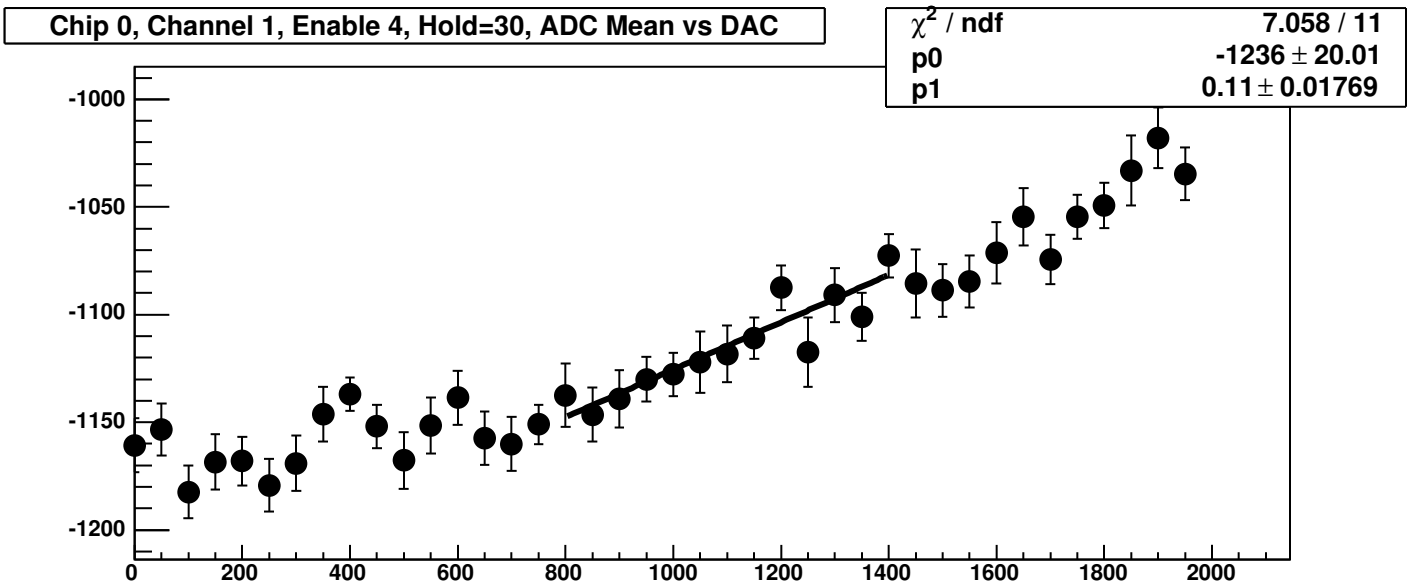

Chip 0, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

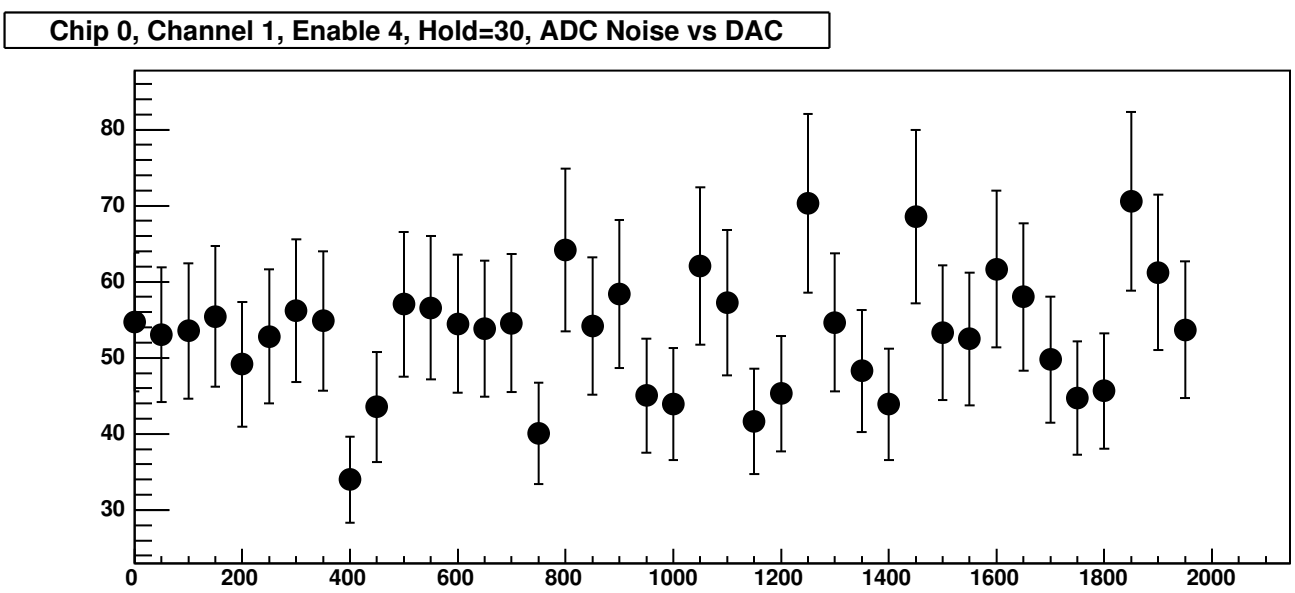

Chip 0, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

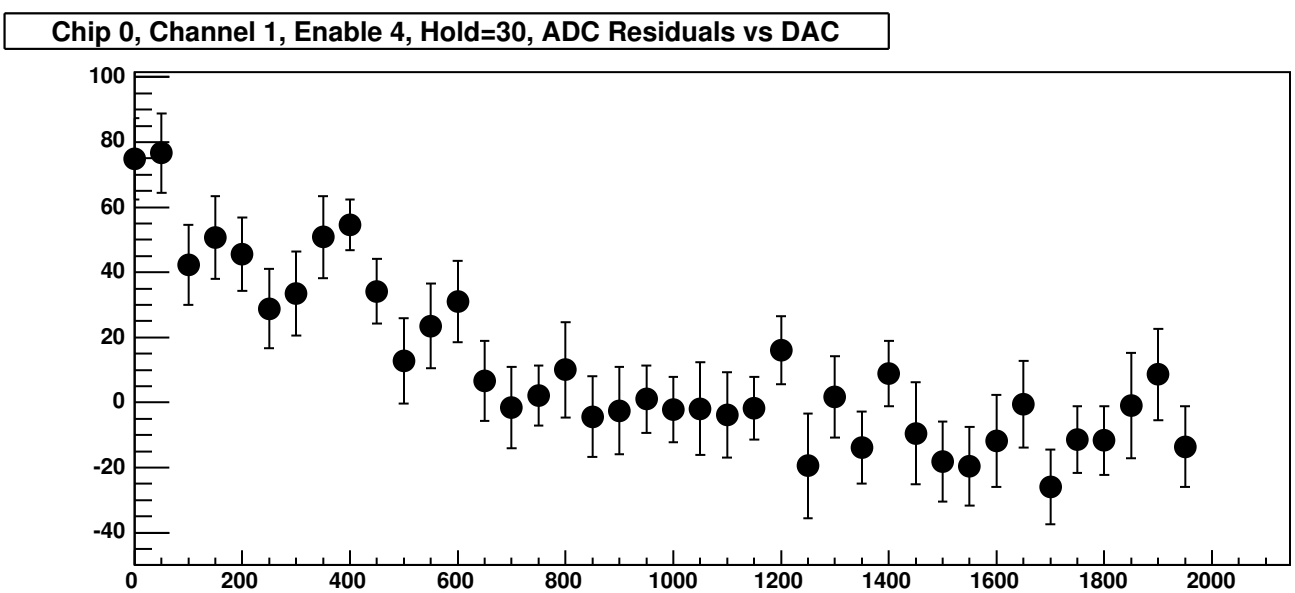

Chip 0, Channel 1, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 39.36 / 11
p0 -1098 ± 20.28
p1 18.09 ± 0.01777
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

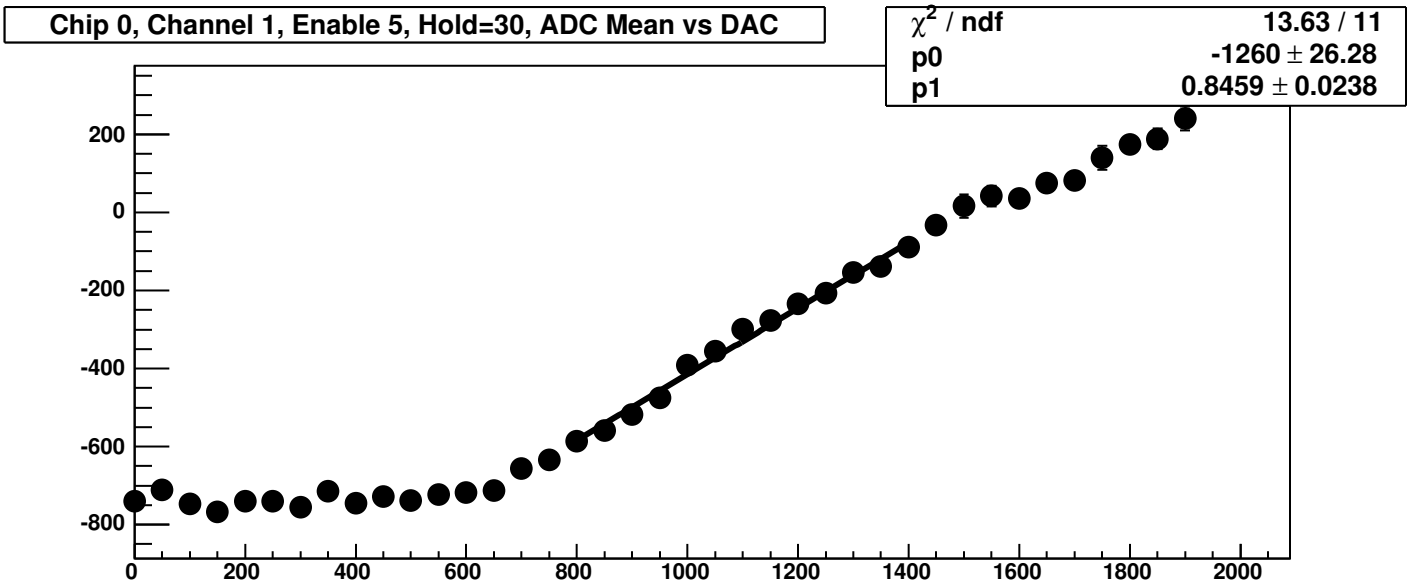

Chip 0, Channel 1, Enable 3!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

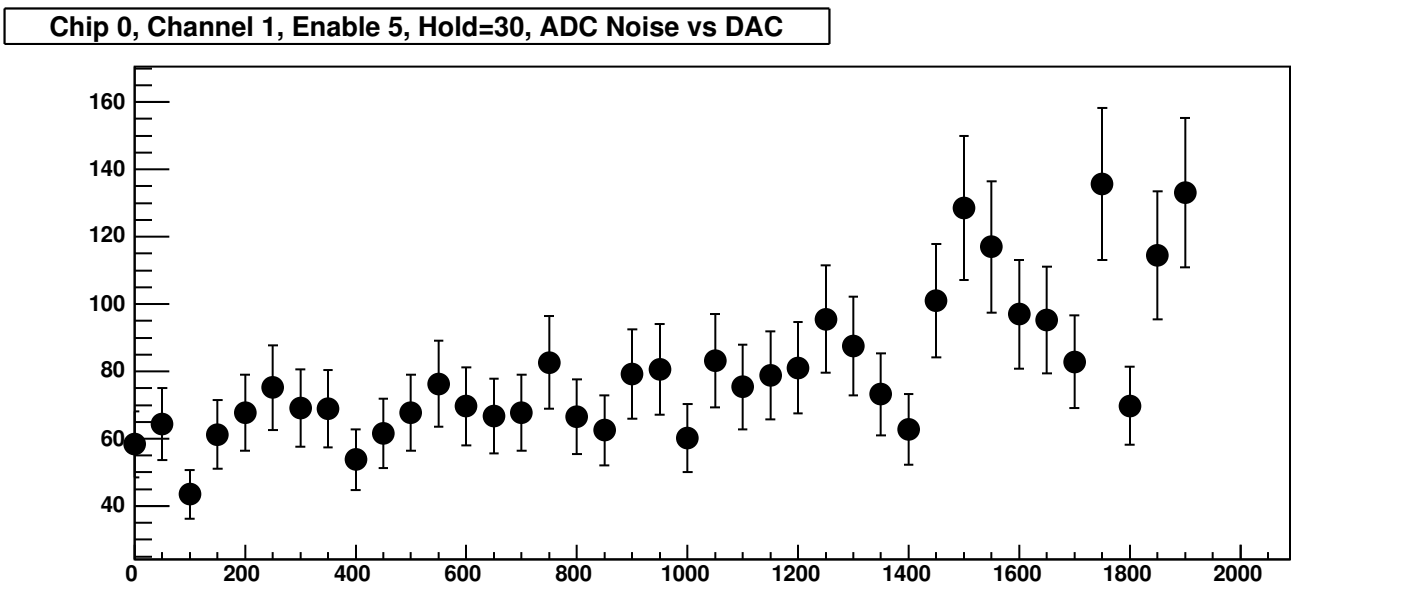

Chip 0, Channel 1, Enable 3!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

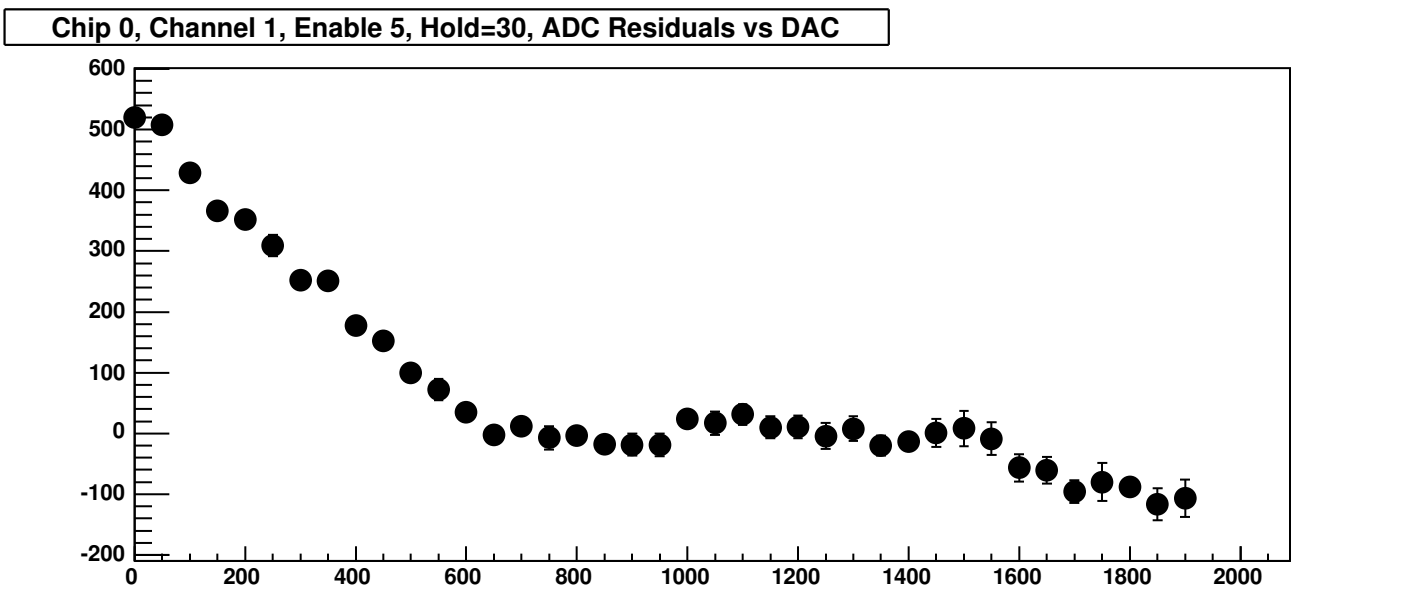

Chip 0, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7.058 / 11
p0 -1236 ± 20.01
p1 0.11 ± 0.01769
-1000
-1050
-1100
-1150
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

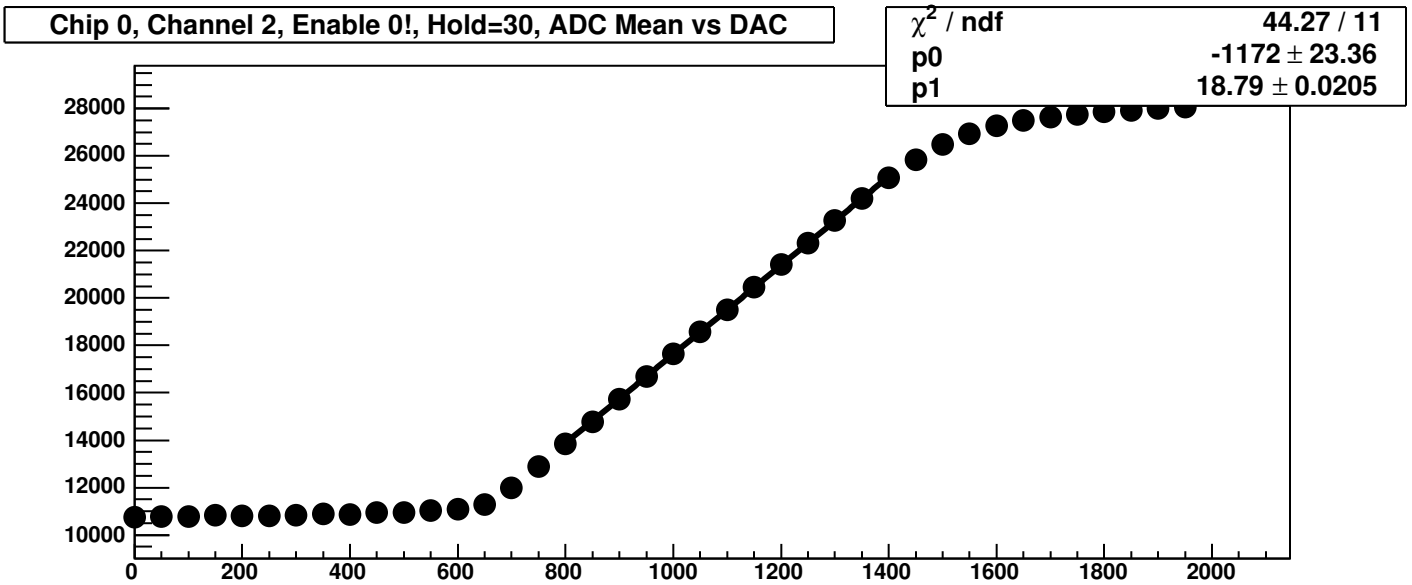

Chip 0, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

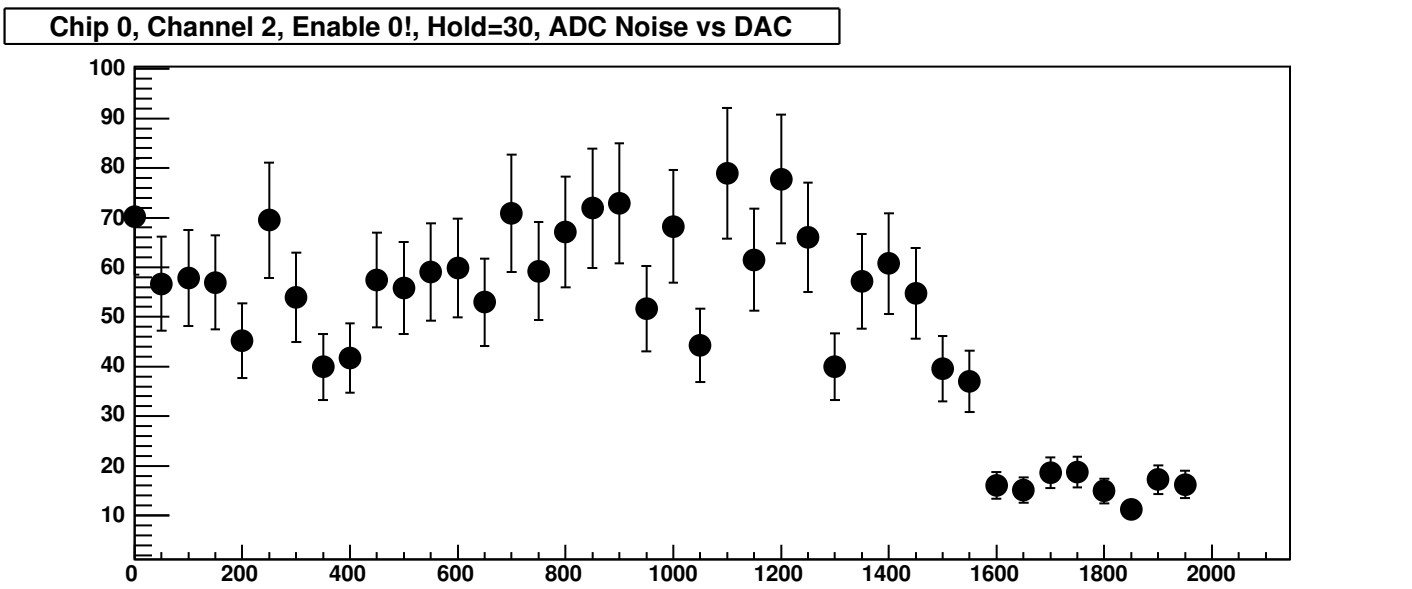

Chip 0, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

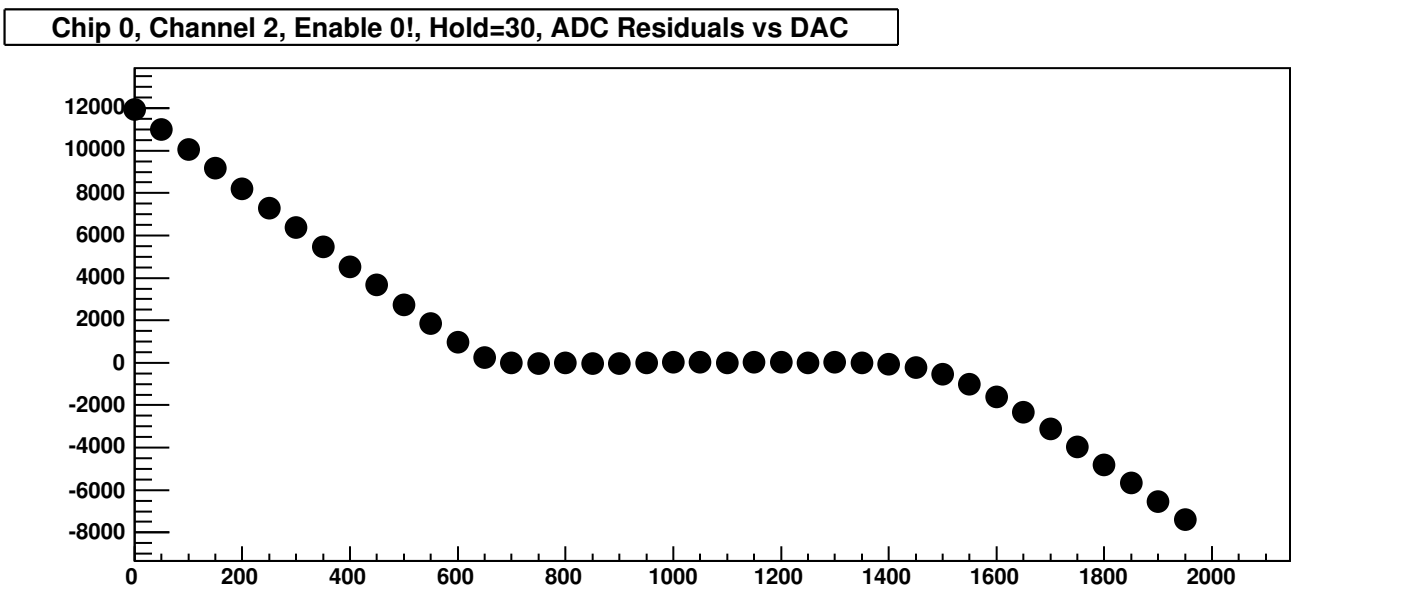

Chip 0, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 13.63 / 11
p0 -1260 ± 26.28
p1 0.8459 ± 0.0238
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

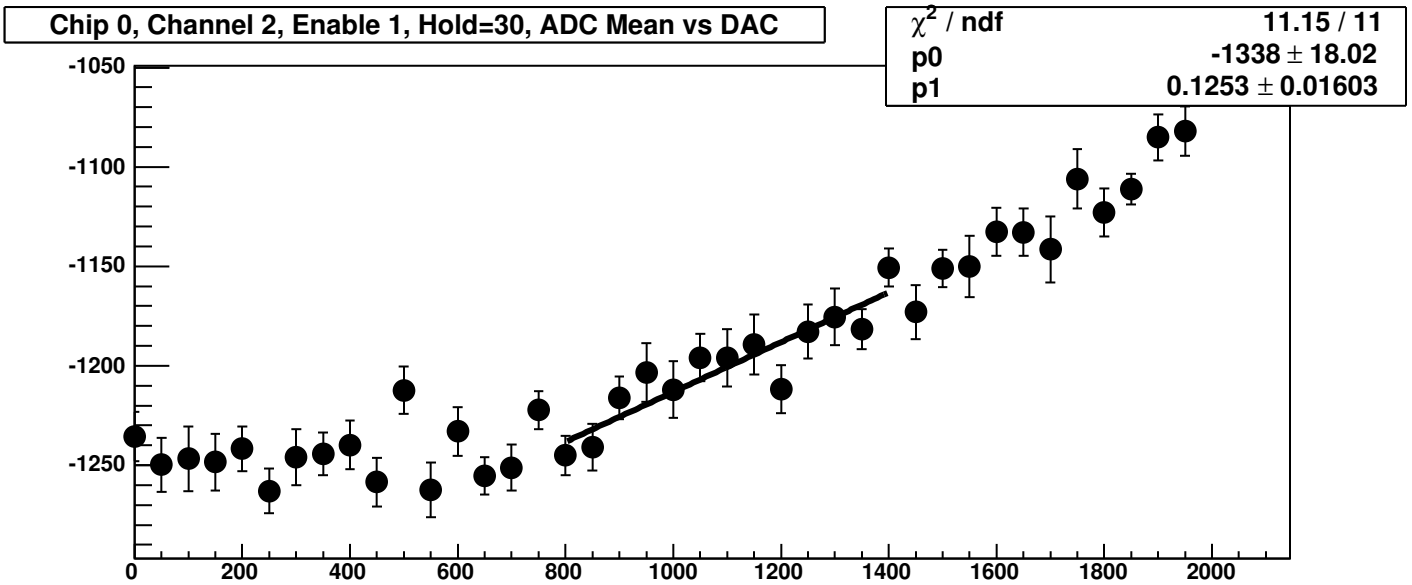

Chip 0, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

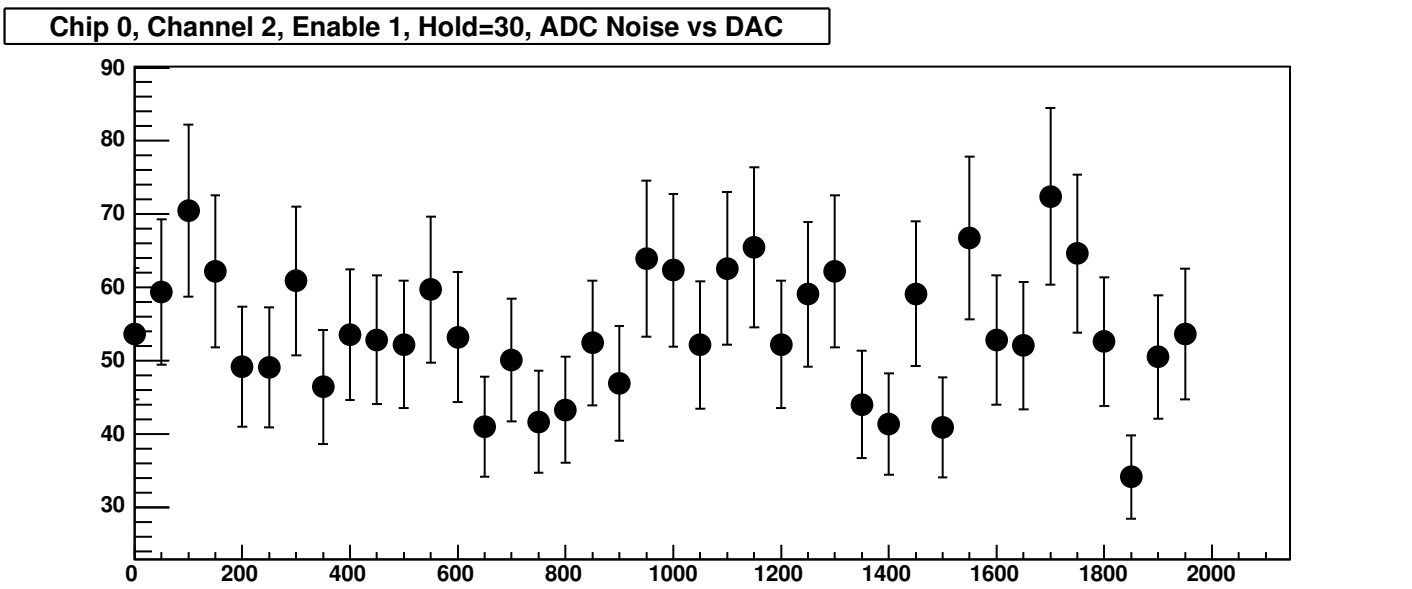

Chip 0, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

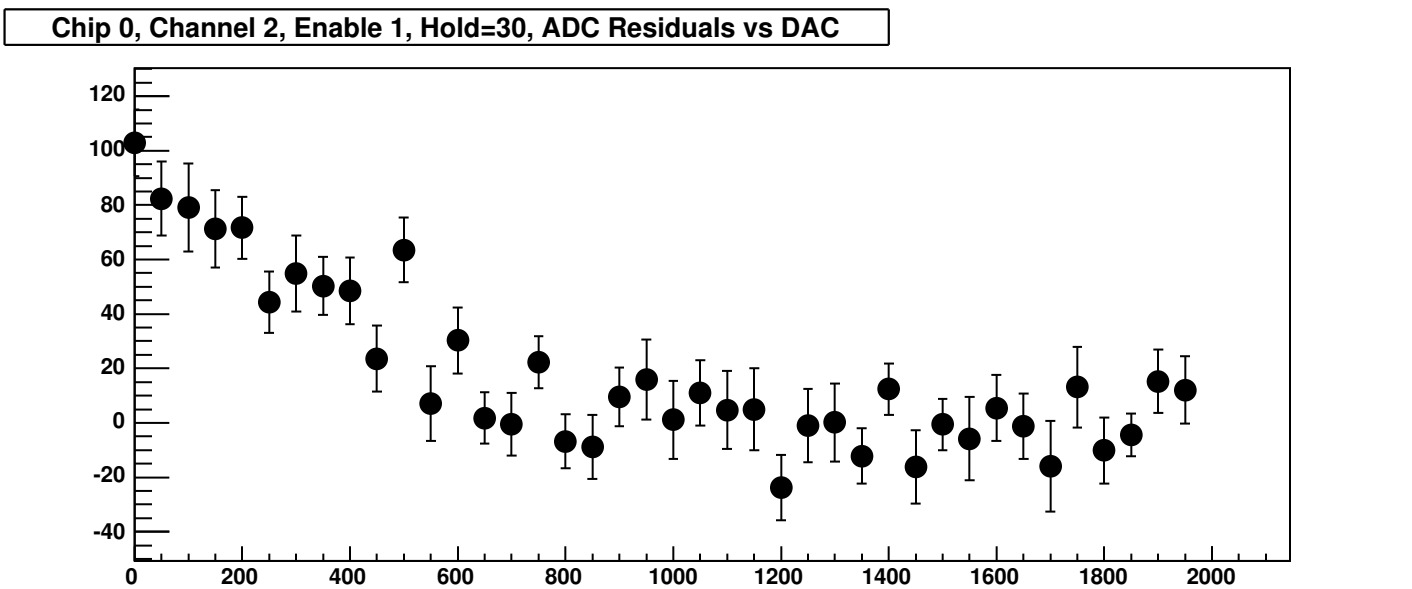

Chip 0, Channel 2, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 44.27 / 11
p0 -1172 ± 23.36
p1 18.79 ± 0.0205
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

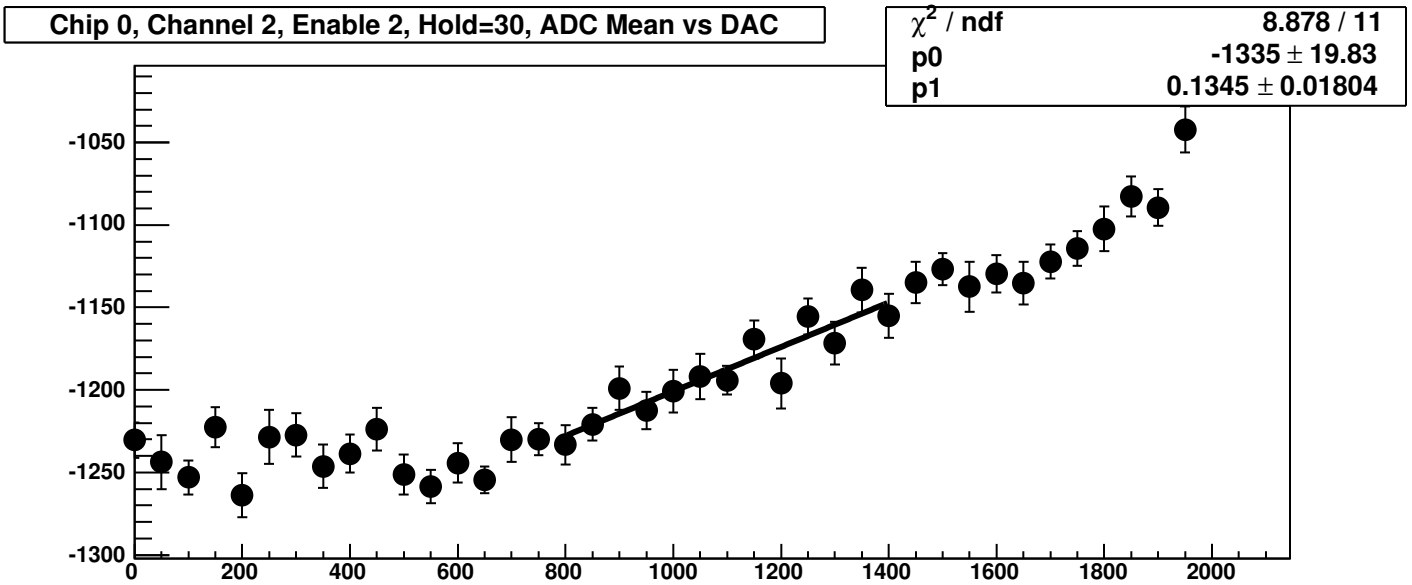

Chip 0, Channel 2, Enable 0!, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

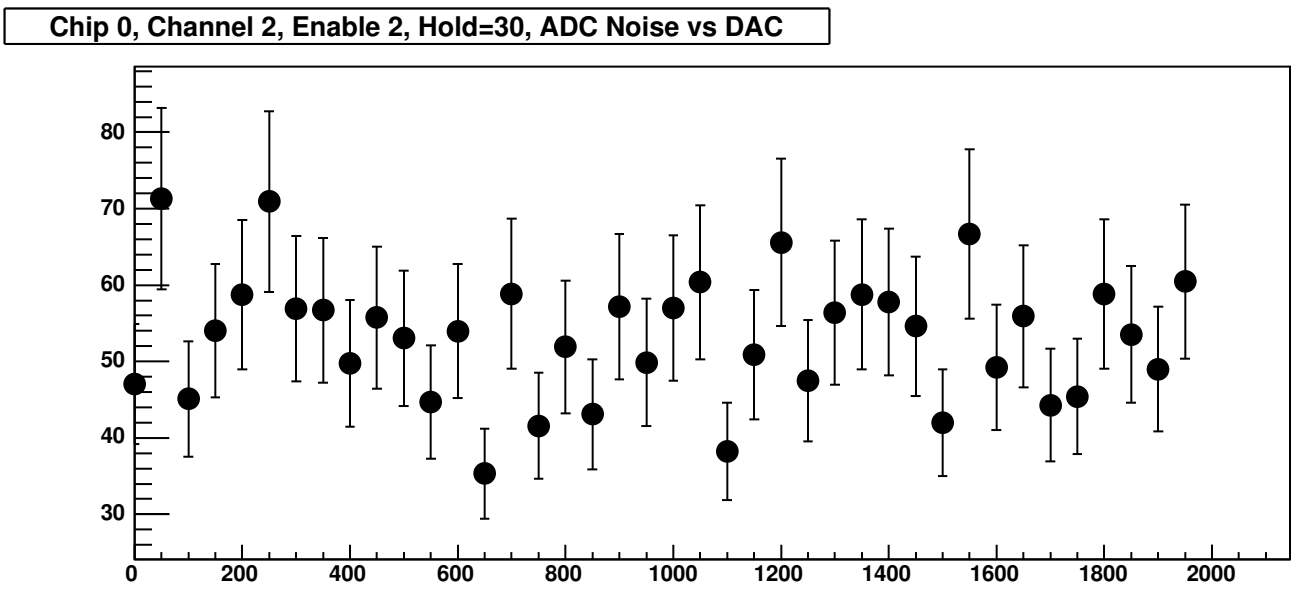

Chip 0, Channel 2, Enable 0!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

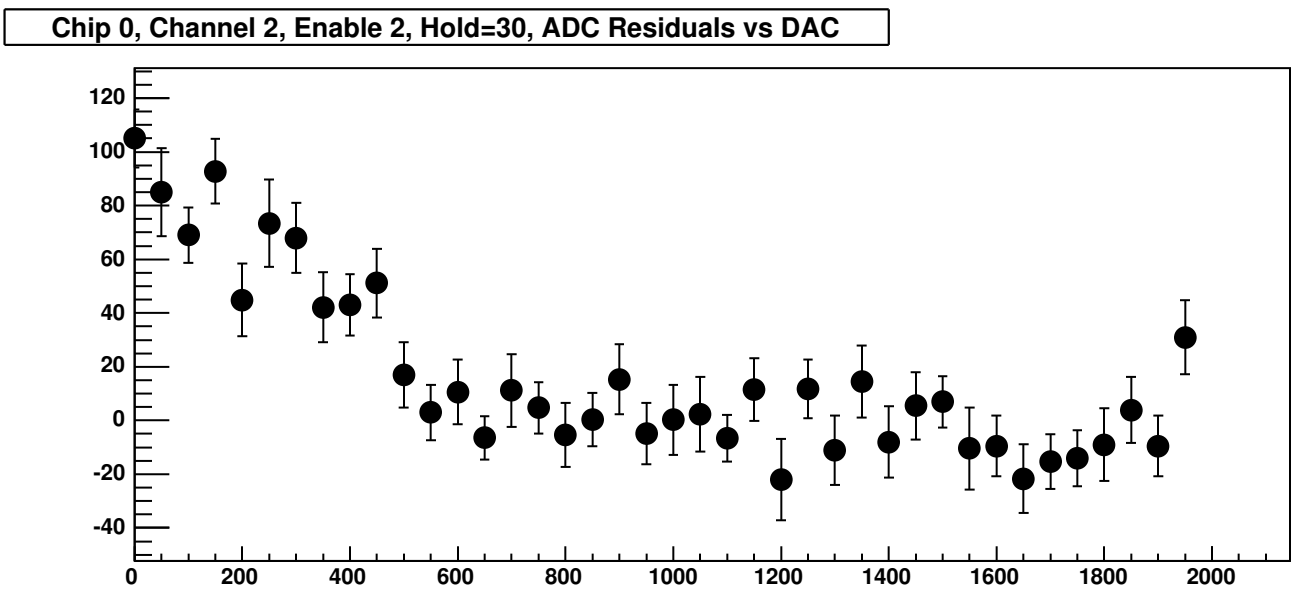

Chip 0, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 11.15 / 11
p0 -1338 ± 18.02
p1 0.1253 ± 0.01603
-1050
-1100
-1150
-1200
-1250
0
200
400
600
800
1000
1200
1400
1600
1800
2000

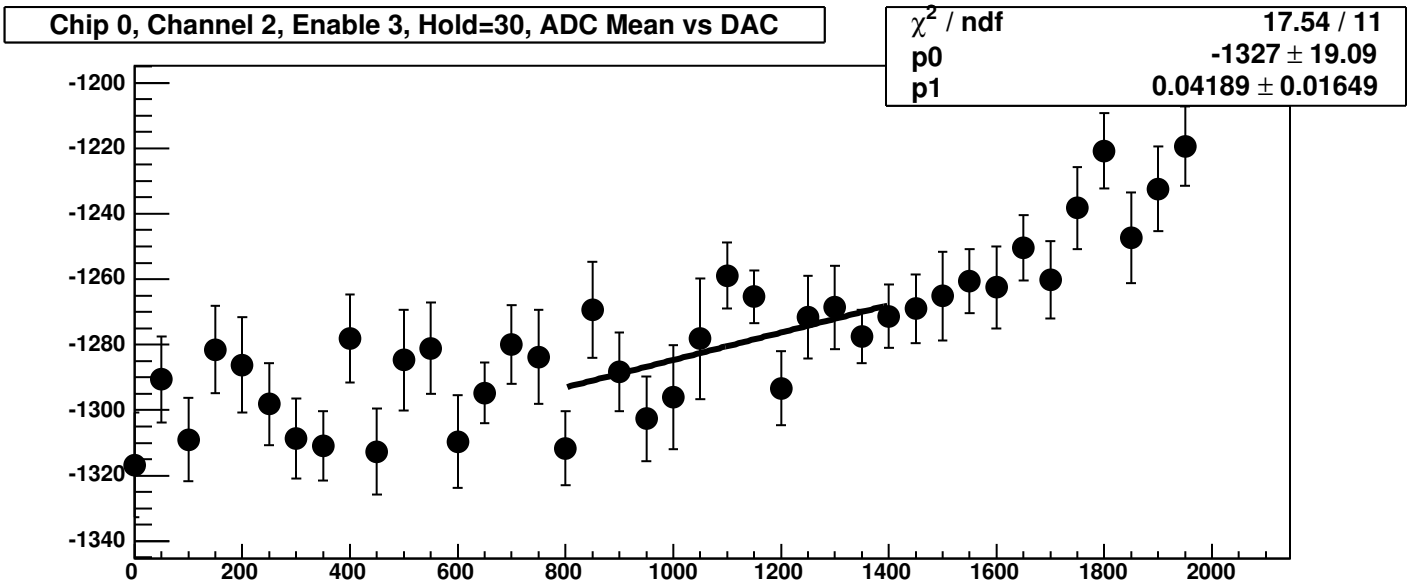

Chip 0, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

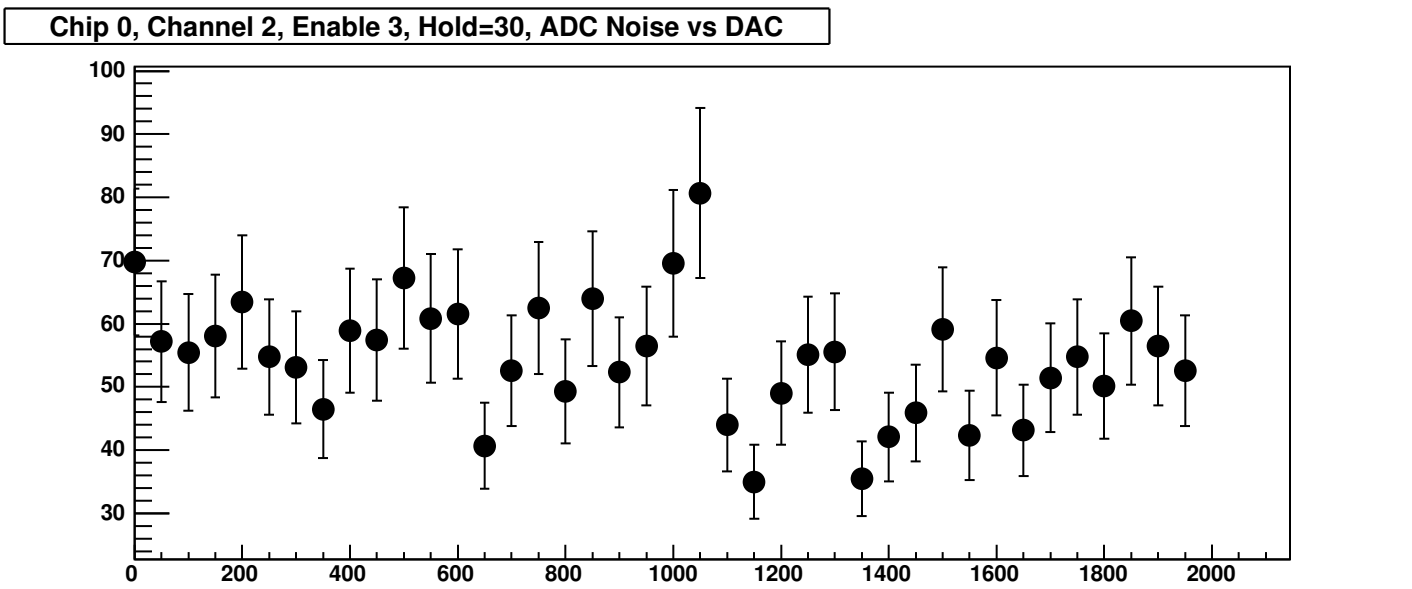

Chip 0, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

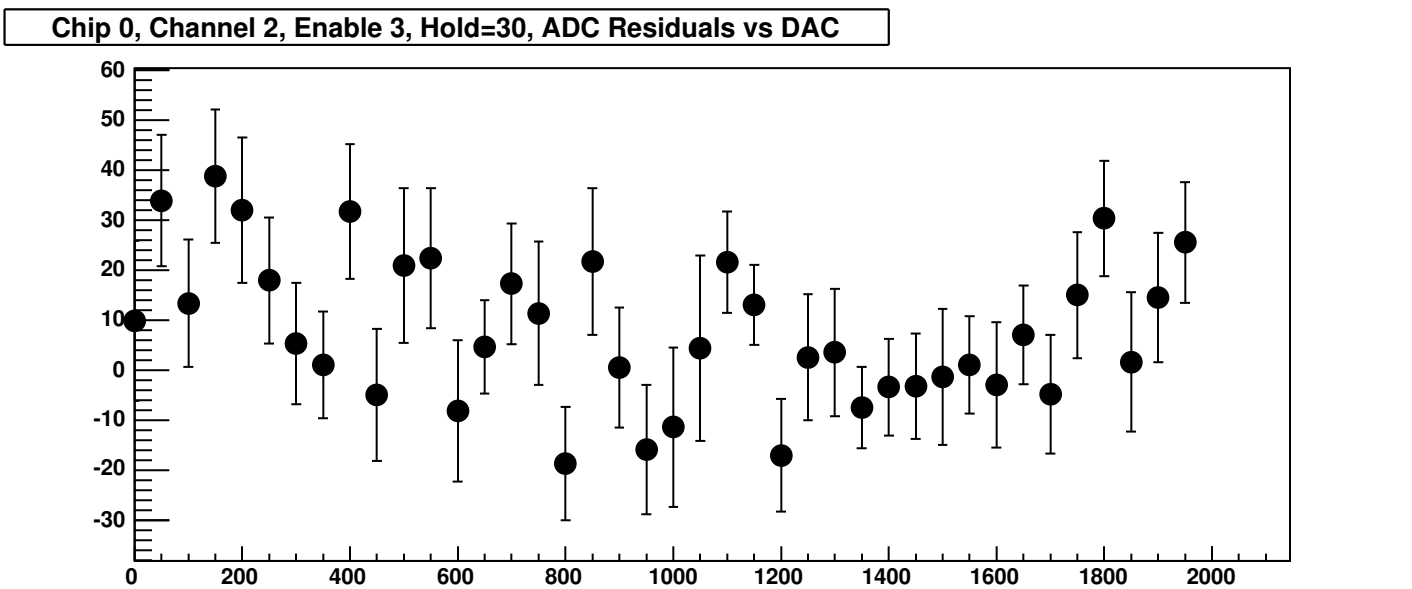

Chip 0, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 8.878 / 11
p0 -1335 ± 19.83
p1 0.1345 ± 0.01804
-1050
-1100
-1150
-1200
-1250
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

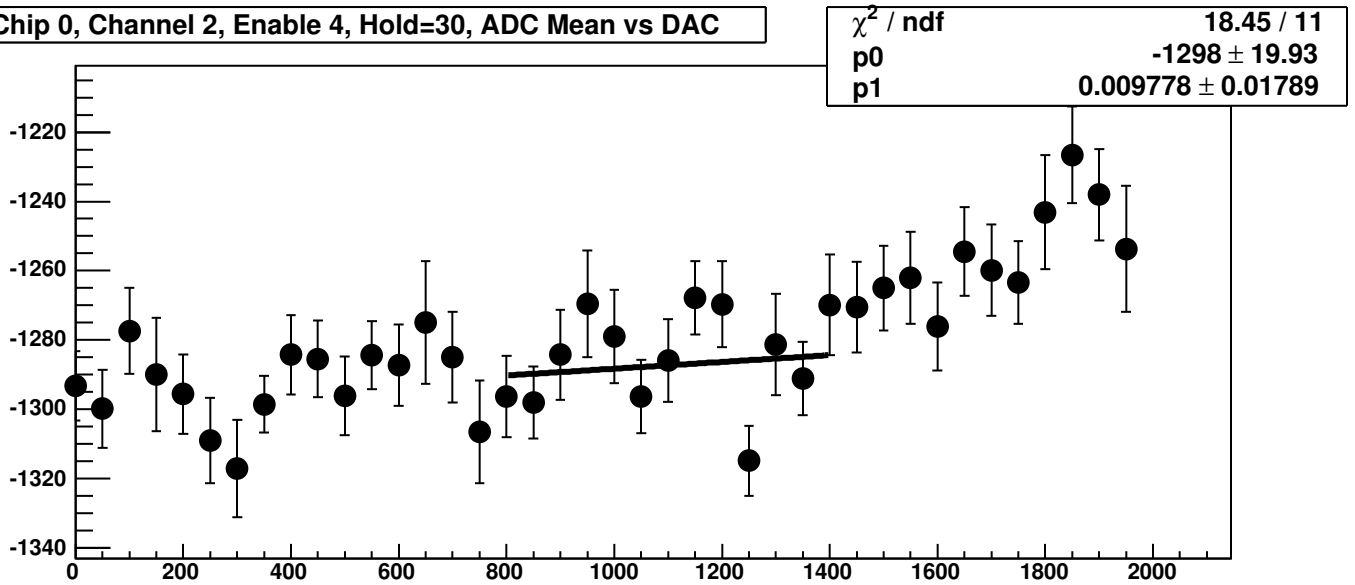

Chip 0, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 2, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 17.54 / 11
p0 -1327 ± 19.09
p1 0.04189 ± 0.01649
-1200
-1220
-1240
-1260
-1280
-1300
-1320
-1340
0
200
400
600
800
1000
1200
1400
1600
1800
2000

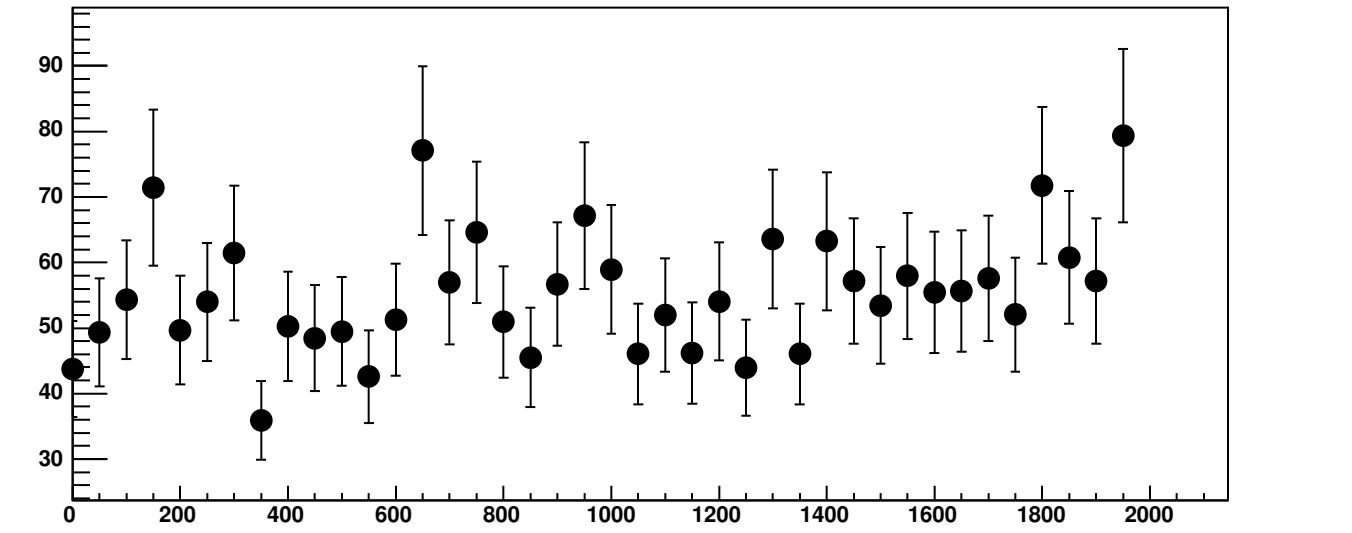

Chip 0, Channel 2, Enable 3, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 2, Enable 3, Hold=30, ADC Residuals vs DAC
60
50
40
30
20
10
0
-10
-20
-30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

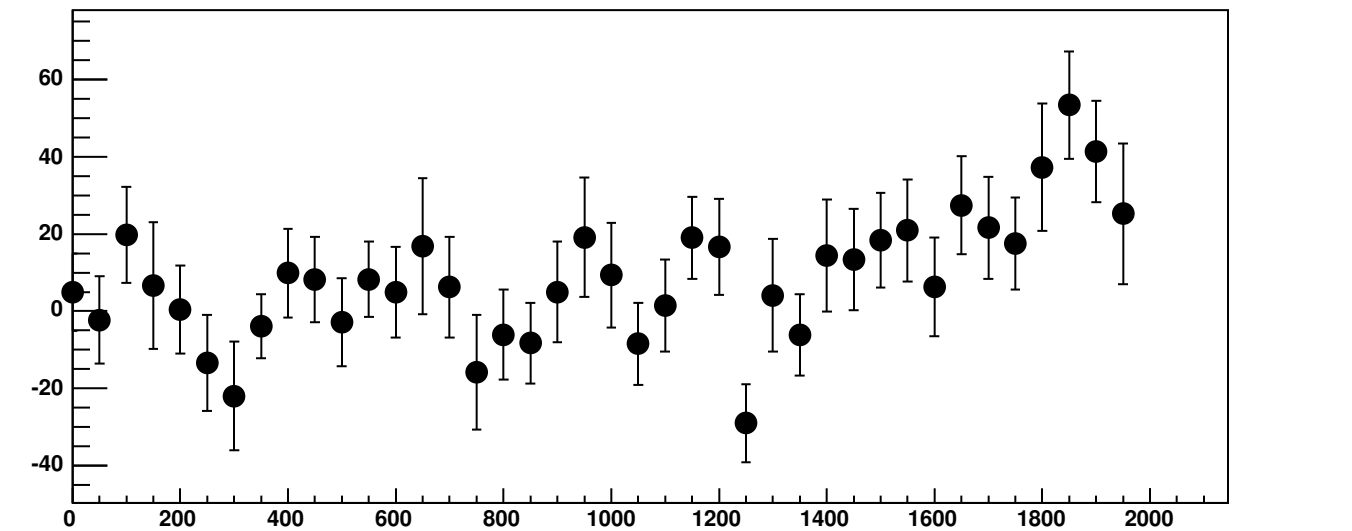
Chip 0, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC

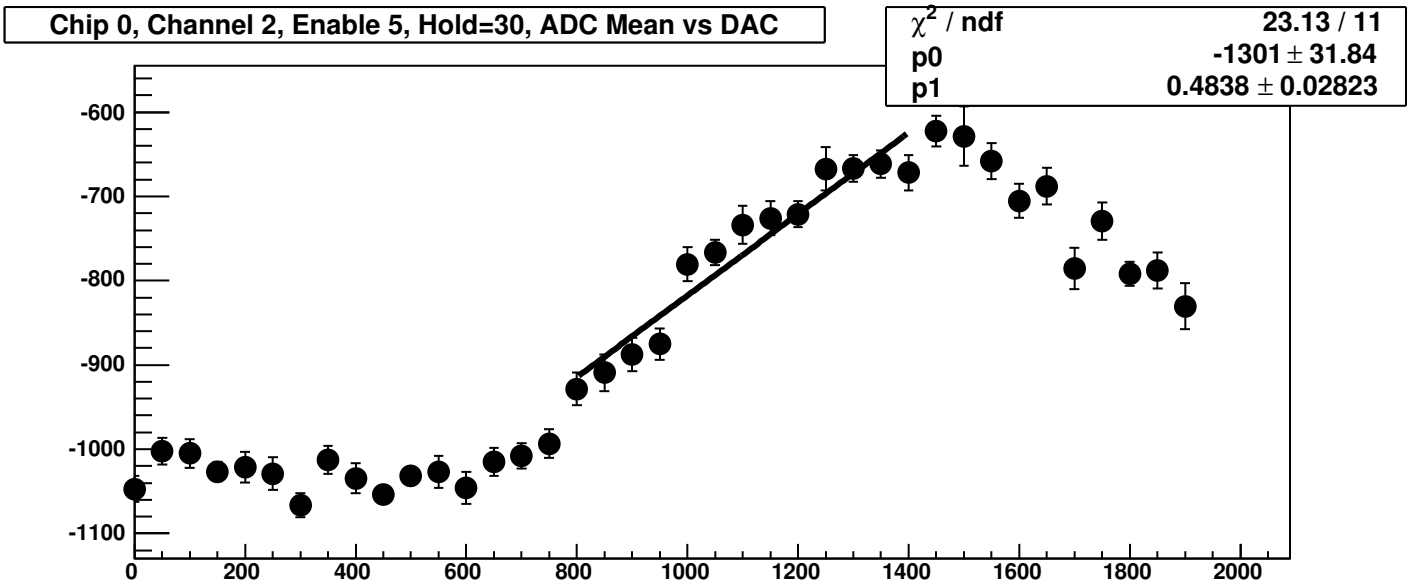


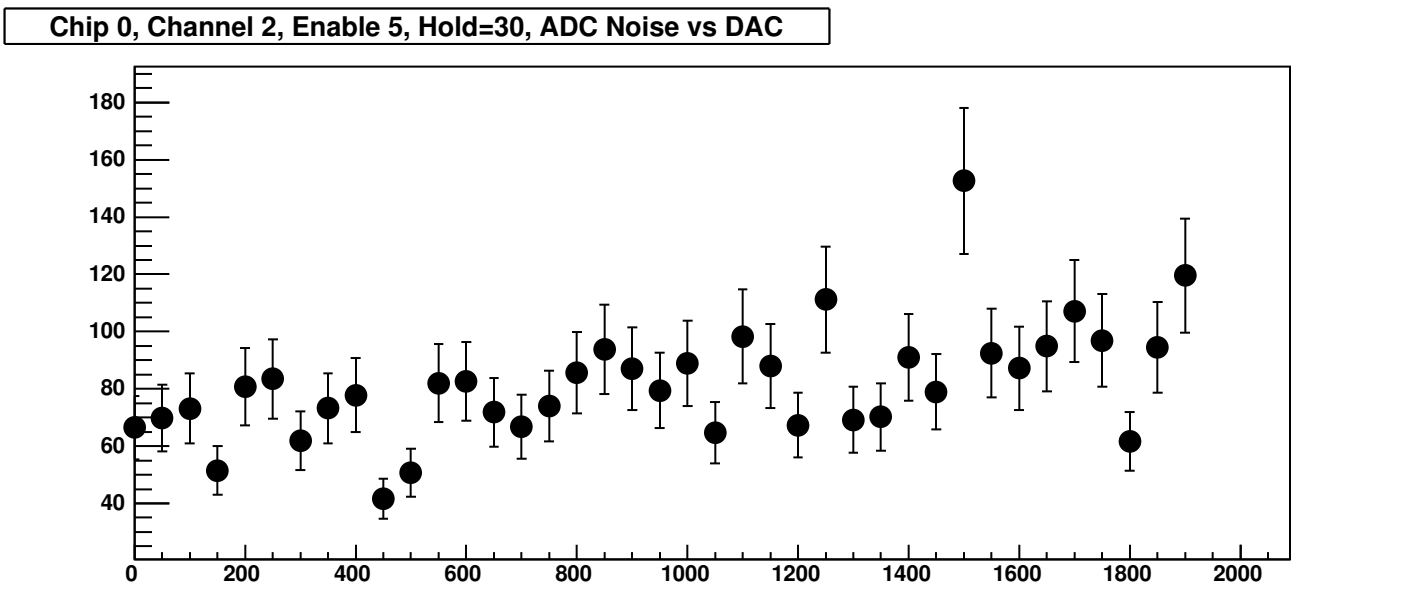
Chip 0, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC

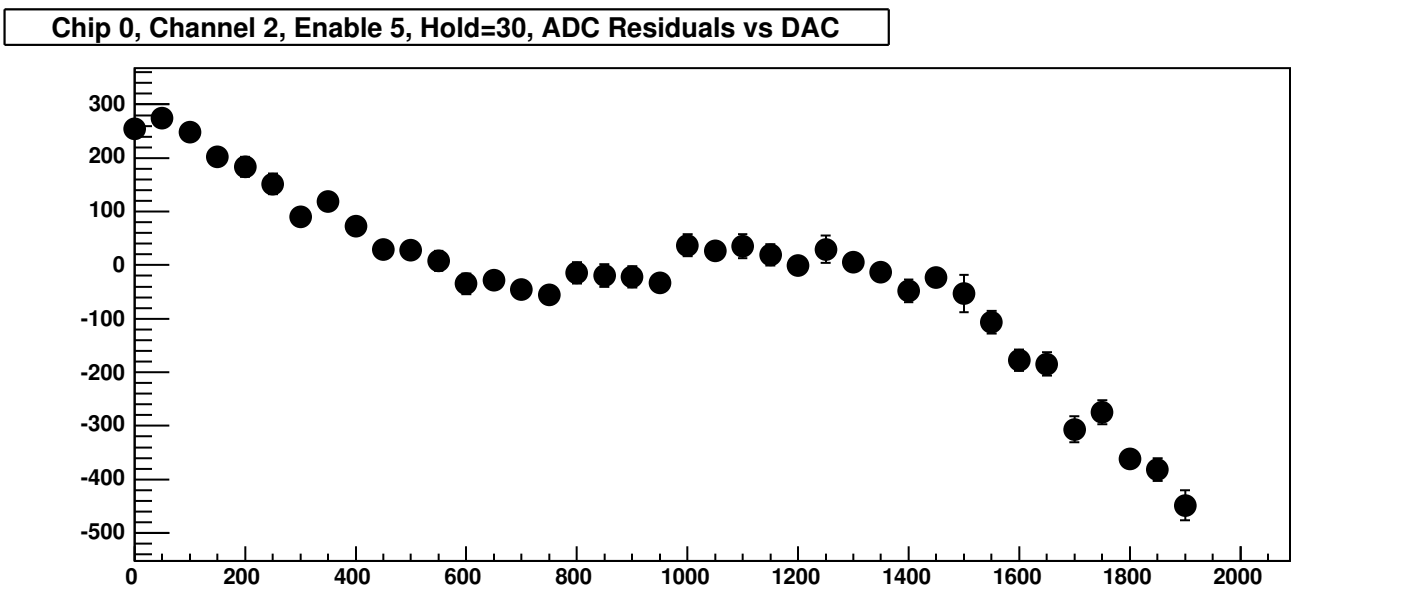


Chip 0, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC

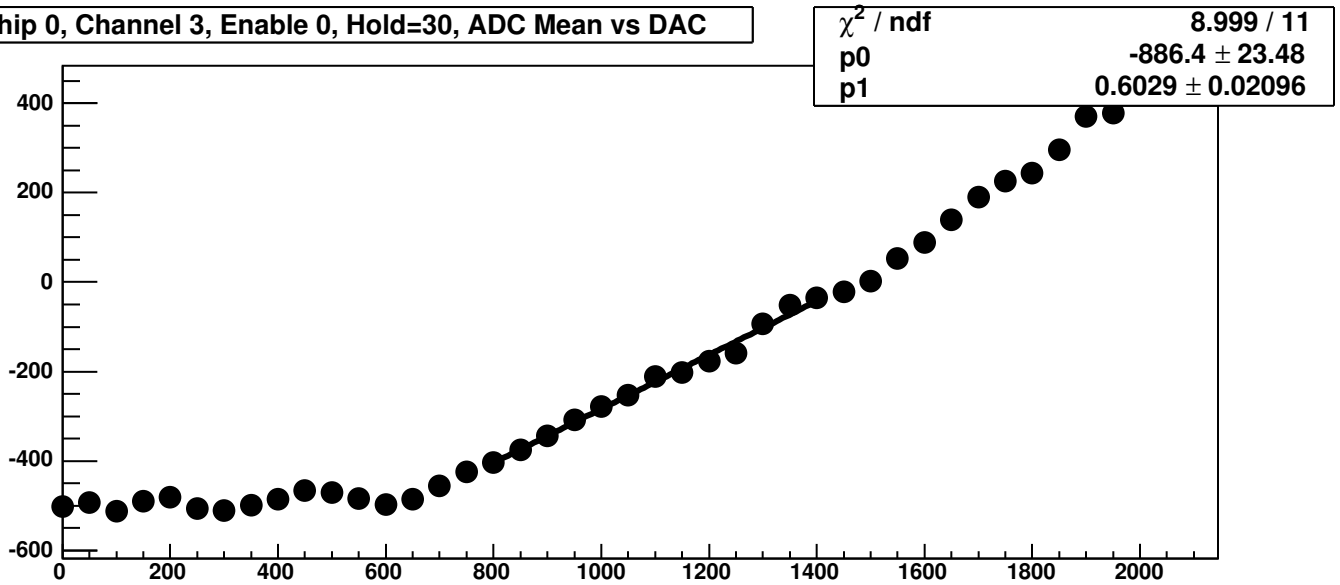



Chip 0, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 23.13 / 11
p0 -1301 ± 31.84
p1 0.4838 ± 0.02823
-600
-700
-800
-900
-1000
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

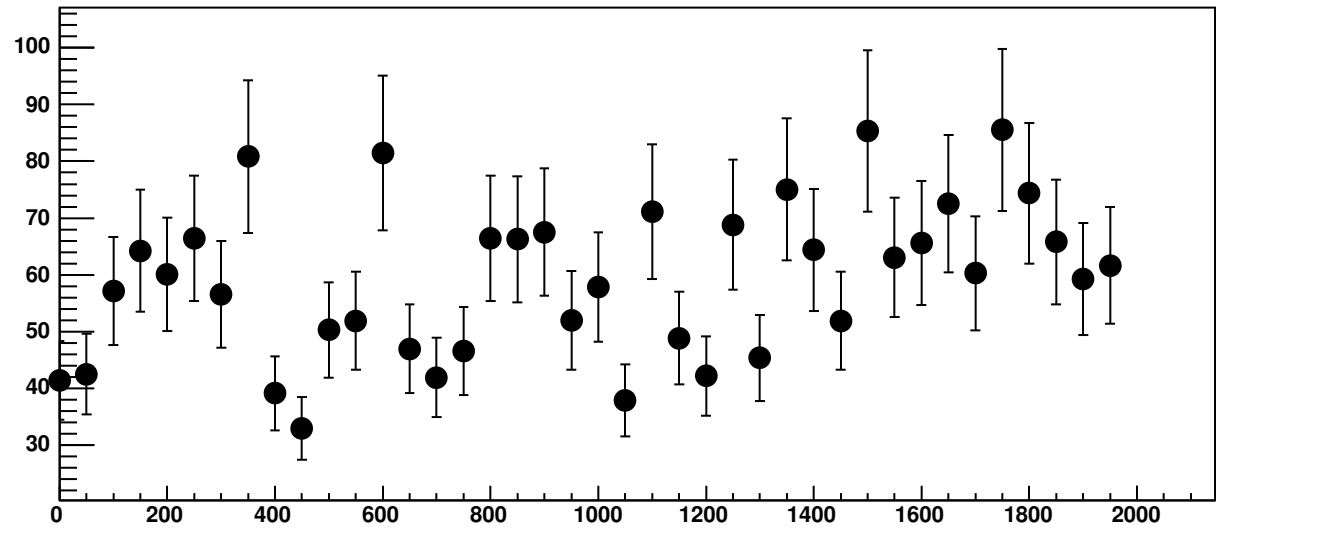

Chip 0, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC
300
200
100
0
-100
-200
-300
-400
-500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

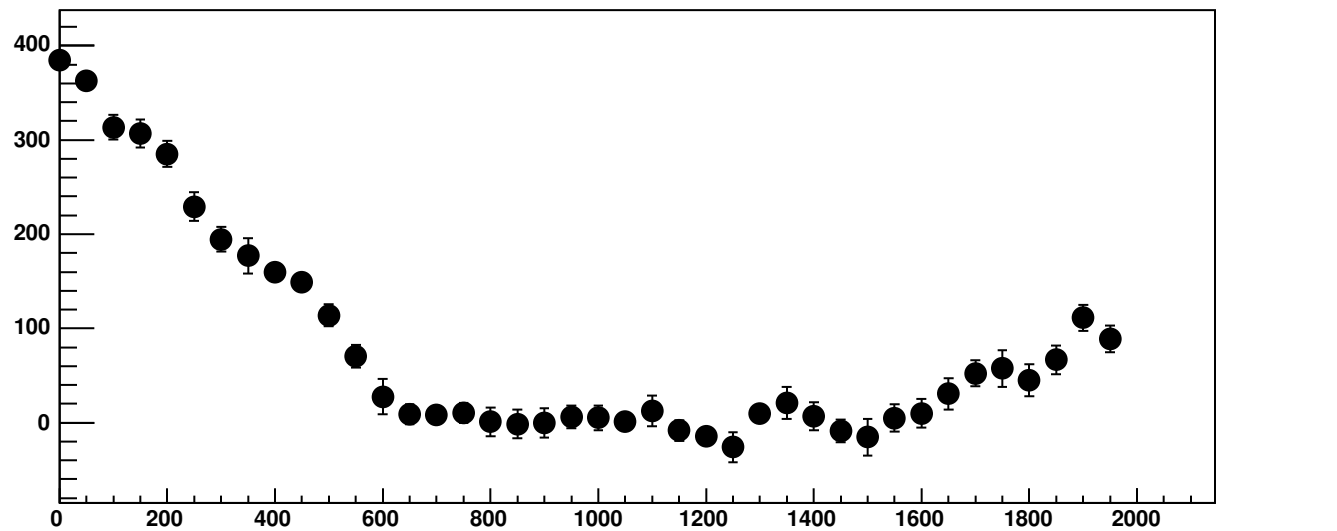
Chip 0, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC

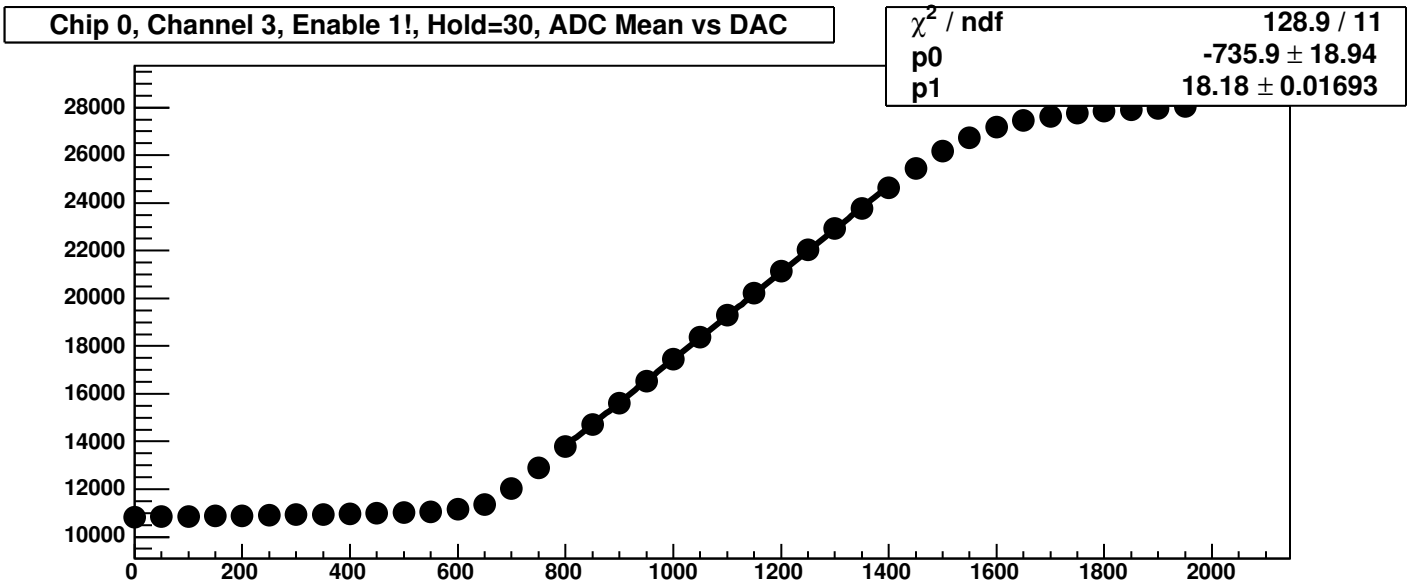


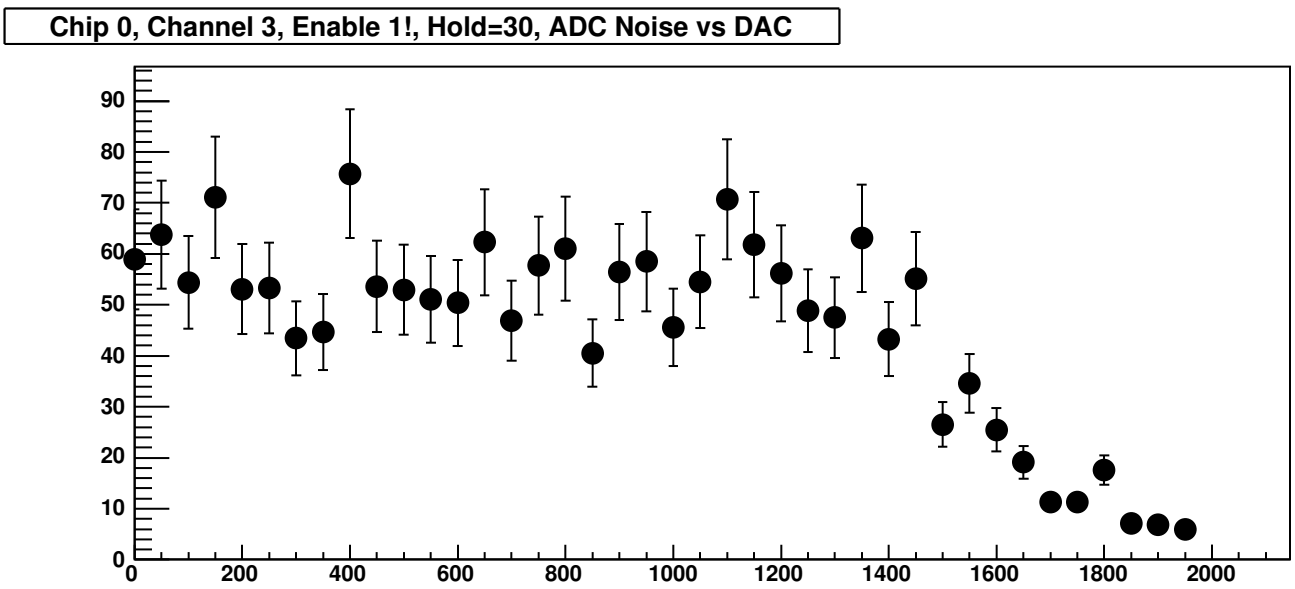
Chip 0, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC

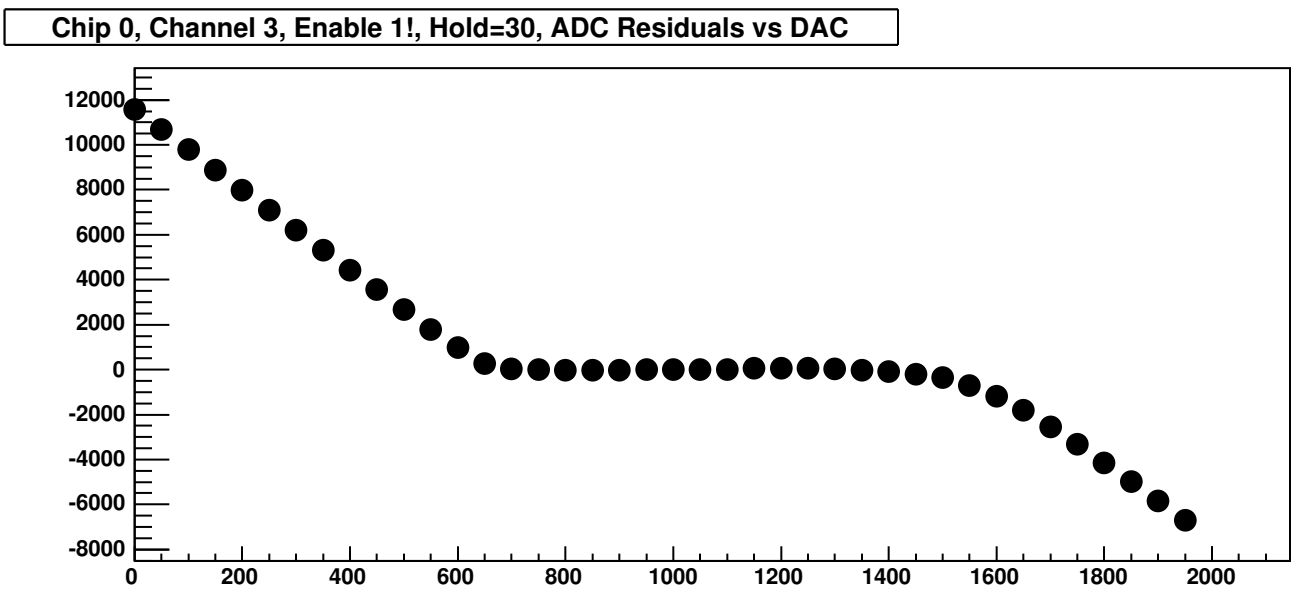


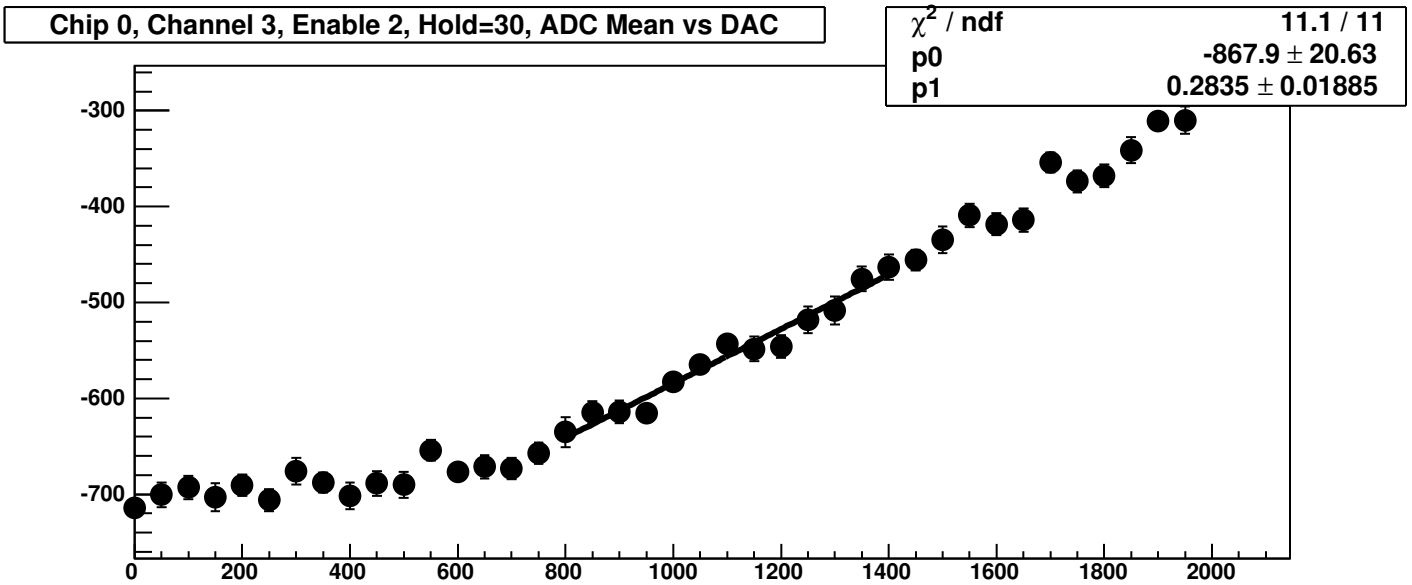
Chip 0, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC

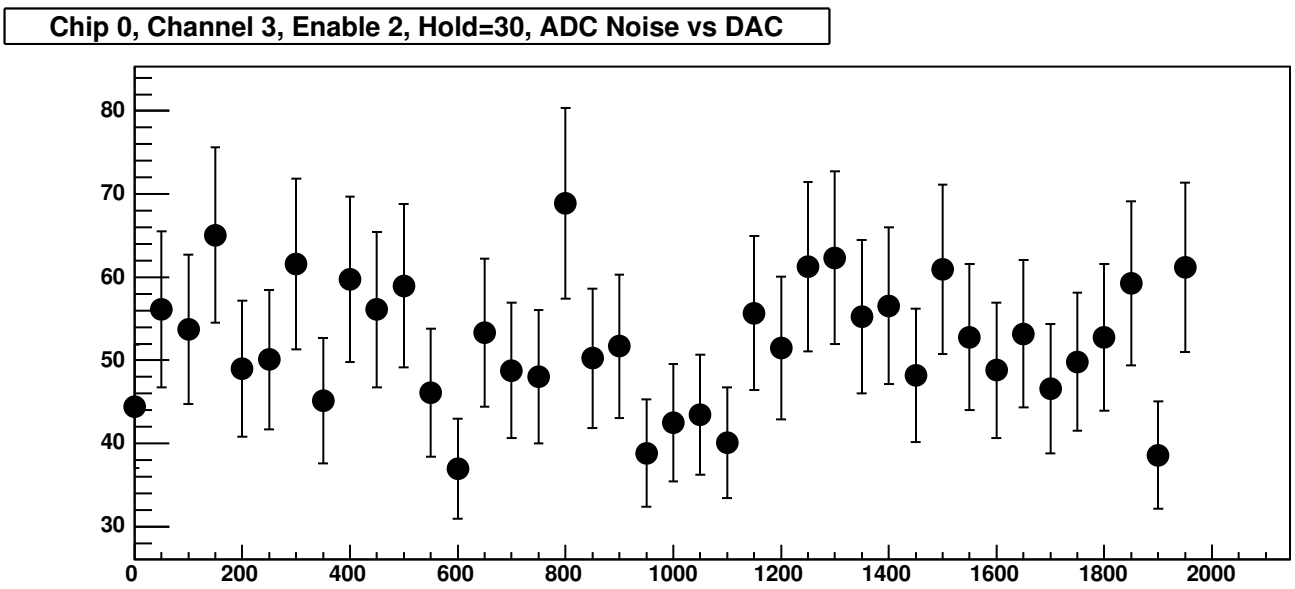


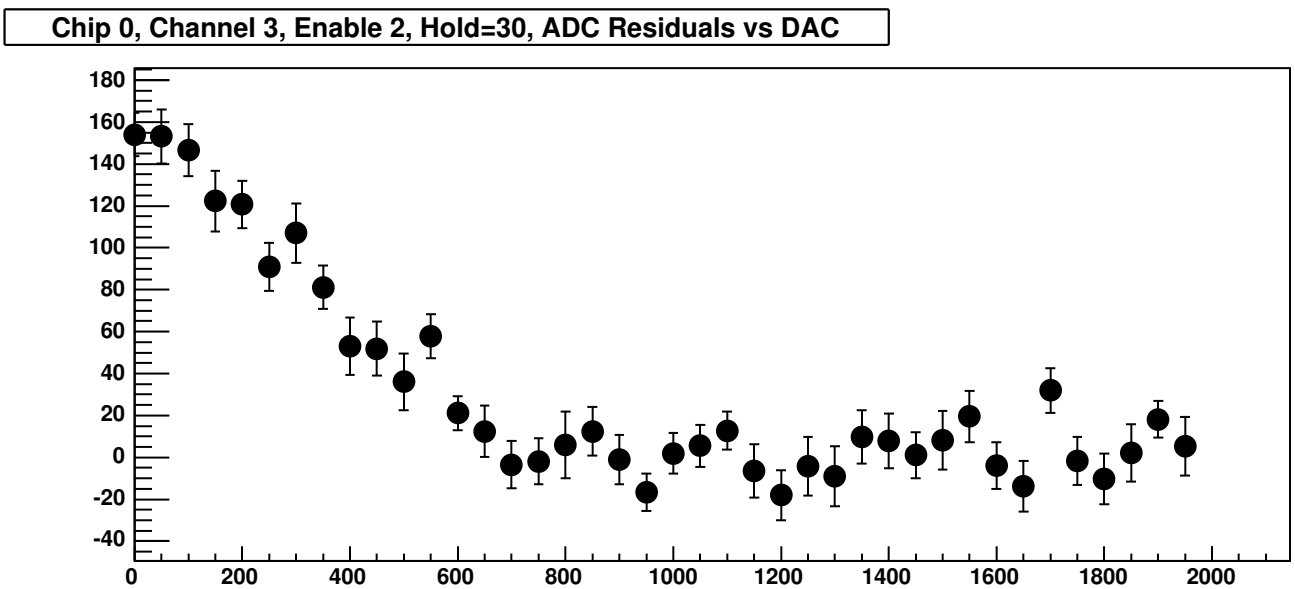

Chip 0, Channel 3, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 128.9 / 11
p0 -735.9 ± 18.94
p1 18.18 ± 0.01693
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 3, Enable 1!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

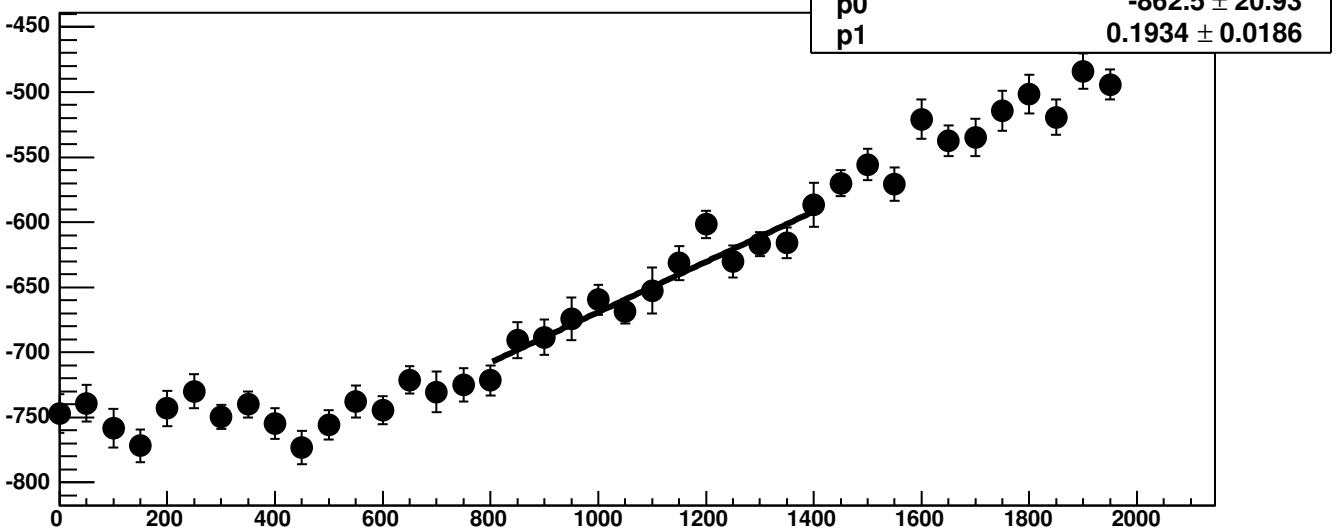

Chip 0, Channel 3, Enable 1!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 11.1 / 11
p0 -867.9 ± 20.63
p1 0.2835 ± 0.01885
-300
-400
-500
-600
-700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

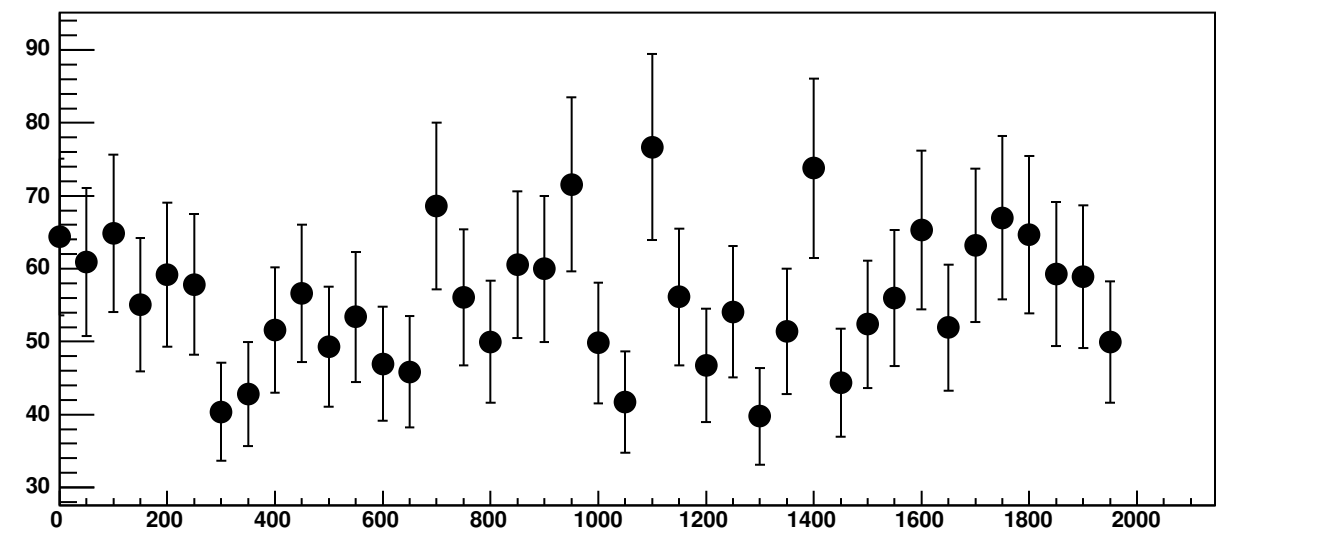

Chip 0, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC
180
160
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

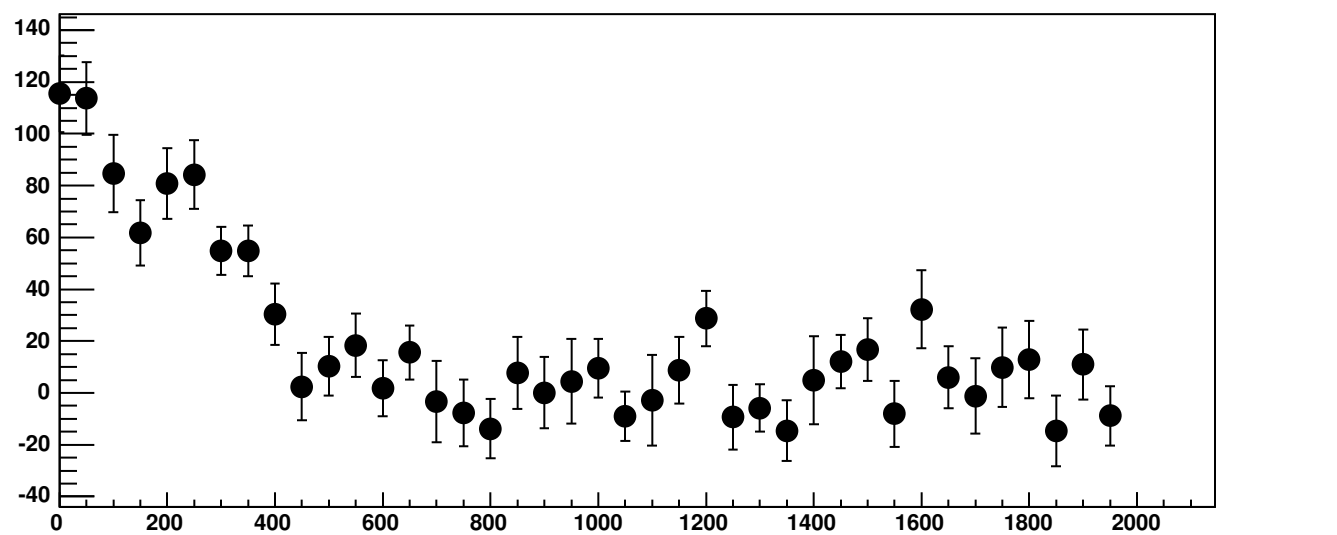
Chip 0, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC

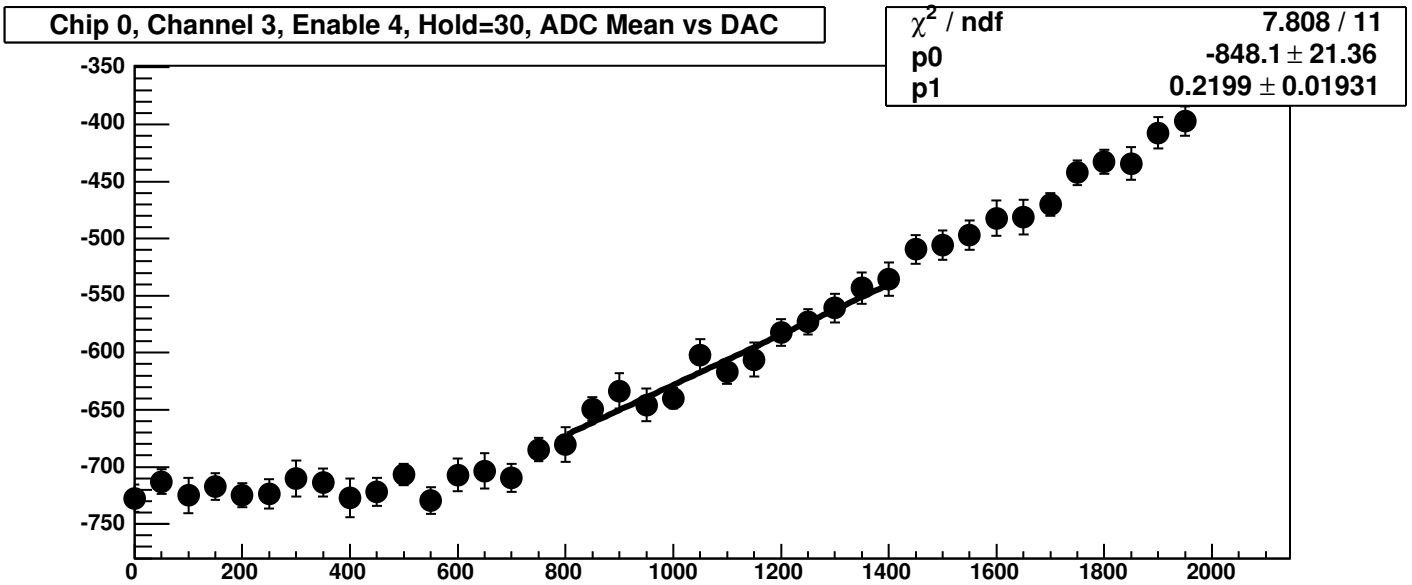


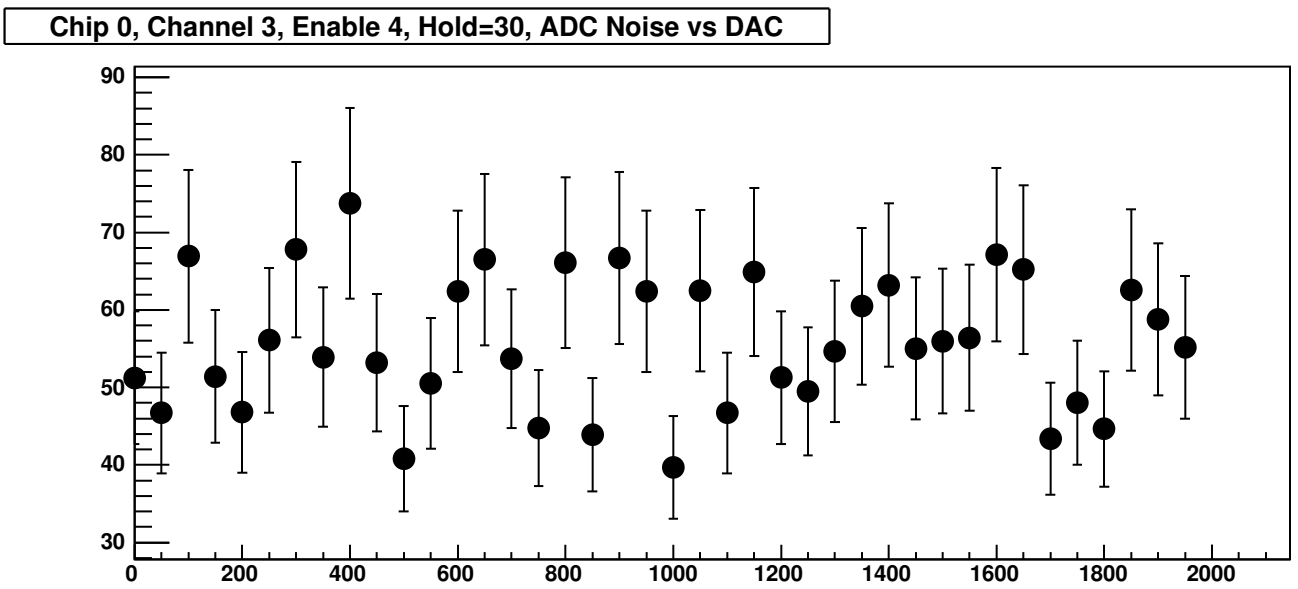
Chip 0, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC

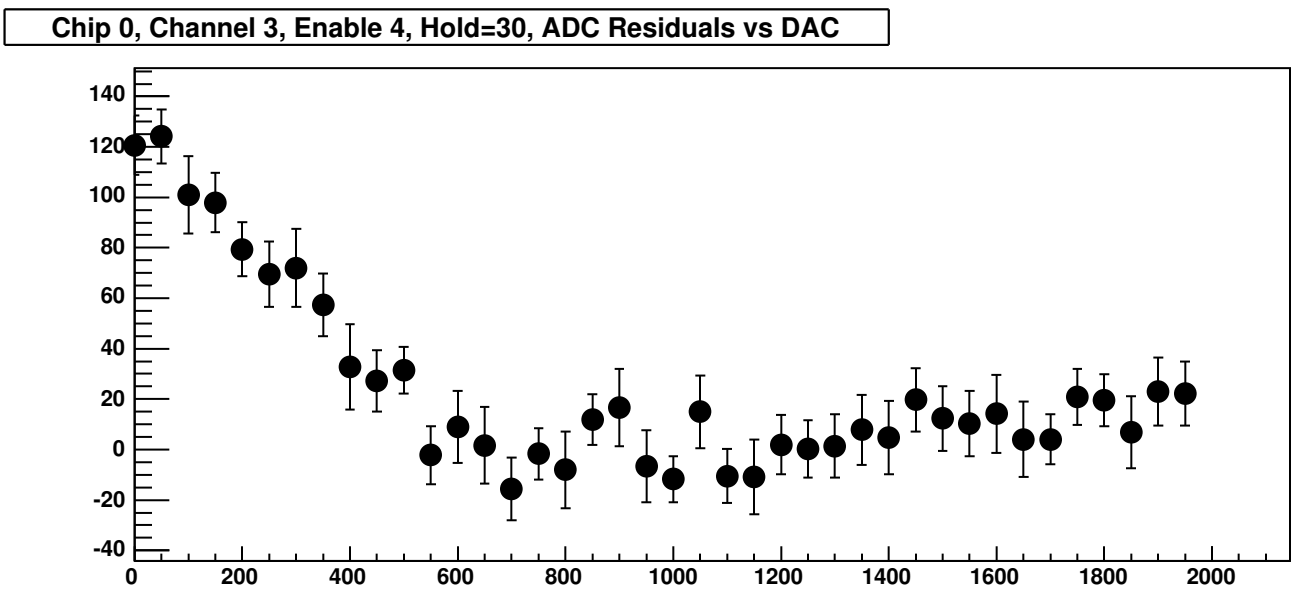


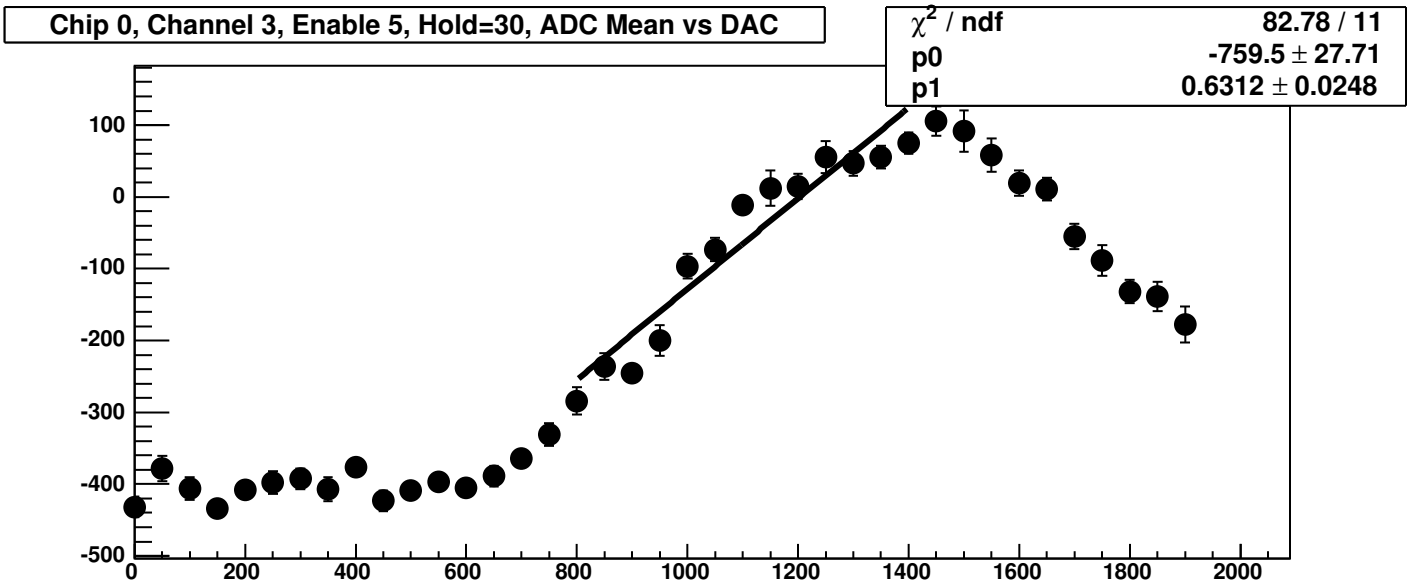
Chip 0, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC

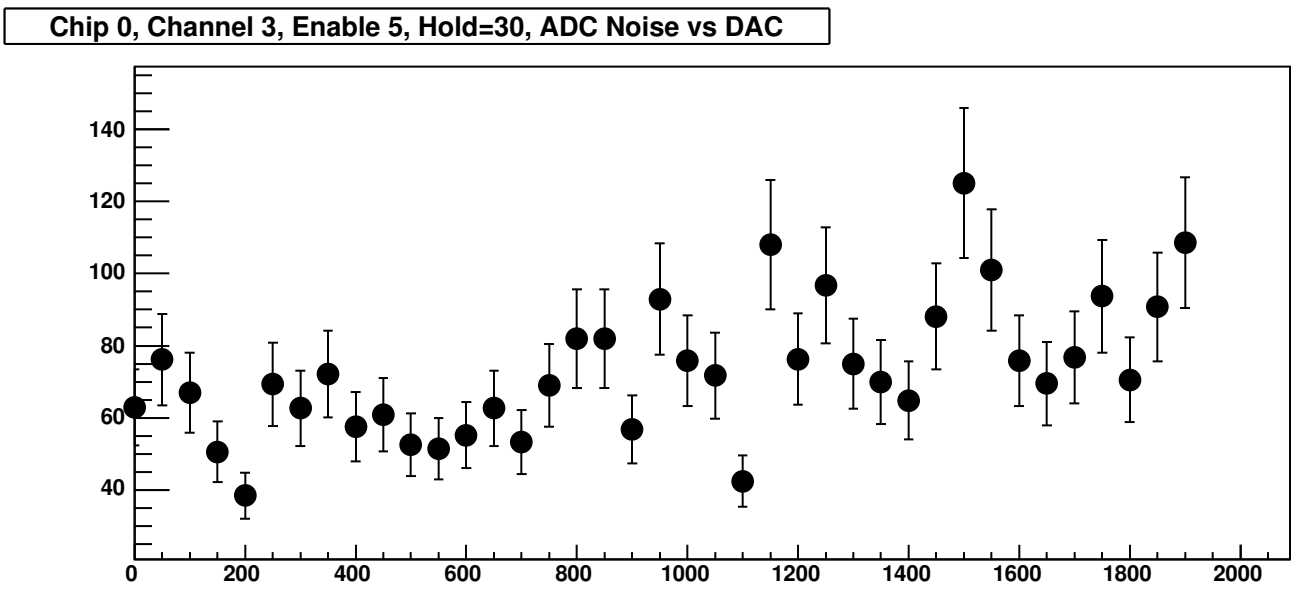


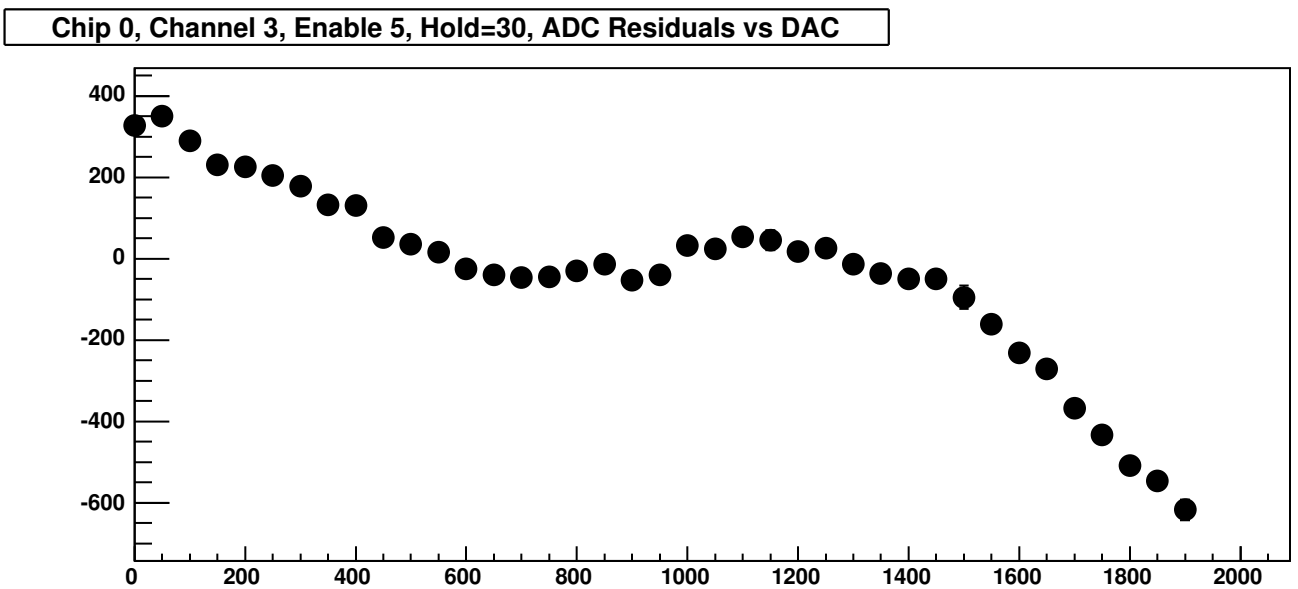

Chip 0, Channel 3, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7.808 / 11
p0 -848.1 ± 21.36
p1 0.2199 ± 0.01931
-350
-400
-450
-500
-550
-600
-650
-700
-750
0
200
400
600
800
1000
1200
1400
1600
1800
2000

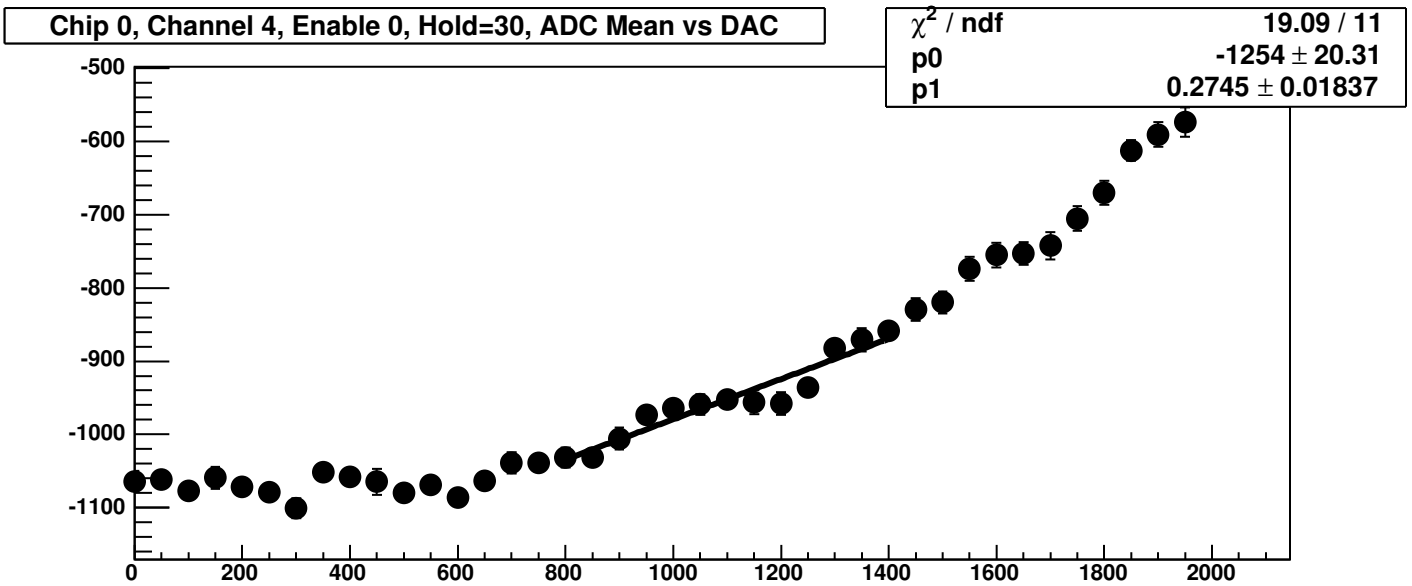

Chip 0, Channel 3, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

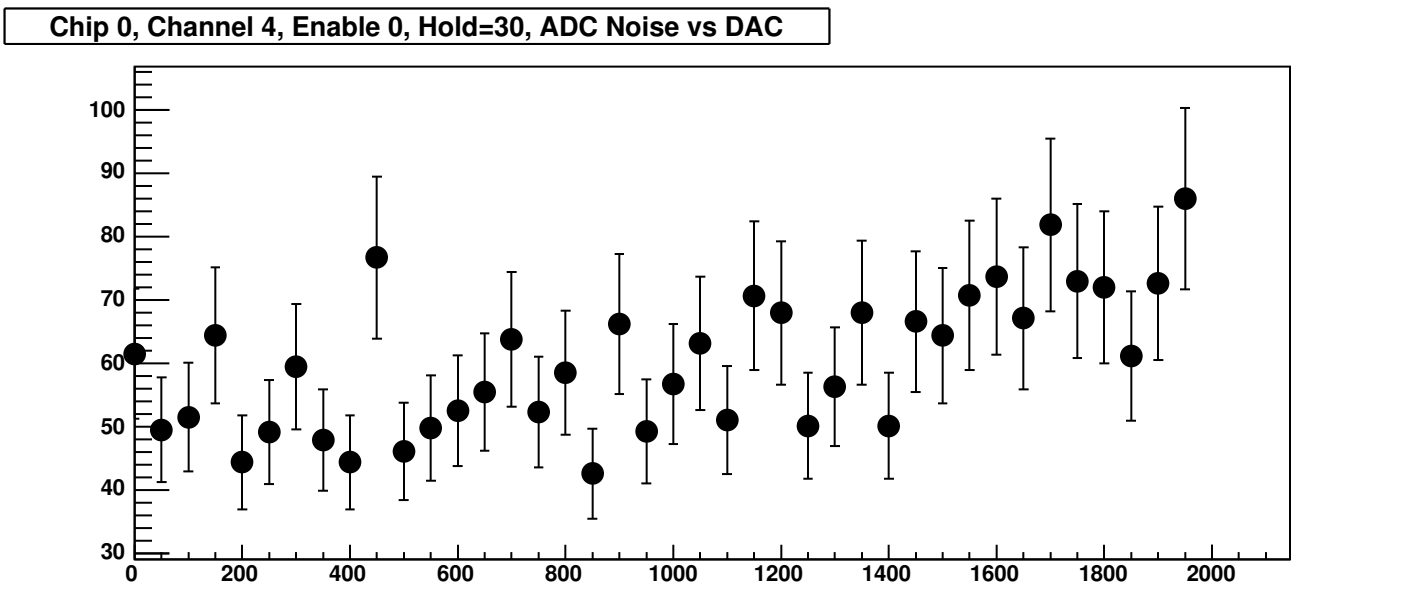

Chip 0, Channel 3, Enable 4, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

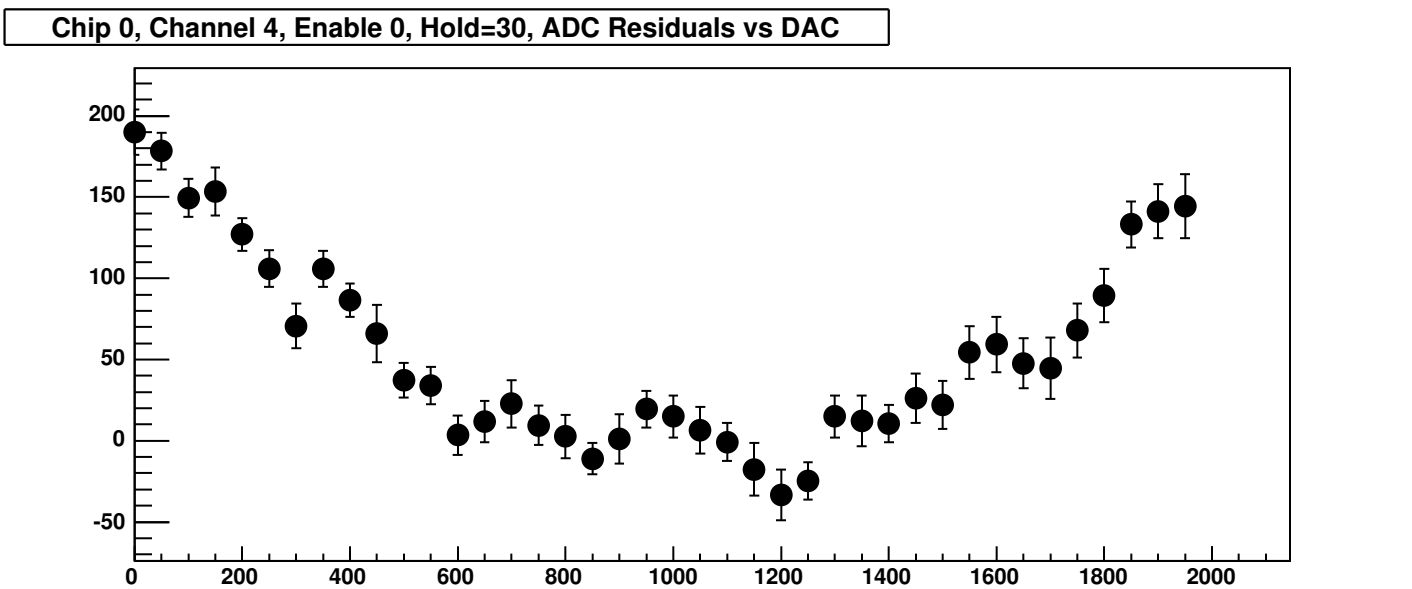

Chip 0, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 82.78 / 11
p0 -759.5 ± 27.71
p1 0.6312 ± 0.0248
100
0
-100
-200
-300
-400
-500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

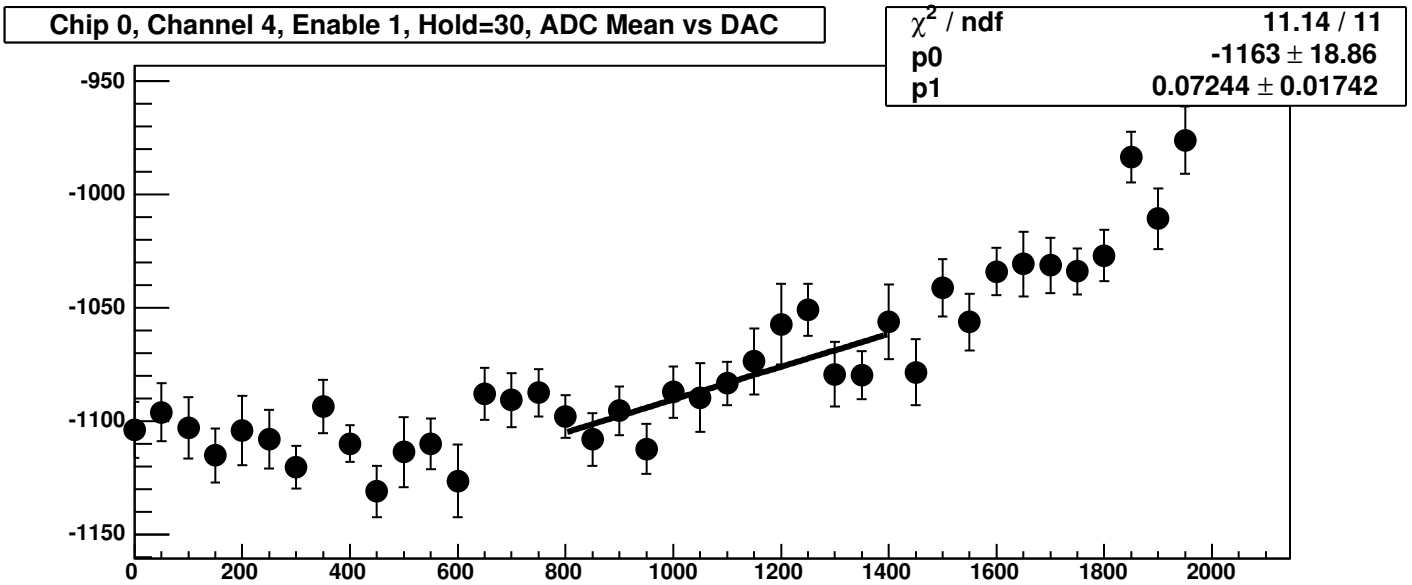

Chip 0, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

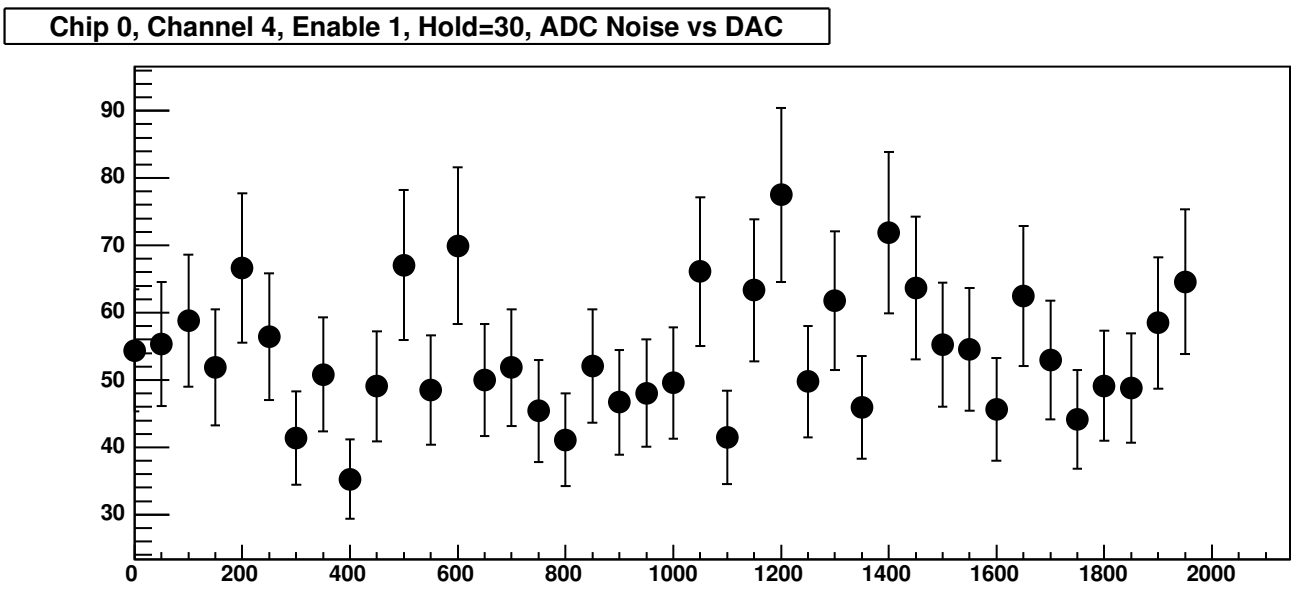

Chip 0, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

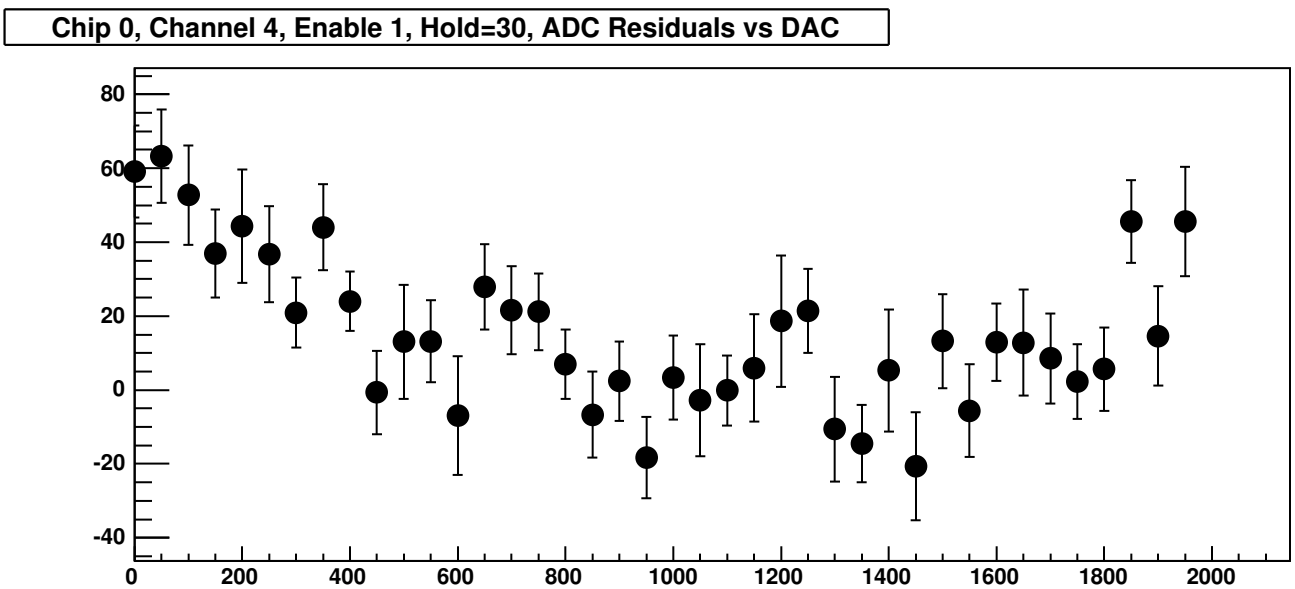

Chip 0, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 19.09 / 11
p0 -1254 ± 20.31
p1 0.2745 ± 0.01837
-500
-600
-700
-800
-900
-1000
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

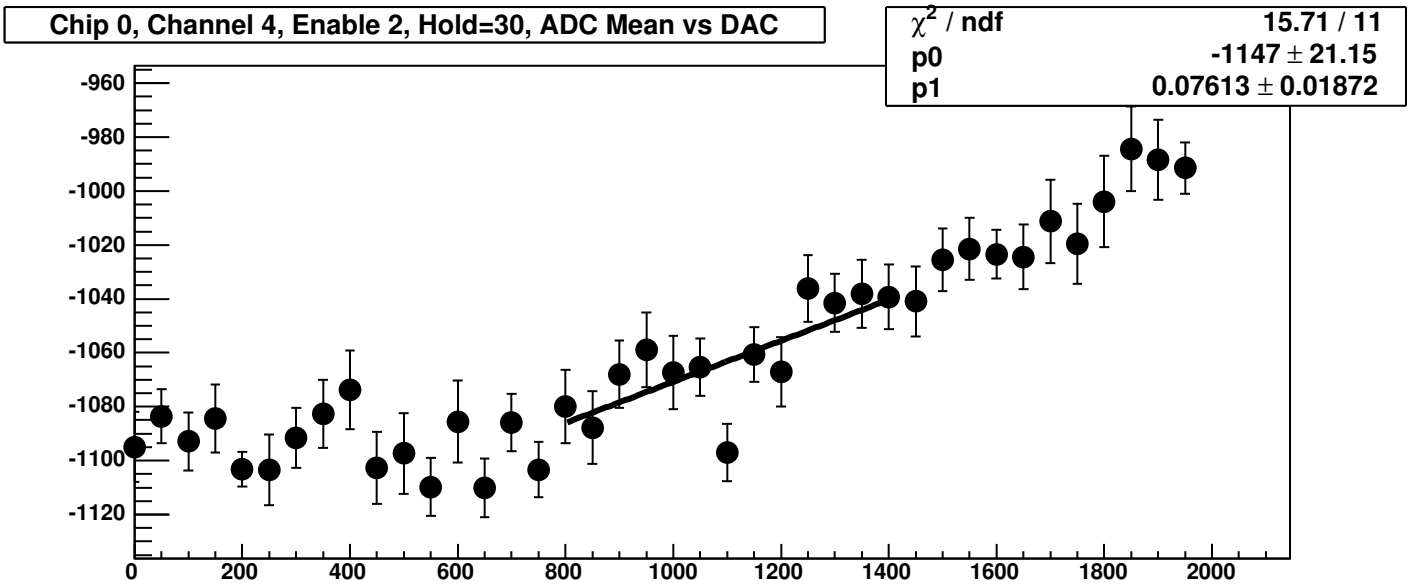

Chip 0, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

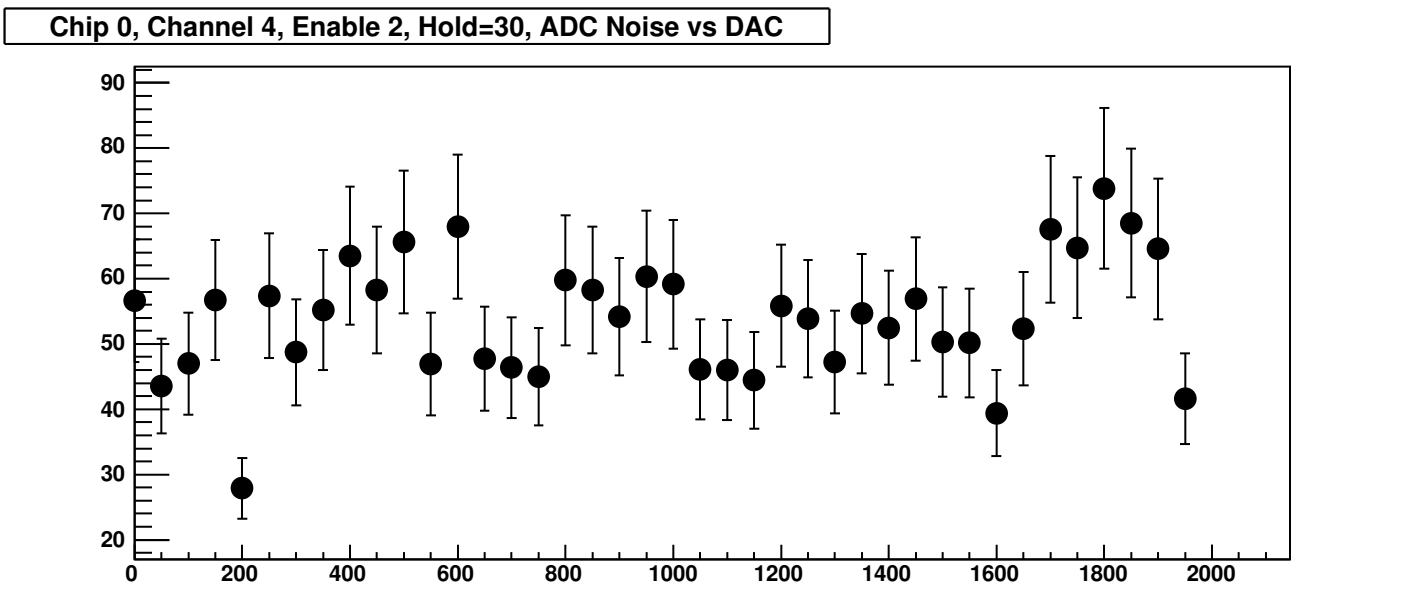

Chip 0, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

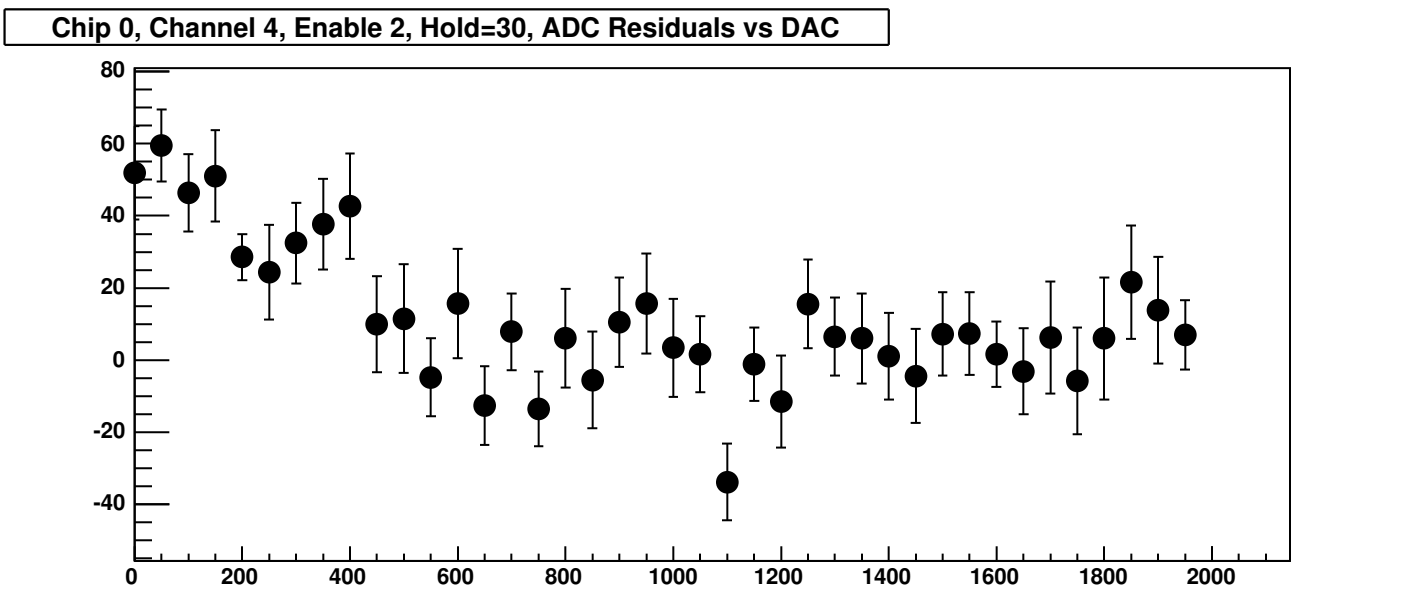

Chip 0, Channel 4, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 11.14 / 11
p0 -1163 ± 18.86
p1 0.07244 ± 0.01742
-950
-1000
-1050
-1100
-1150
0
200
400
600
800
1000
1200
1400
1600
1800
2000

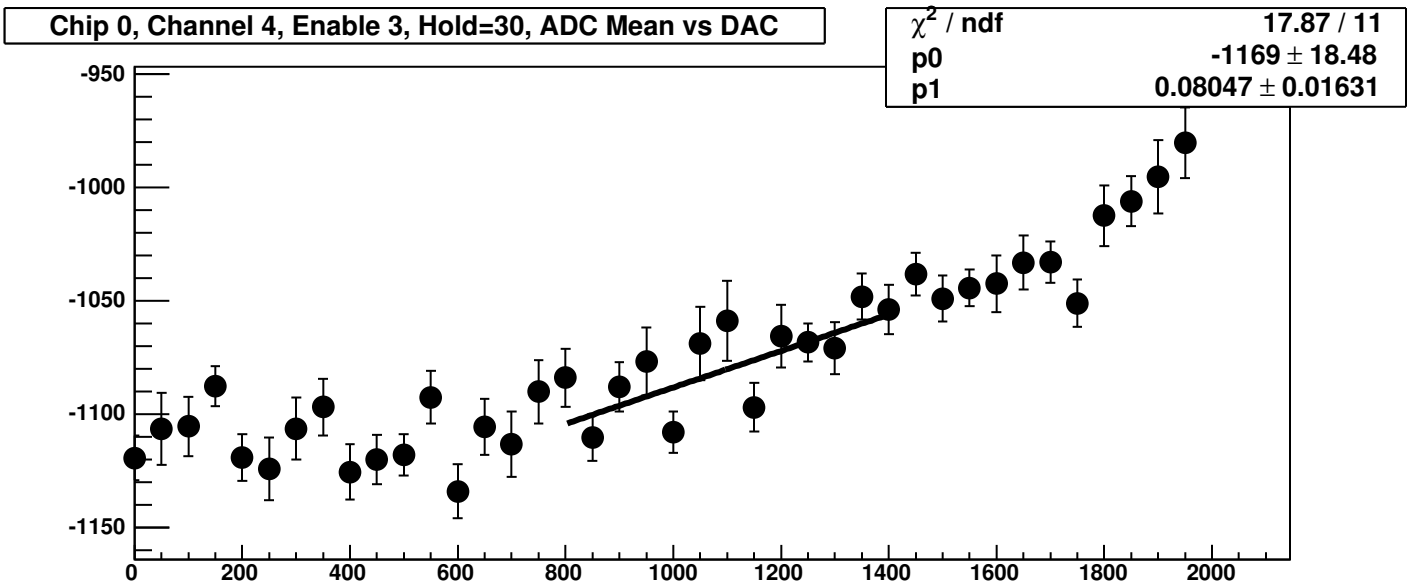

Chip 0, Channel 4, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

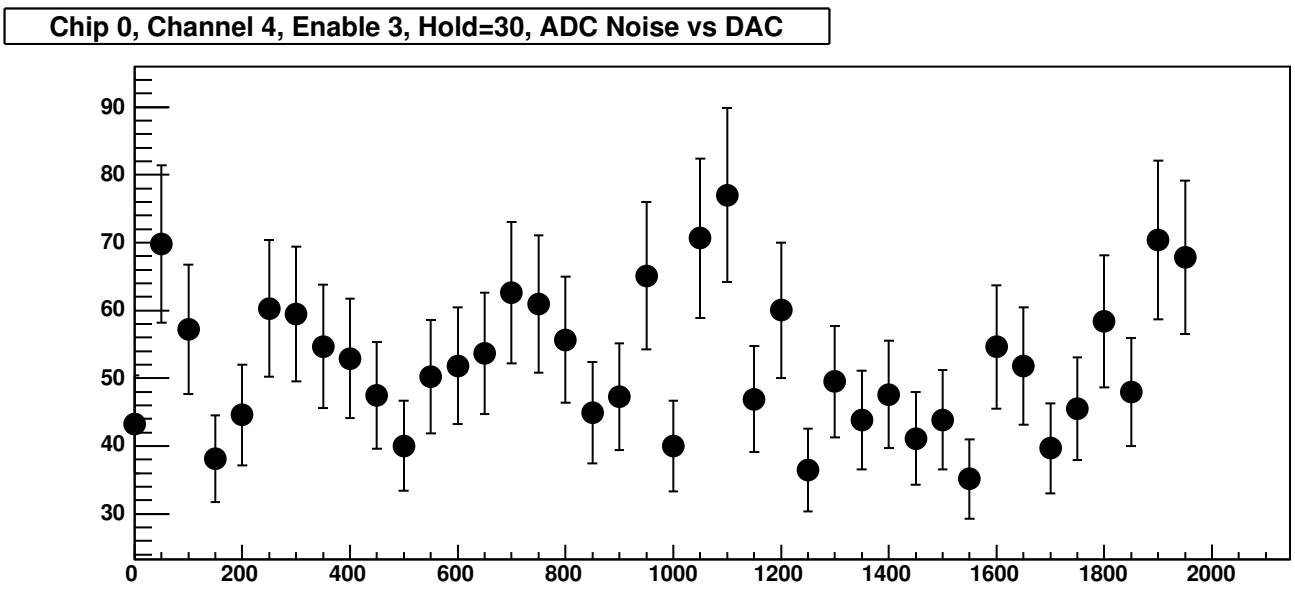

Chip 0, Channel 4, Enable 1, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

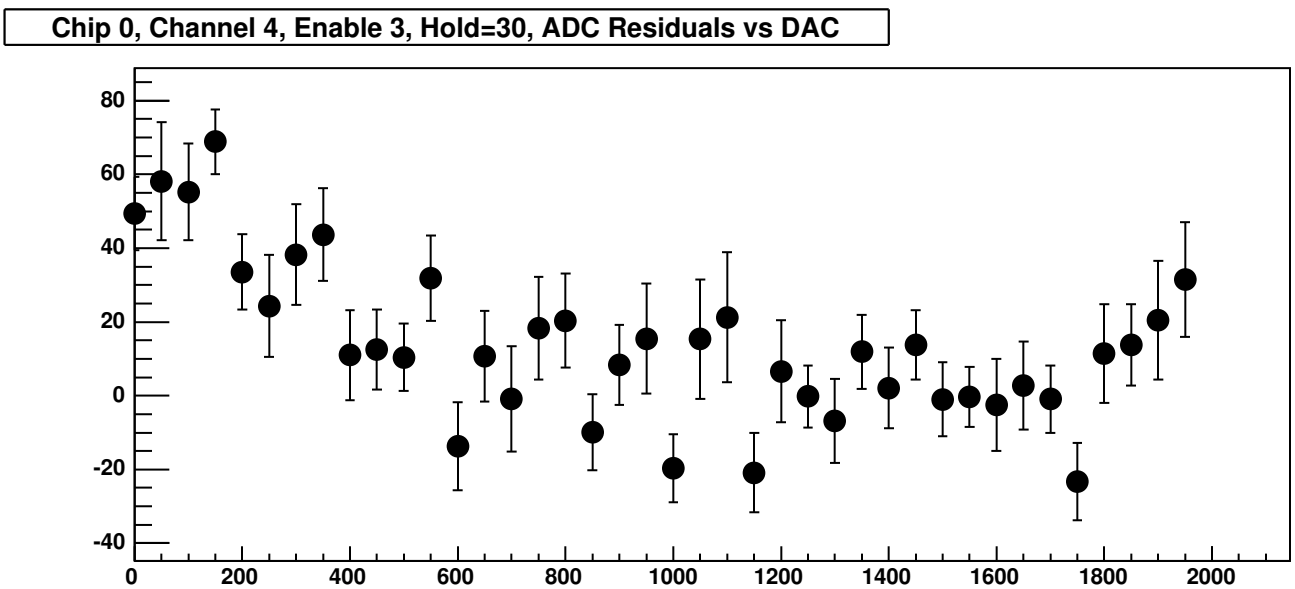

Chip 0, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 15.71 / 11
p0 -1147 ± 21.15
p1 0.07613 ± 0.01872
-960
-980
-1000
-1020
-1040
-1060
-1080
-1100
-1120
0
200
400
600
800
1000
1200
1400
1600
1800
2000

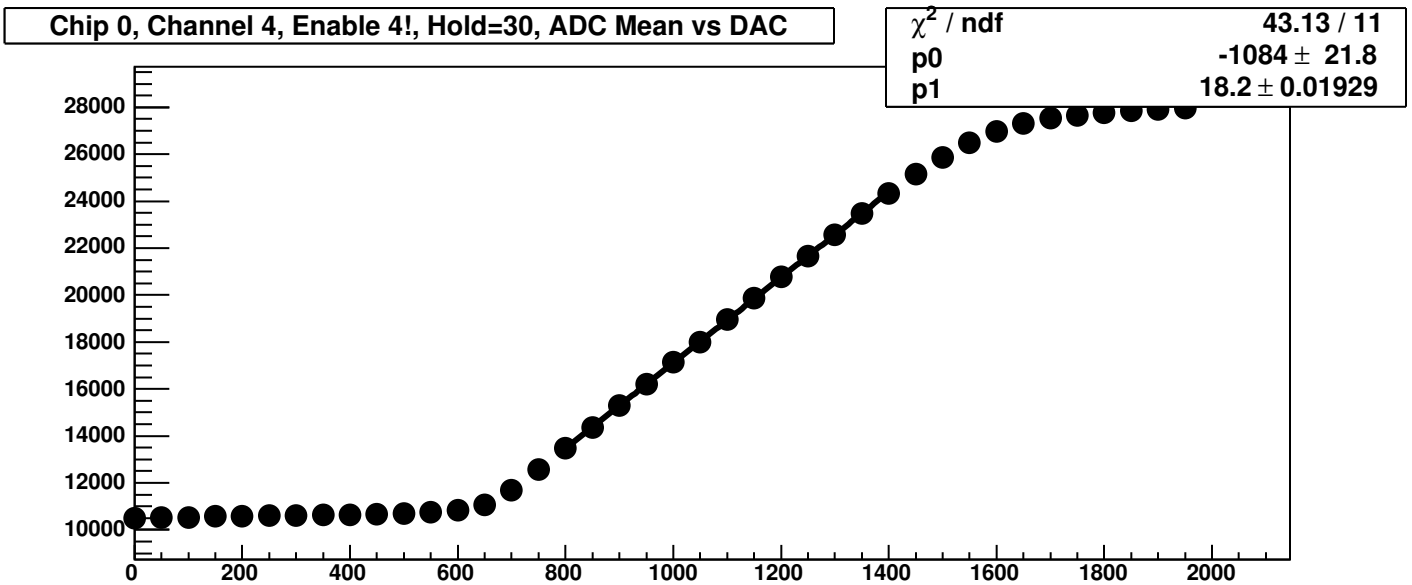

Chip 0, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

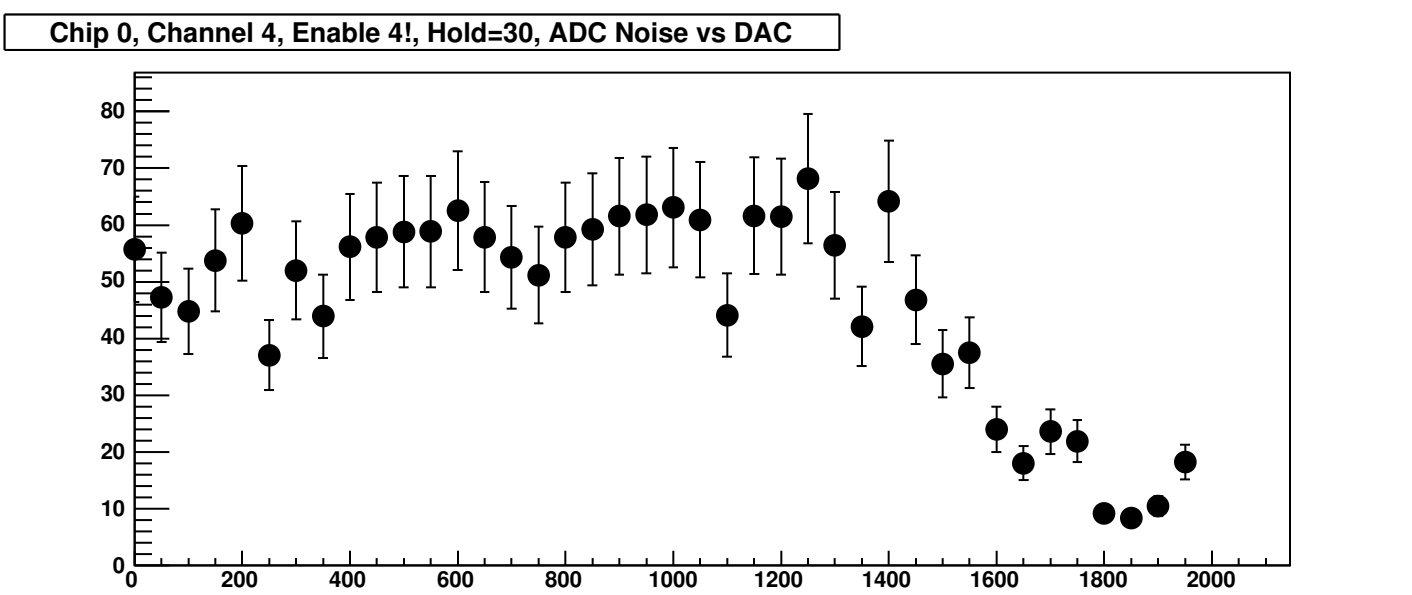

Chip 0, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

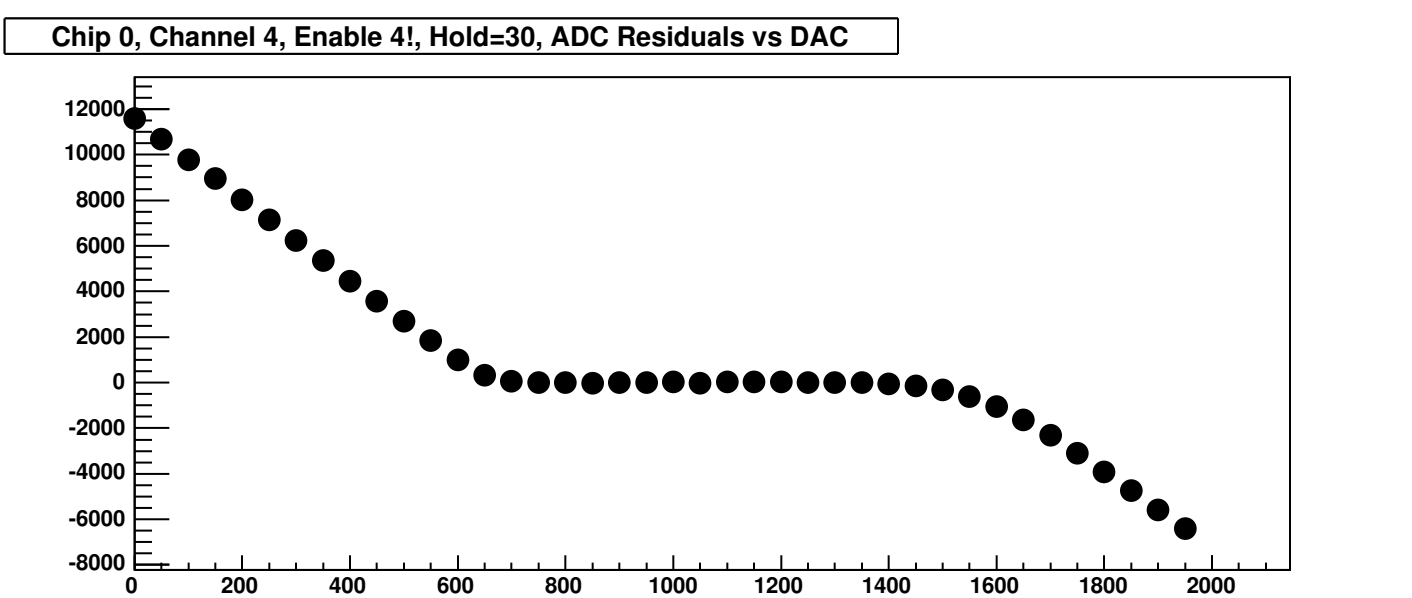

Chip 0, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 17.87 / 11
p0 -1169 ± 18.48
p1 0.08047 ± 0.01631
-950
-1000
-1050
-1100
-1150
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

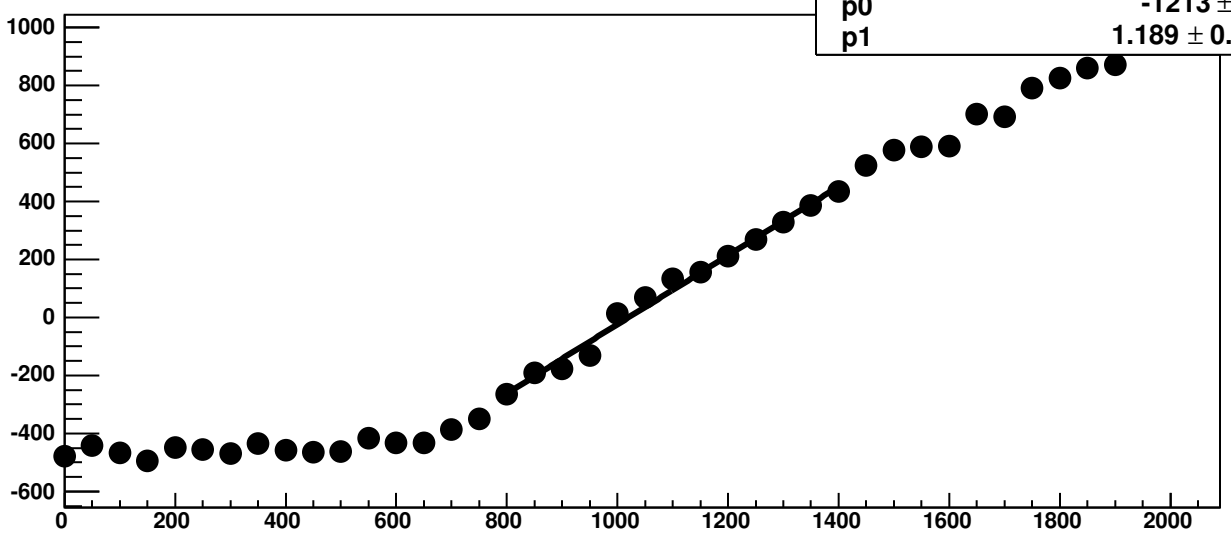

Chip 0, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 4, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 43.13 / 11
p0 -1084 ± 21.8
p1 18.2 ± 0.01929
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

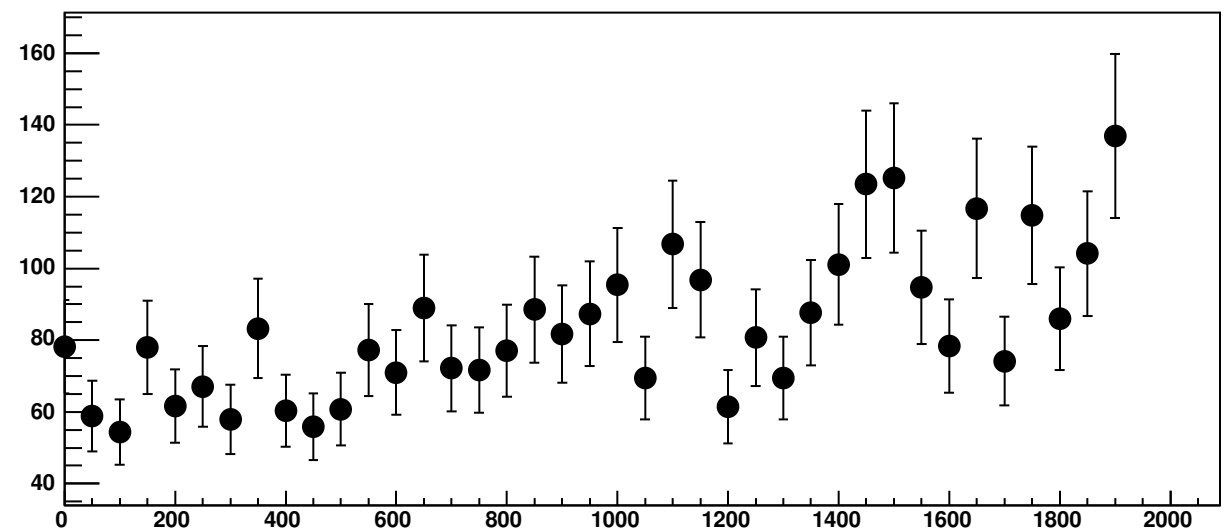

Chip 0, Channel 4, Enable 4!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 4, Enable 4!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

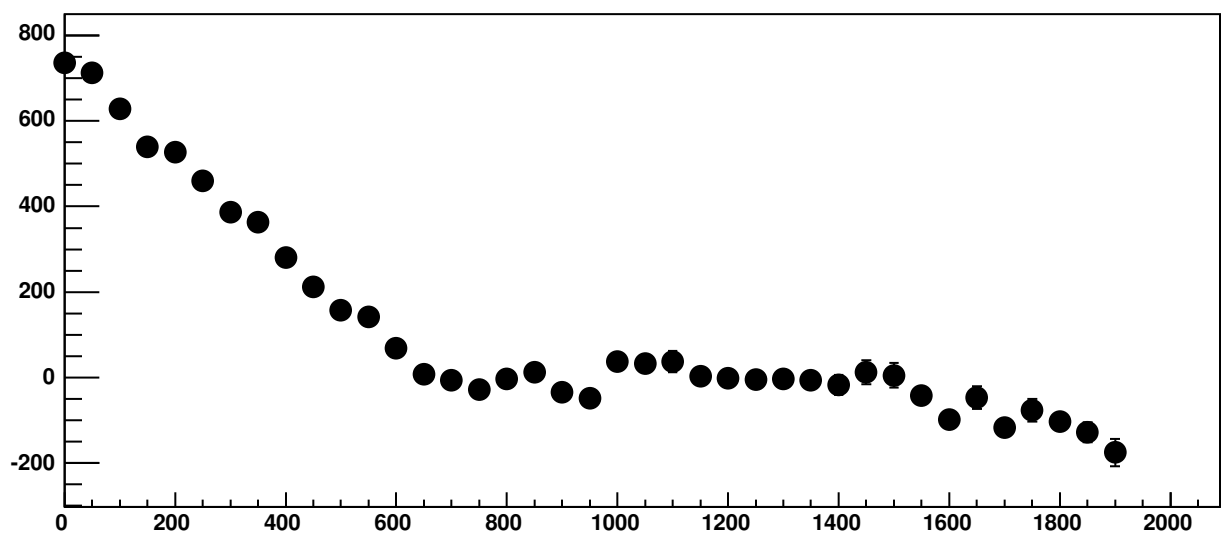
Chip 0, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC

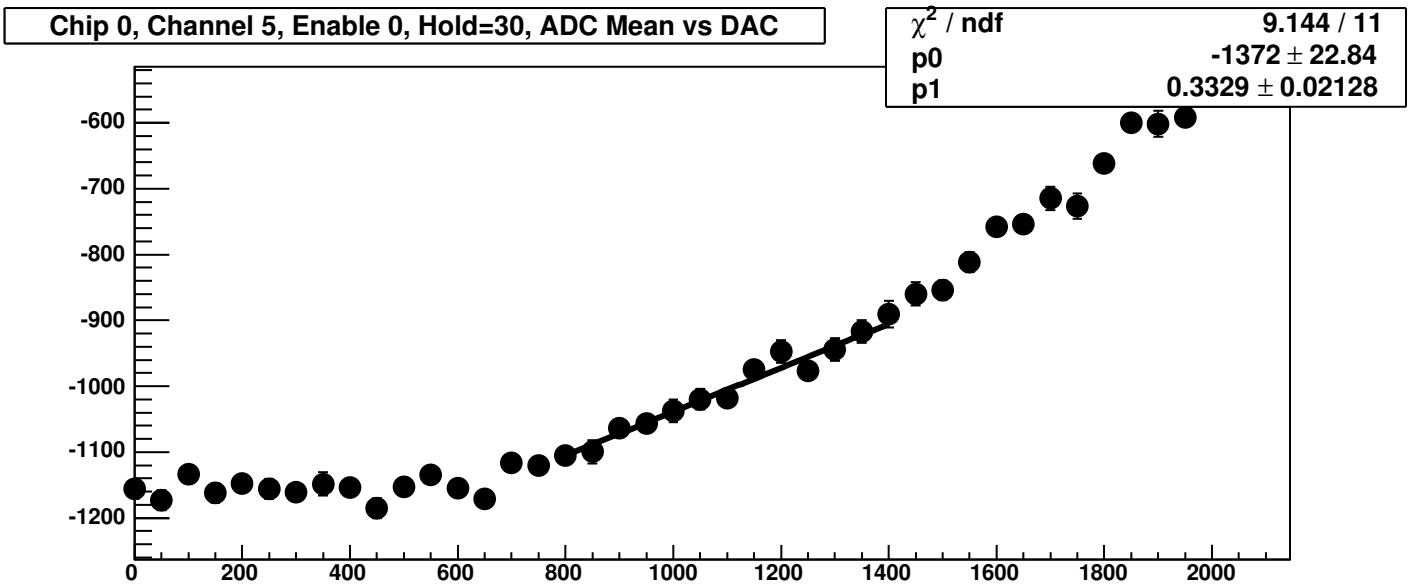


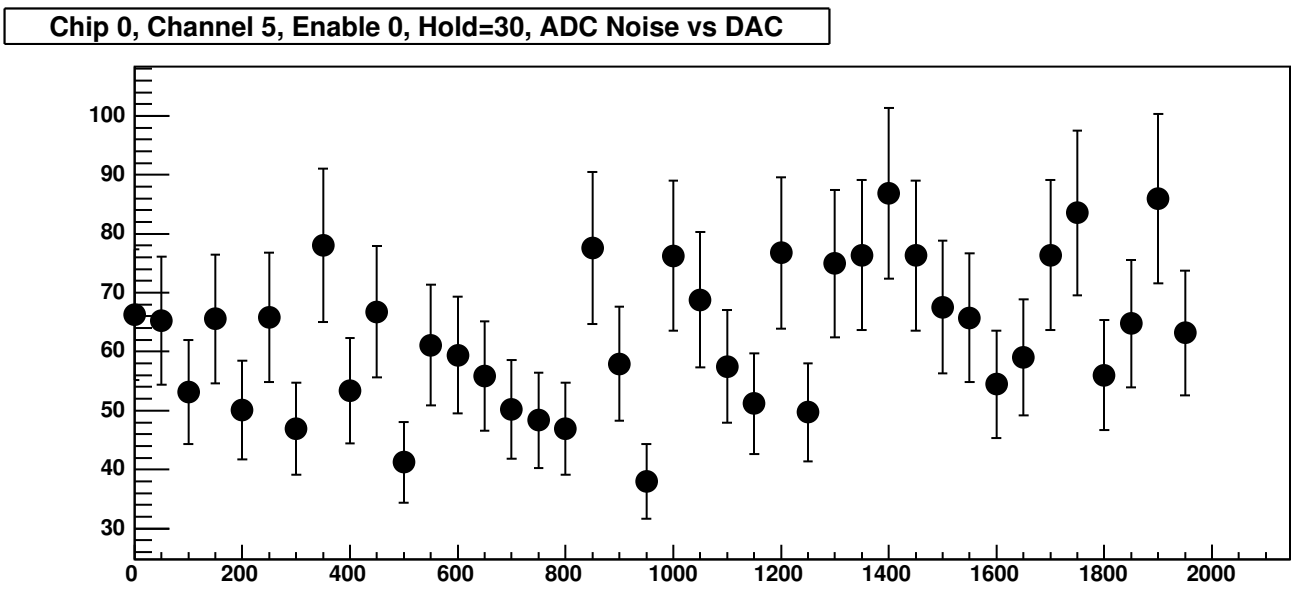
Chip 0, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC

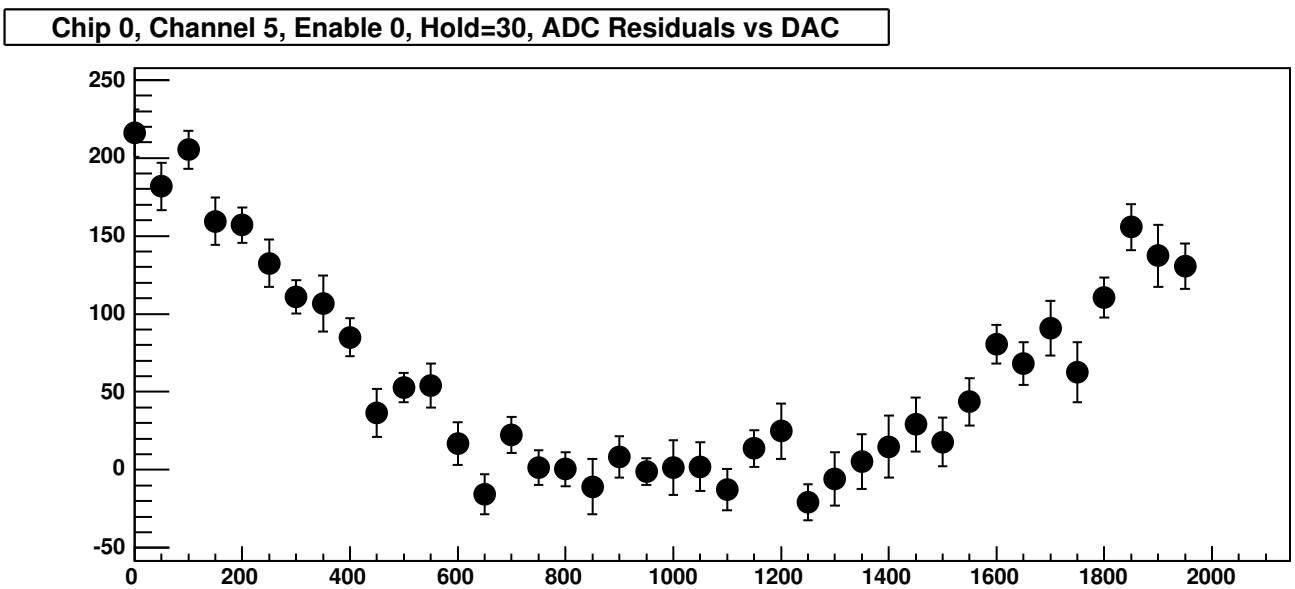


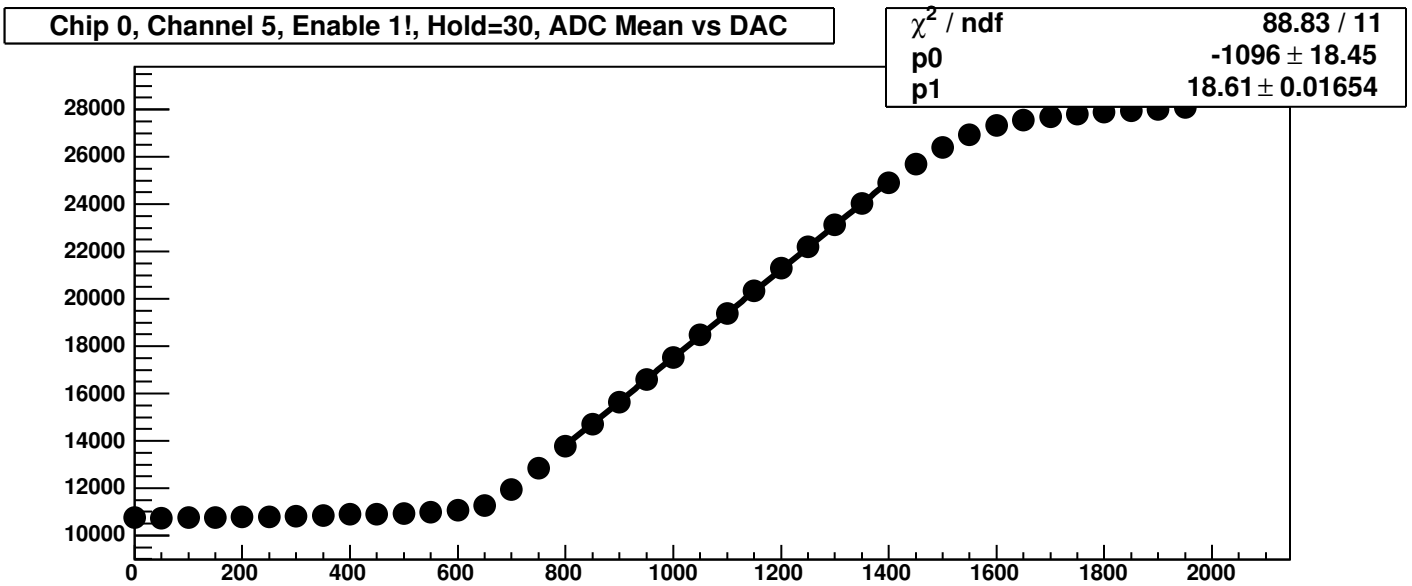
Chip 0, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC

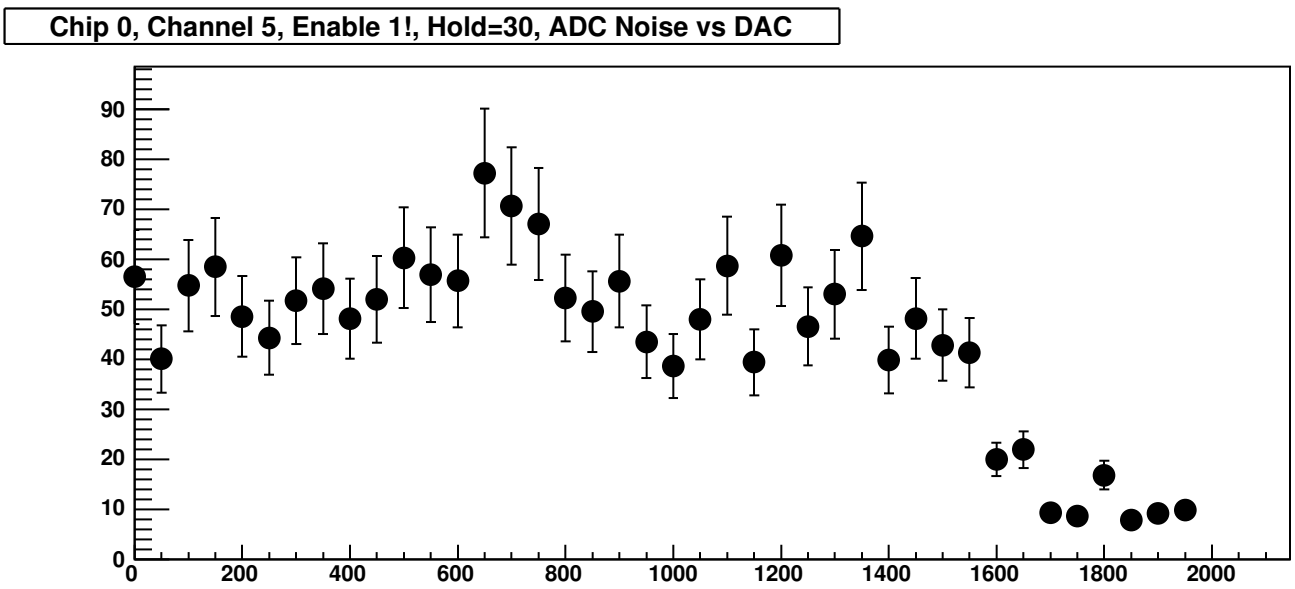


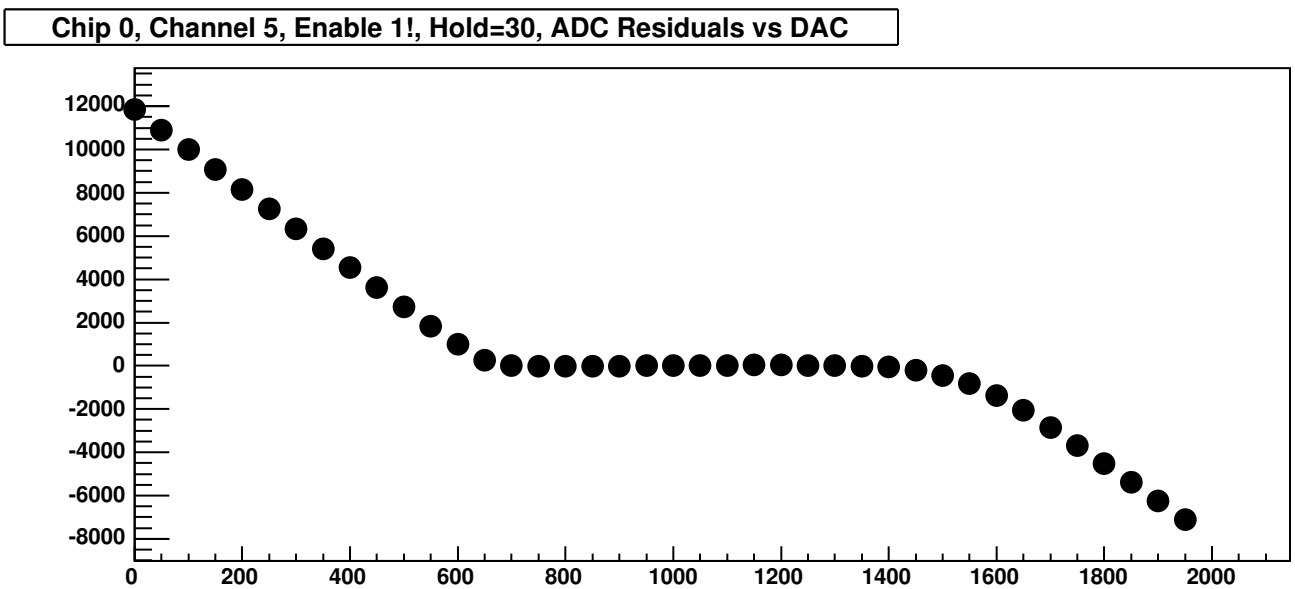

Chip 0, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 9.144 / 11
p0 -1372 ± 22.84
p1 0.3329 ± 0.02128
-600
-700
-800
-900
-1000
-1100
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

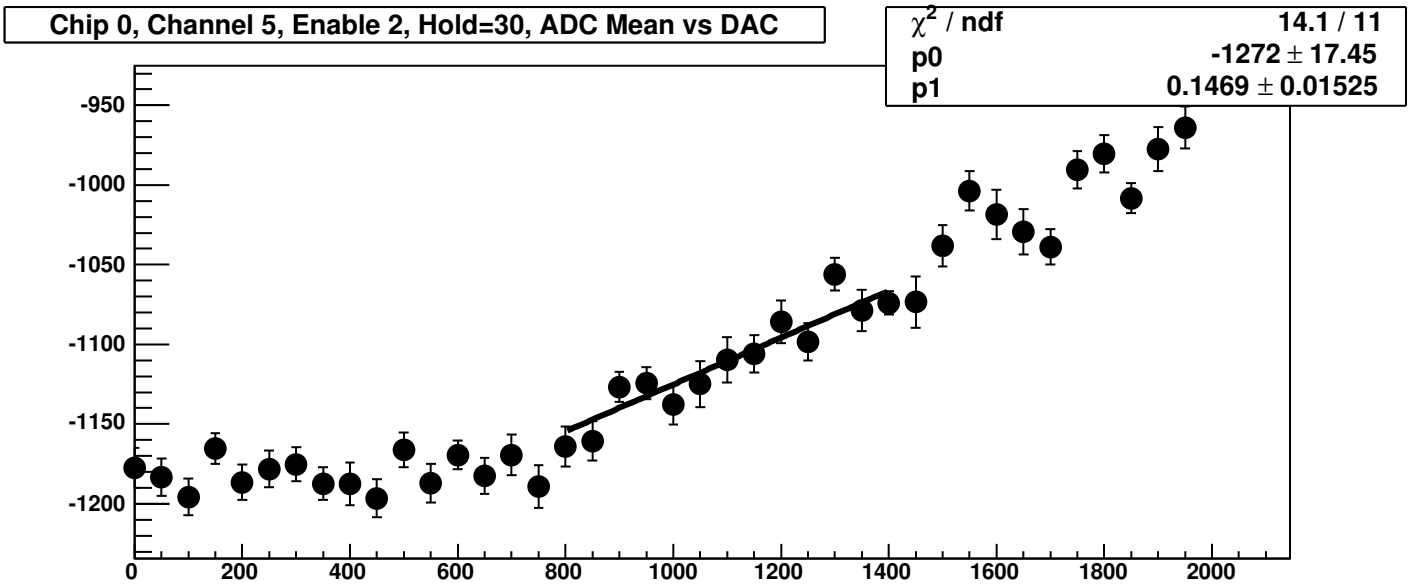

Chip 0, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

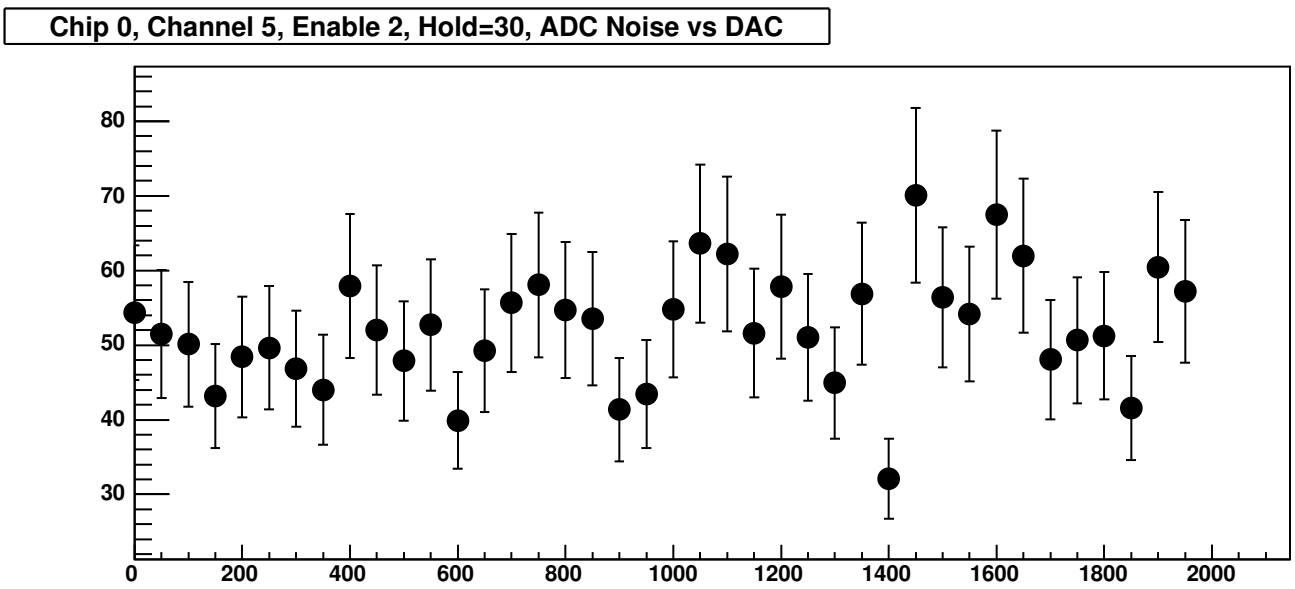

Chip 0, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

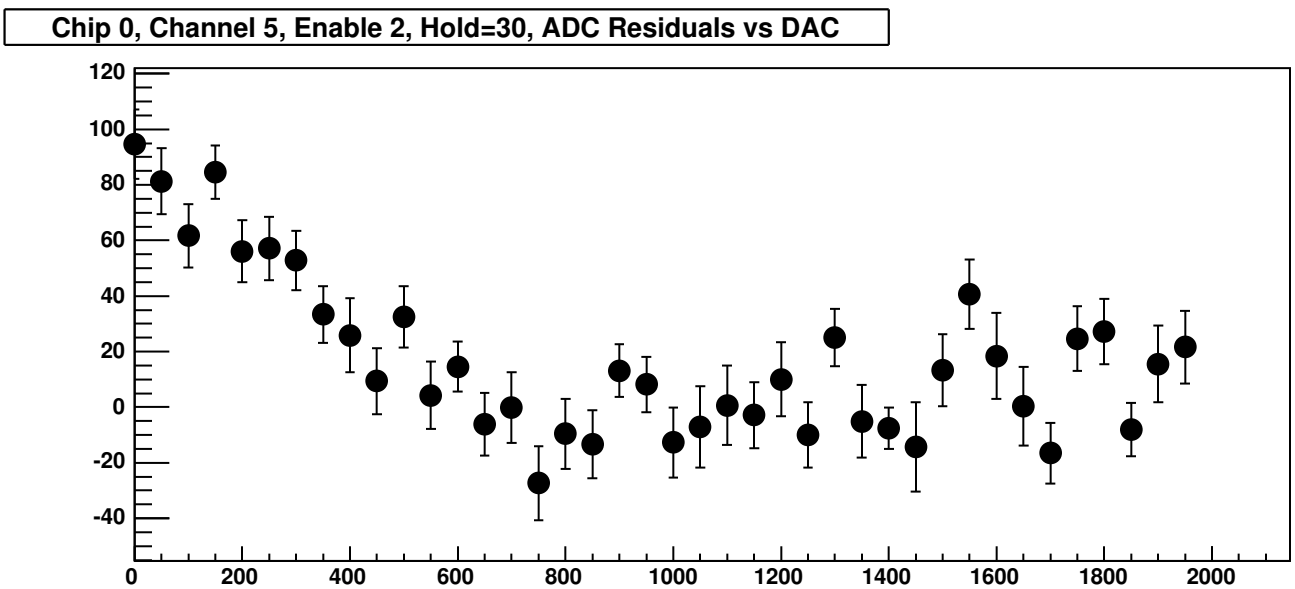

Chip 0, Channel 5, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 88.83 / 11
p0 -1096 ± 18.45
p1 18.61 ± 0.01654
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

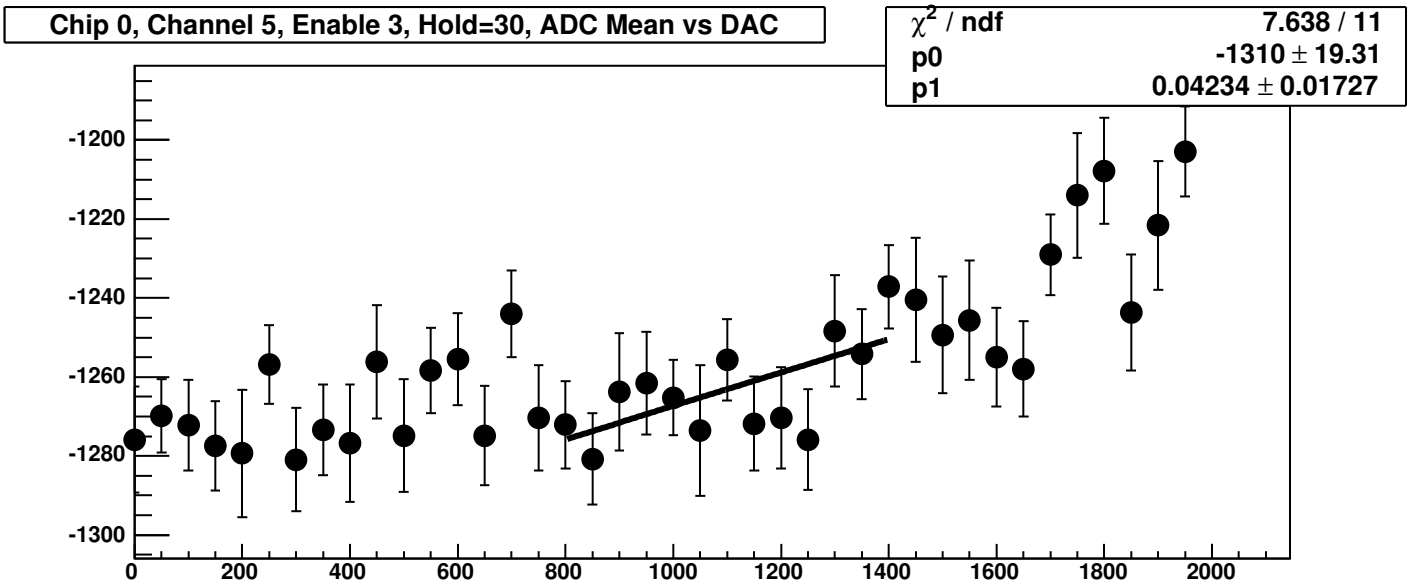

Chip 0, Channel 5, Enable 1!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

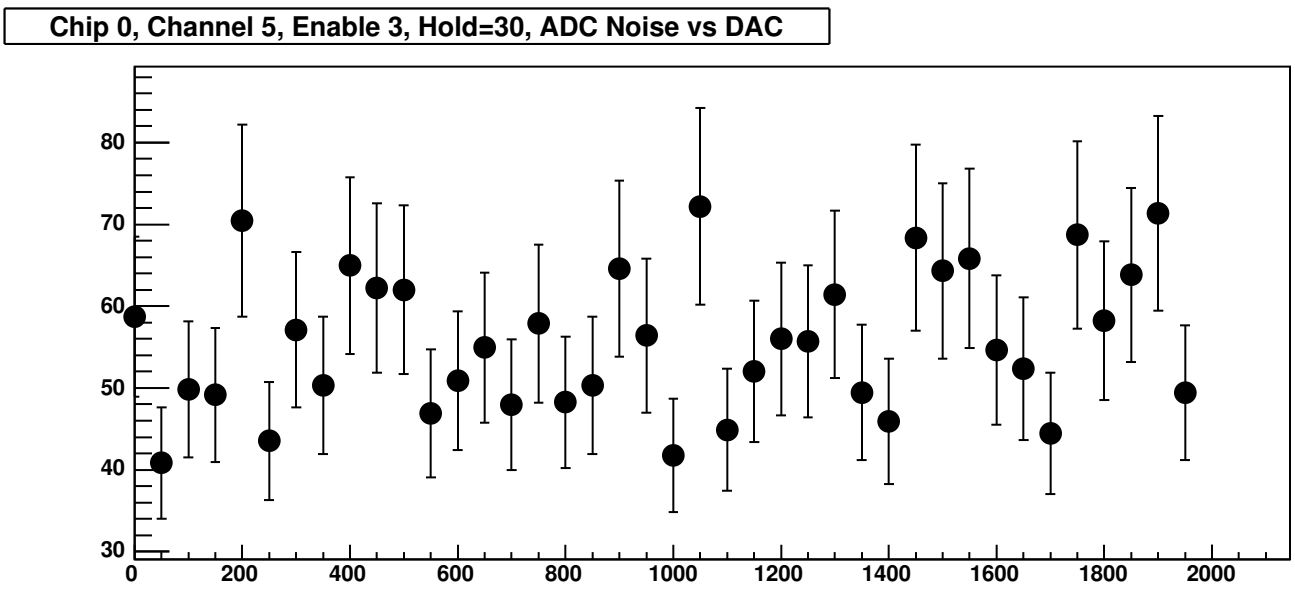

Chip 0, Channel 5, Enable 1!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

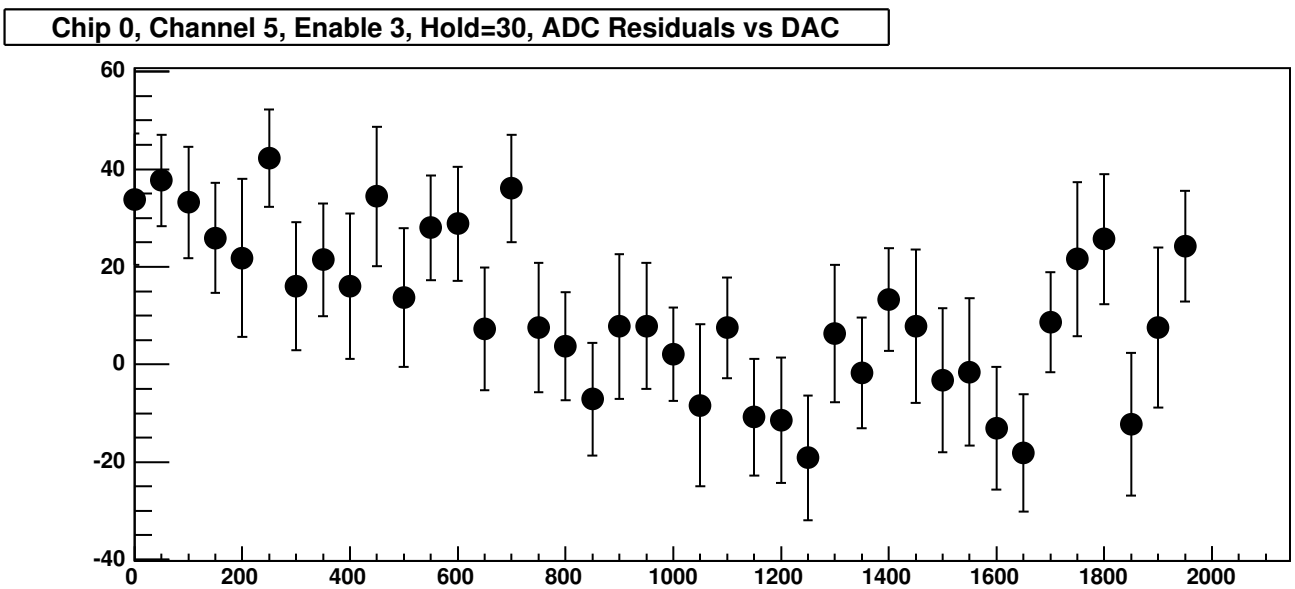

Chip 0, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 14.1 / 11
p0 -1272 ± 17.45
p1 0.1469 ± 0.01525
-950
-1000
-1050
-1100
-1150
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

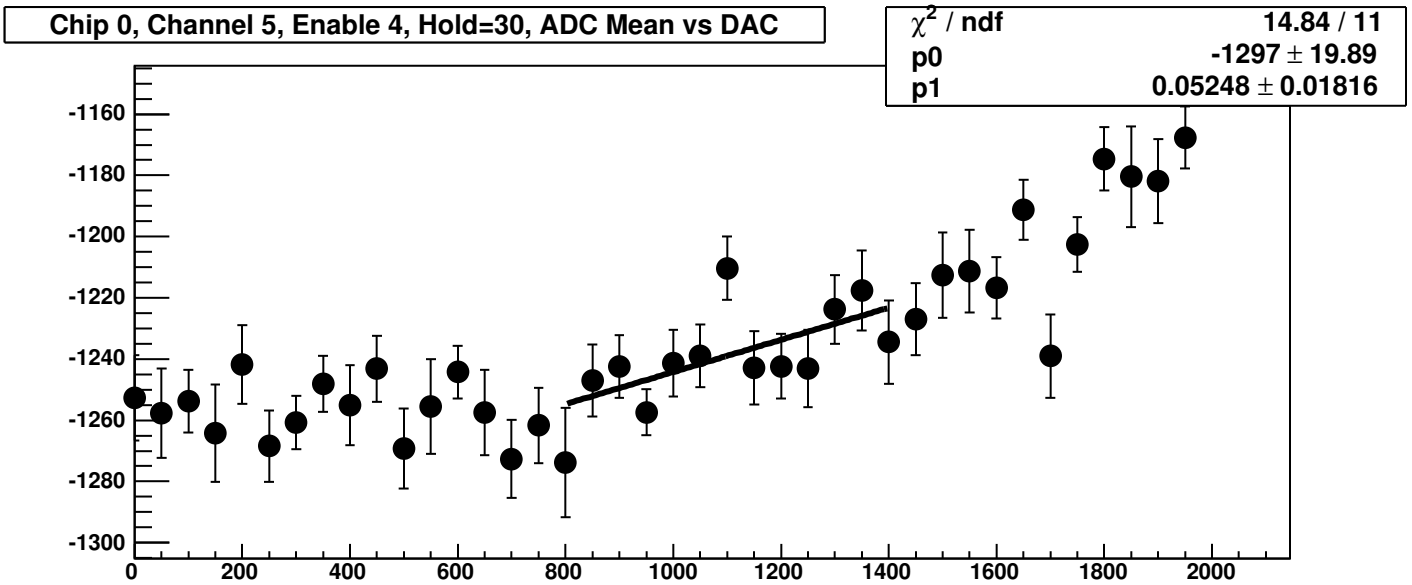

Chip 0, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

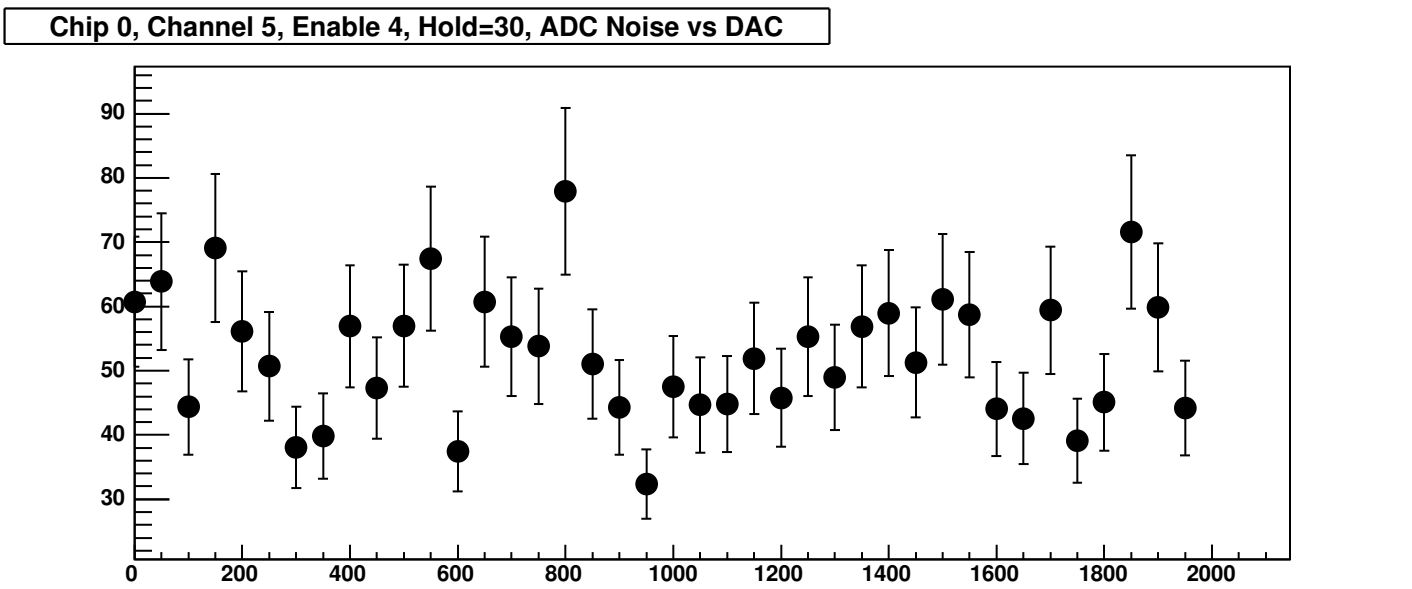

Chip 0, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

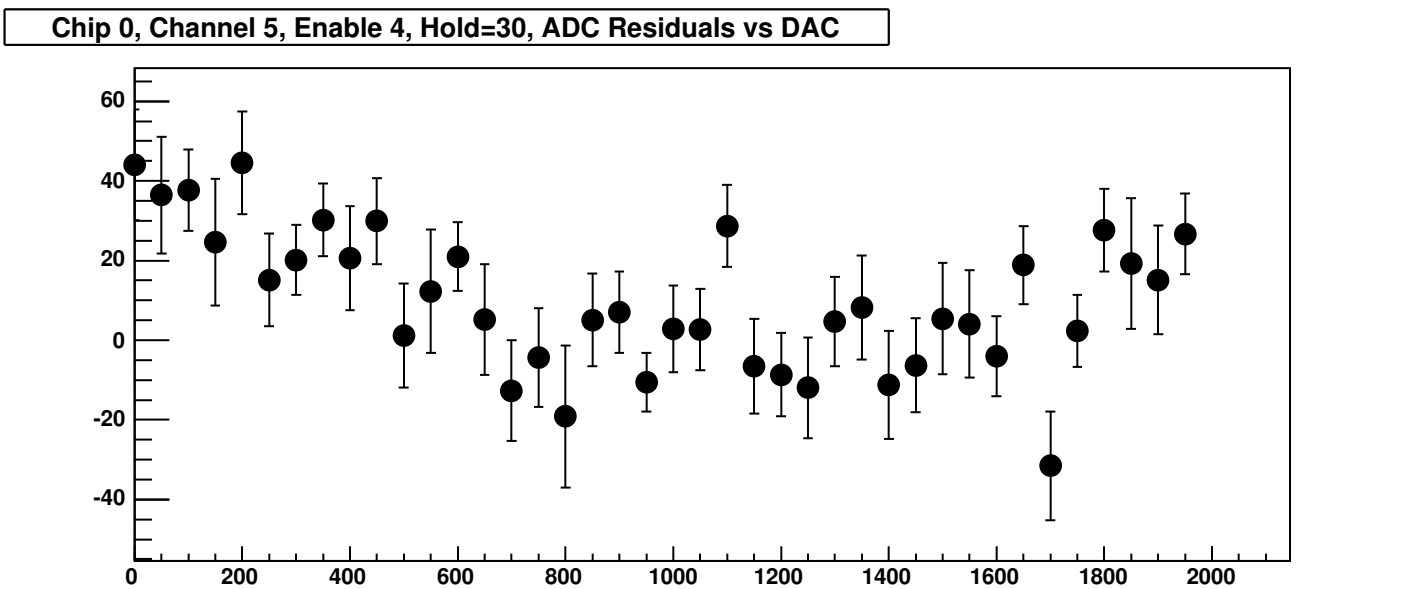

Chip 0, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7.638 / 11
p0 -1310 ± 19.31
p1 0.04234 ± 0.01727
-1200
-1220
-1240
-1260
-1280
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

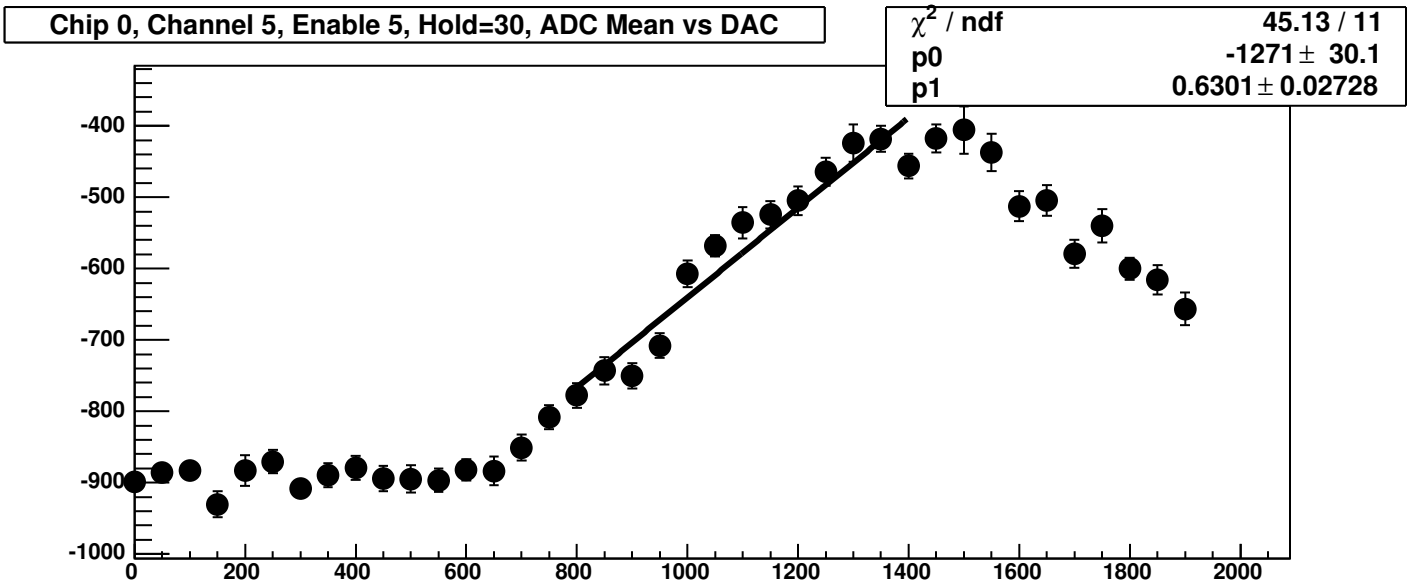

Chip 0, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

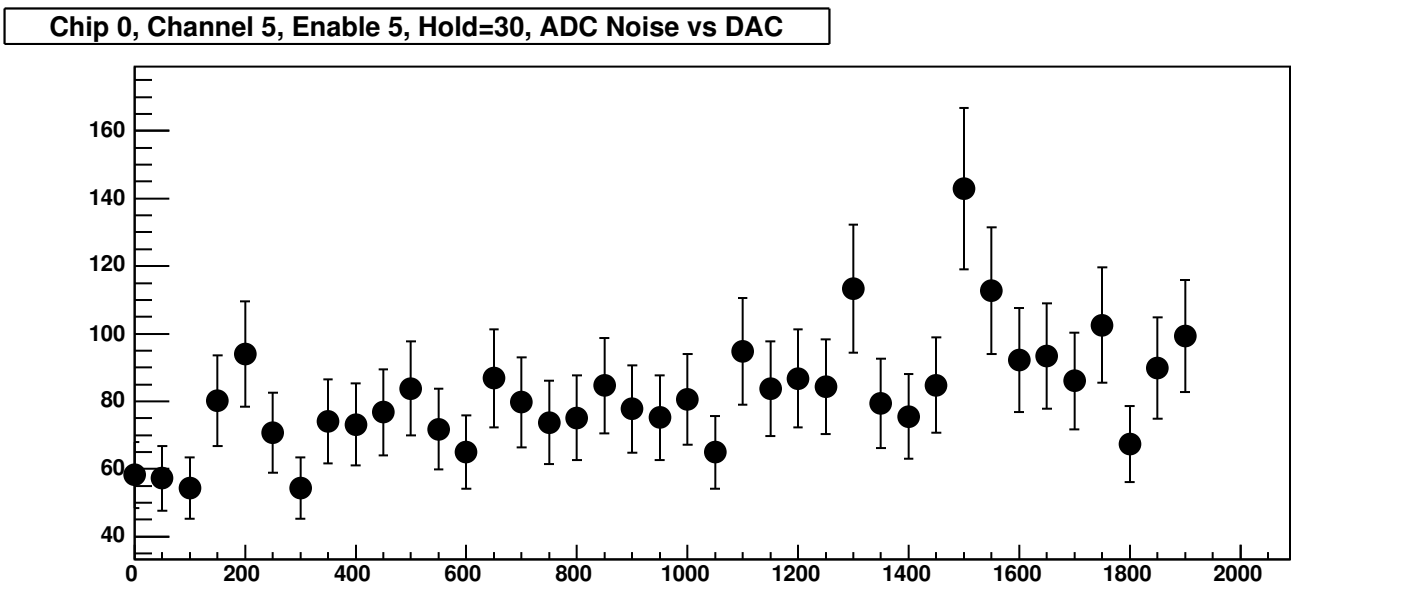

Chip 0, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

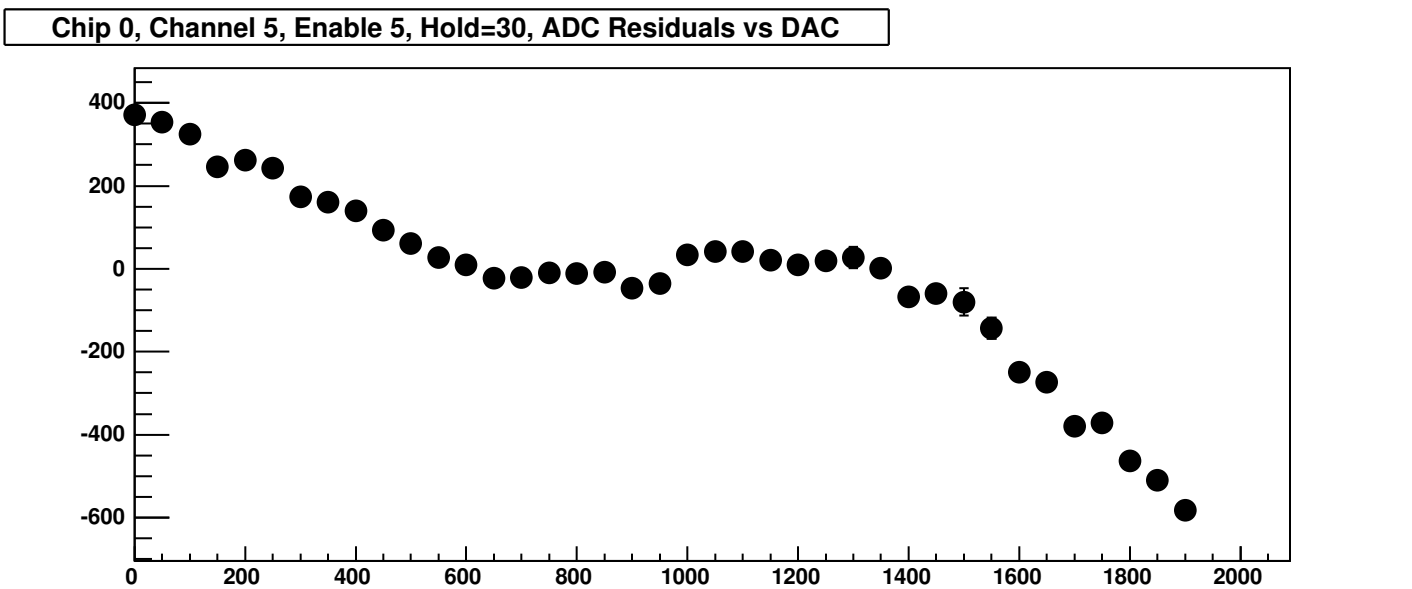

Chip 0, Channel 5, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 14.84 / 11
p0 -1297 ± 19.89
p1 0.05248 ± 0.01816
-1160
-1180
-1200
-1220
-1240
-1260
-1280
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

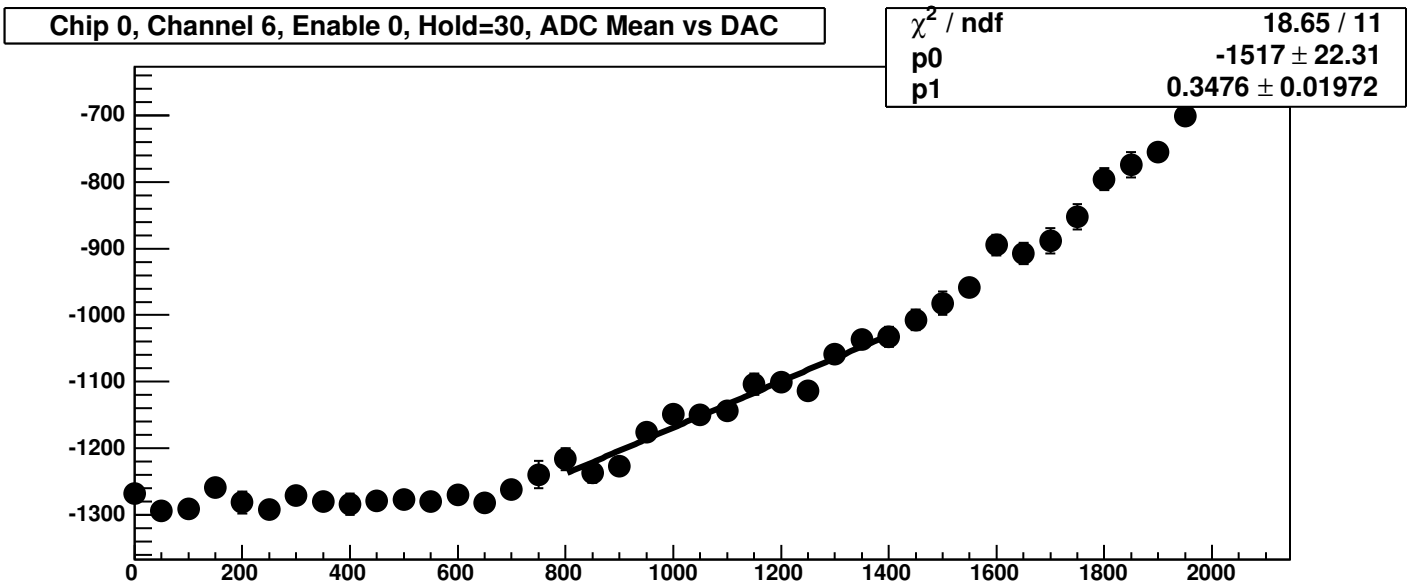

Chip 0, Channel 5, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

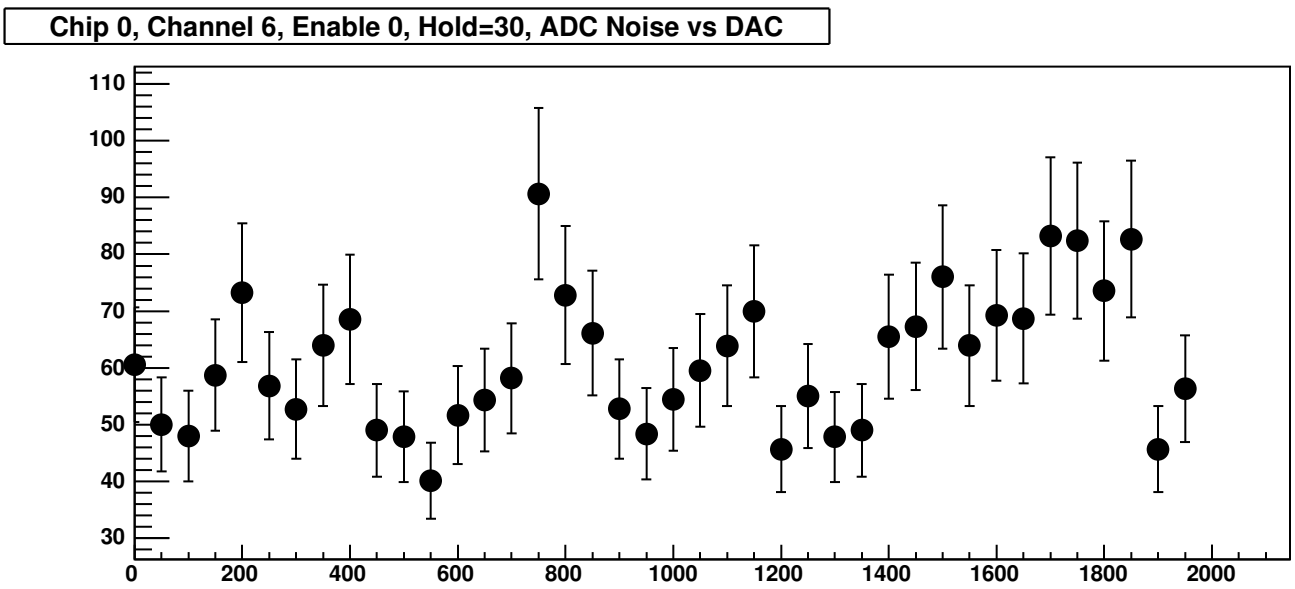

Chip 0, Channel 5, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

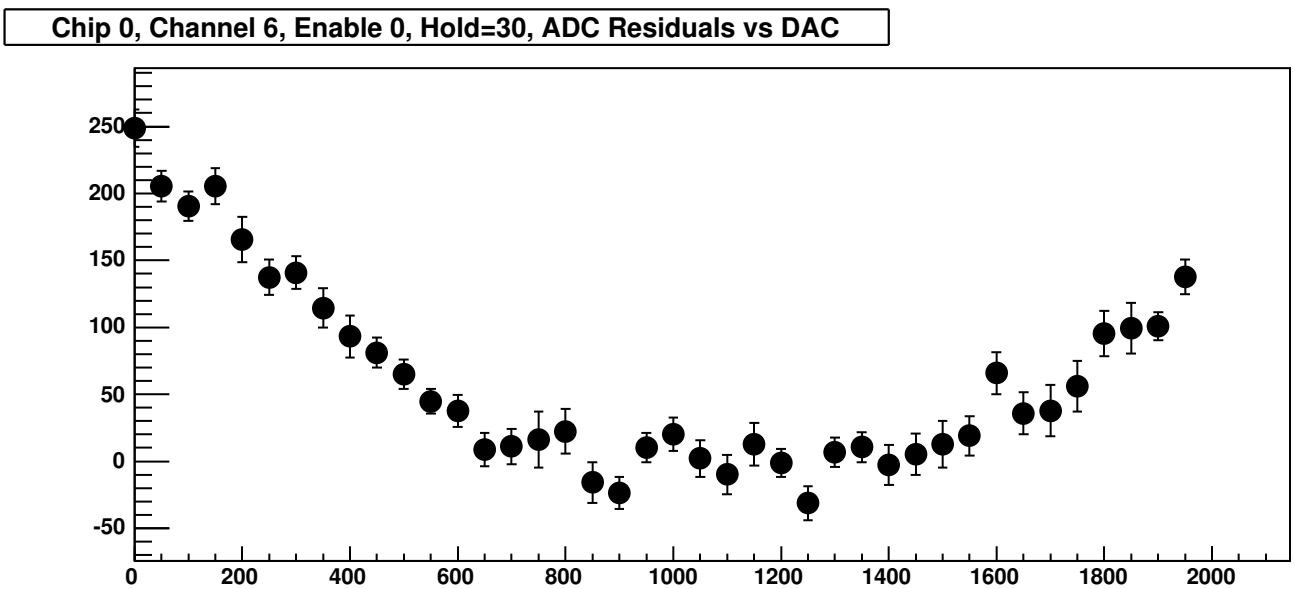

Chip 0, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 45.13 / 11
p0 -1271 ± 30.1
p1 0.6301 ± 0.02728
-400
-500
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

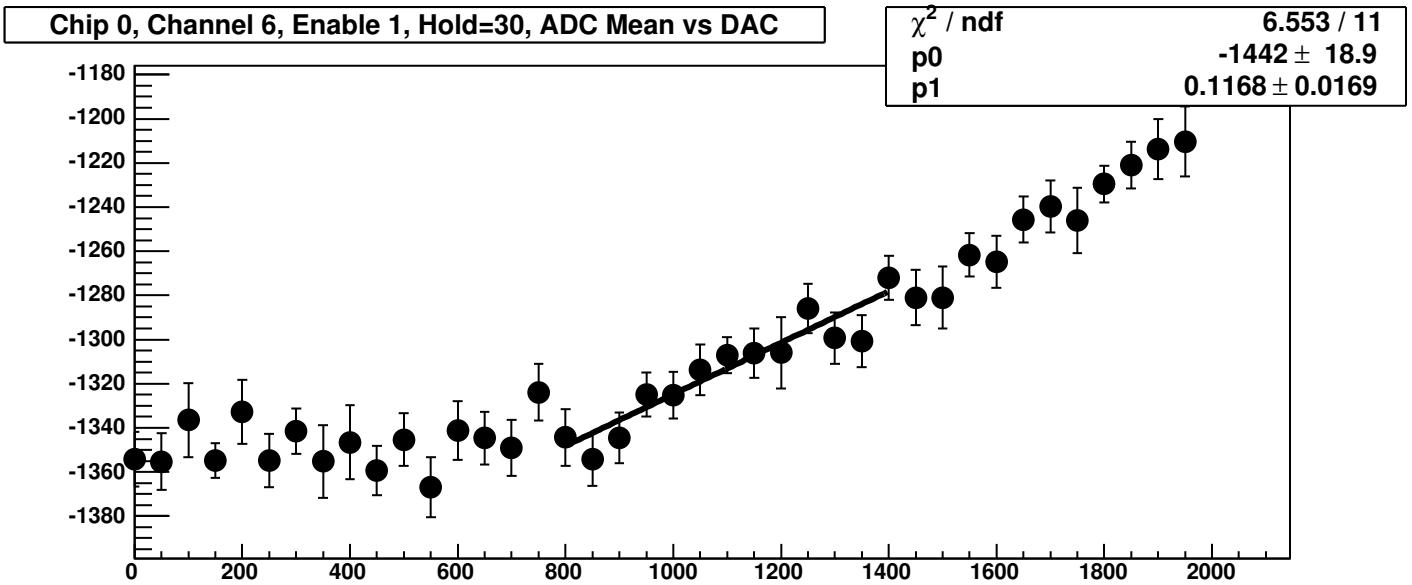

Chip 0, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

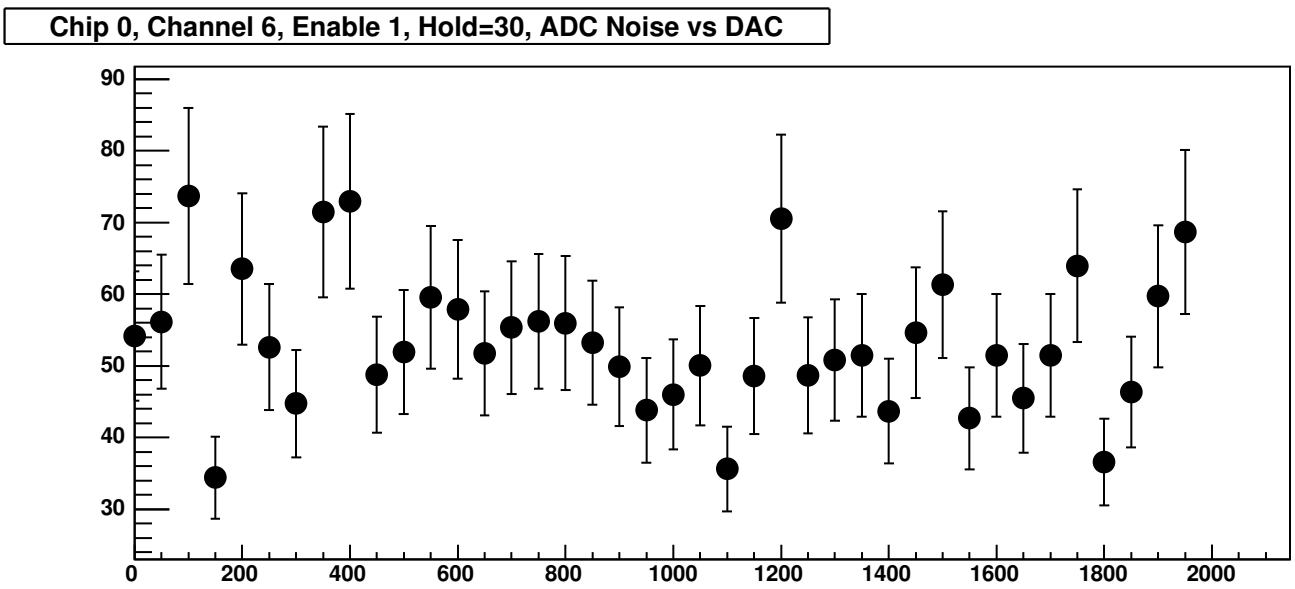

Chip 0, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

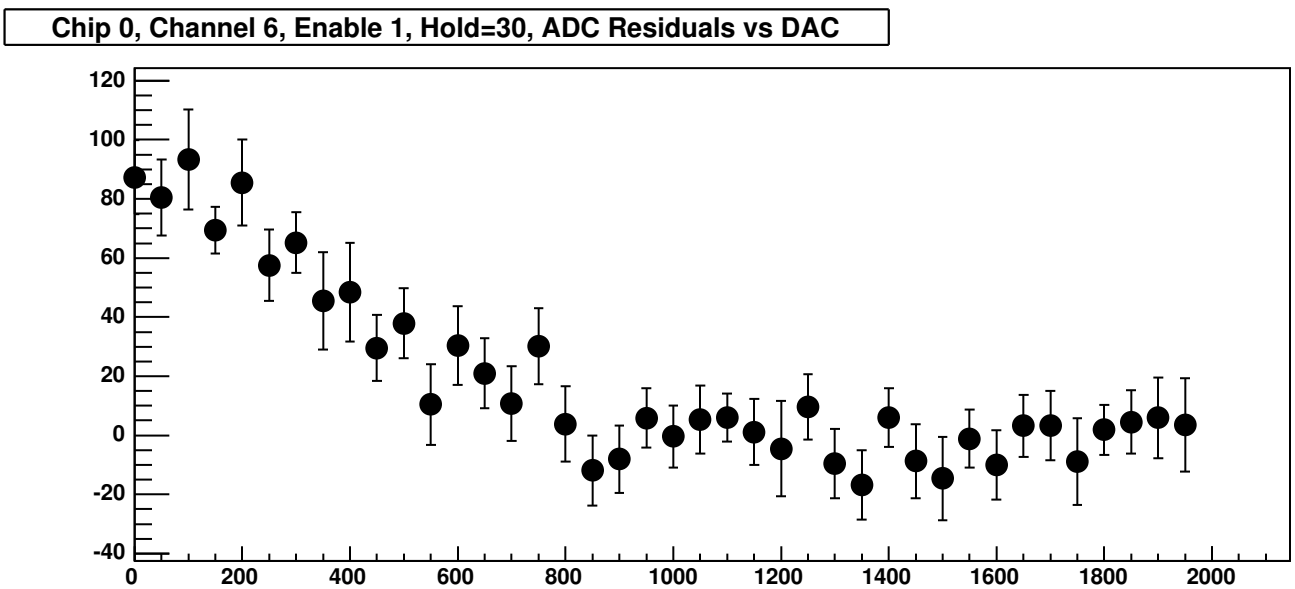

Chip 0, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 18.65 / 11
p0 -1517 ± 22.31
p1 0.3476 ± 0.01972
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

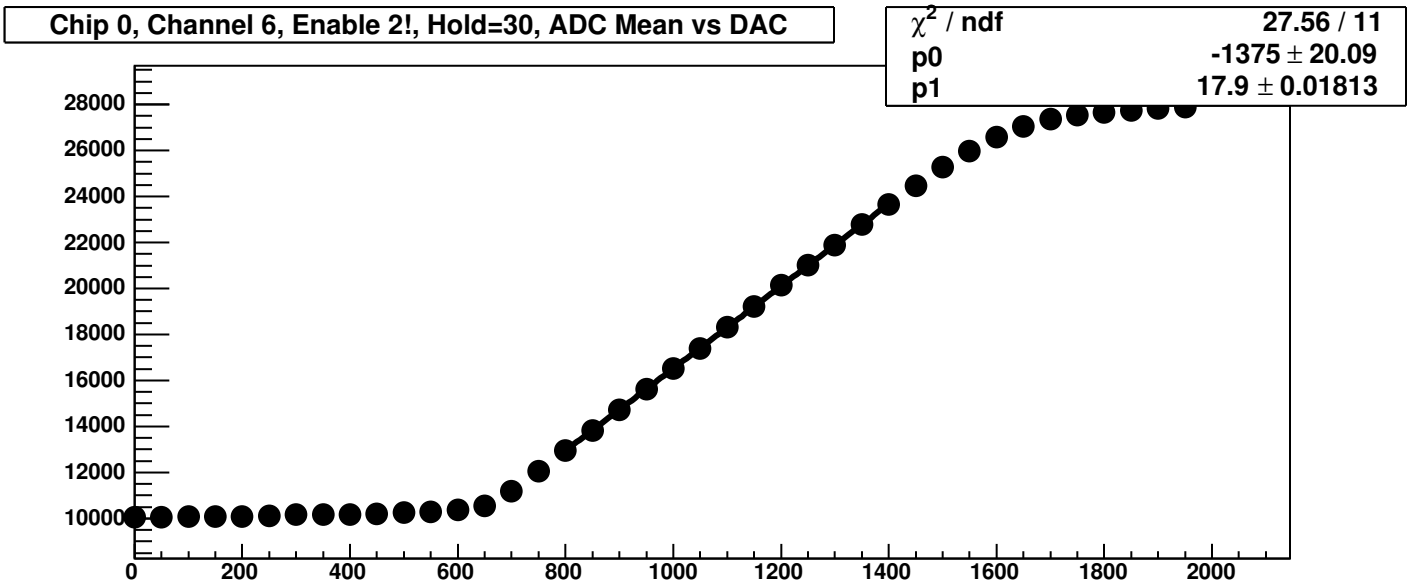

Chip 0, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

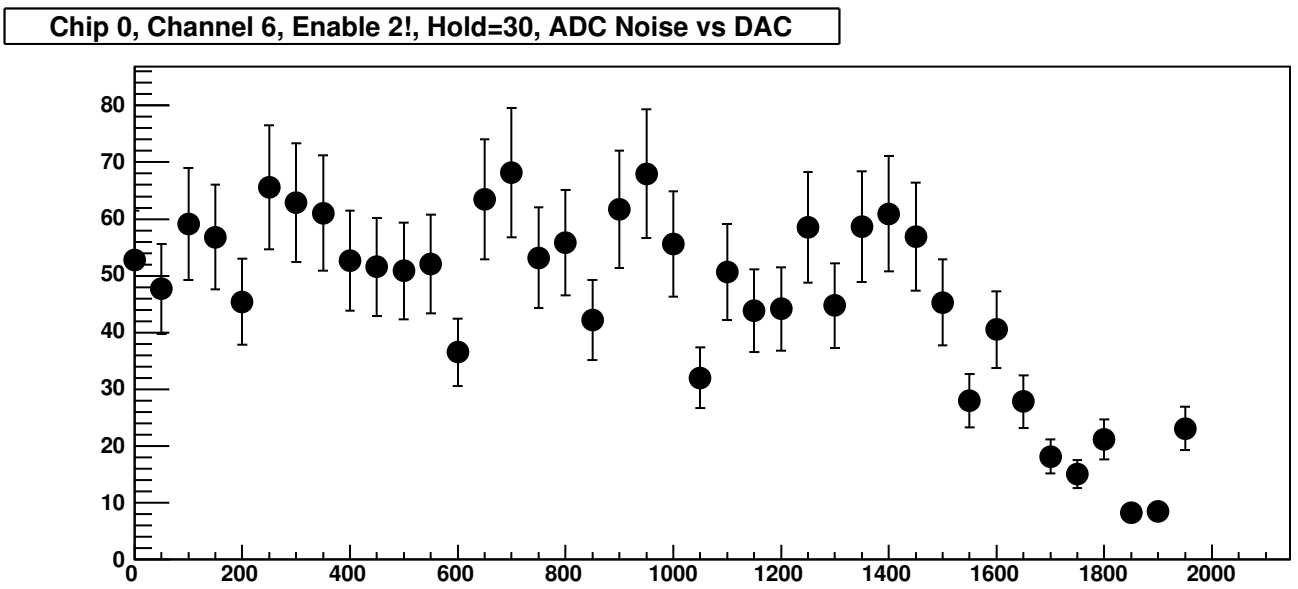

Chip 0, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

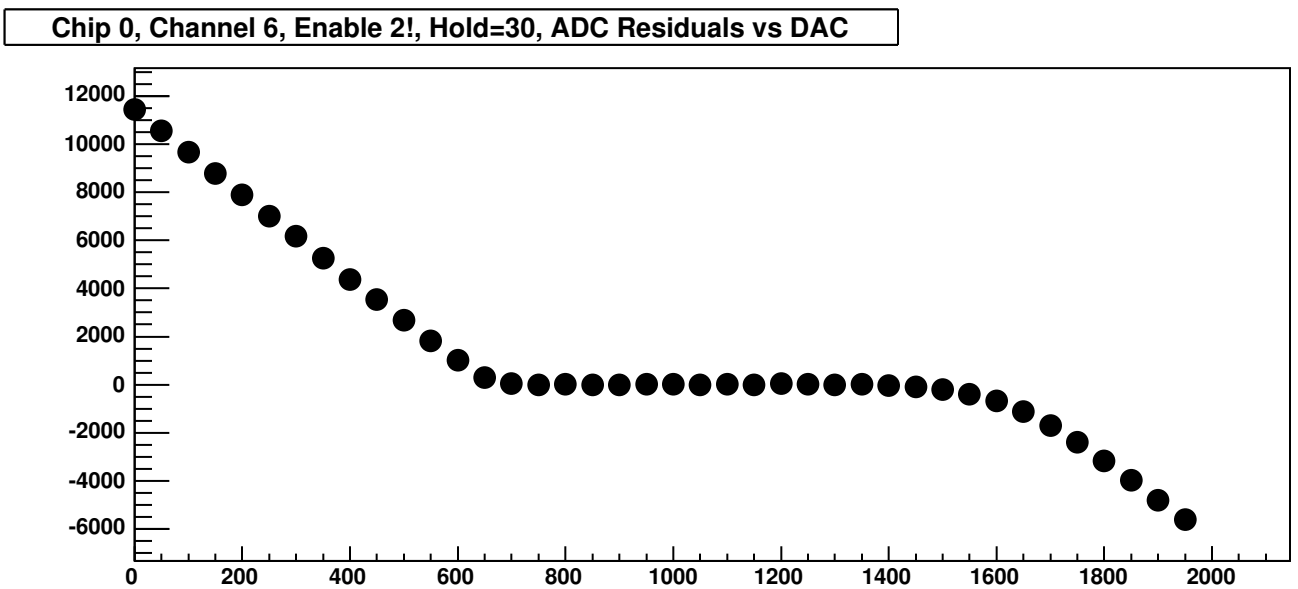

Chip 0, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 6.553 / 11
p0 -1442 ± 18.9
p1 0.1168 ± 0.0169
-1180
-1200
-1220
-1240
-1260
-1280
-1300
-1320
-1340
-1360
-1380
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

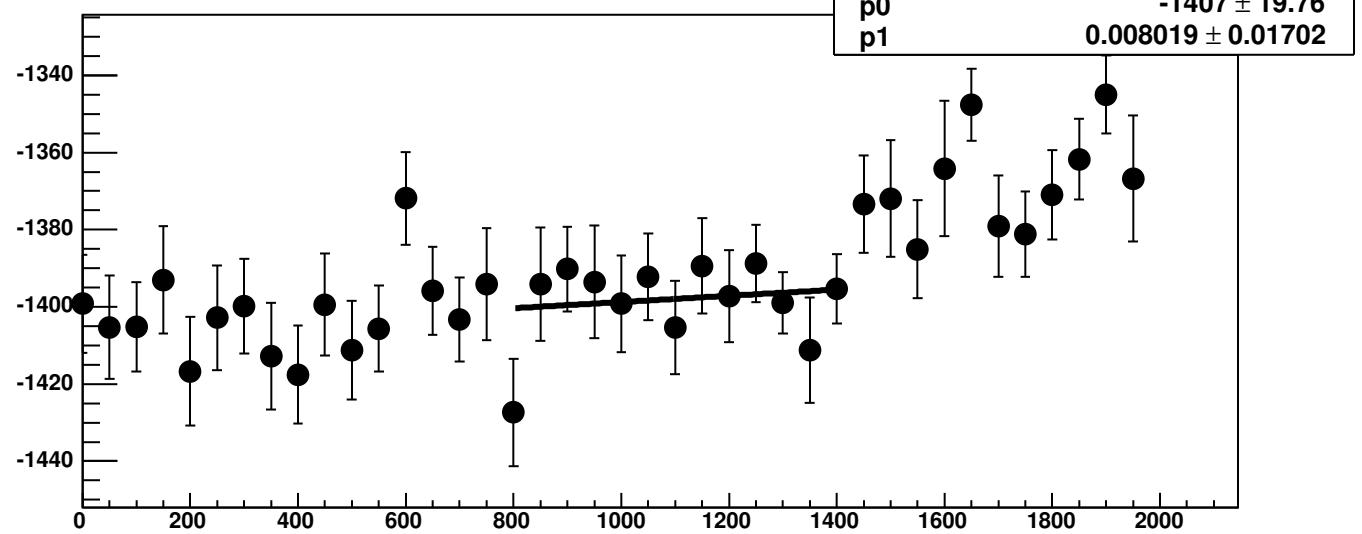

Chip 0, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 6, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 27.56 / 11
p0 -1375 ± 20.09
p1 17.9 ± 0.01813
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

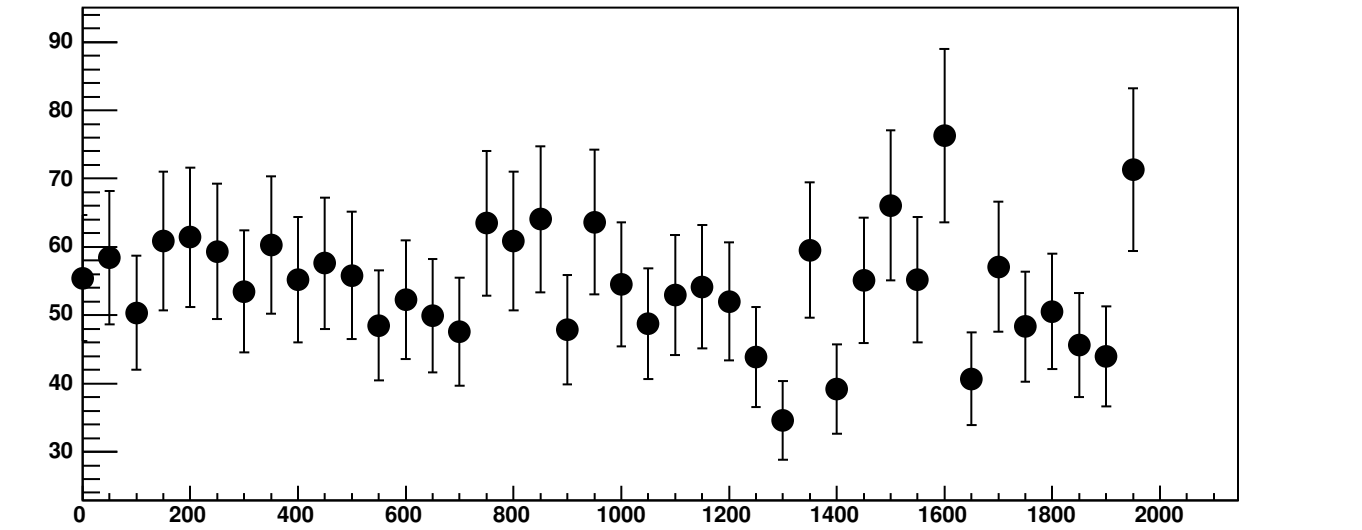

Chip 0, Channel 6, Enable 2!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 6, Enable 2!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

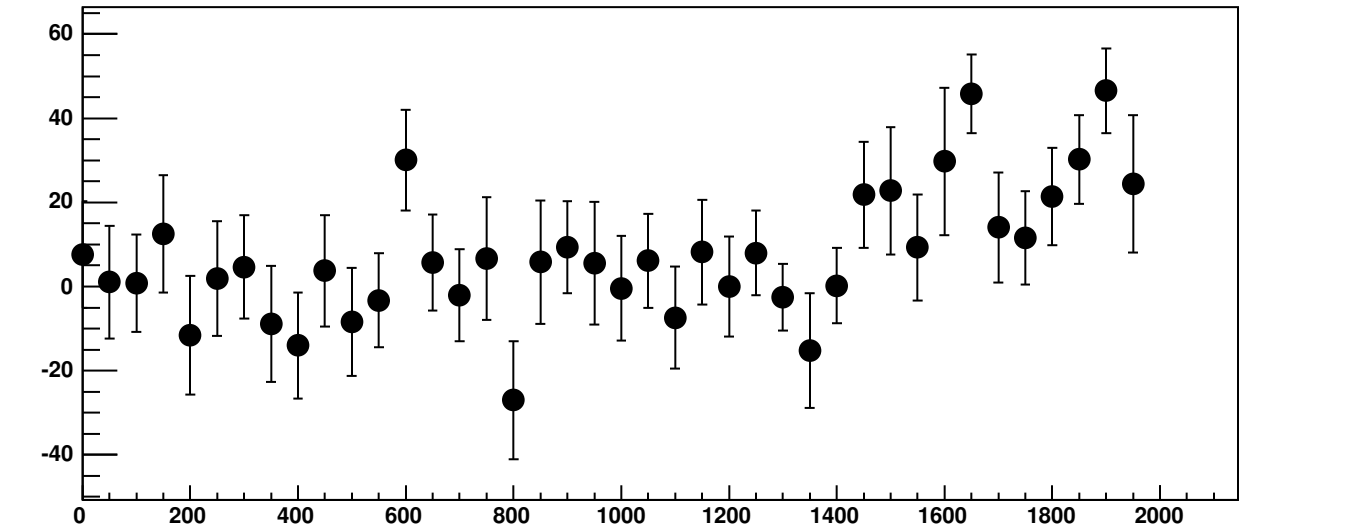
Chip 0, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC



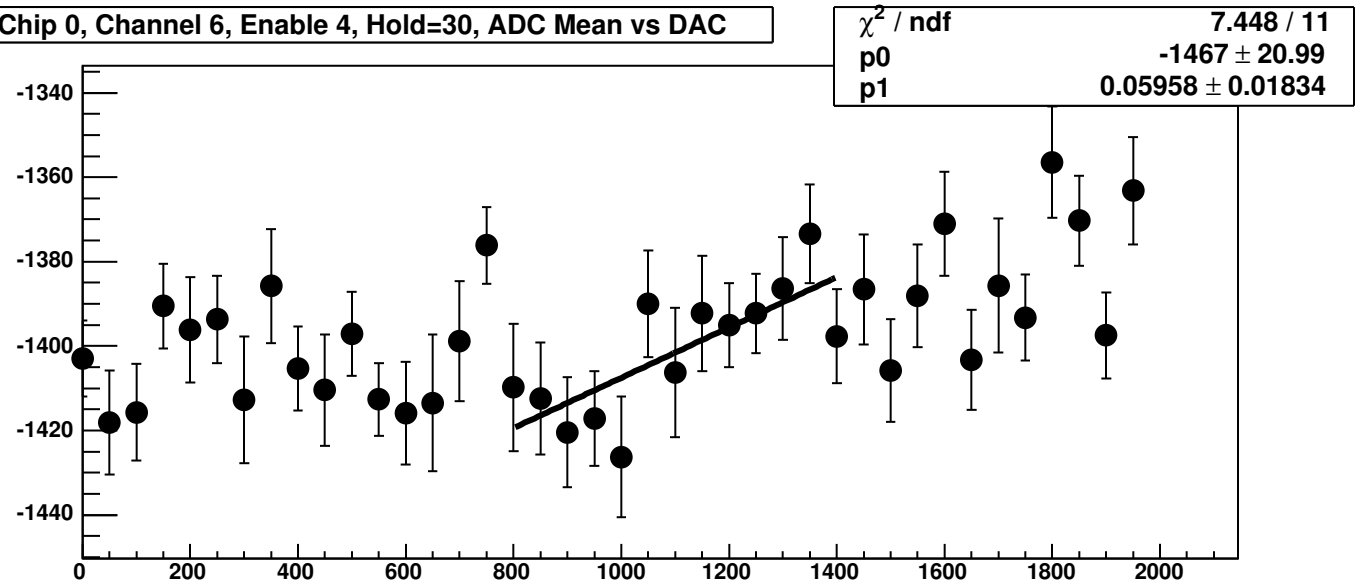
Chip 0, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC



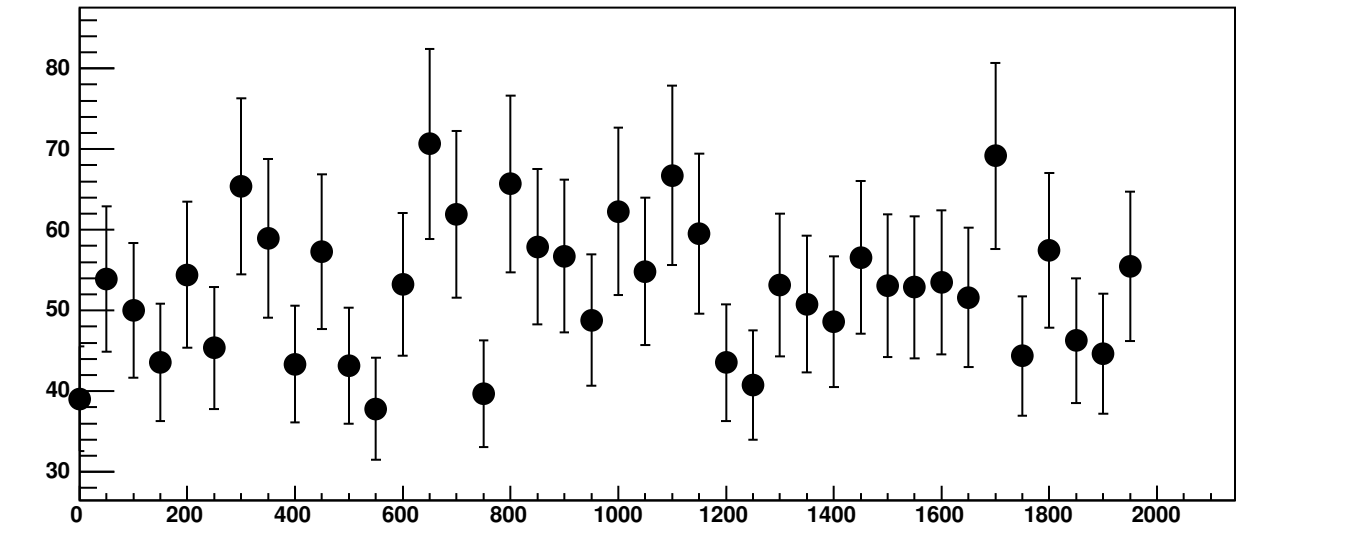
Chip 0, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC



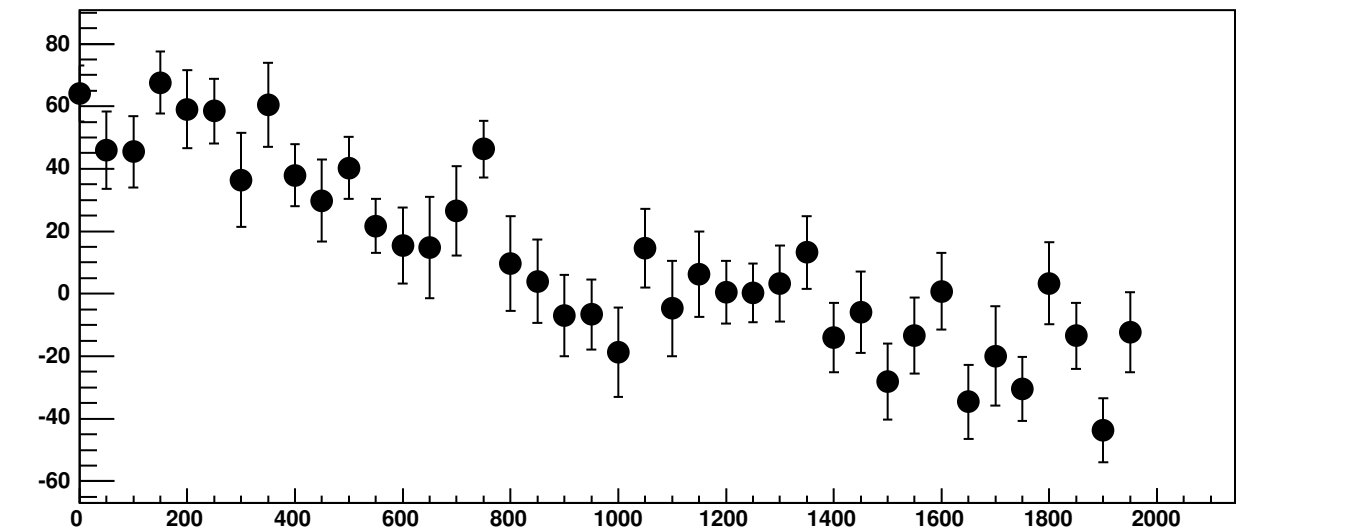
Chip 0, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC

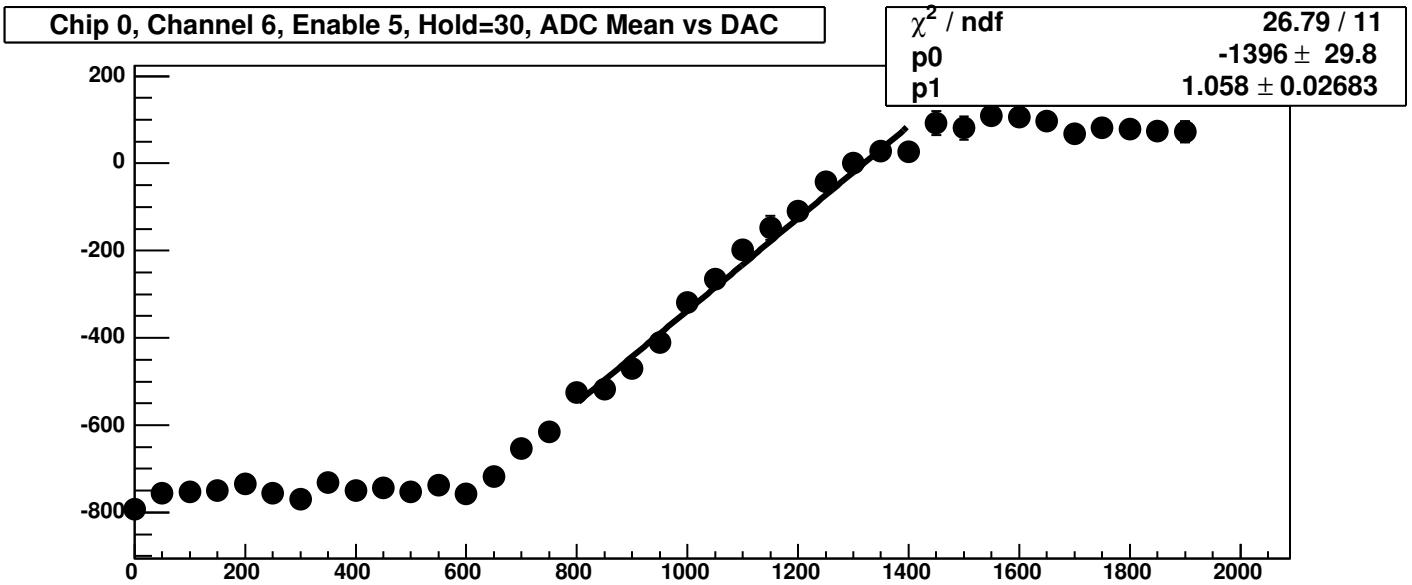


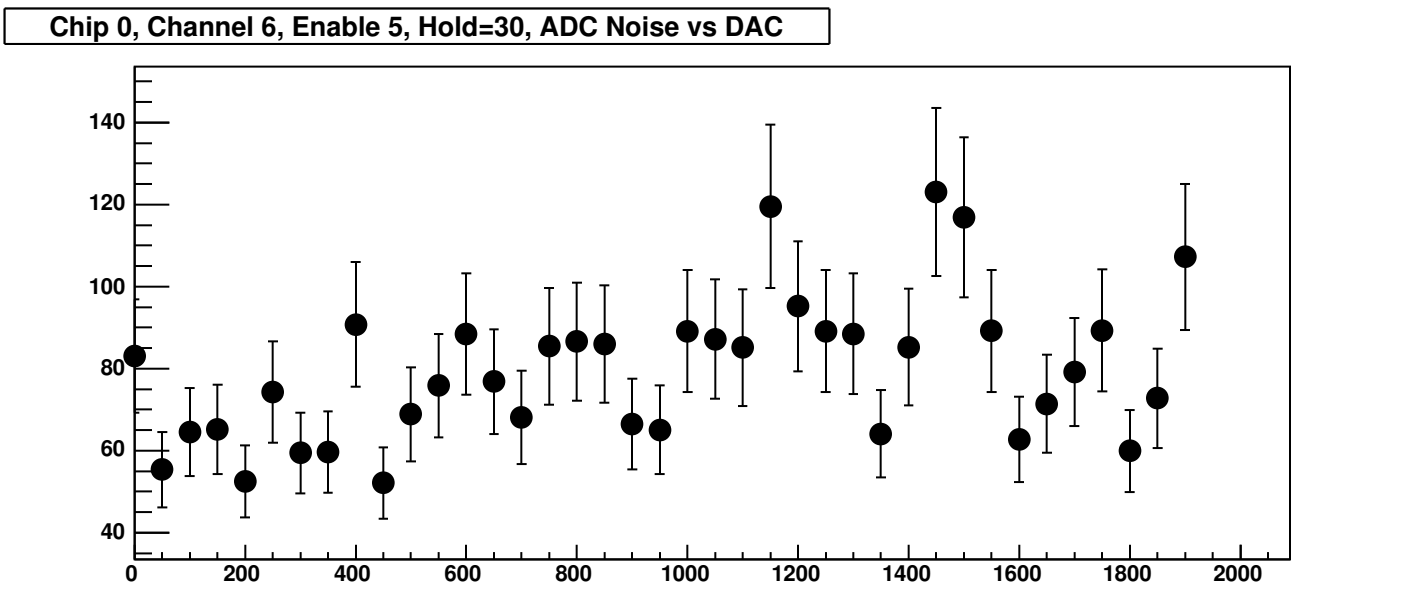
Chip 0, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC

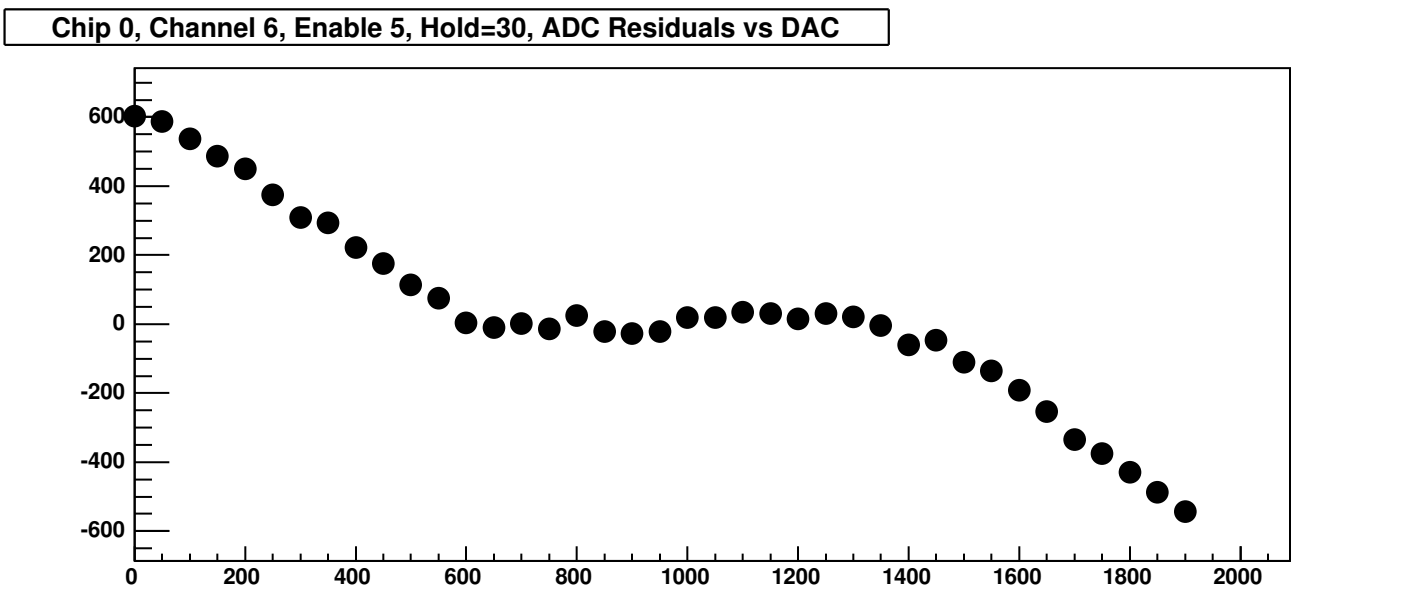


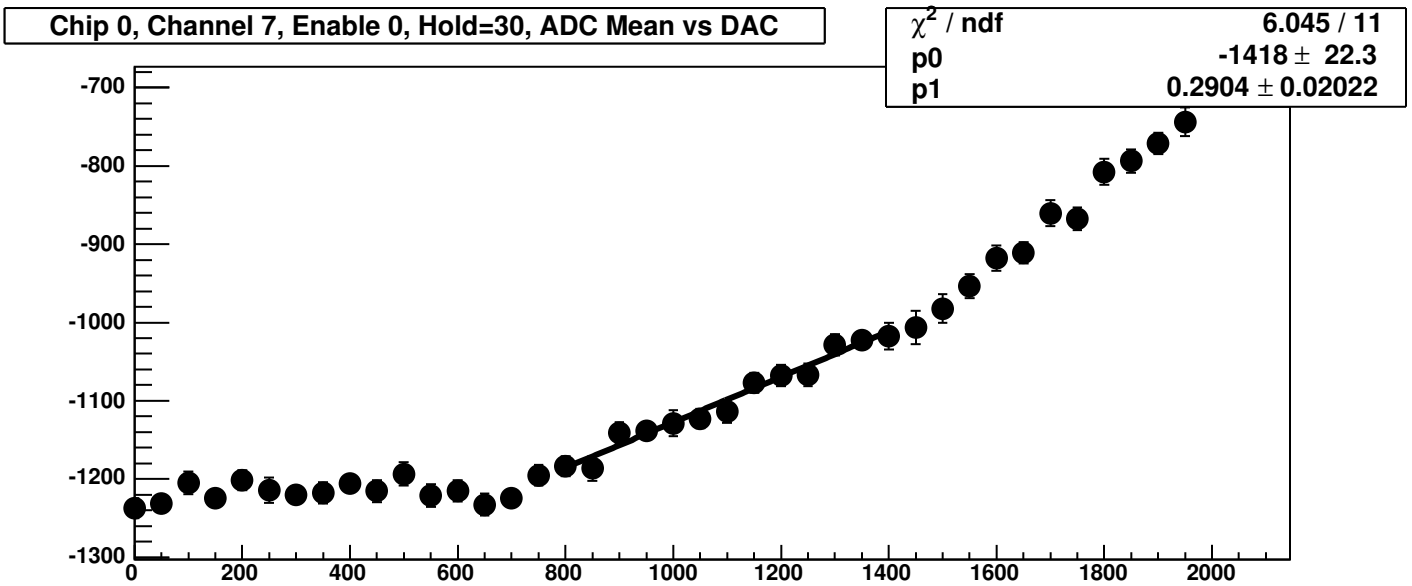
Chip 0, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC

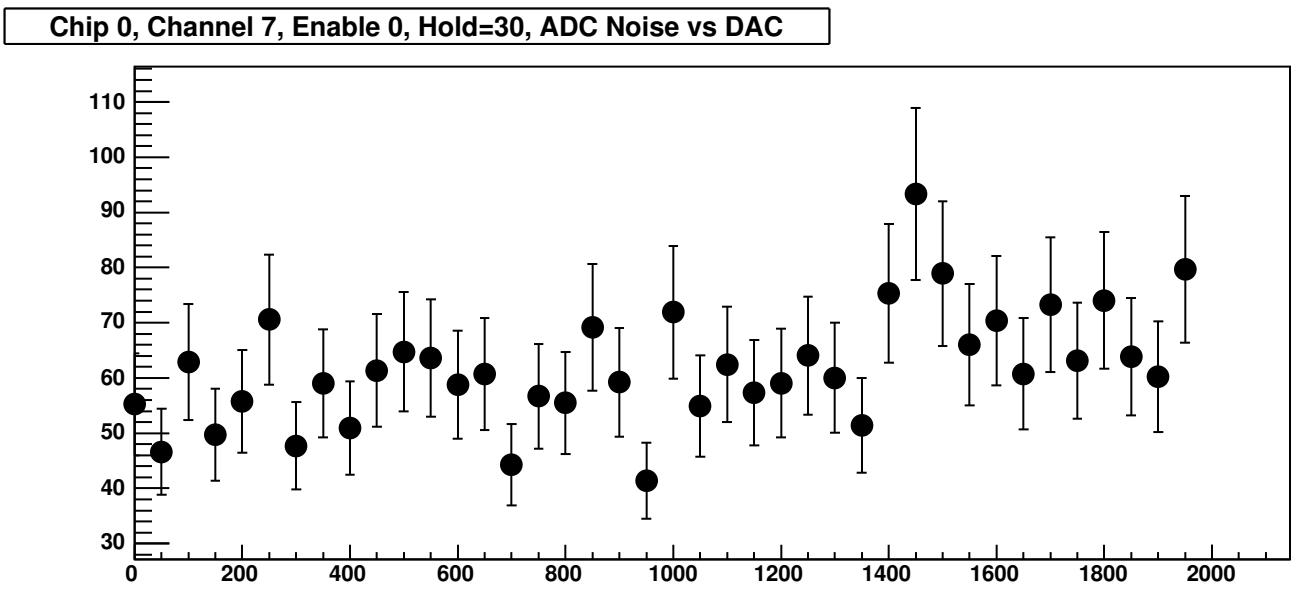


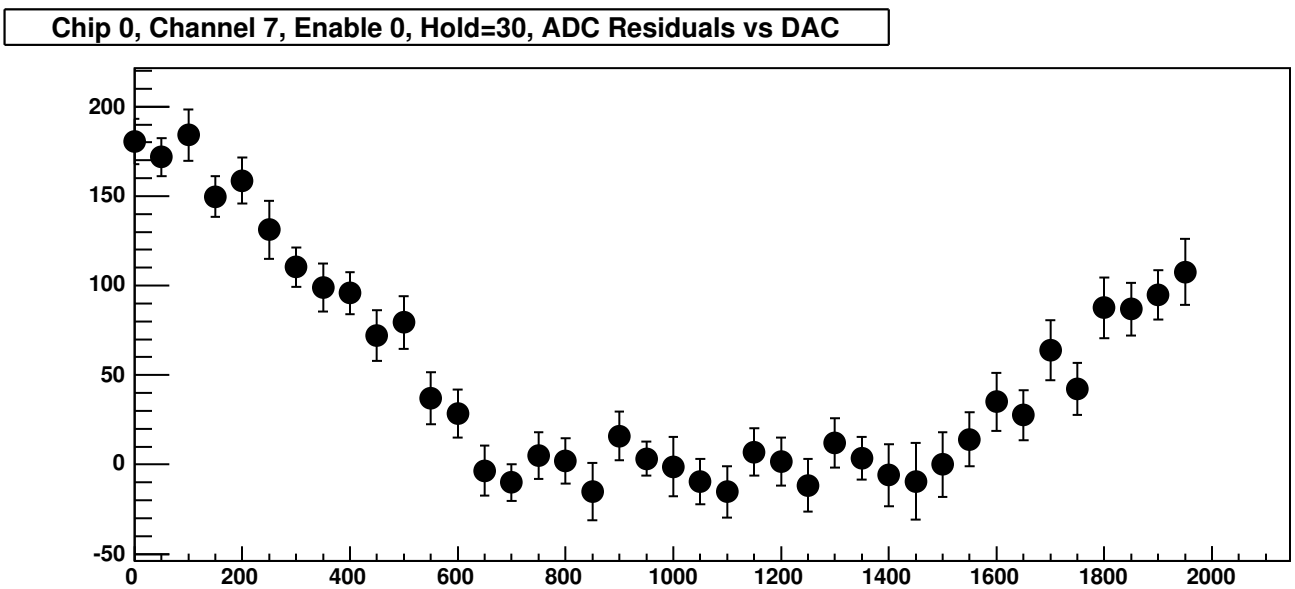

Chip 0, Channel 6, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 26.79 / 11
p0 -1396 ± 29.8
p1 1.058 ± 0.02683
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

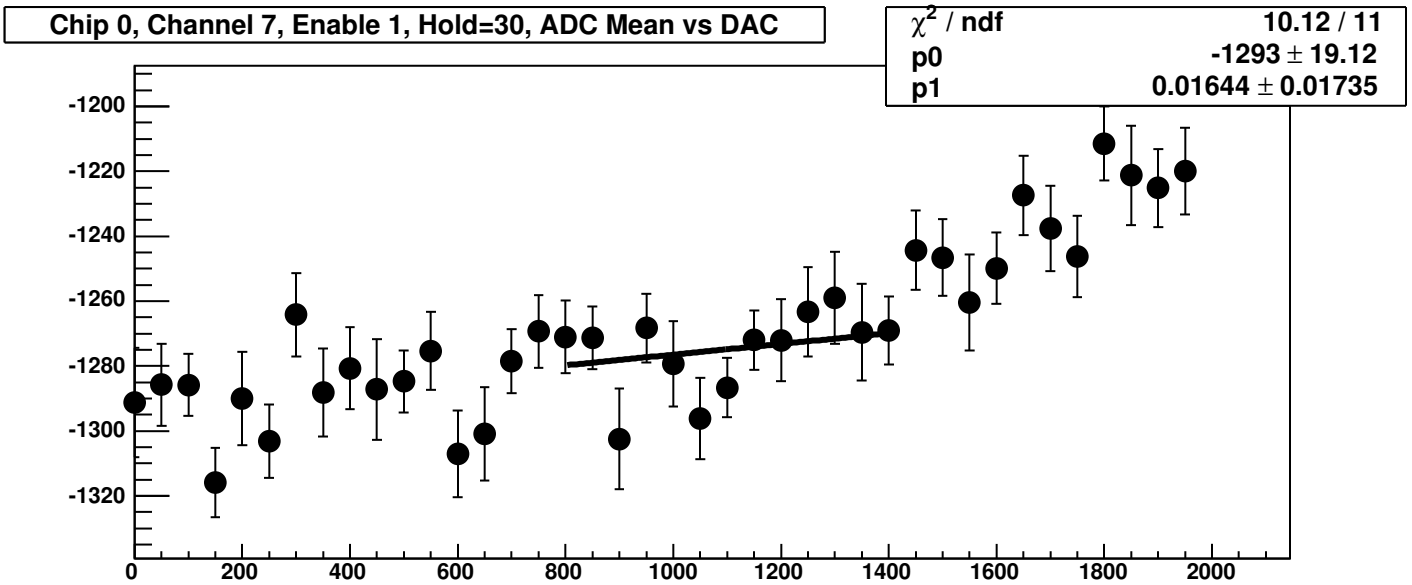

Chip 0, Channel 6, Enable 5, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

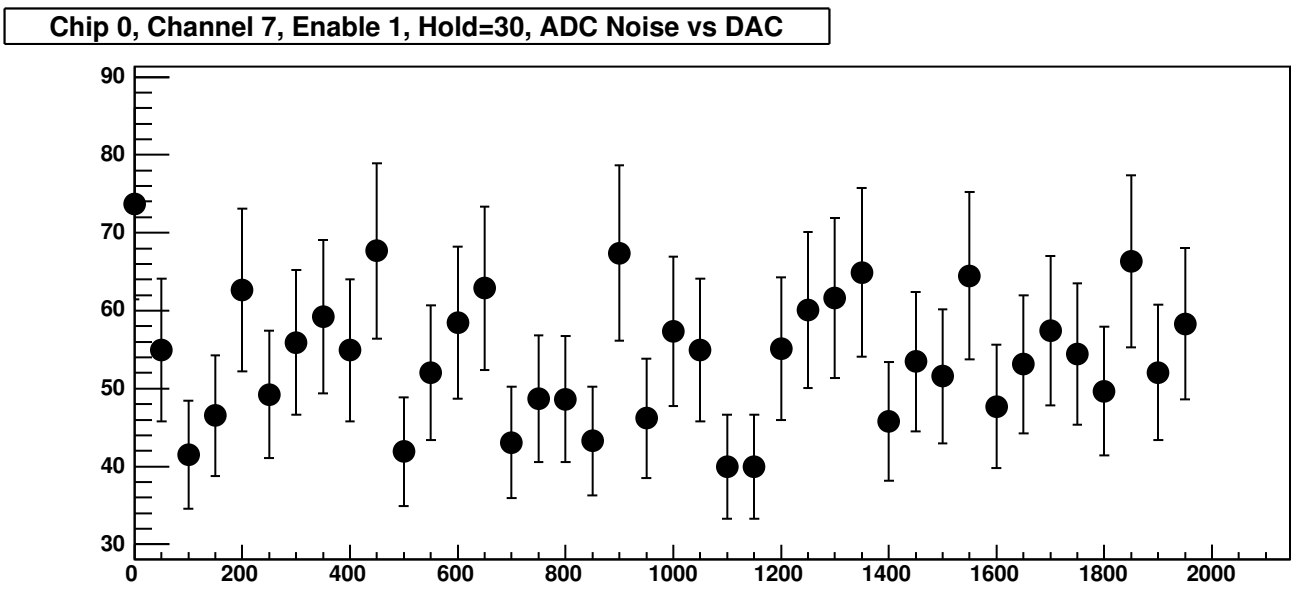

Chip 0, Channel 6, Enable 5, Hold=30, ADC Residuals vs DAC
600
400
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

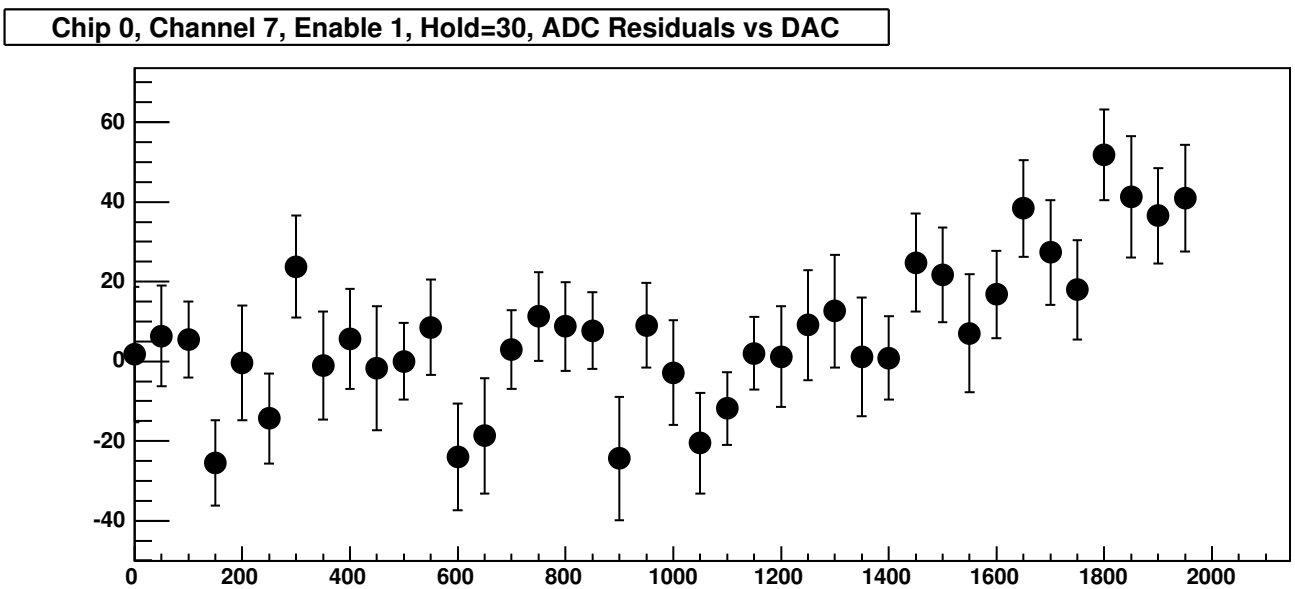

Chip 0, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 6.045 / 11
p0 -1418 ± 22.3
p1 0.2904 ± 0.02022
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

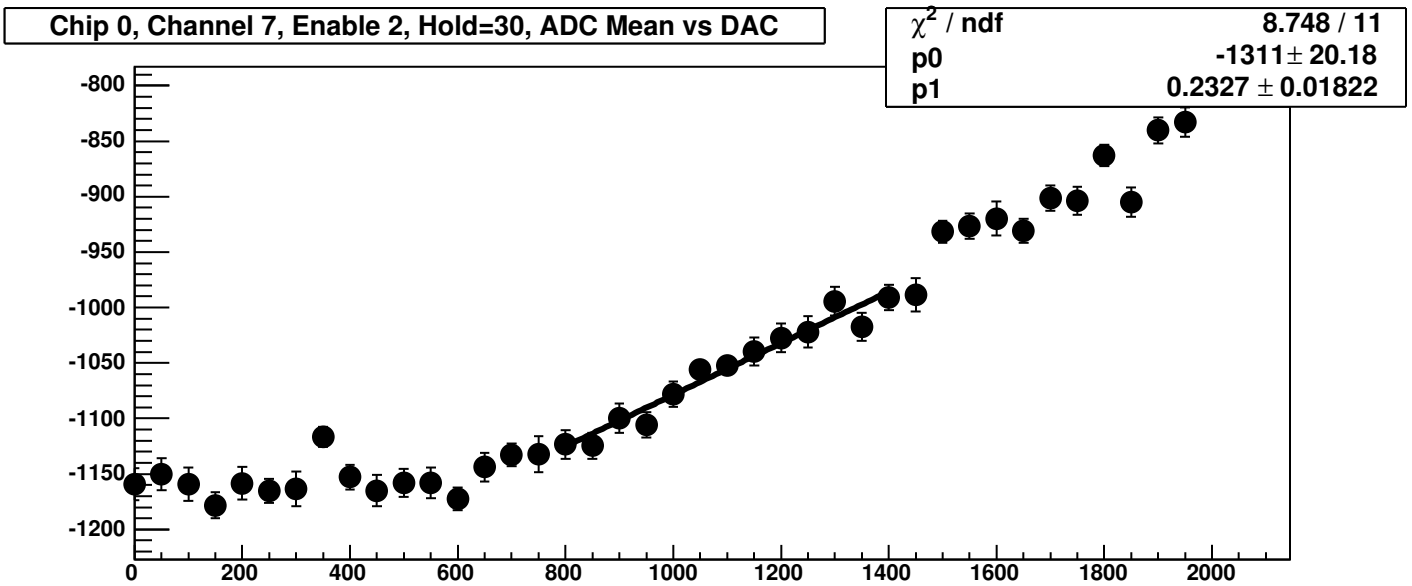

Chip 0, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

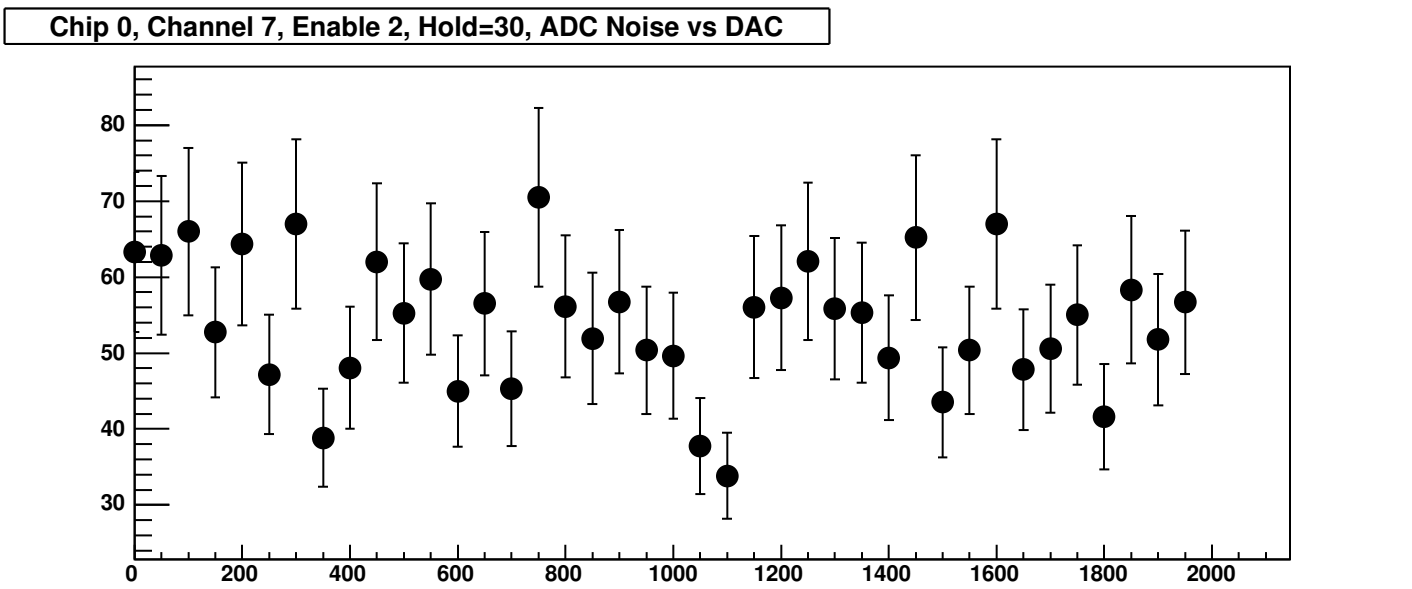

Chip 0, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

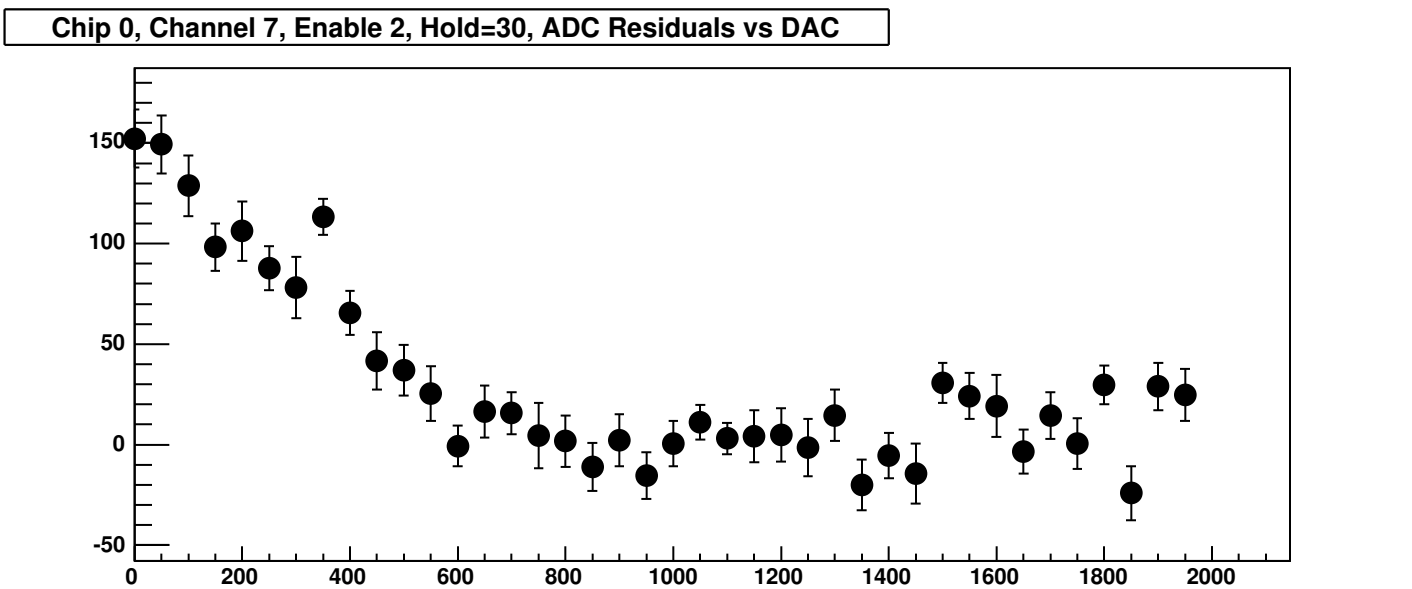

Chip 0, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 10.12 / 11
p0 -1293 ± 19.12
p1 0.01644 ± 0.01735
-1200
-1220
-1240
-1260
-1280
-1300
-1320
0
200
400
600
800
1000
1200
1400
1600
1800
2000

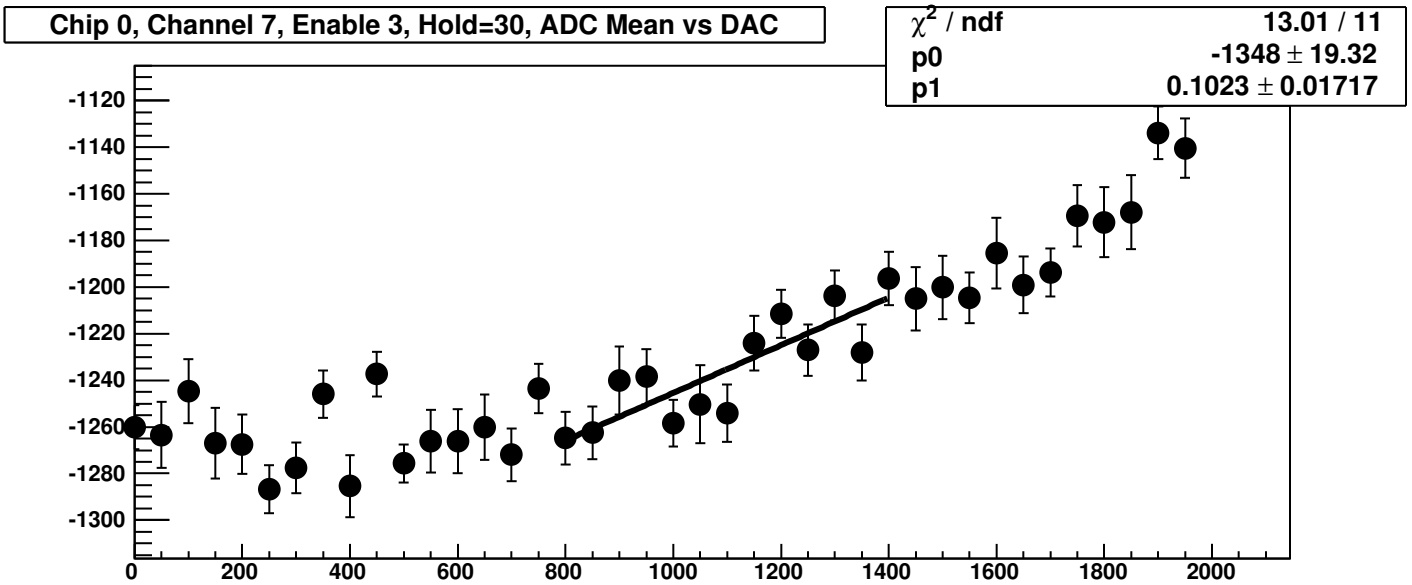

Chip 0, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

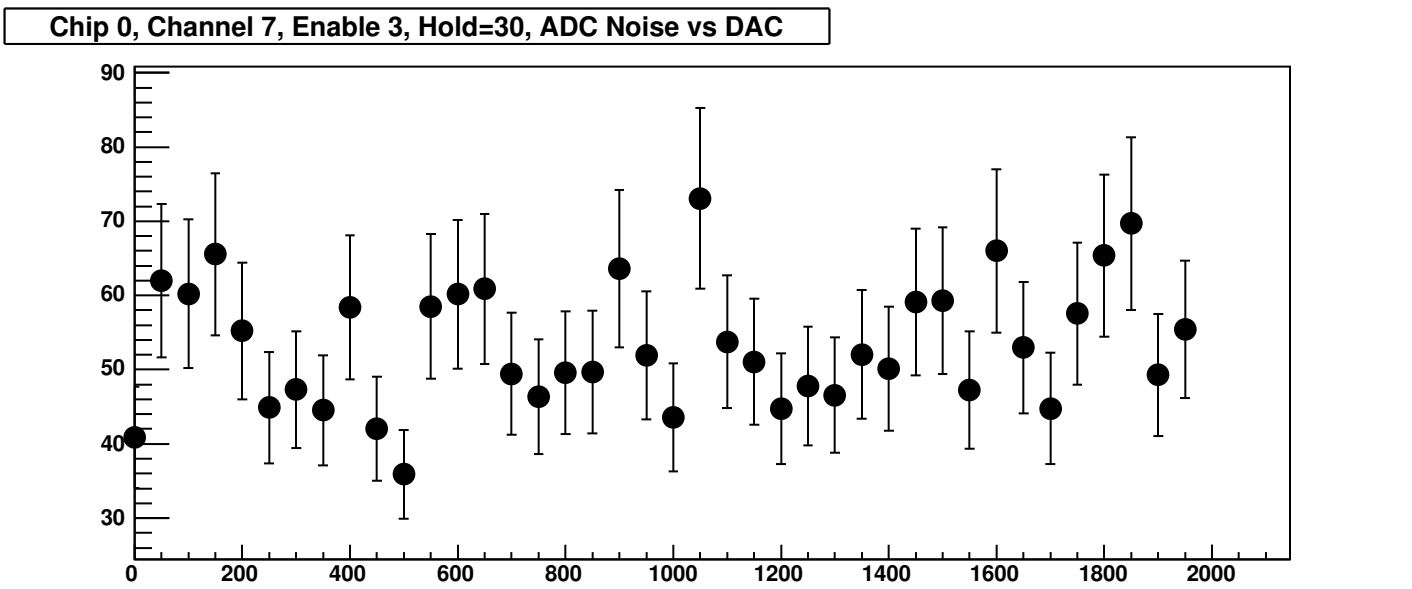

Chip 0, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

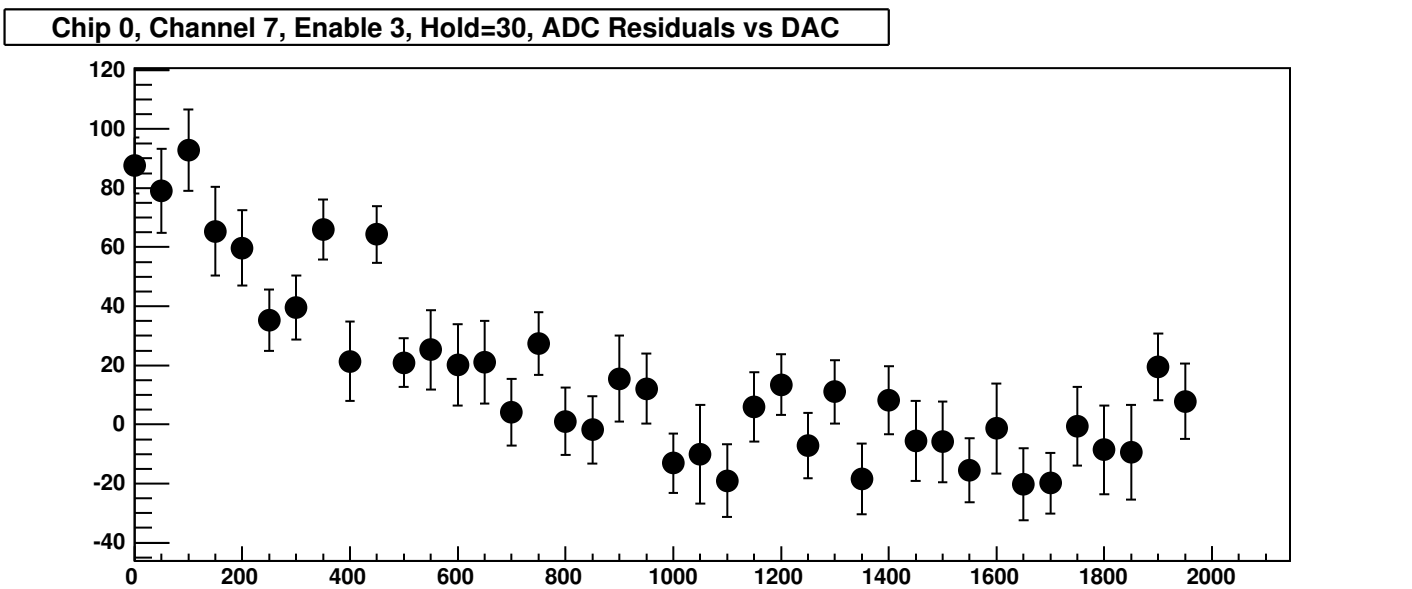

Chip 0, Channel 7, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 8.748 / 11
p0 -1311 ± 20.18
p1 0.2327 ± 0.01822
-800
-850
-900
-950
-1000
-1050
-1100
-1150
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

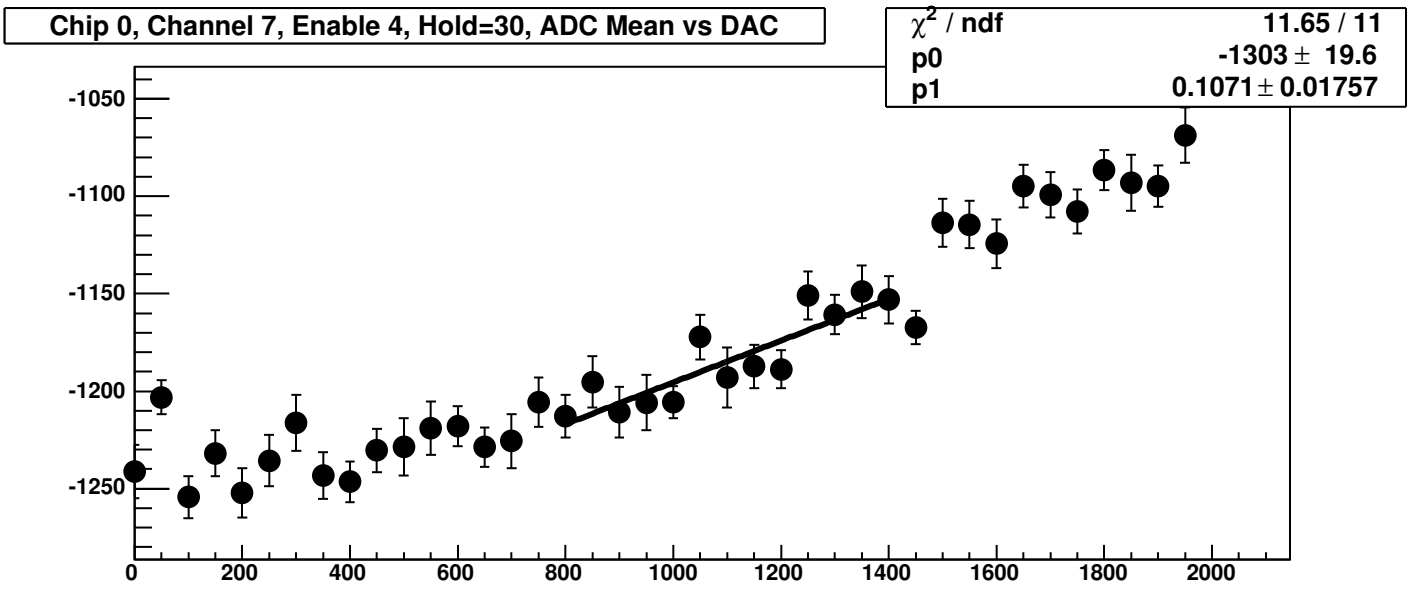

Chip 0, Channel 7, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

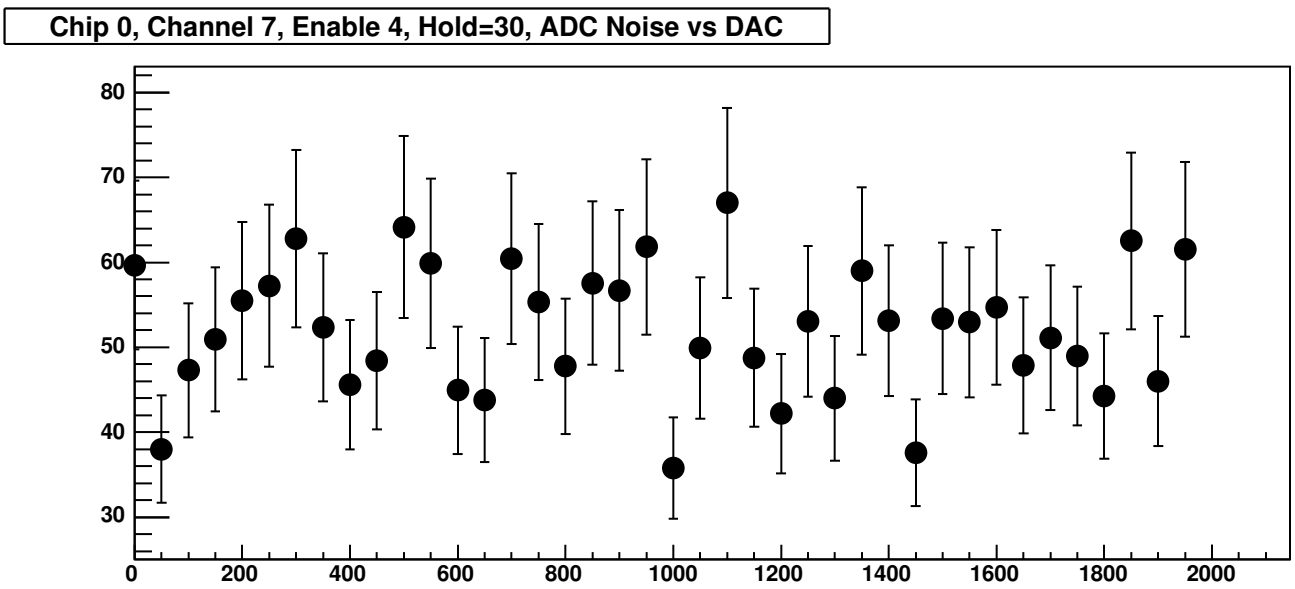

Chip 0, Channel 7, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

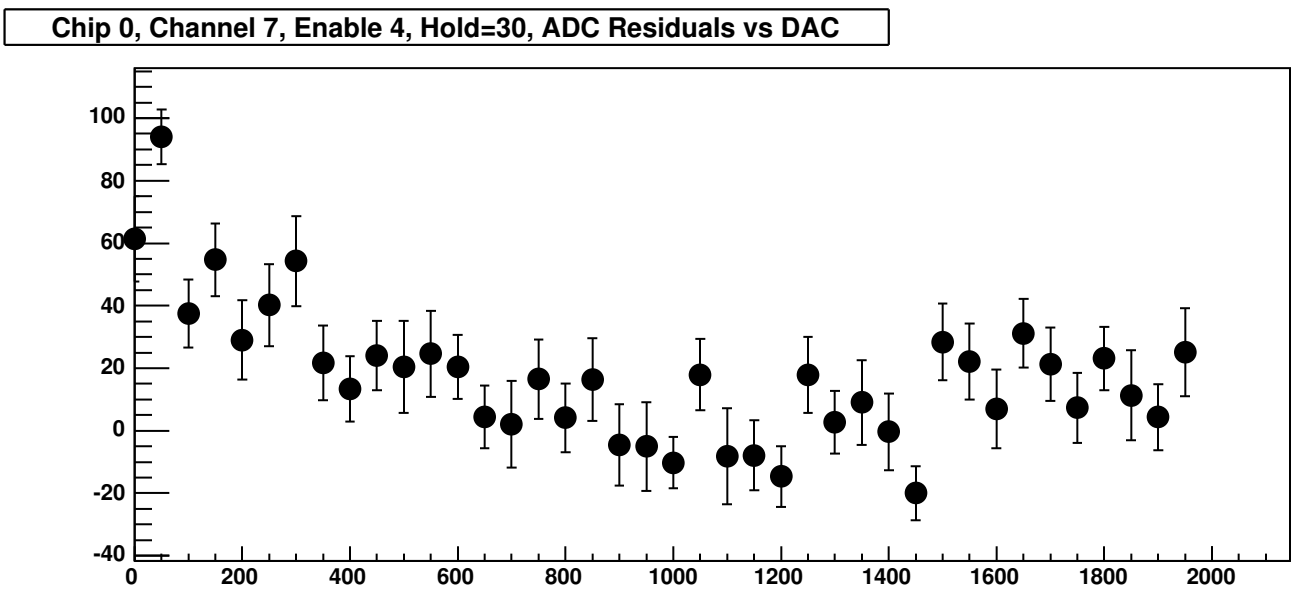

Chip 0, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 13.01 / 11
p0 -1348 ± 19.32
p1 0.1023 ± 0.01717
-1120
-1140
-1160
-1180
-1200
-1220
-1240
-1260
-1280
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

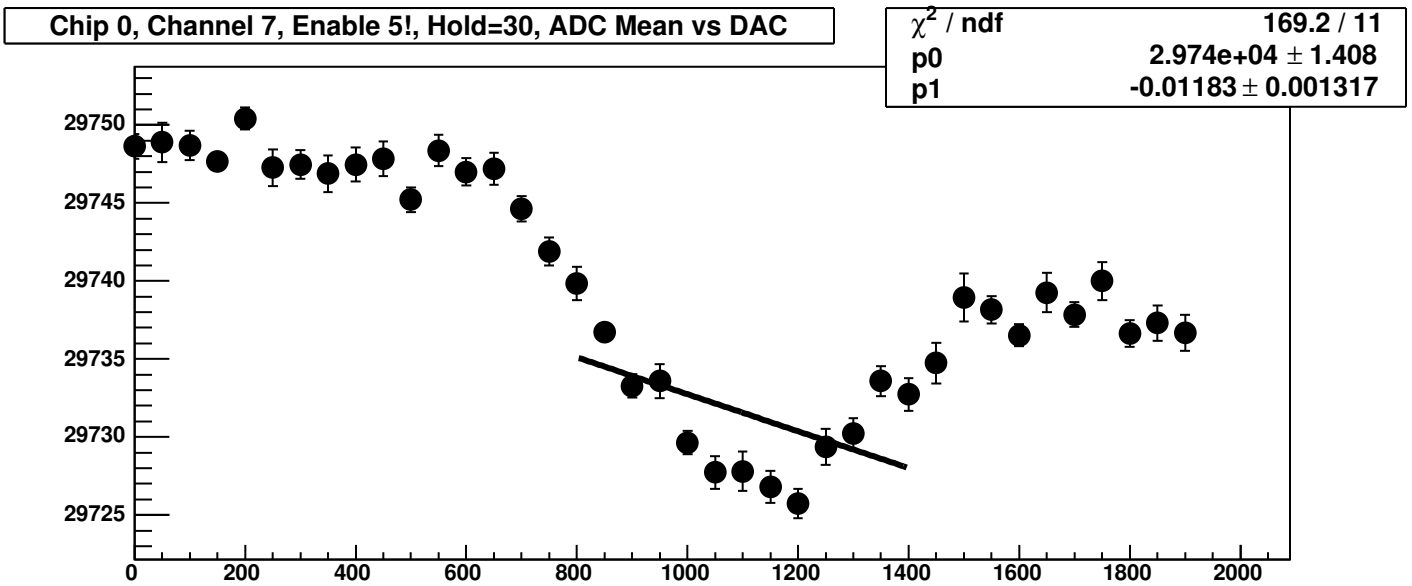

Chip 0, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

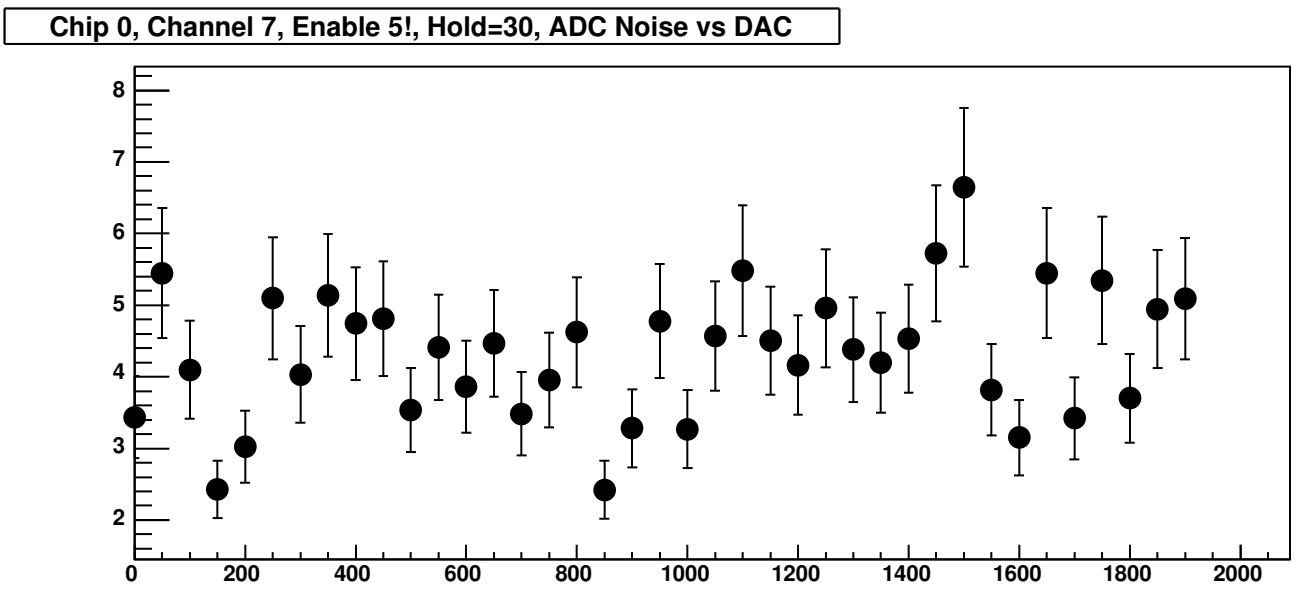

Chip 0, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

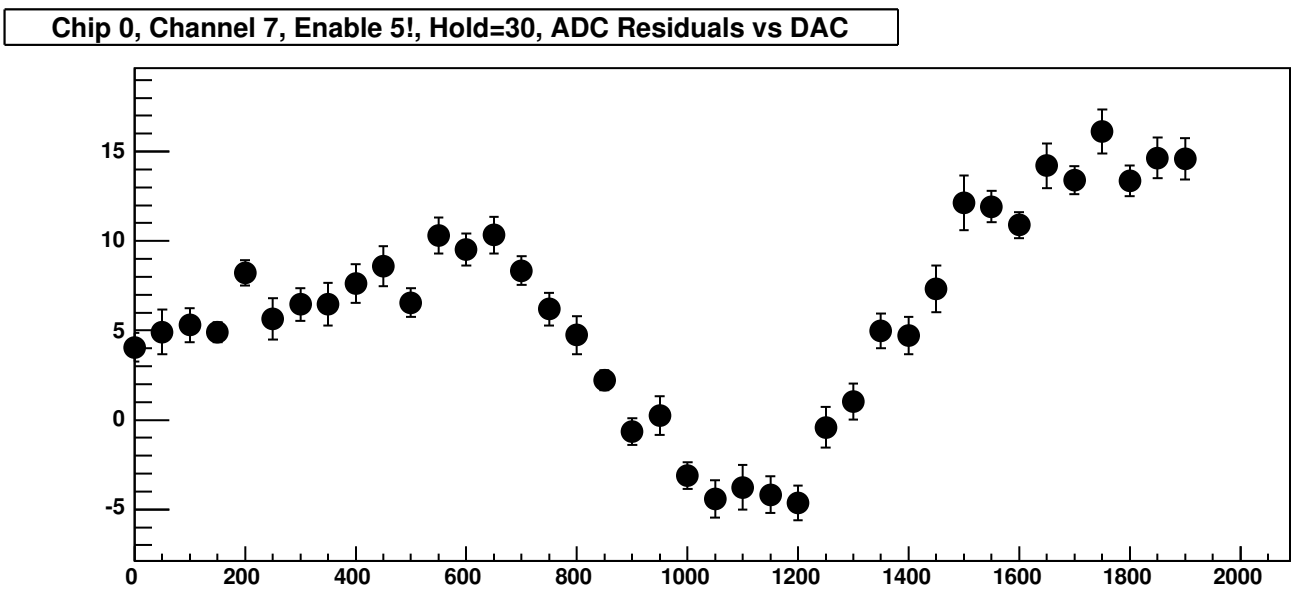

Chip 0, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 11.65 / 11
p0 -1303 ± 19.6
p1 0.1071 ± 0.01757
-1050
-1100
-1150
-1200
-1250
0
200
400
600
800
1000
1200
1400
1600
1800
2000

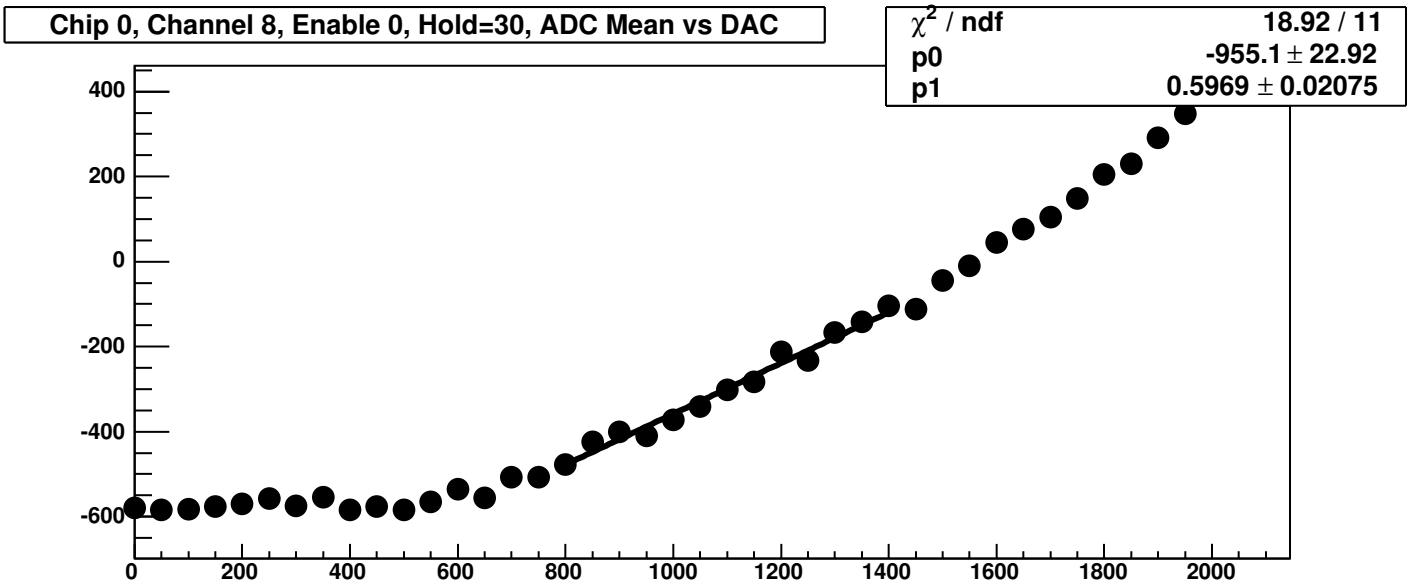

Chip 0, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

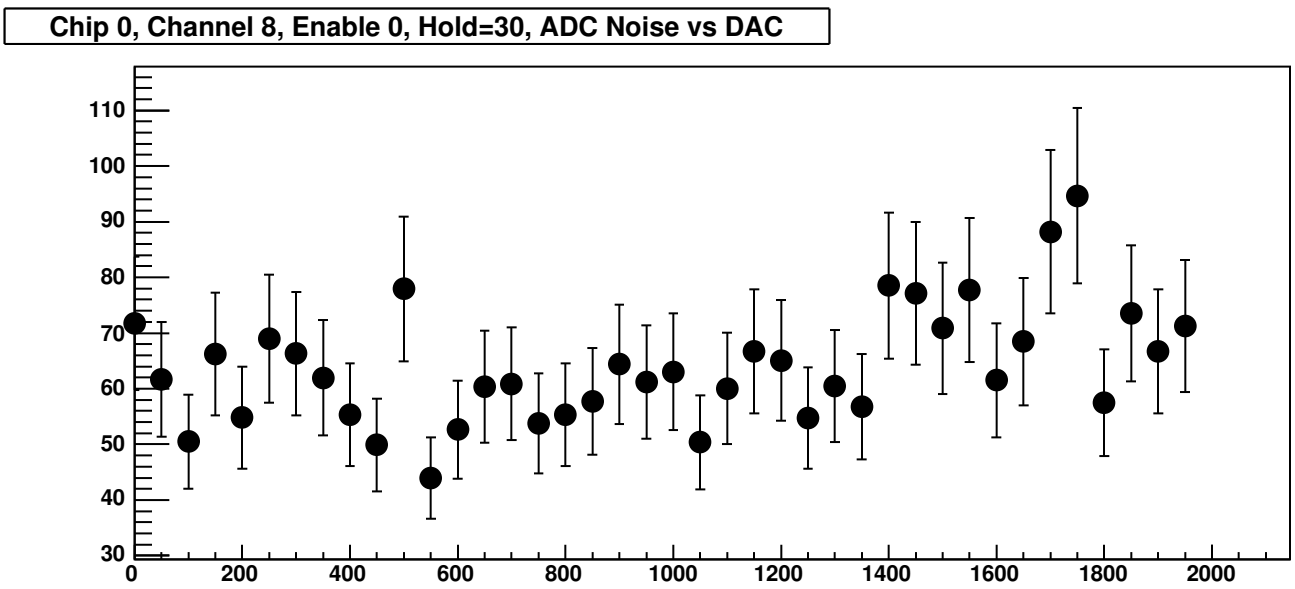

Chip 0, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

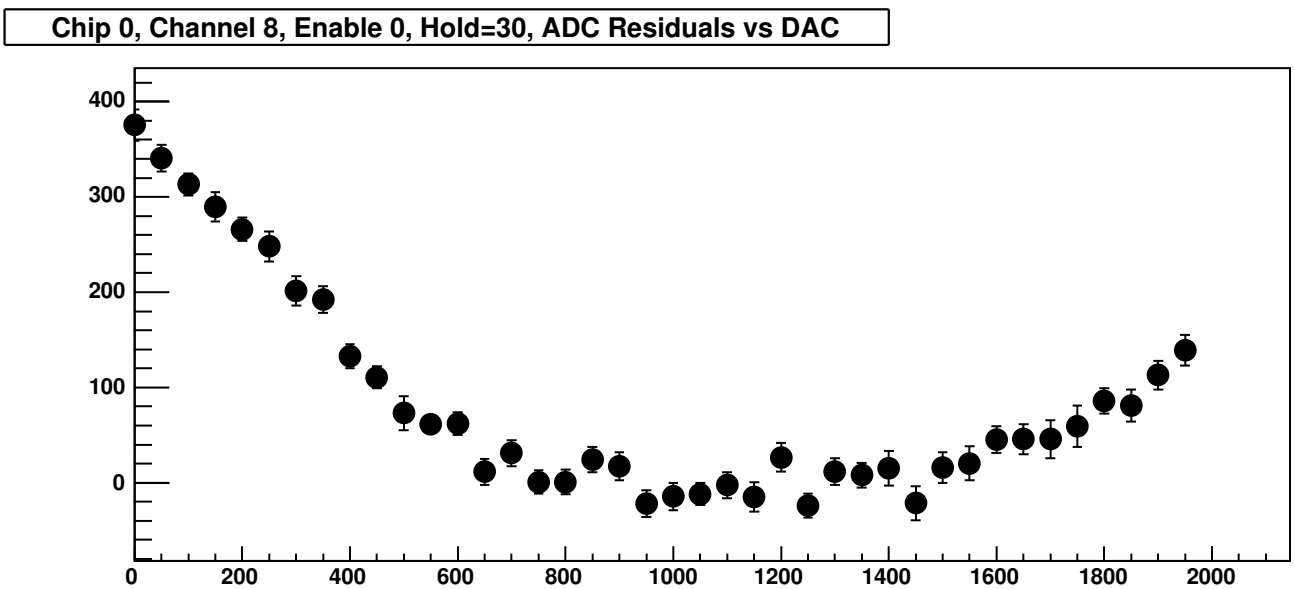

Chip 0, Channel 7, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 169.2 / 11
p0 2.974e+04 ± 1.408
p1 -0.01183 ± 0.001317
29750
29745
29740
29735
29730
29725
0
200
400
600
800
1000
1200
1400
1600
1800
2000

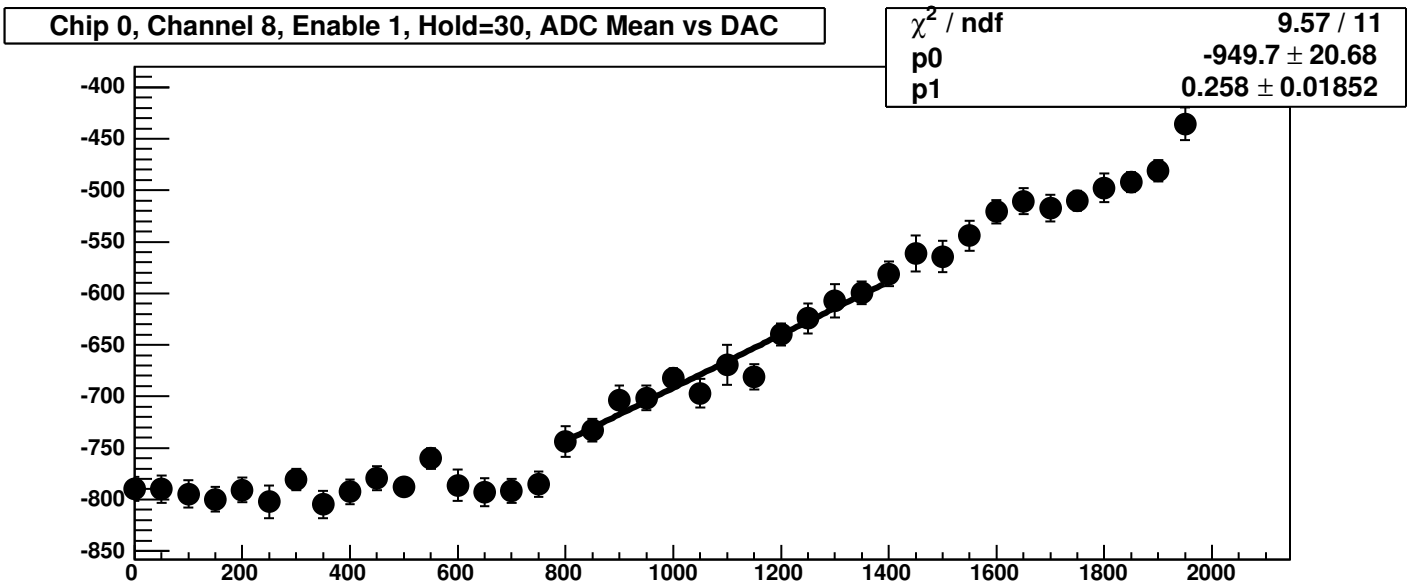

Chip 0, Channel 7, Enable 5!, Hold=30, ADC Noise vs DAC
8
7
6
5
4
3
2
0
200
400
600
800
1000
1200
1400
1600
1800
2000

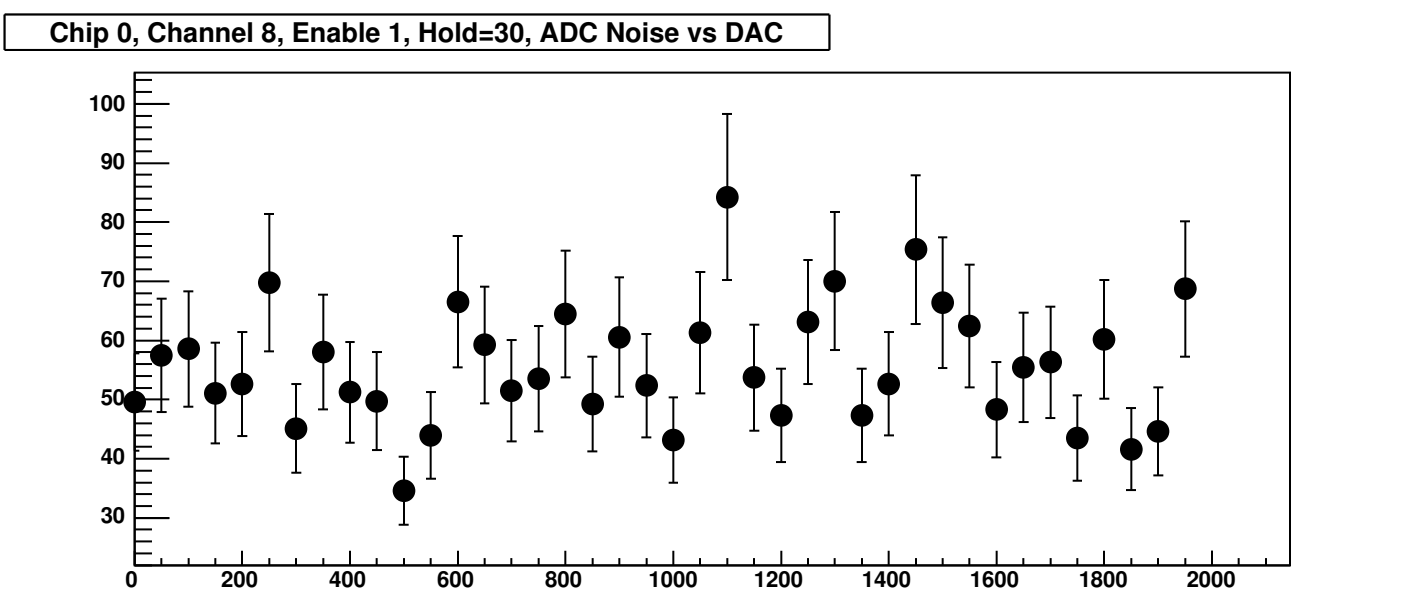

Chip 0, Channel 7, Enable 5!, Hold=30, ADC Residuals vs DAC
15
10
5
0
-5
0
200
400
600
800
1000
1200
1400
1600
1800
2000

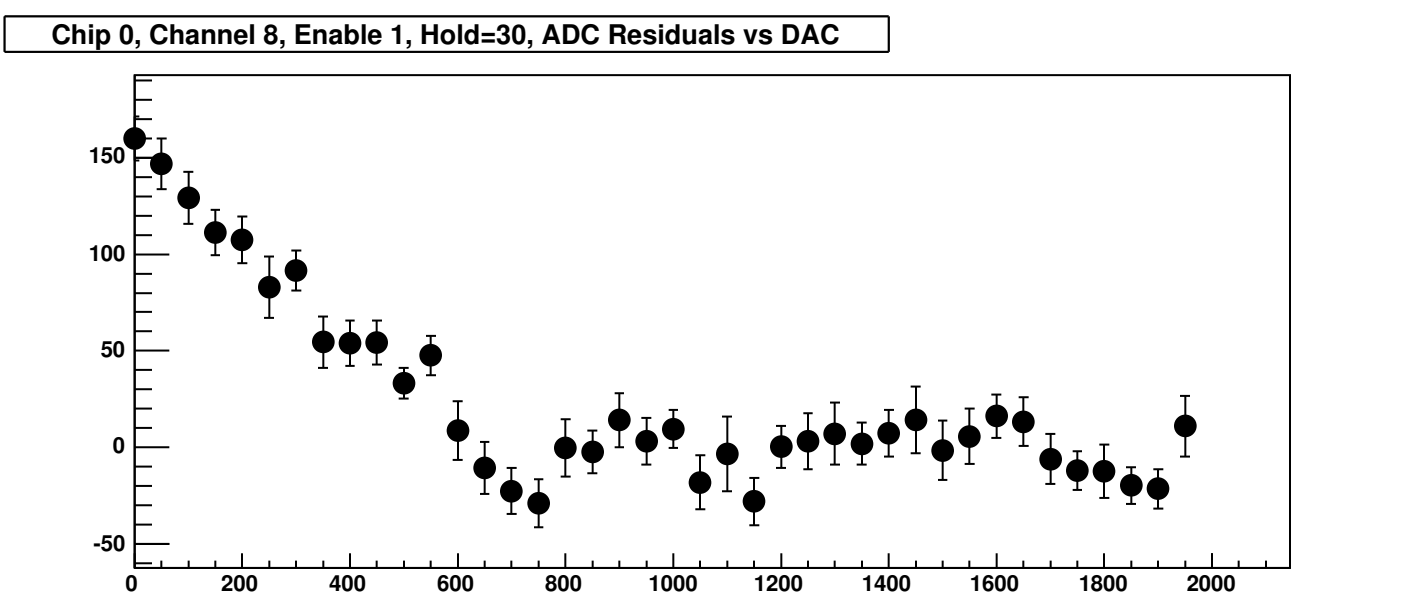

Chip 0, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 18.92 / 11
p0 -955.1 ± 22.92
p1 0.5969 ± 0.02075
400
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

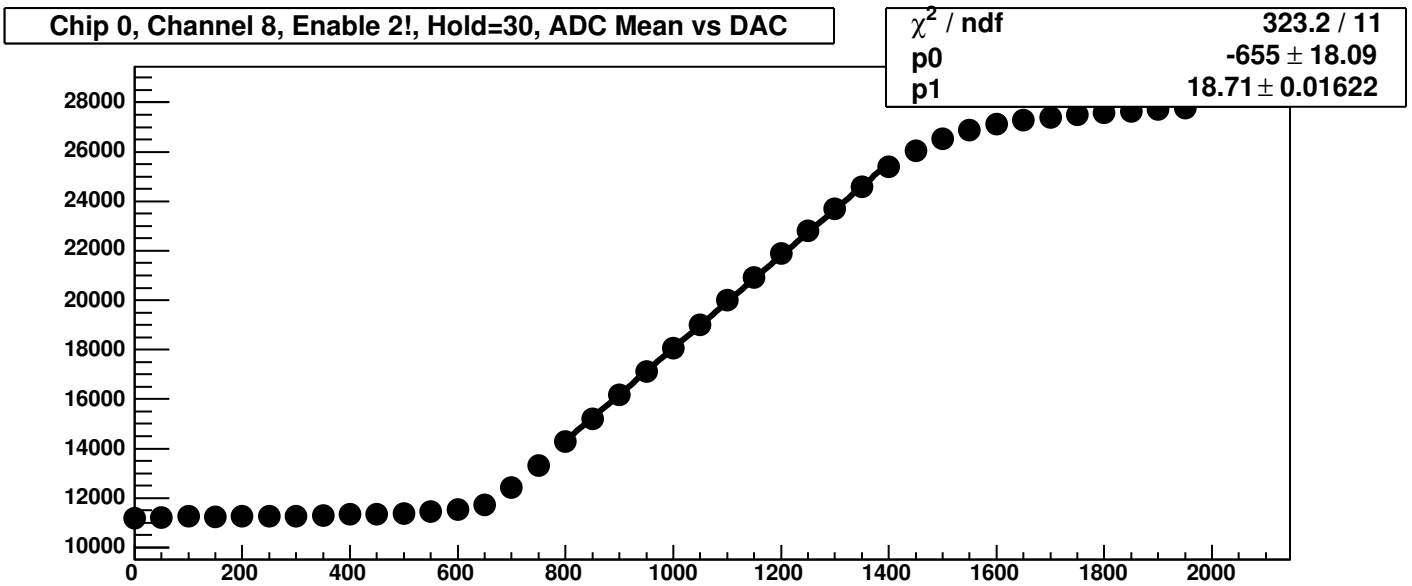

Chip 0, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

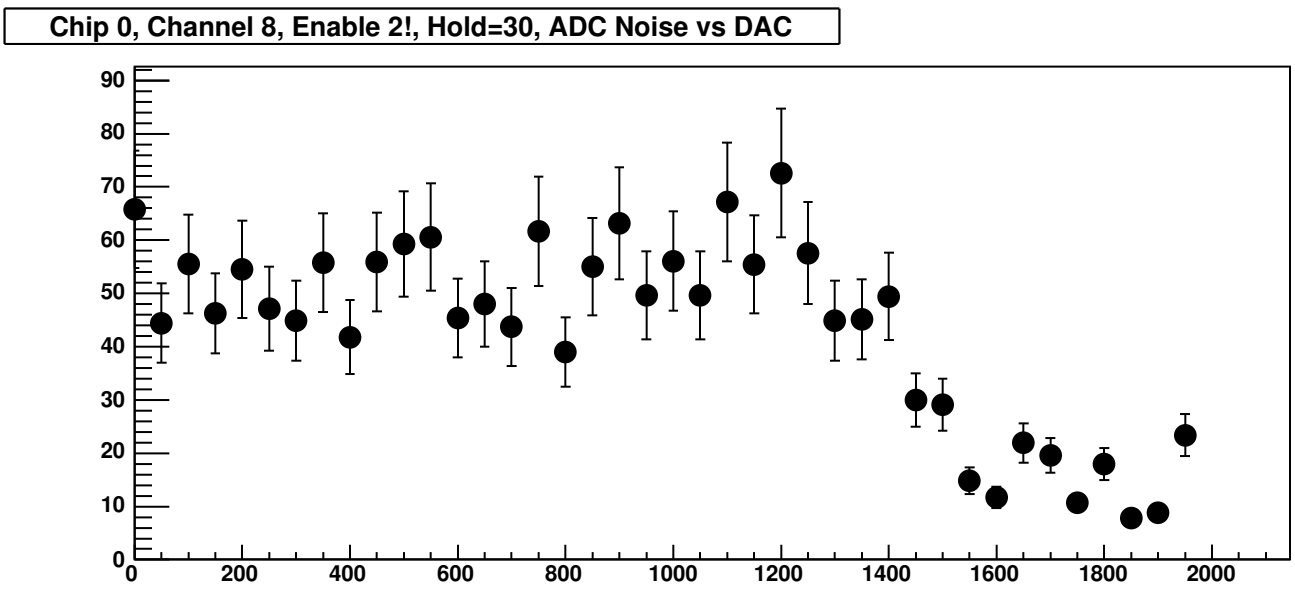

Chip 0, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC
400
300
200
100
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

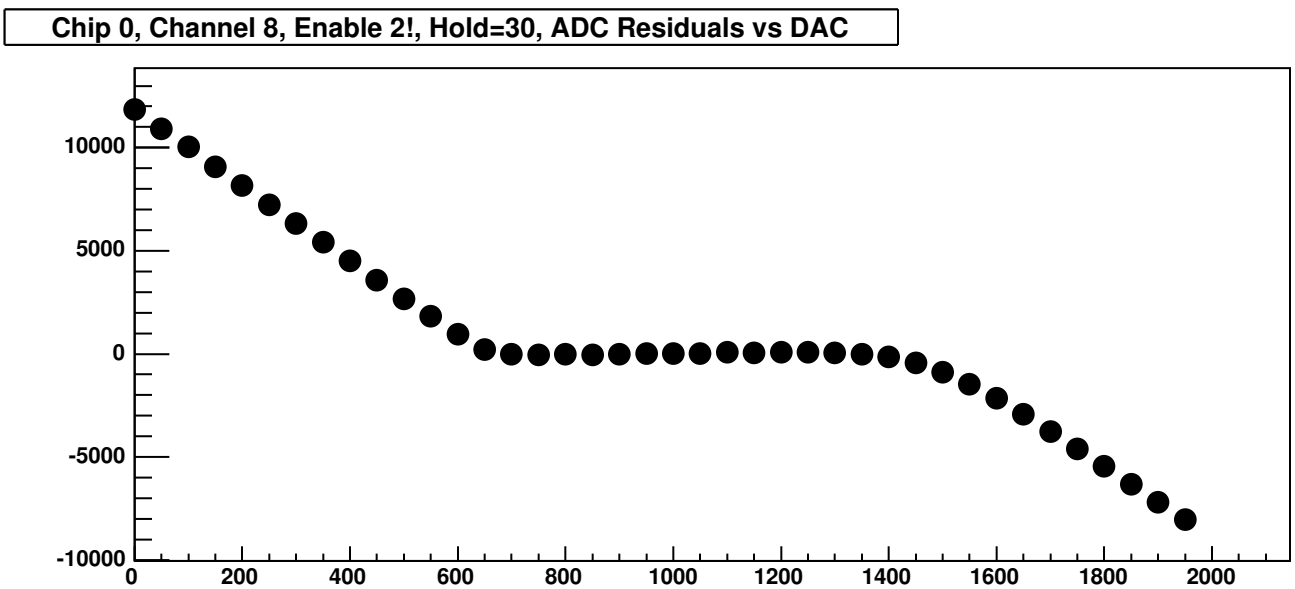

Chip 0, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 9.57 / 11
p0 -949.7 ± 20.68
p1 0.258 ± 0.01852
-400
-450
-500
-550
-600
-650
-700
-750
-800
-850
0
200
400
600
800
1000
1200
1400
1600
1800
2000

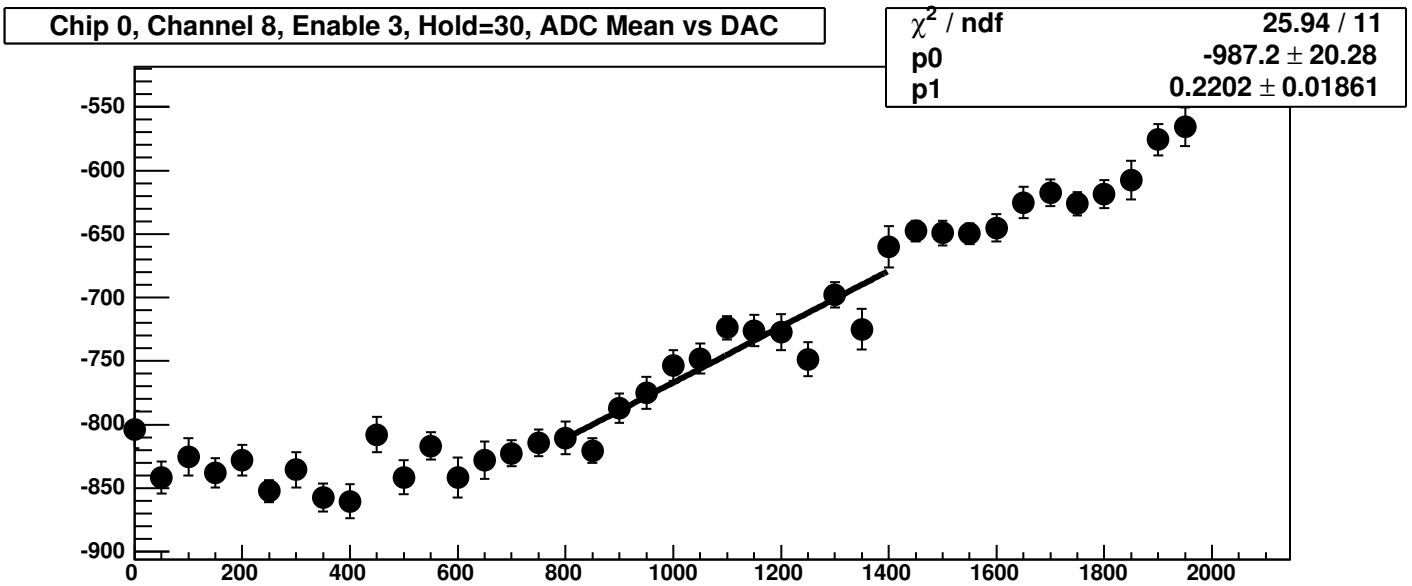

Chip 0, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

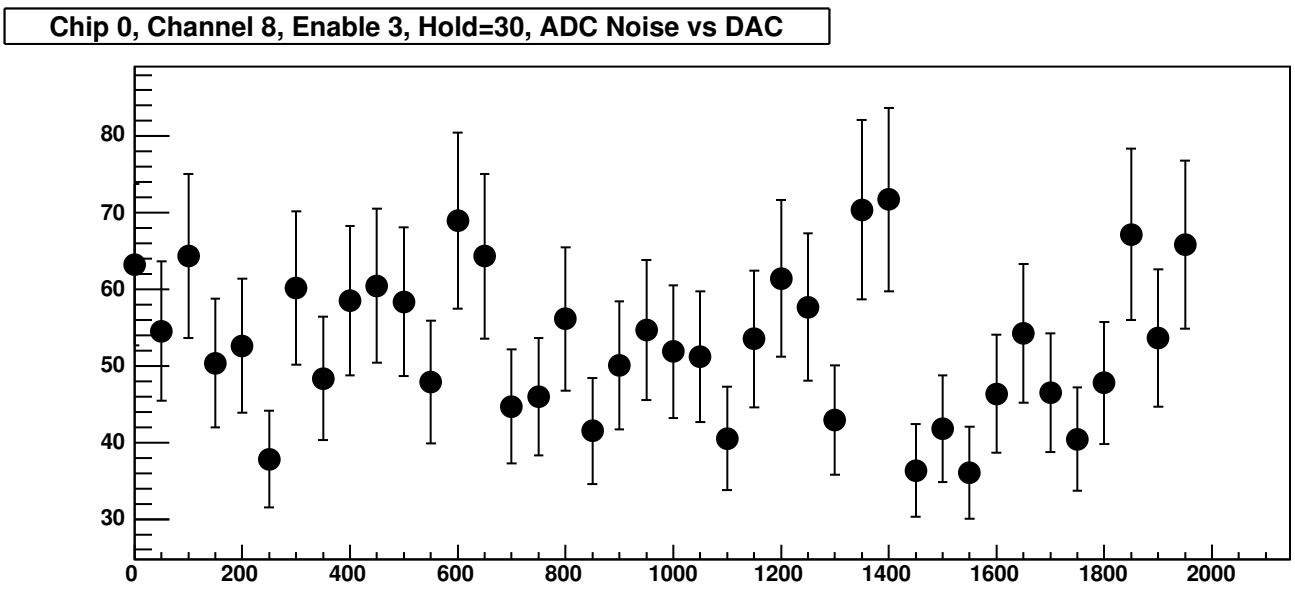

Chip 0, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

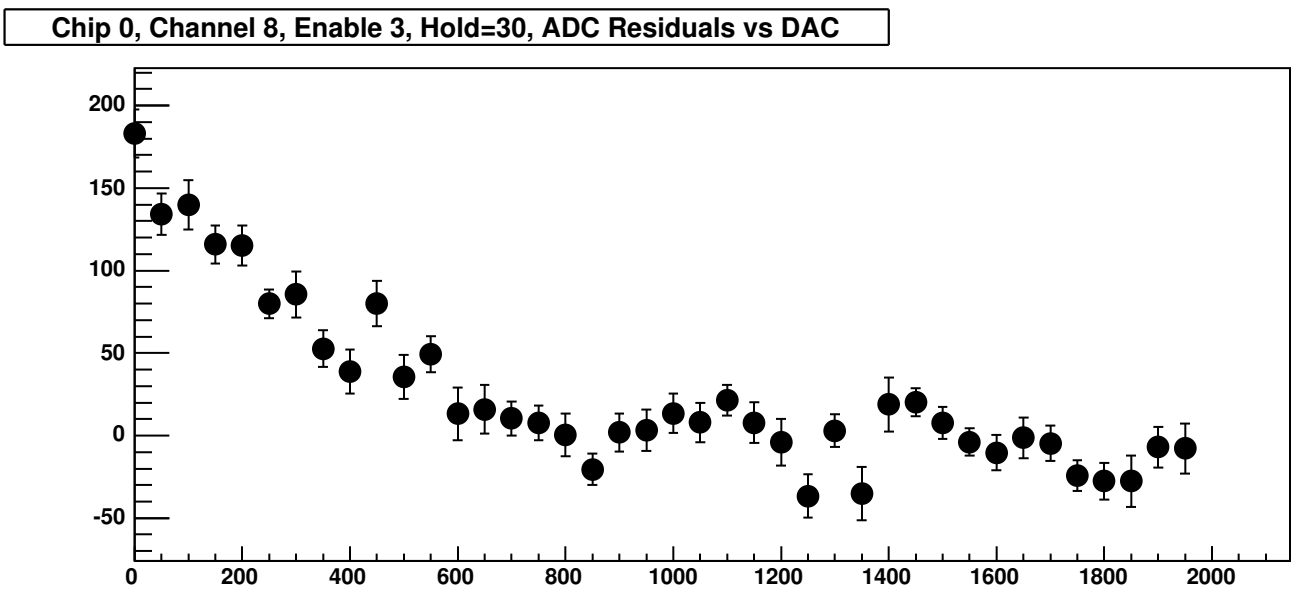

Chip 0, Channel 8, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 323.2 / 11
p0 -655 ± 18.09
p1 18.71 ± 0.01622
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

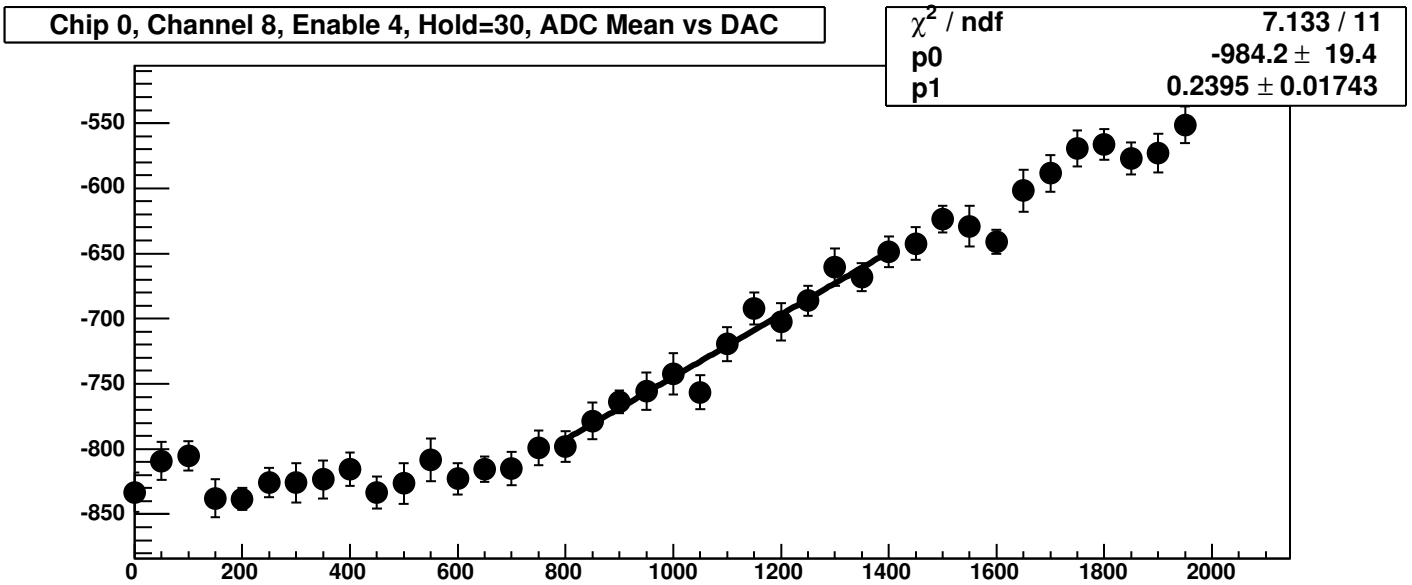

Chip 0, Channel 8, Enable 2!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

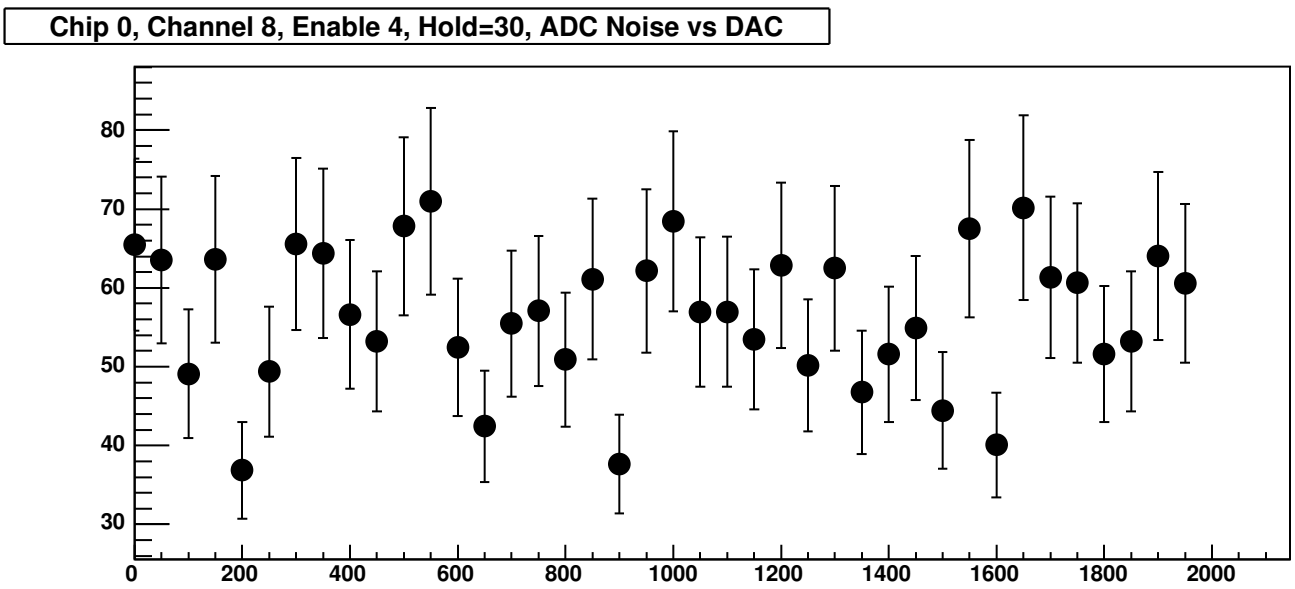

Chip 0, Channel 8, Enable 2!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

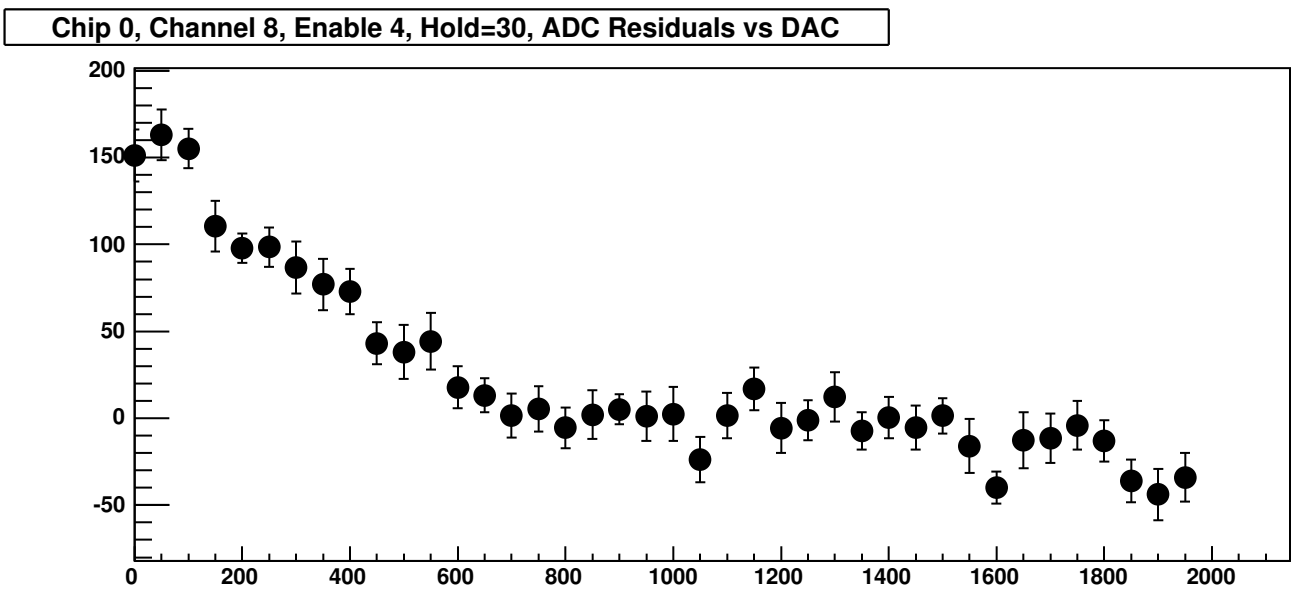

Chip 0, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 25.94 / 11
p0 -987.2 ± 20.28
p1 0.2202 ± 0.01861
-550
-600
-650
-700
-750
-800
-850
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

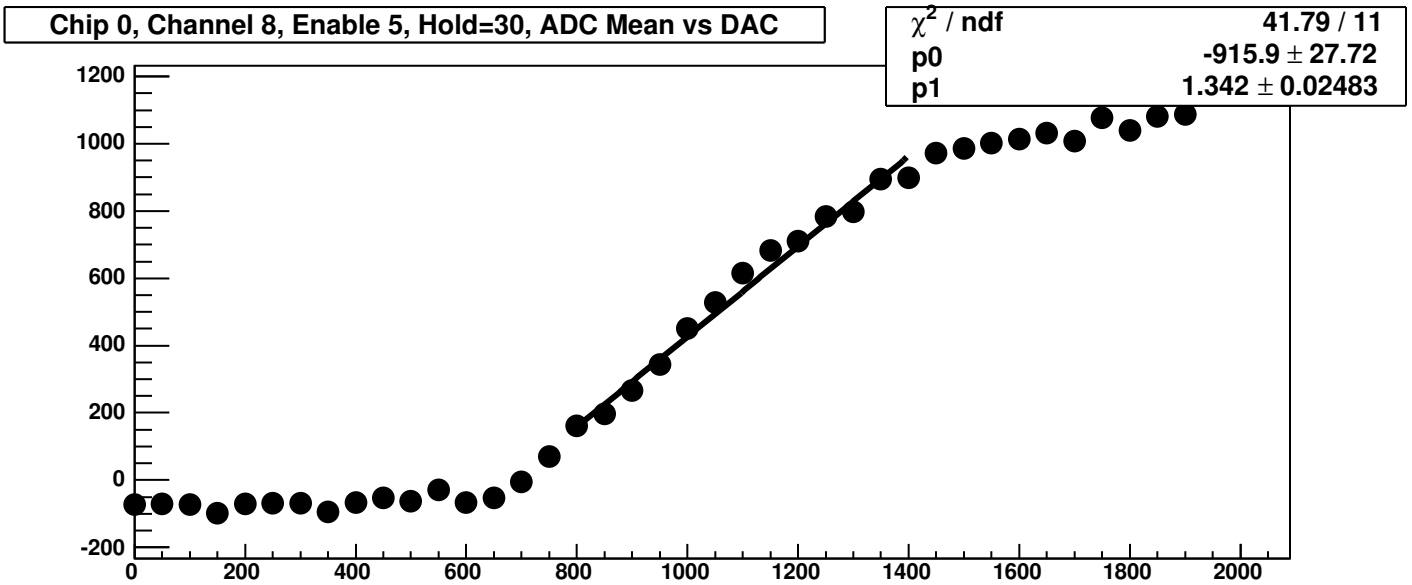

Chip 0, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

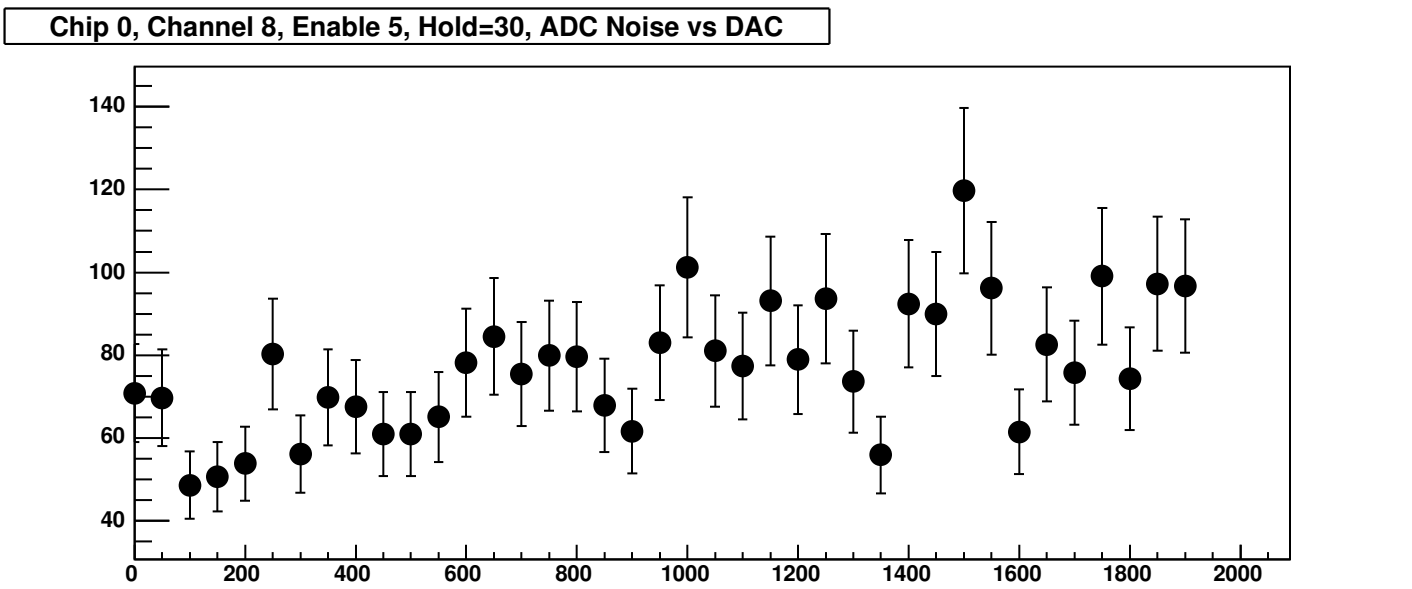

Chip 0, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

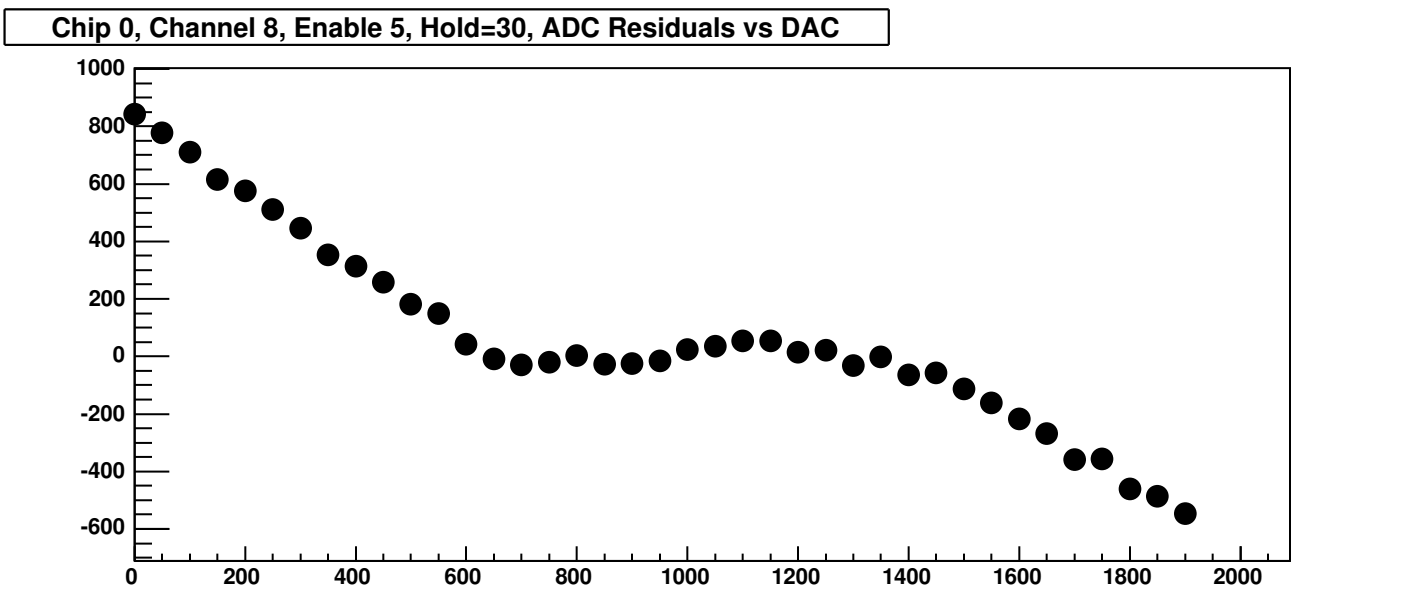

Chip 0, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7.133 / 11
p0 -984.2 ± 19.4
p1 0.2395 ± 0.01743
-550
-600
-650
-700
-750
-800
-850
0
200
400
600
800
1000
1200
1400
1600
1800
2000

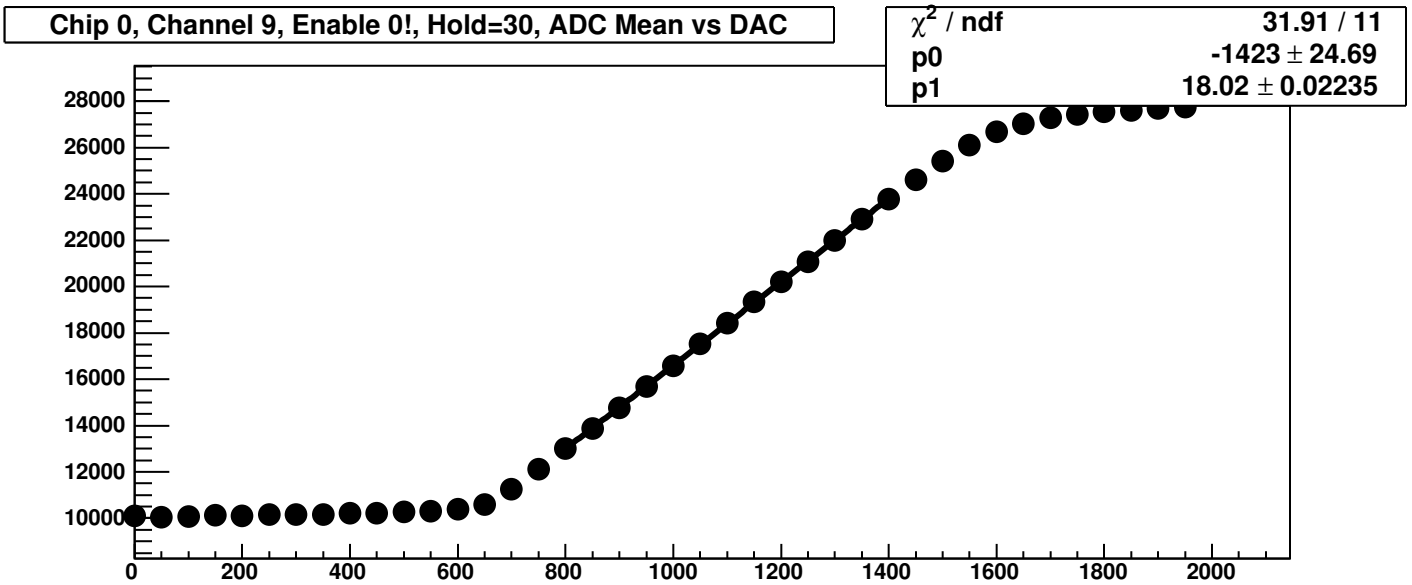

Chip 0, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

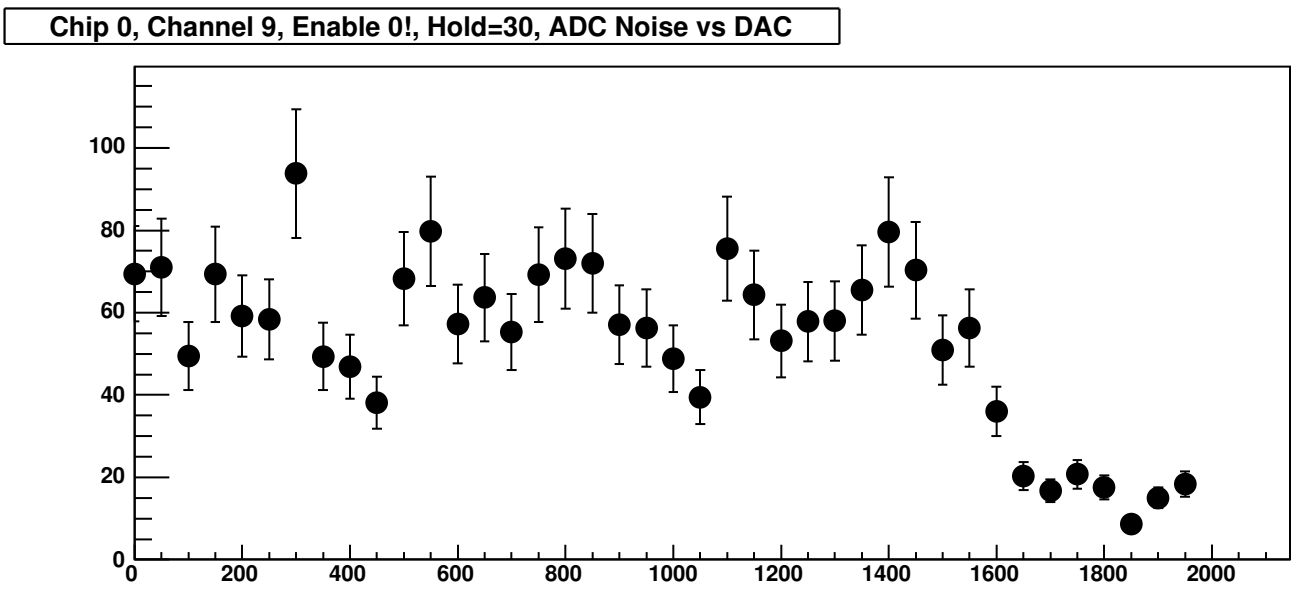

Chip 0, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

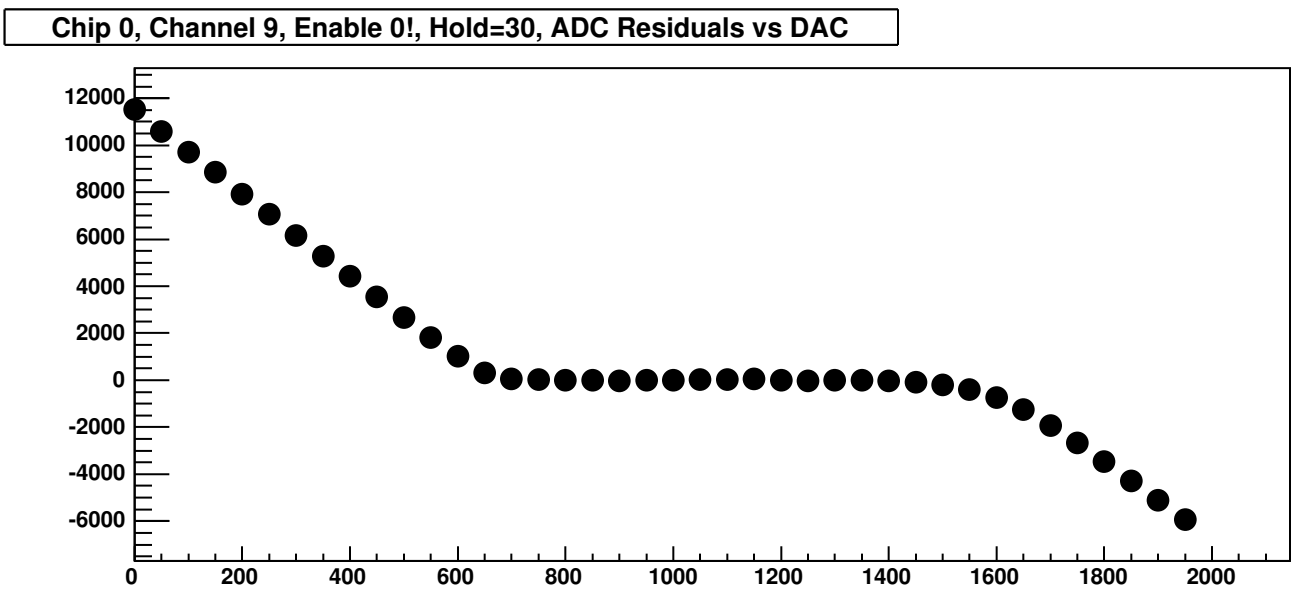

Chip 0, Channel 8, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 41.79 / 11
p0 -915.9 ± 27.72
p1 1.342 ± 0.02483
1200
1000
800
600
400
200
0
-200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

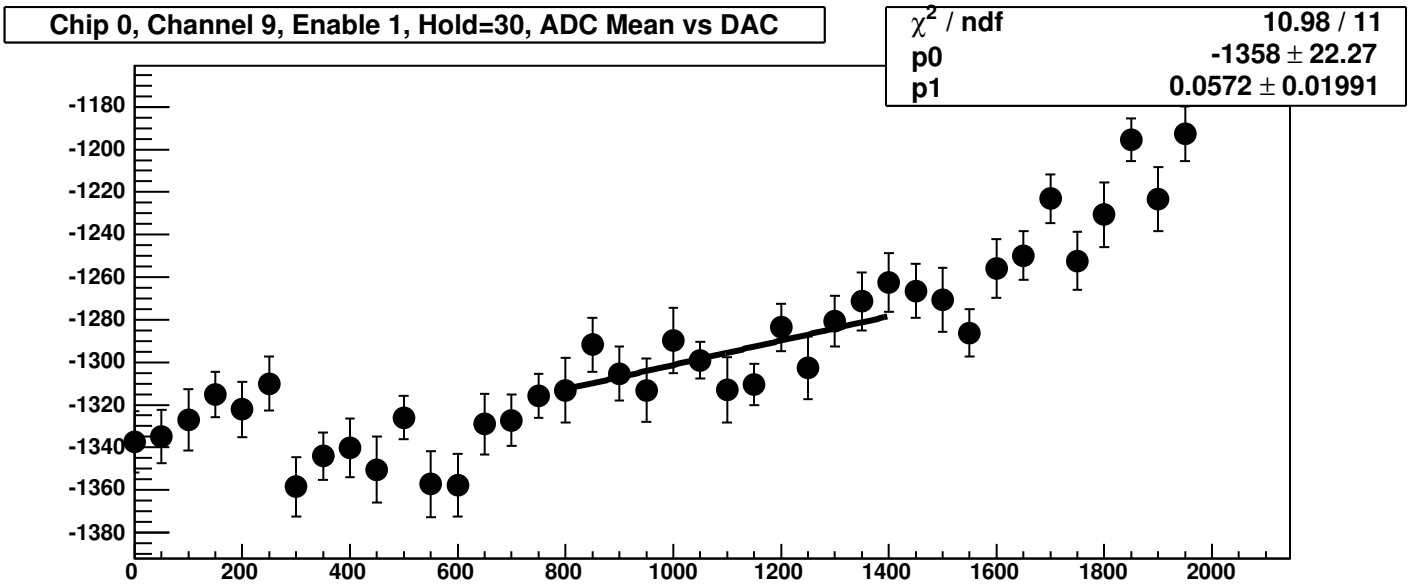

Chip 0, Channel 8, Enable 5, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

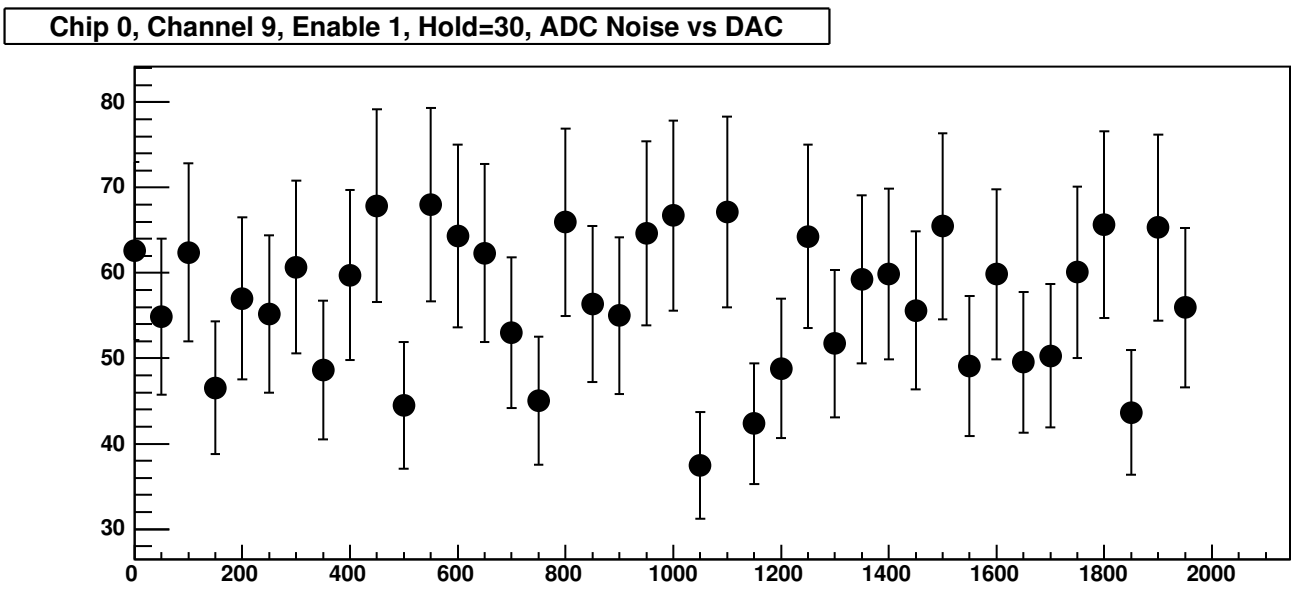

Chip 0, Channel 8, Enable 5, Hold=30, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

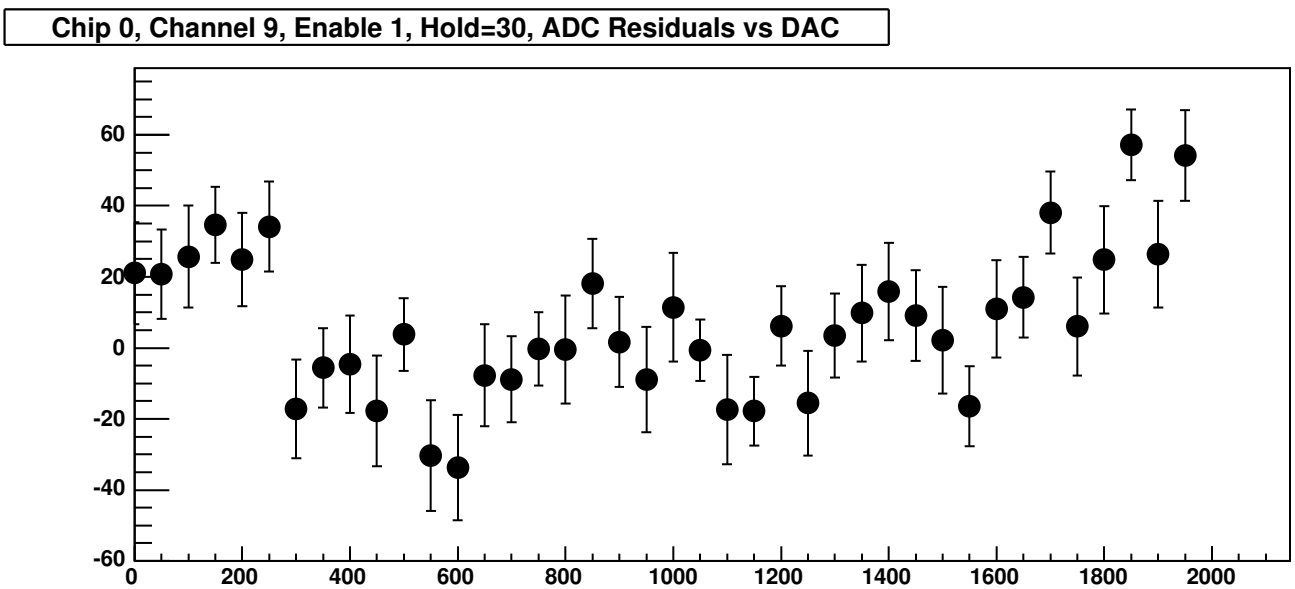

Chip 0, Channel 9, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 31.91 / 11
p0 -1423 ± 24.69
p1 18.02 ± 0.02235
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

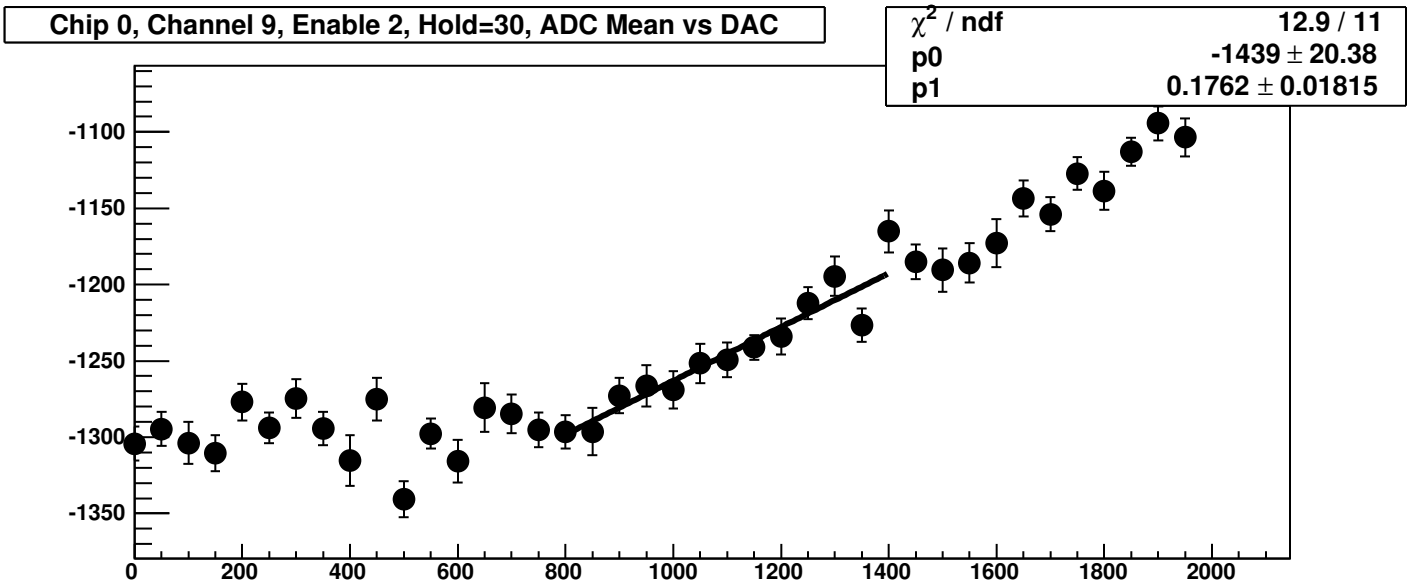

Chip 0, Channel 9, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

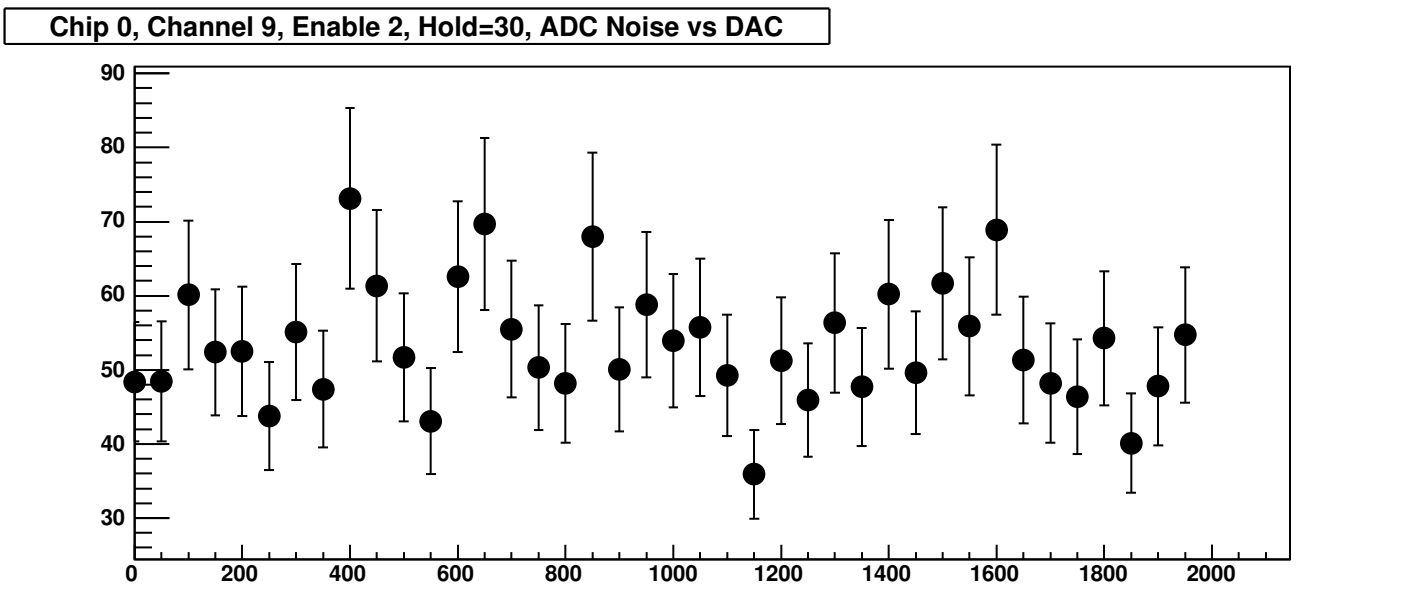

Chip 0, Channel 9, Enable 0!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

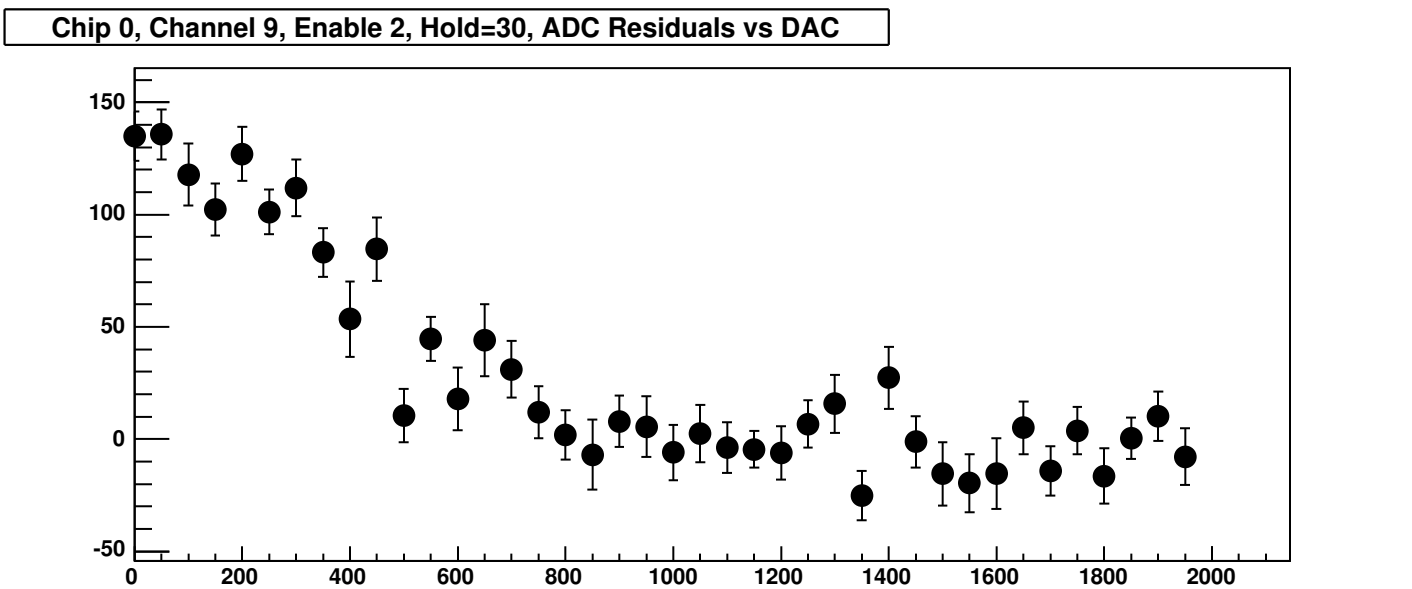

Chip 0, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 10.98 / 11
p0 -1358 ± 22.27
p1 0.0572 ± 0.01991
-1180
-1200
-1220
-1240
-1260
-1280
-1300
-1320
-1340
-1360
-1380
0
200
400
600
800
1000
1200
1400
1600
1800
2000

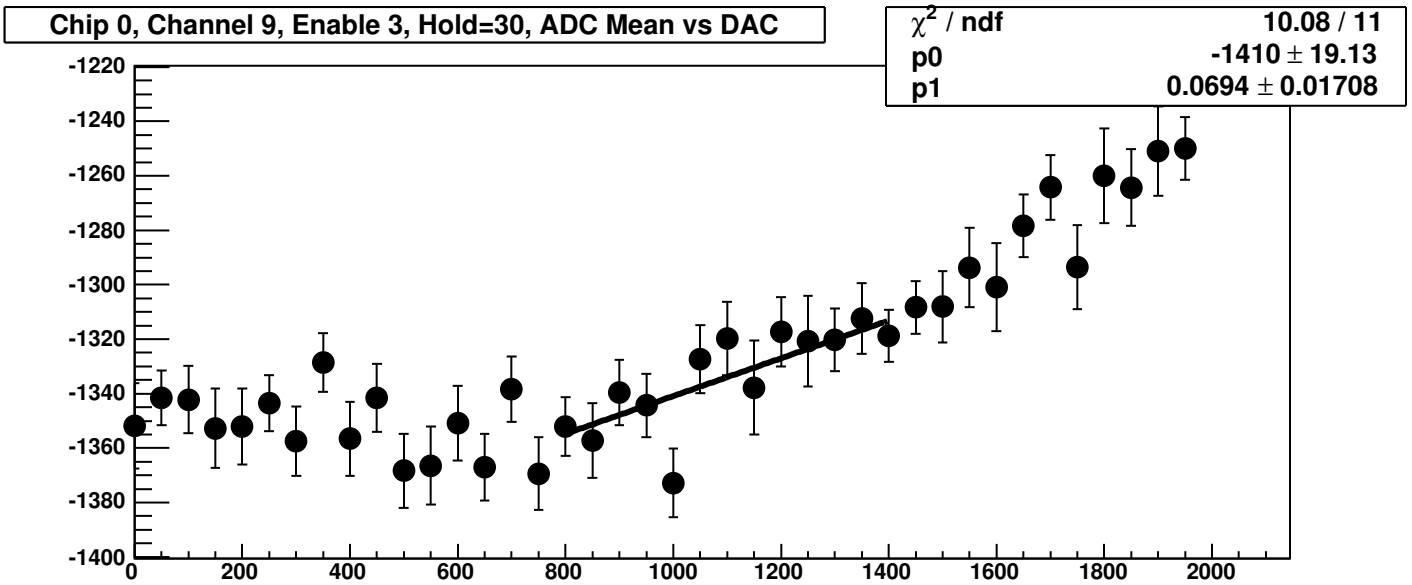

Chip 0, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

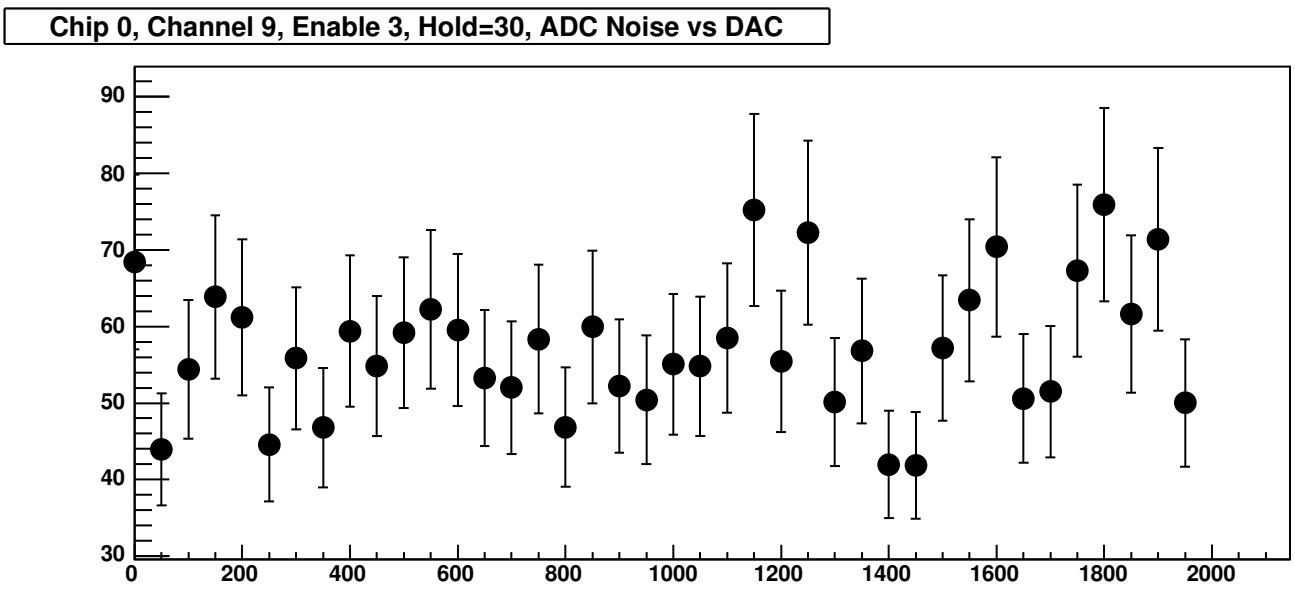

Chip 0, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

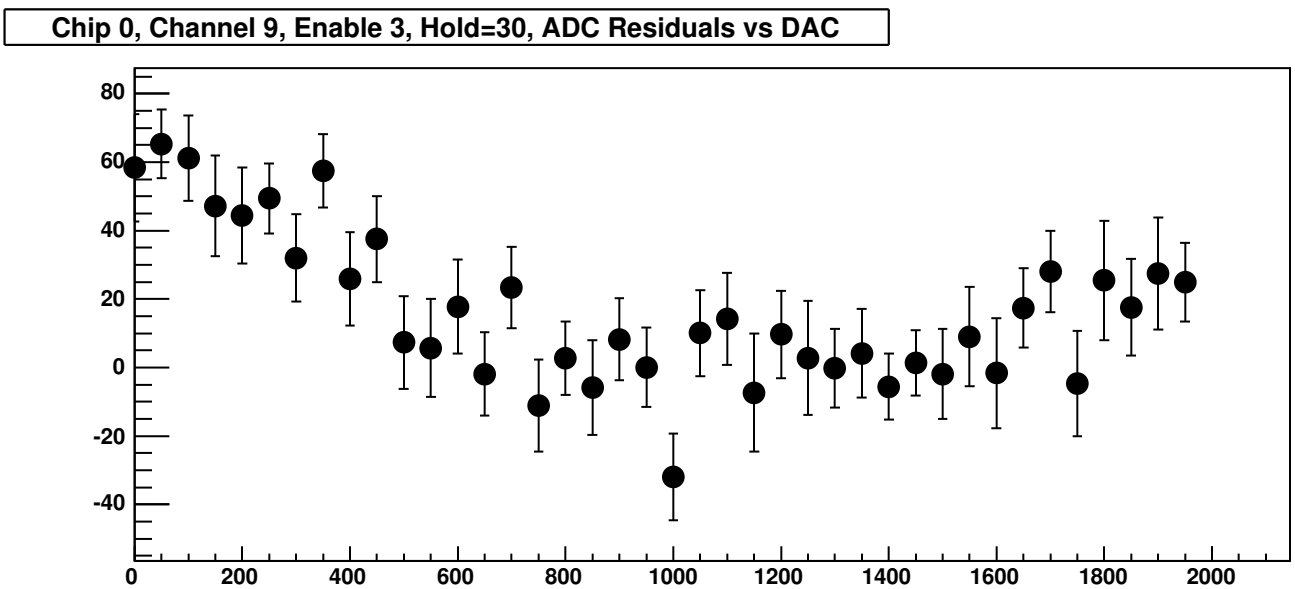

Chip 0, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 12.9 / 11
p0 -1439 ± 20.38
p1 0.1762 ± 0.01815
-1100
-1150
-1200
-1250
-1300
-1350
0
200
400
600
800
1000
1200
1400
1600
1800
2000

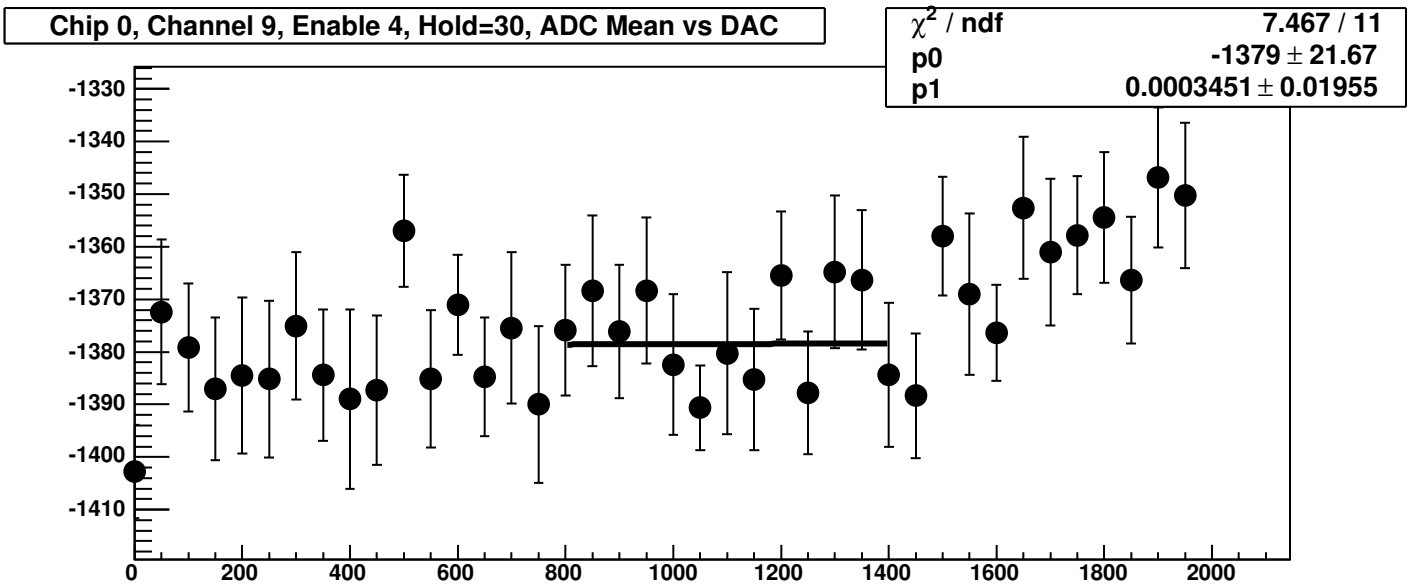

Chip 0, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

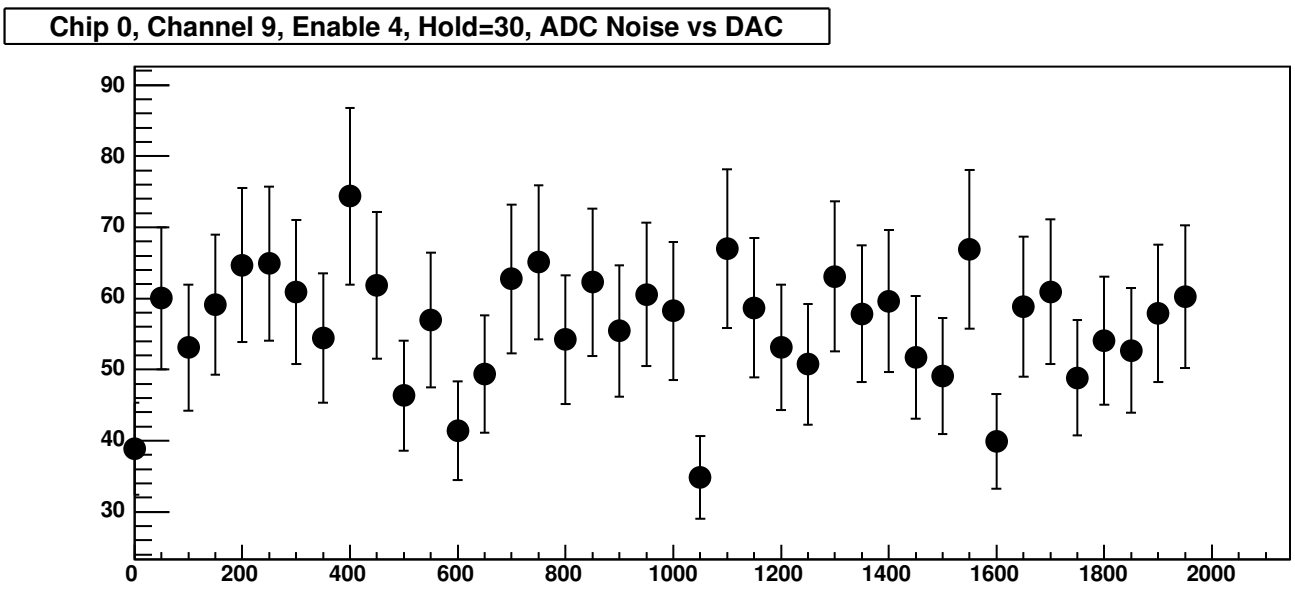

Chip 0, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

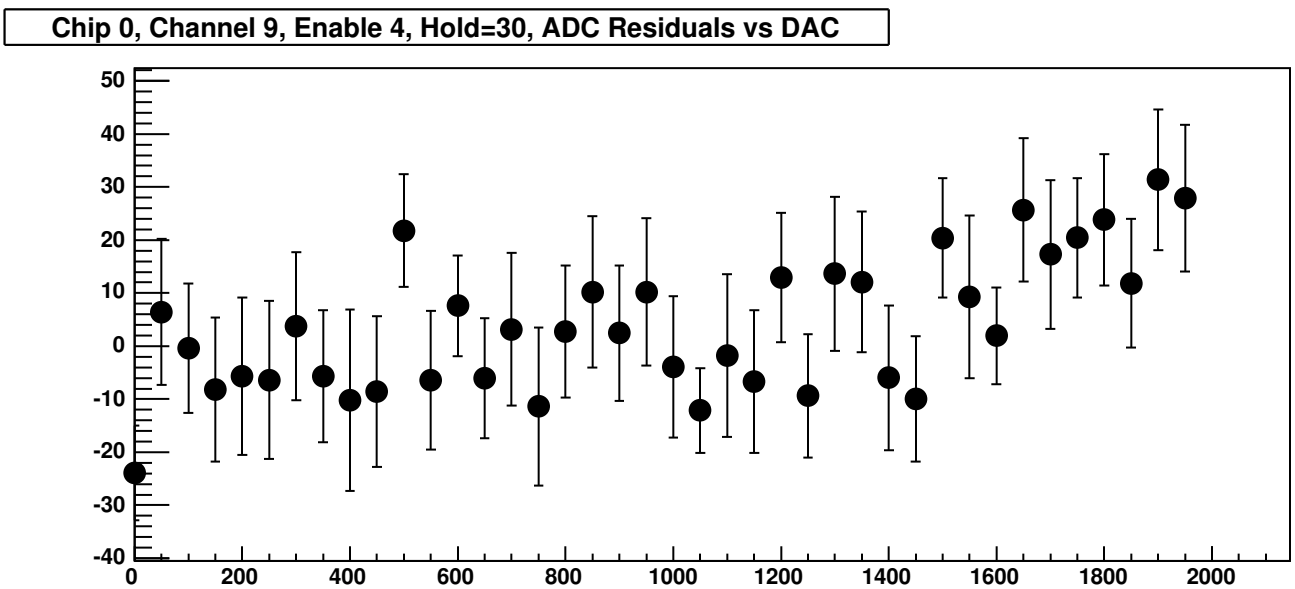

Chip 0, Channel 9, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 10.08 / 11
p0 -1410 ± 19.13
p1 0.0694 ± 0.01708
-1220
-1240
-1260
-1280
-1300
-1320
-1340
-1360
-1380
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

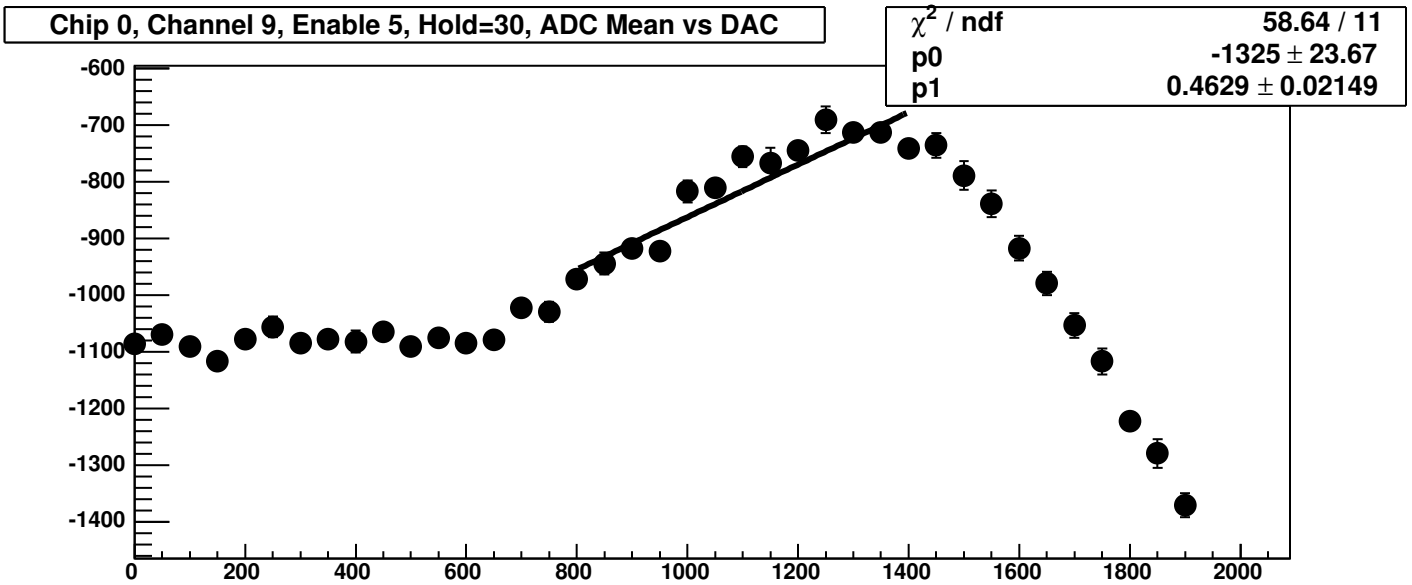

Chip 0, Channel 9, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

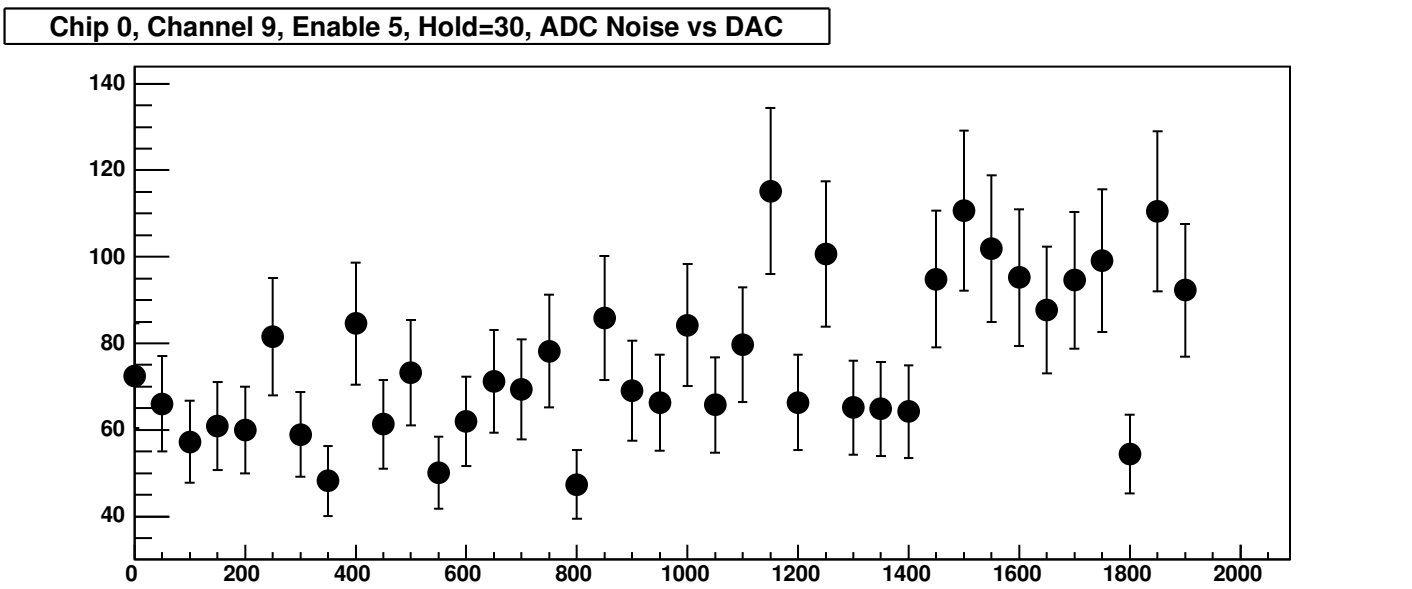

Chip 0, Channel 9, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

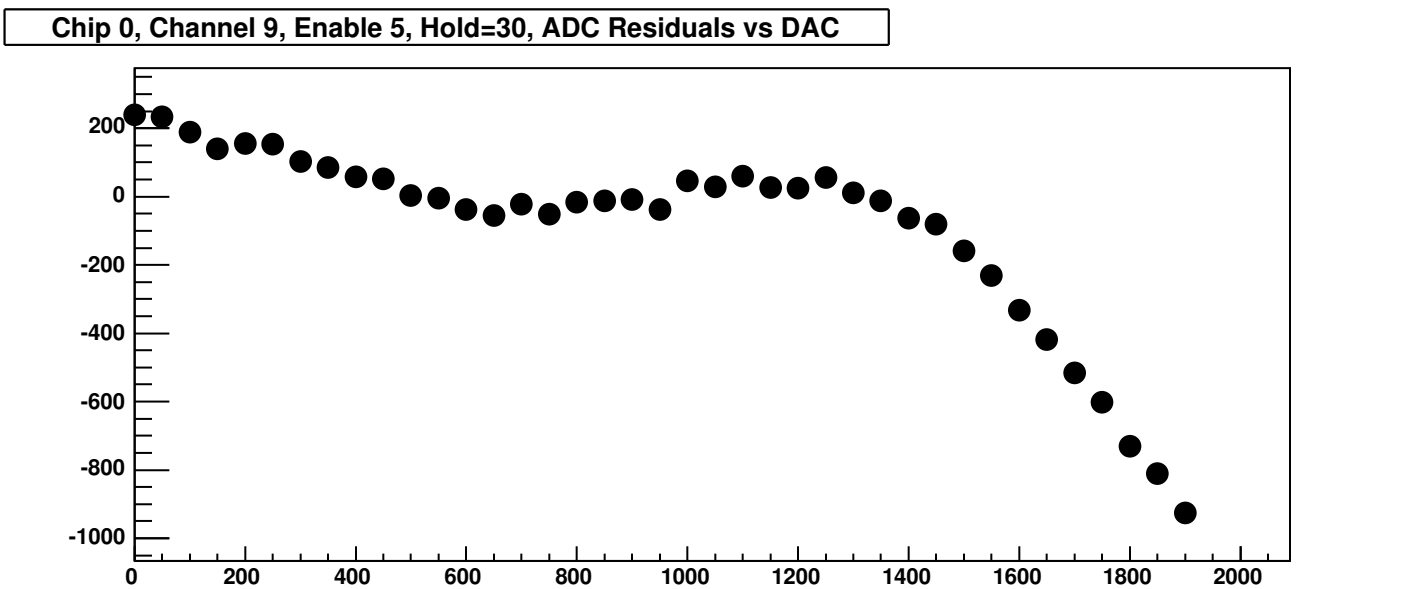

Chip 0, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7.467 / 11
p0 -1379 ± 21.67
p1 0.0003451 ± 0.01955
-1330
-1340
-1350
-1360
-1370
-1380
-1390
-1400
-1410
0
200
400
600
800
1000
1200
1400
1600
1800
2000

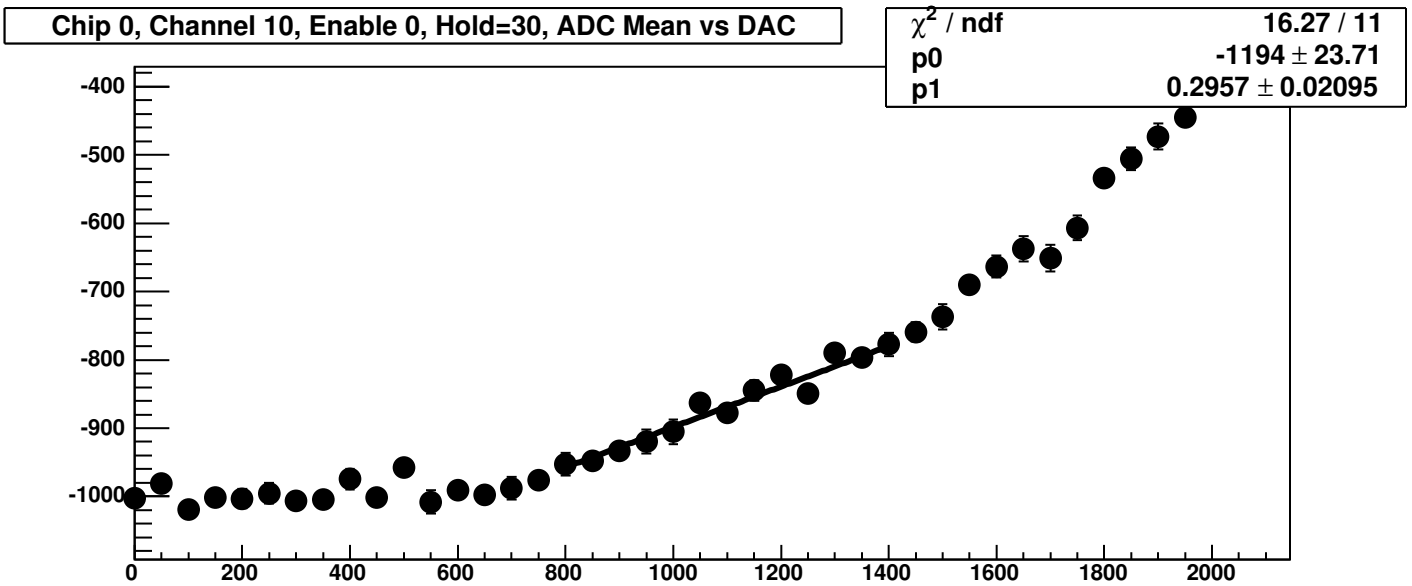

Chip 0, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

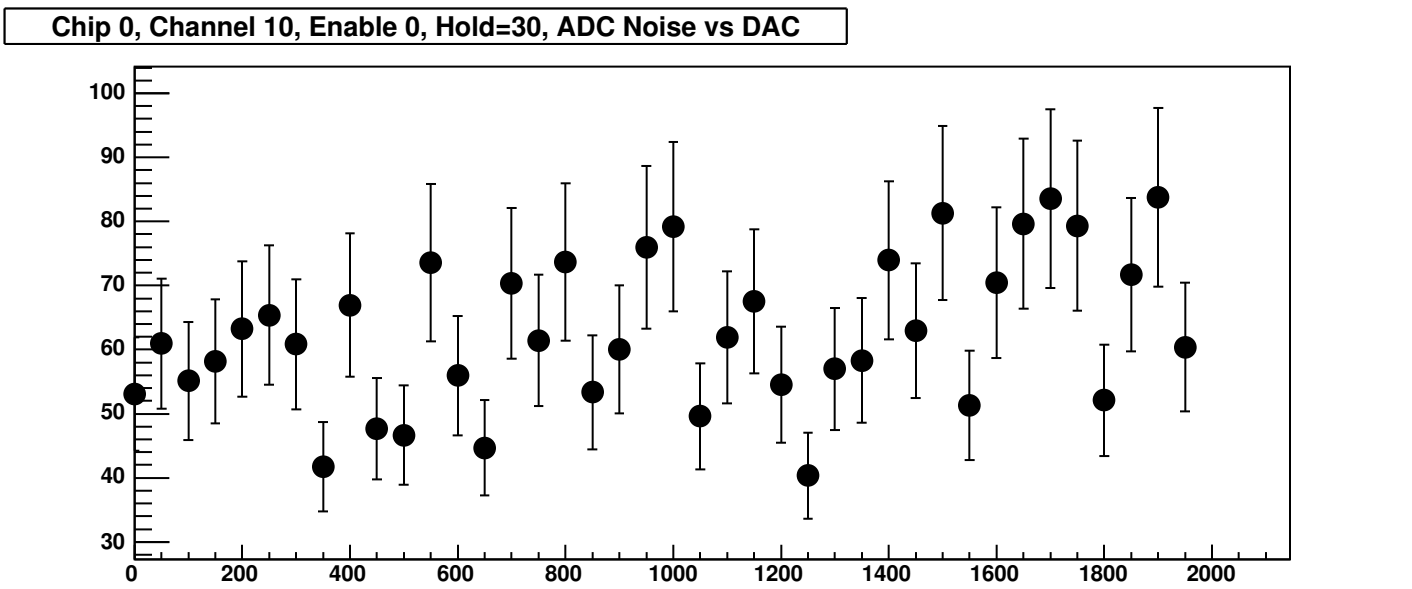

Chip 0, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC
50
40
30
20
10
0
-10
-20
-30
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

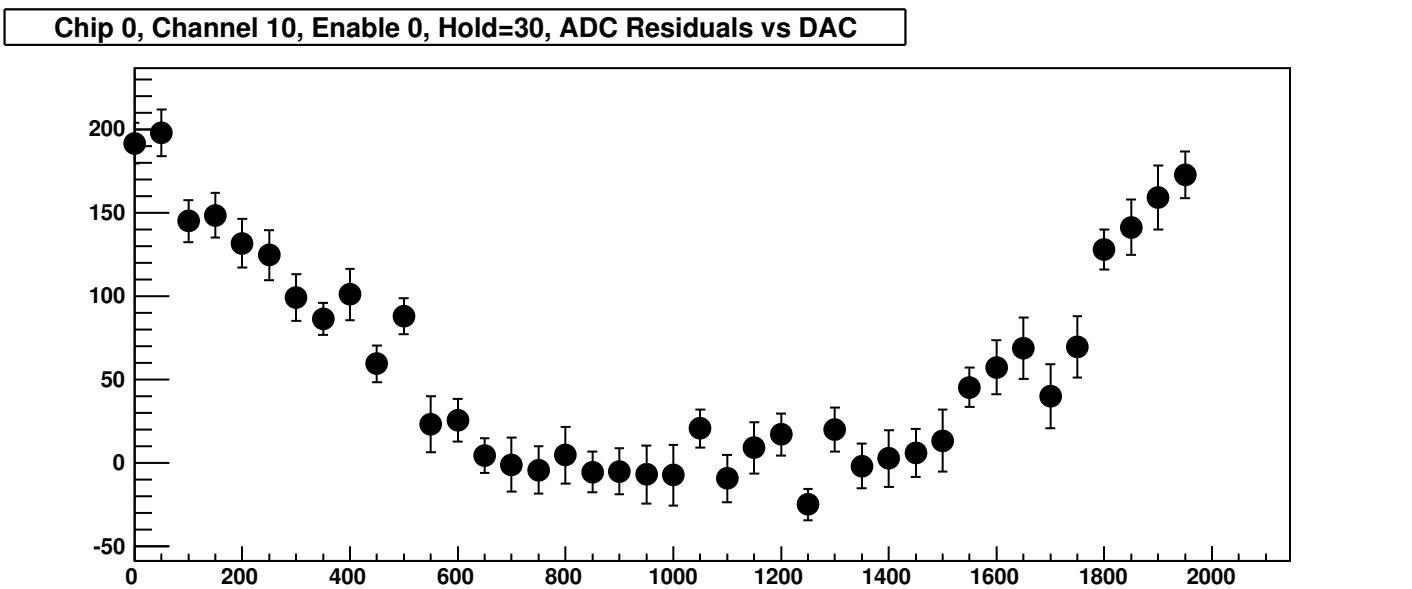

Chip 0, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 58.64 / 11
p0 -1325 ± 23.67
p1 0.4629 ± 0.02149
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

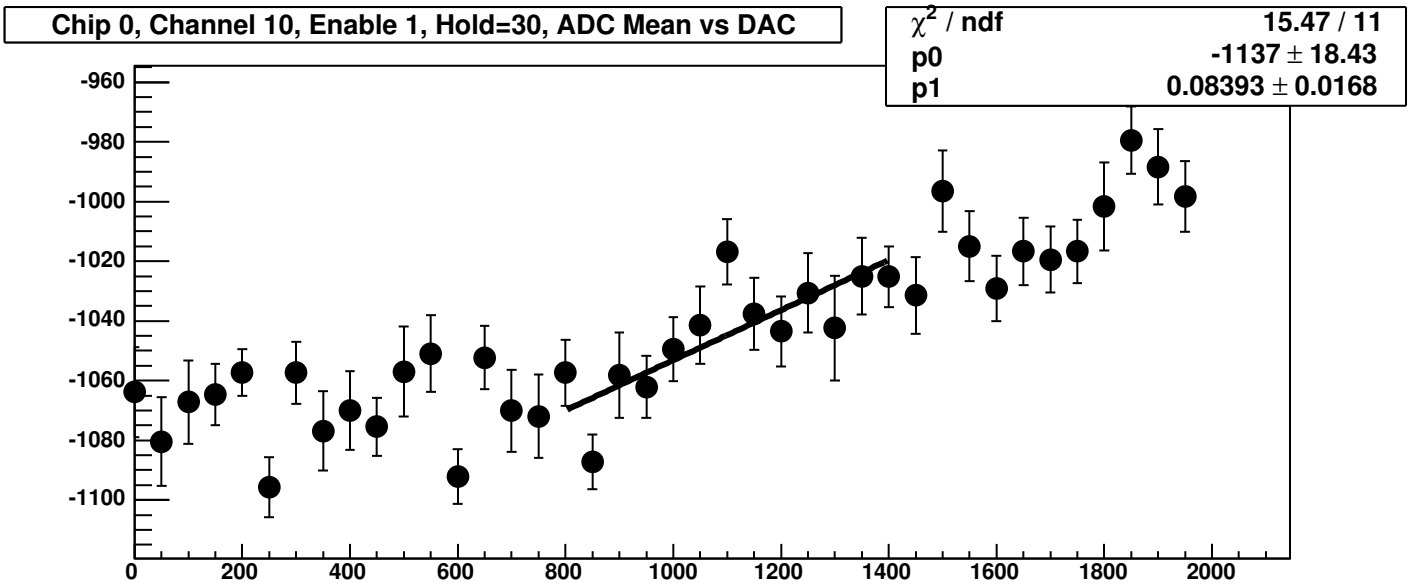

Chip 0, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

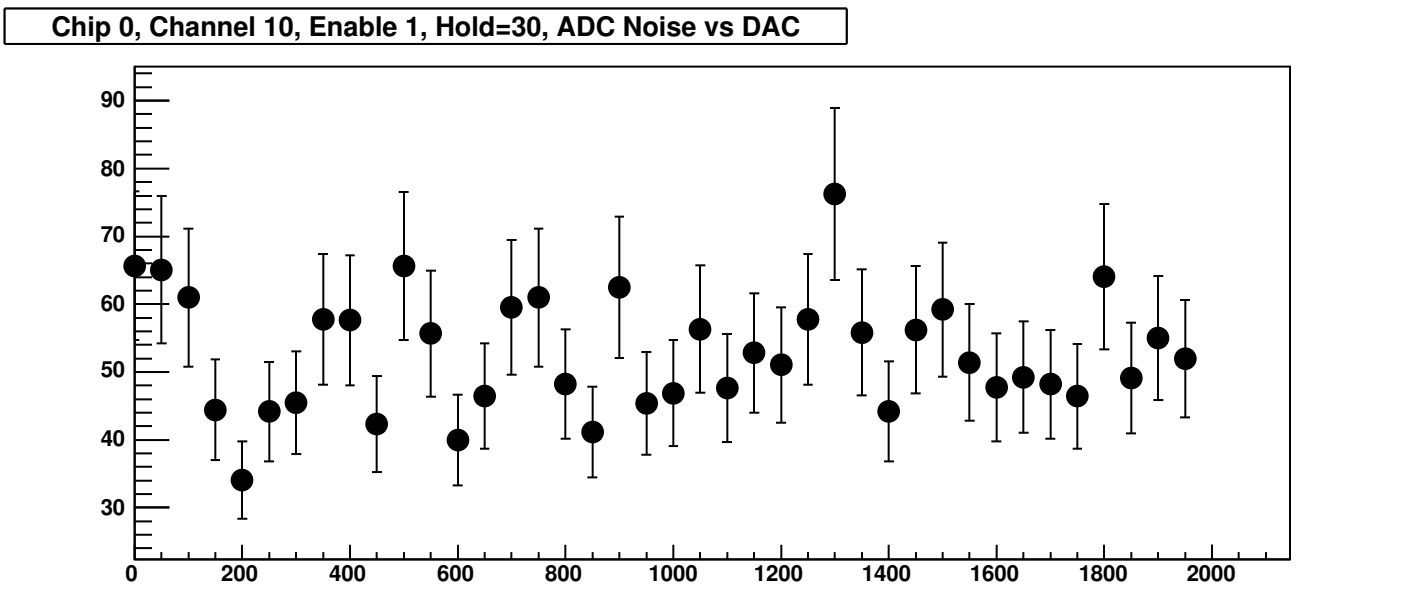

Chip 0, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

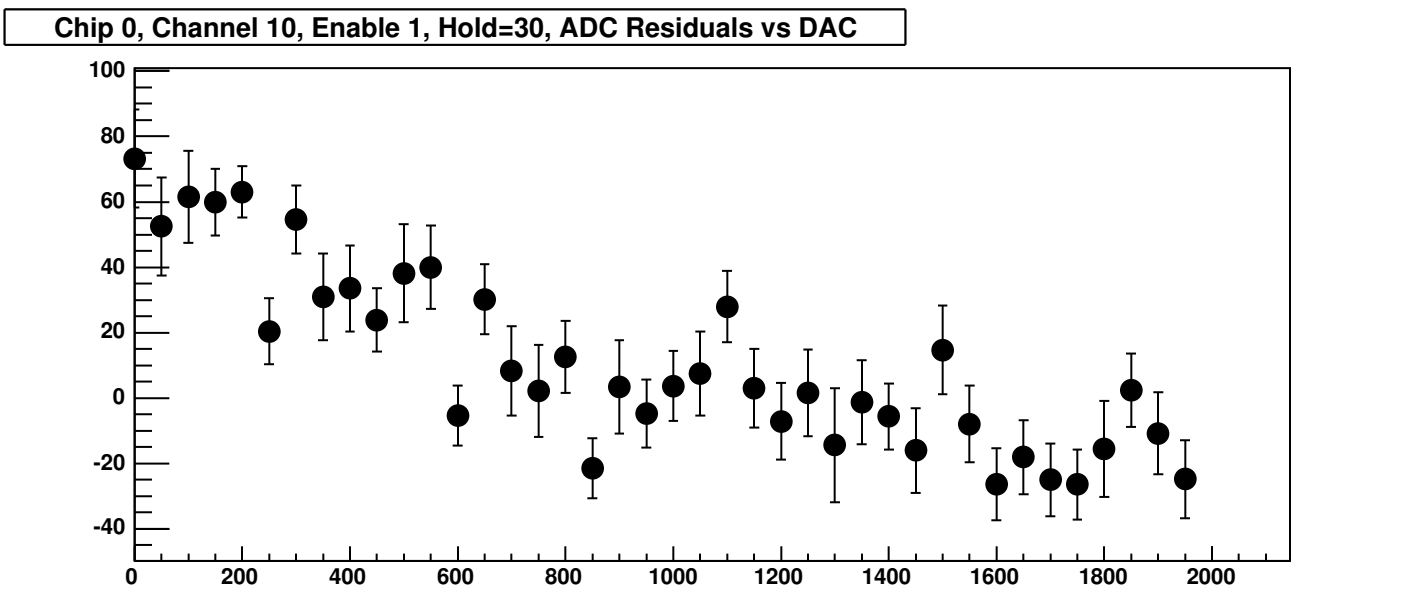

Chip 0, Channel 10, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 16.27 / 11
p0 -1194 ± 23.71
p1 0.2957 ± 0.02095
-400
-500
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

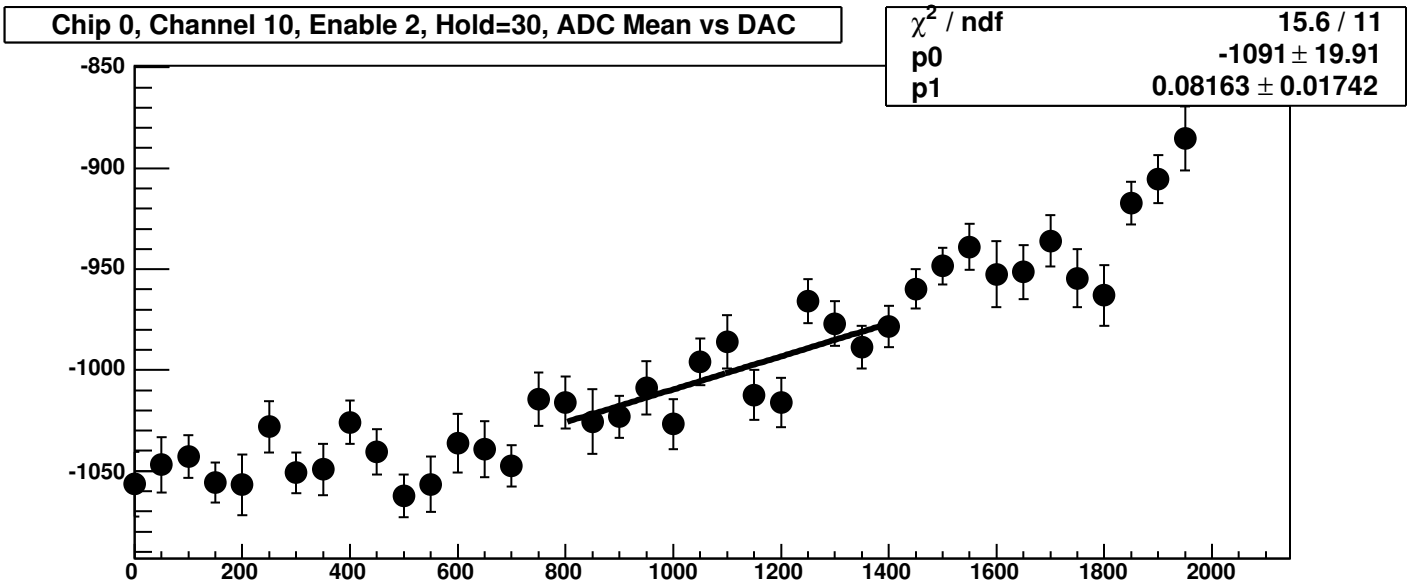

Chip 0, Channel 10, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

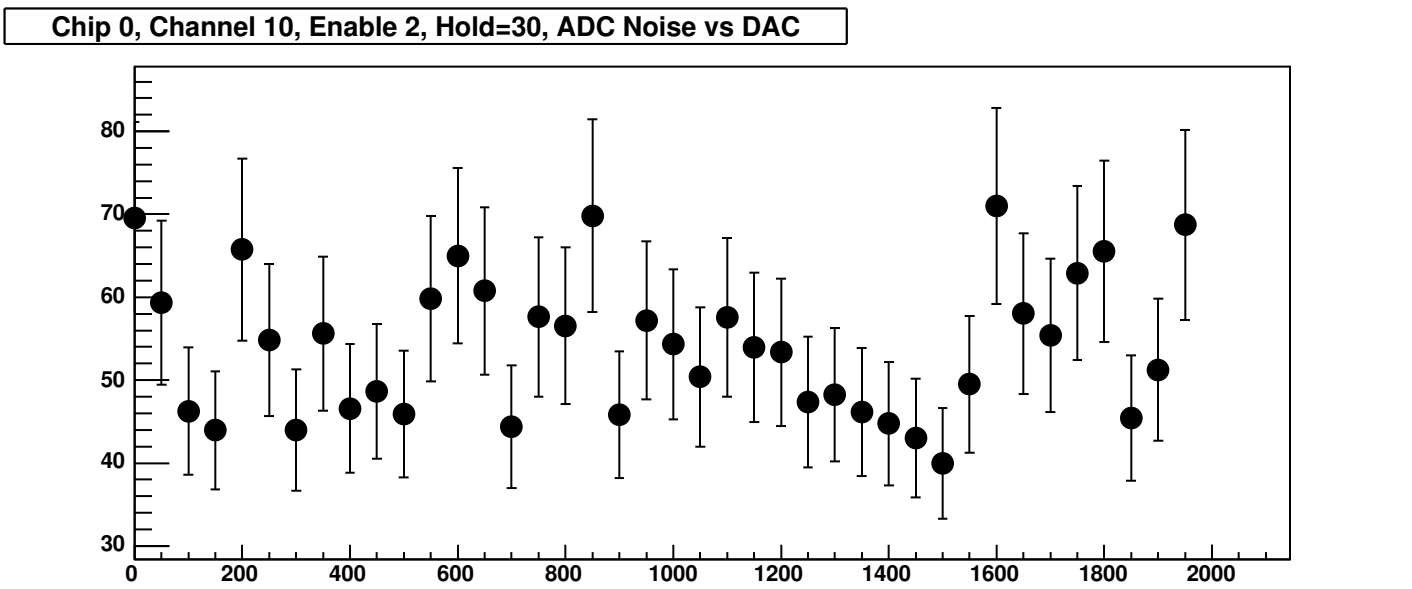

Chip 0, Channel 10, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

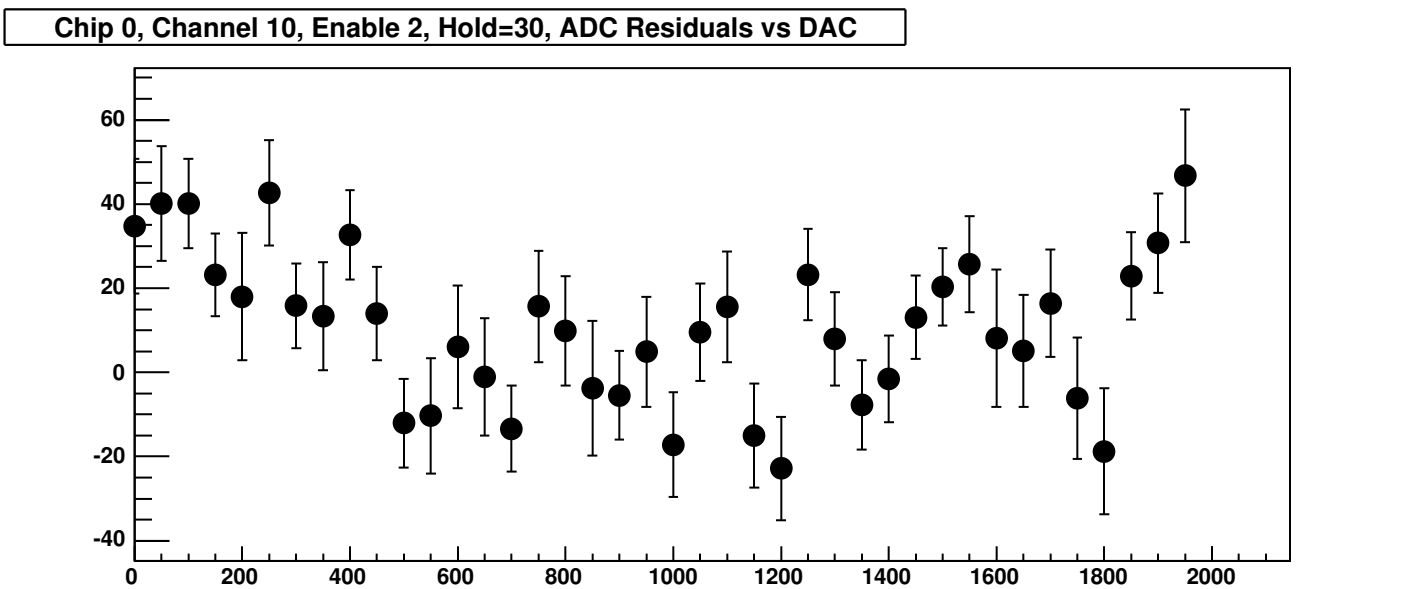

Chip 0, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 15.47 / 11
p0 -1137 ± 18.43
p1 0.08393 ± 0.0168
-960
-980
-1000
-1020
-1040
-1060
-1080
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

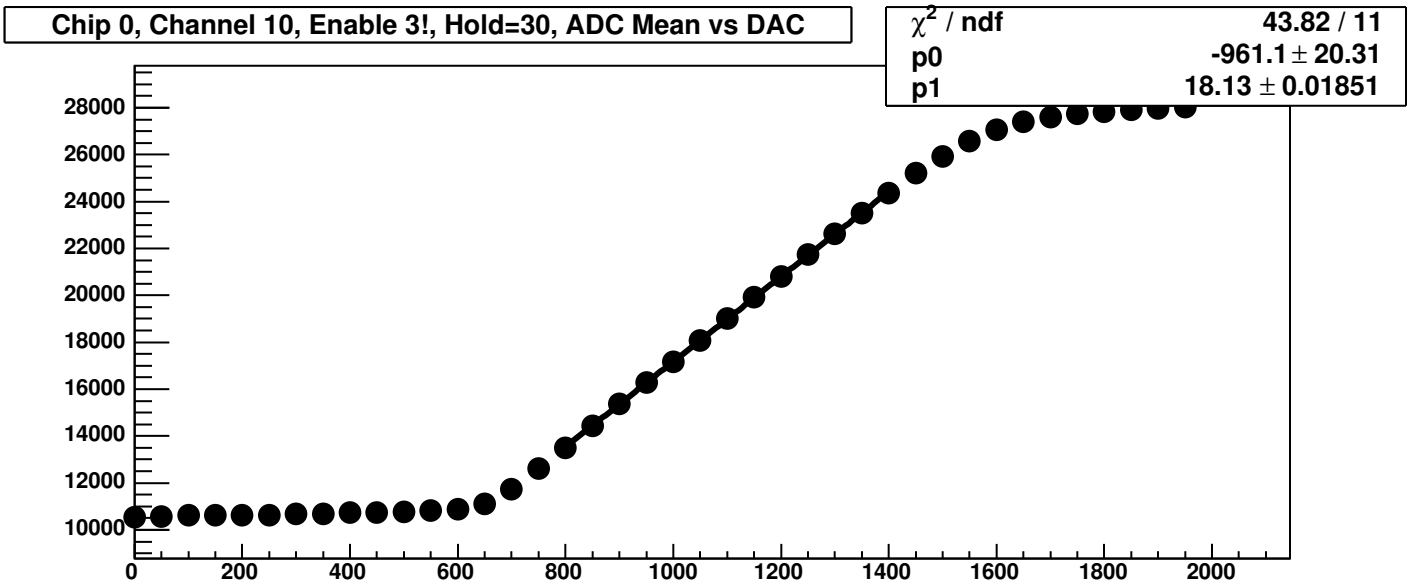

Chip 0, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

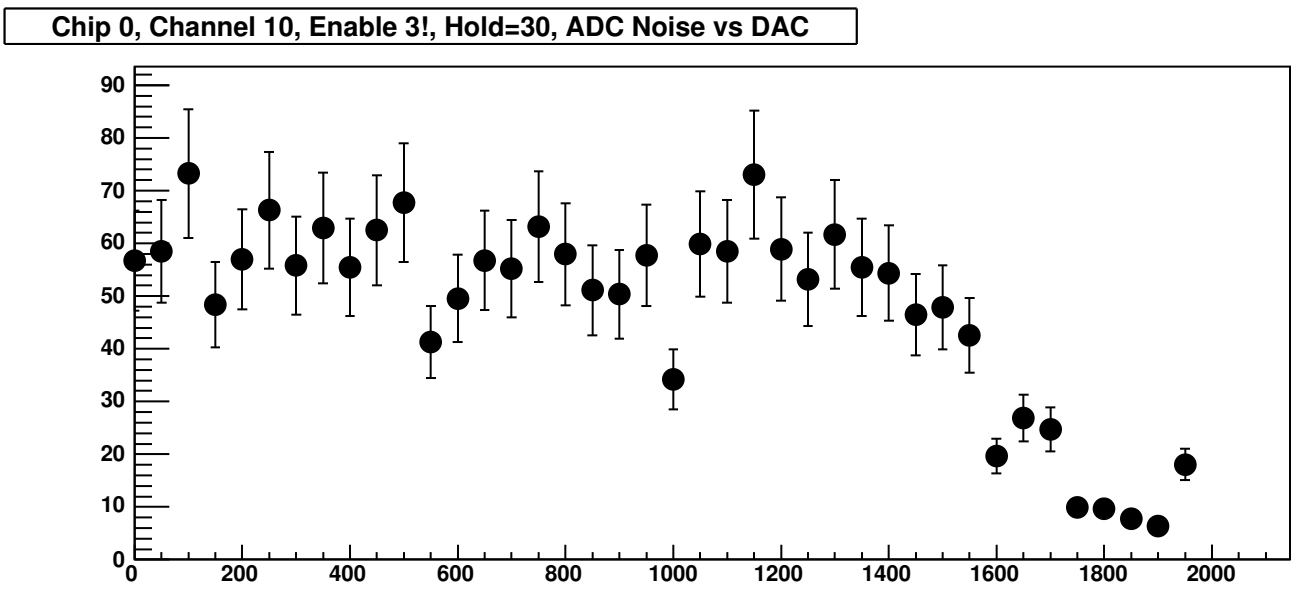

Chip 0, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

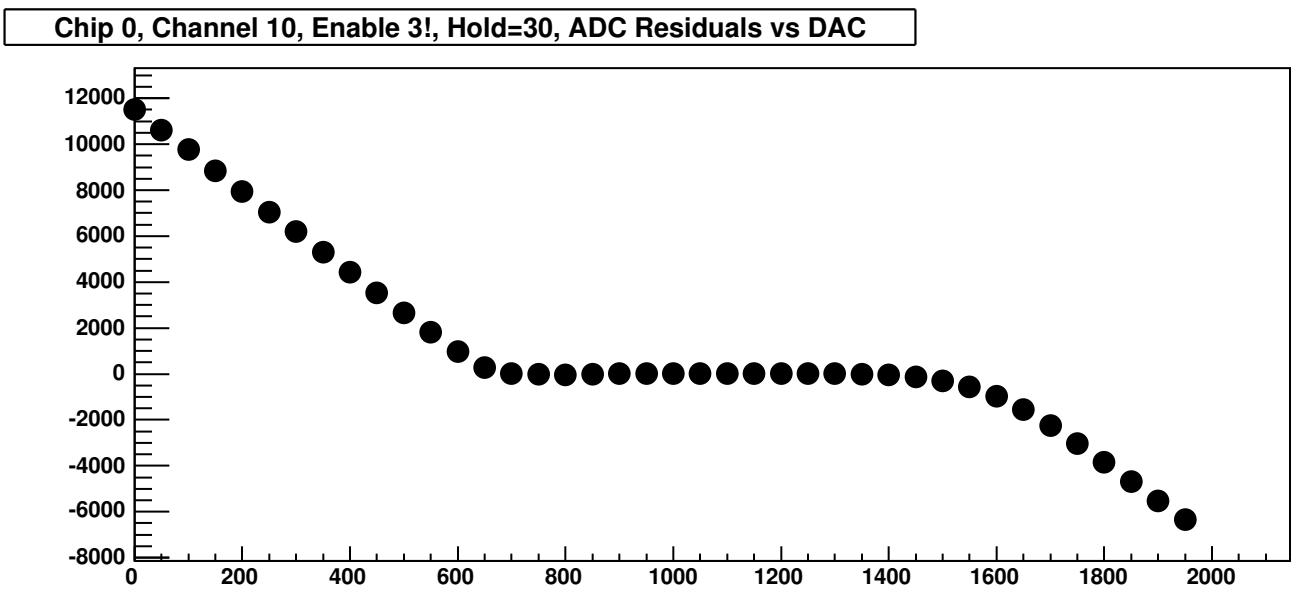

Chip 0, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 15.6 / 11
p0 -1091 ± 19.91
p1 0.08163 ± 0.01742
-850
-900
-950
-1000
-1050
0
200
400
600
800
1000
1200
1400
1600
1800
2000

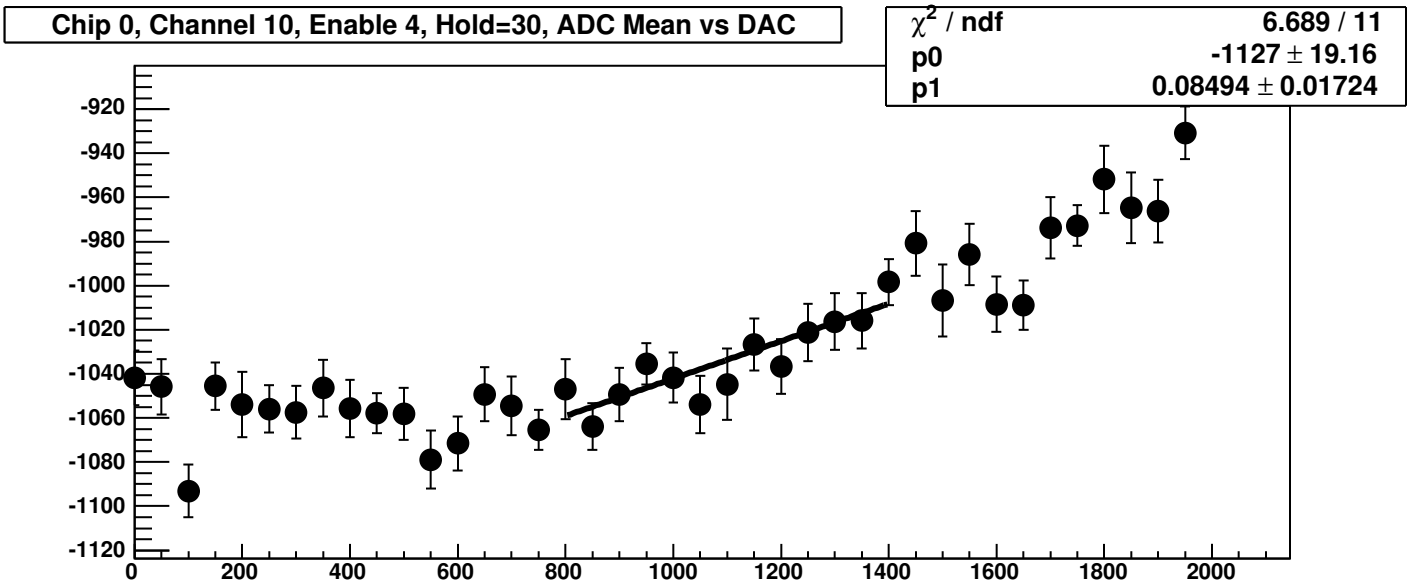

Chip 0, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

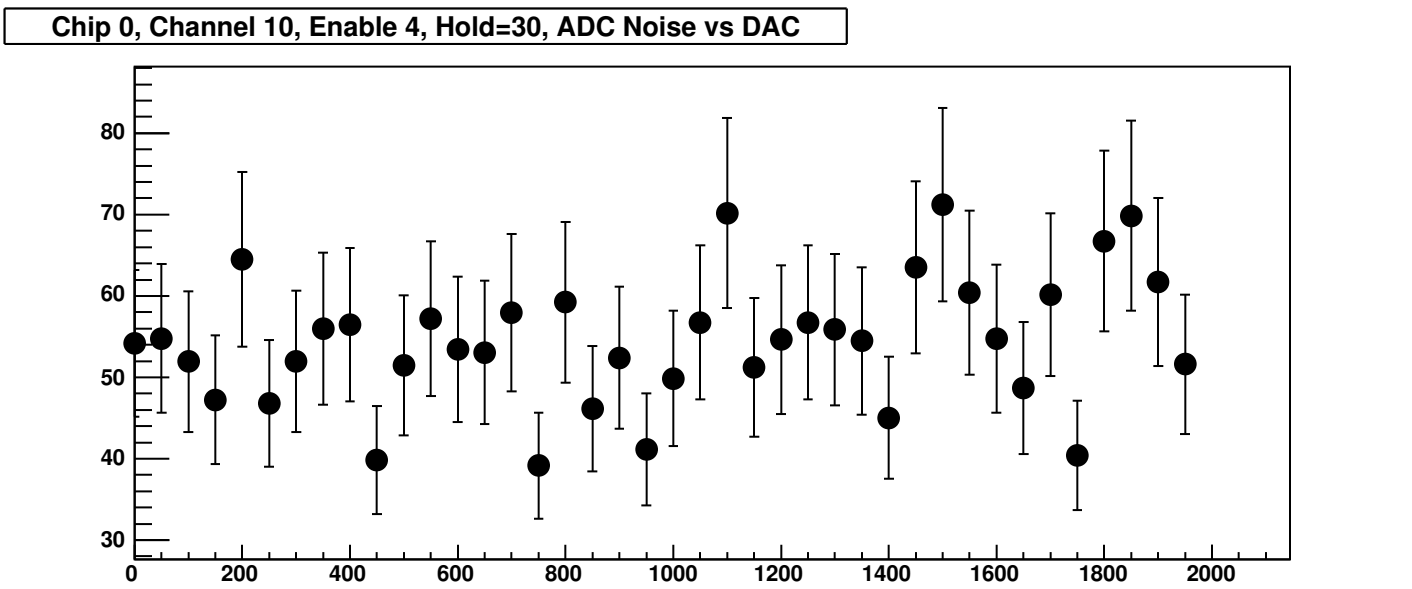

Chip 0, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

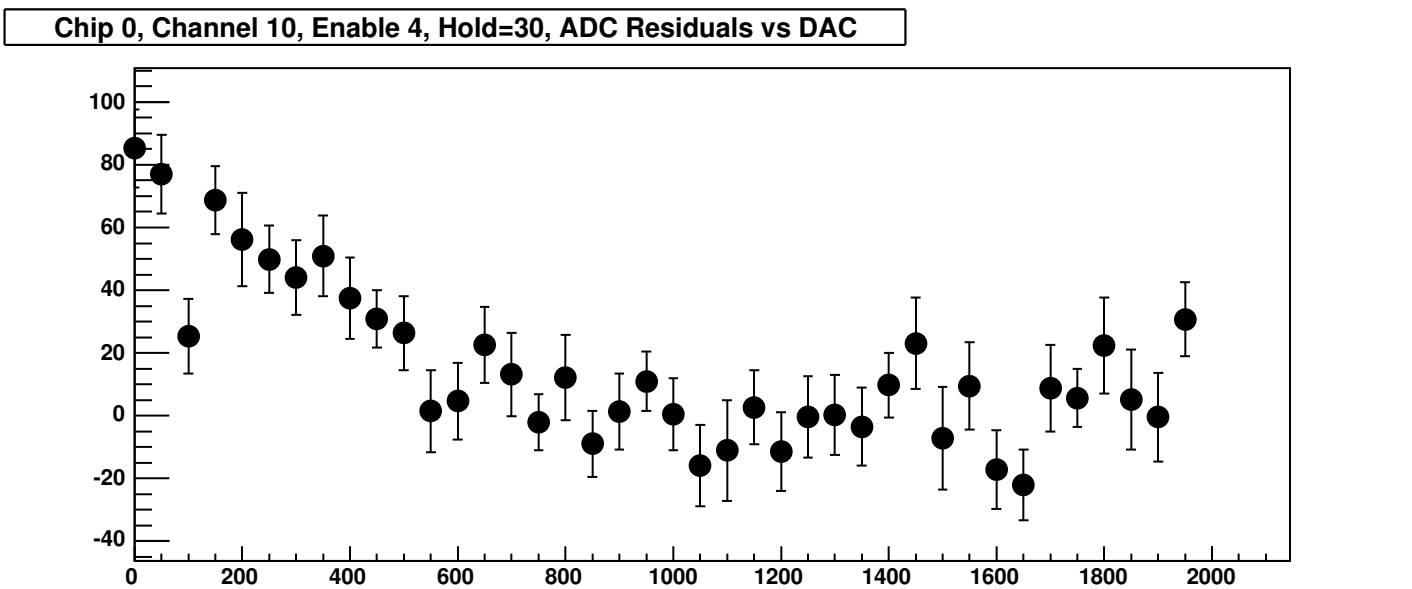

Chip 0, Channel 10, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 43.82 / 11
p0 -961.1 ± 20.31
p1 18.13 ± 0.01851
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

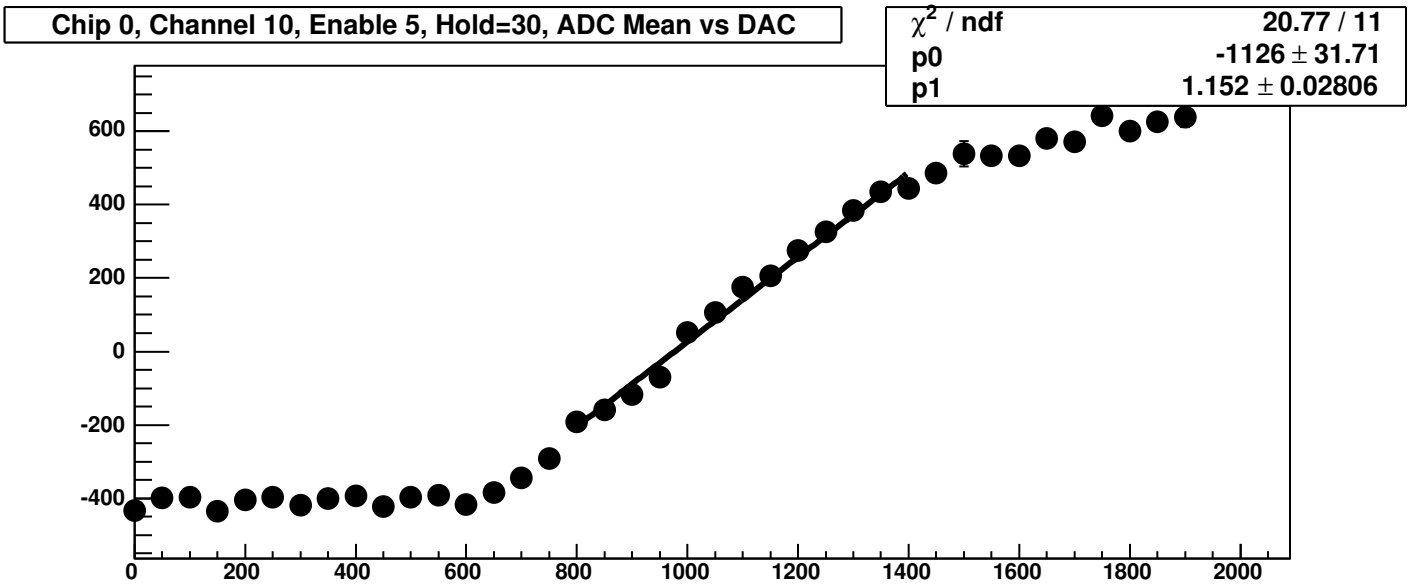

Chip 0, Channel 10, Enable 3!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

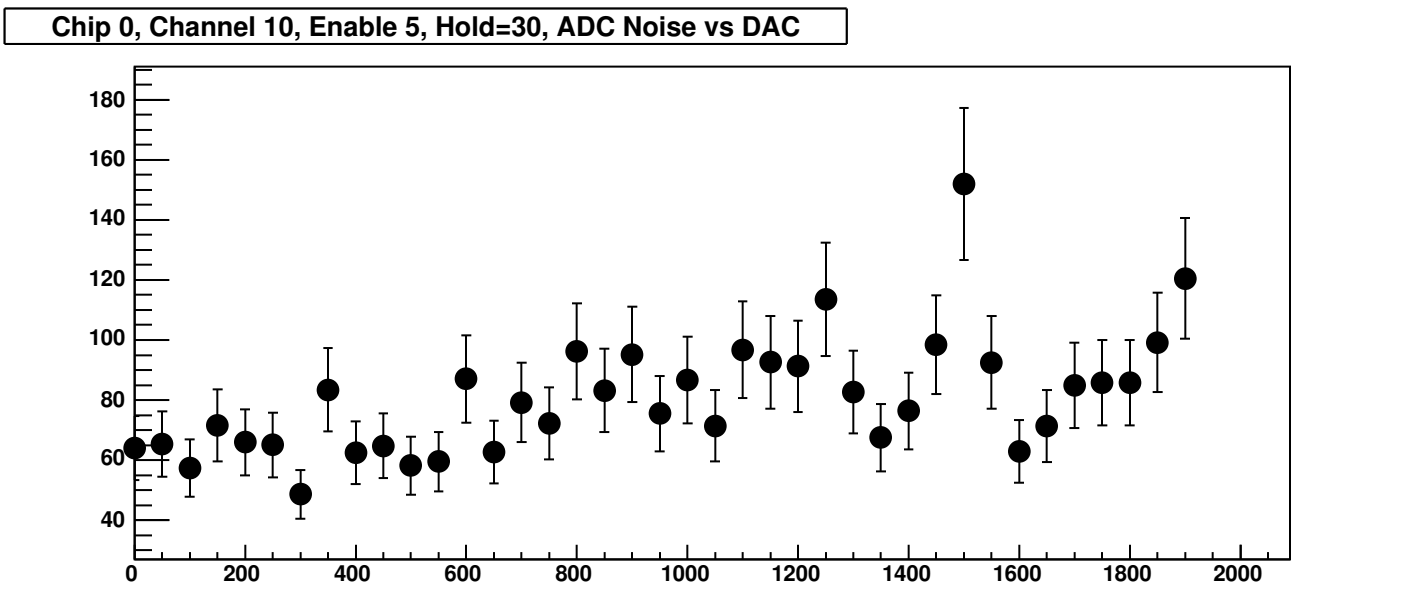

Chip 0, Channel 10, Enable 3!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

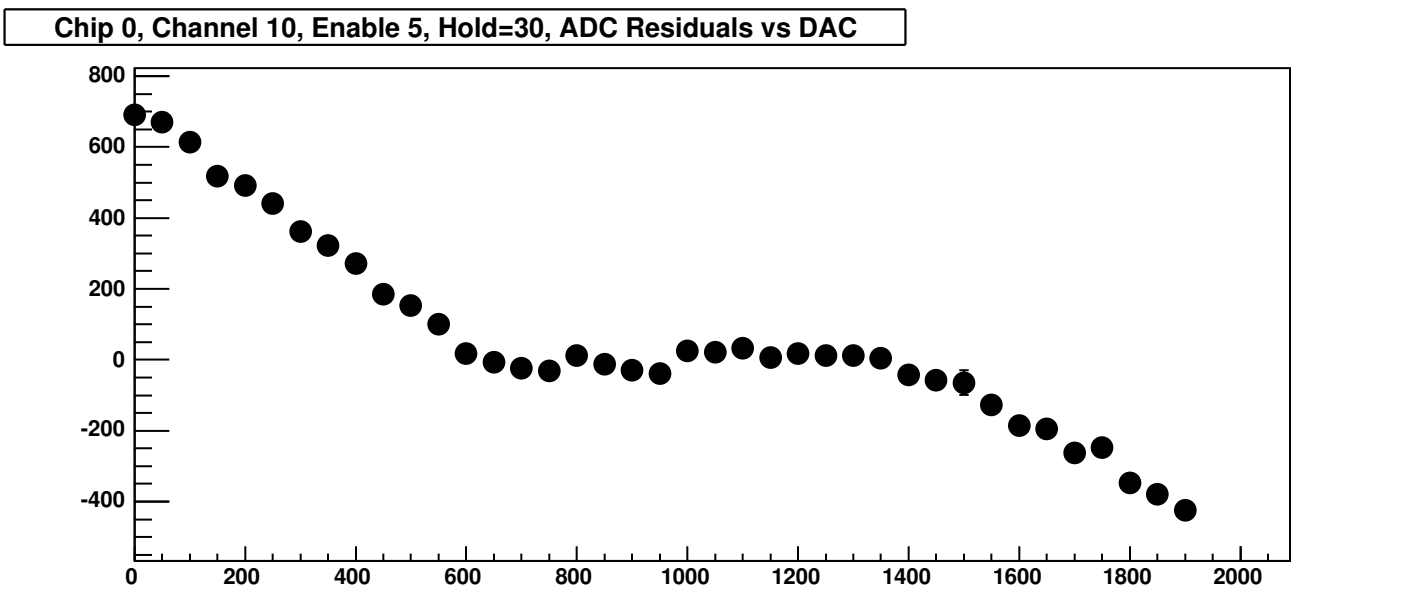

Chip 0, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6.689 / 11
p0 -1127 ± 19.16
p1 0.08494 ± 0.01724
-920
-940
-960
-980
-1000
-1020
-1040
-1060
-1080
-1100
-1120
0
200
400
600
800
1000
1200
1400
1600
1800
2000

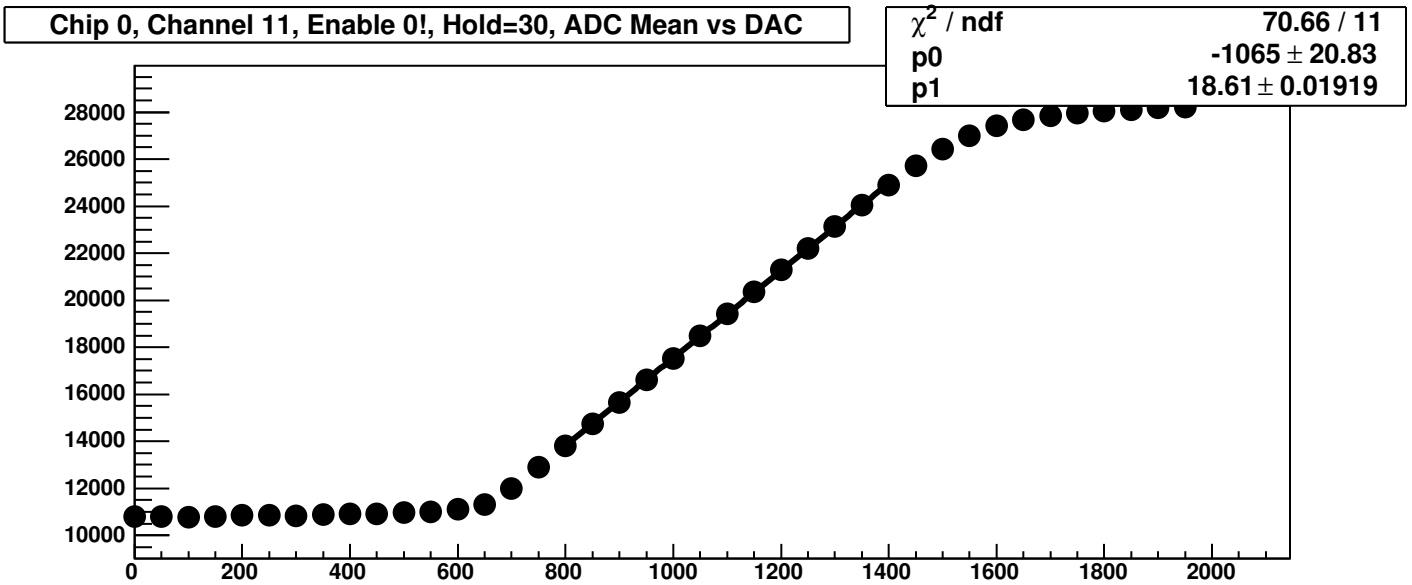

Chip 0, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

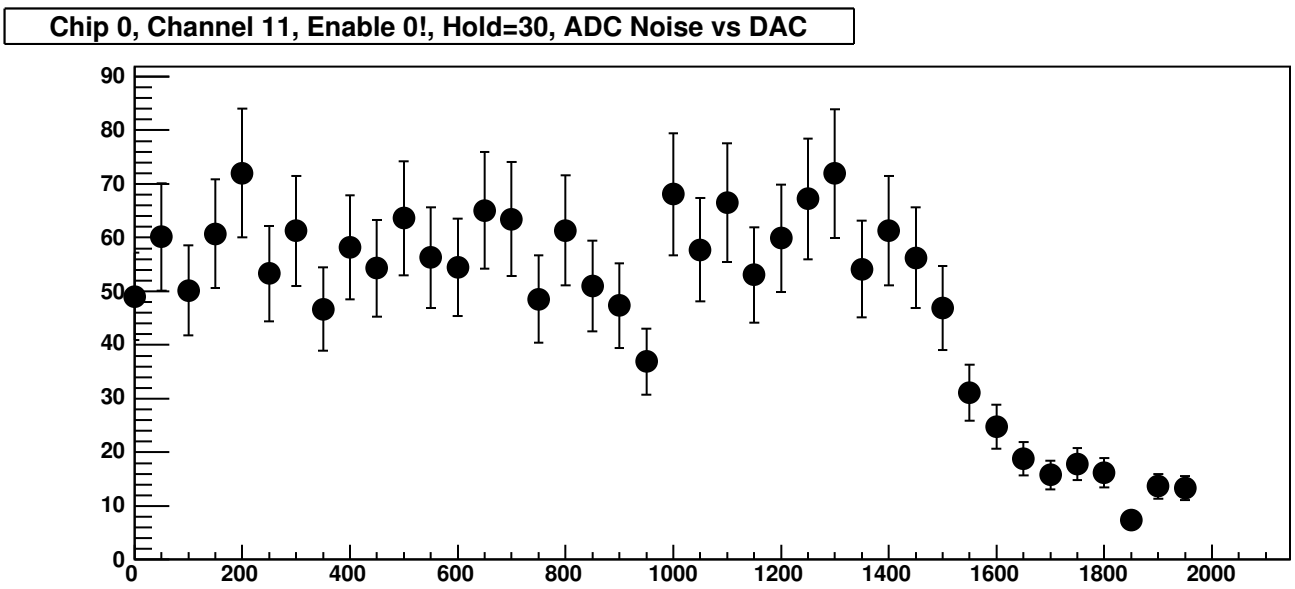

Chip 0, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

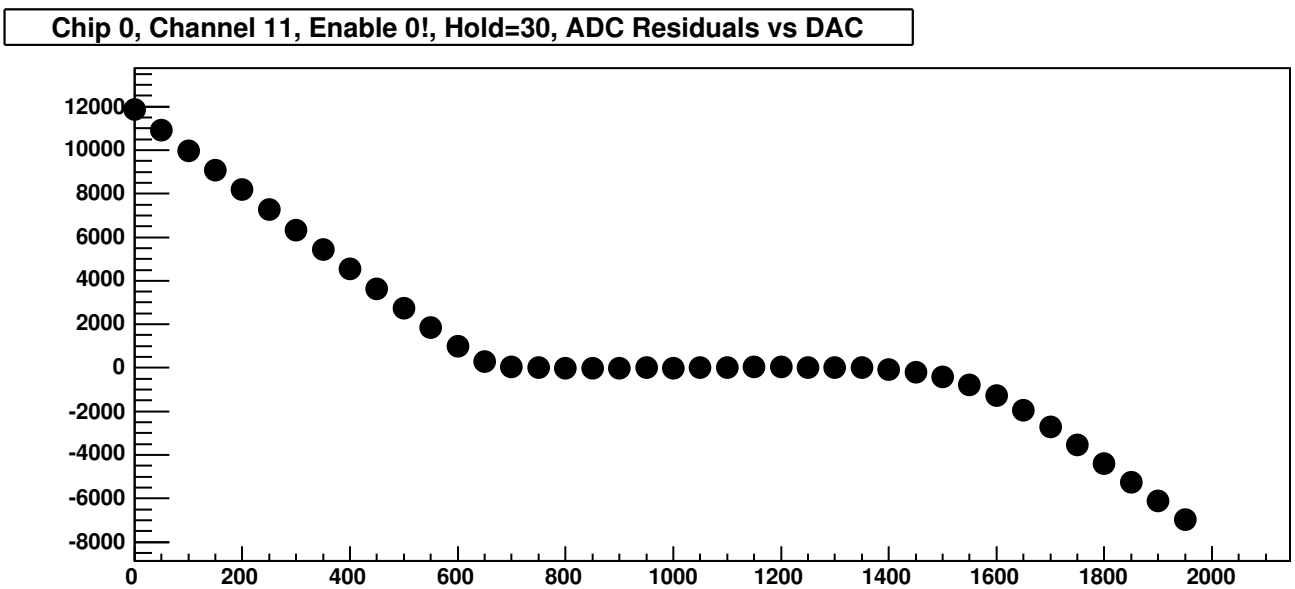

Chip 0, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 20.77 / 11
p0 -1126 ± 31.71
p1 1.152 ± 0.02806
600
400
200
0
-200
-400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

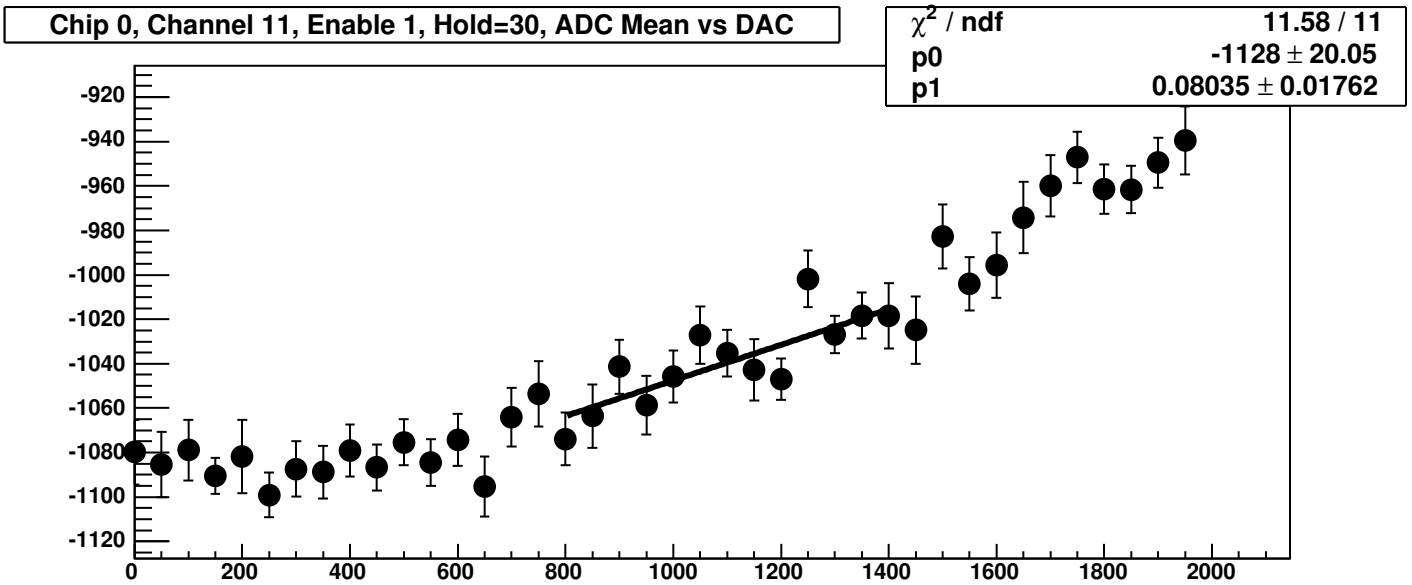

Chip 0, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

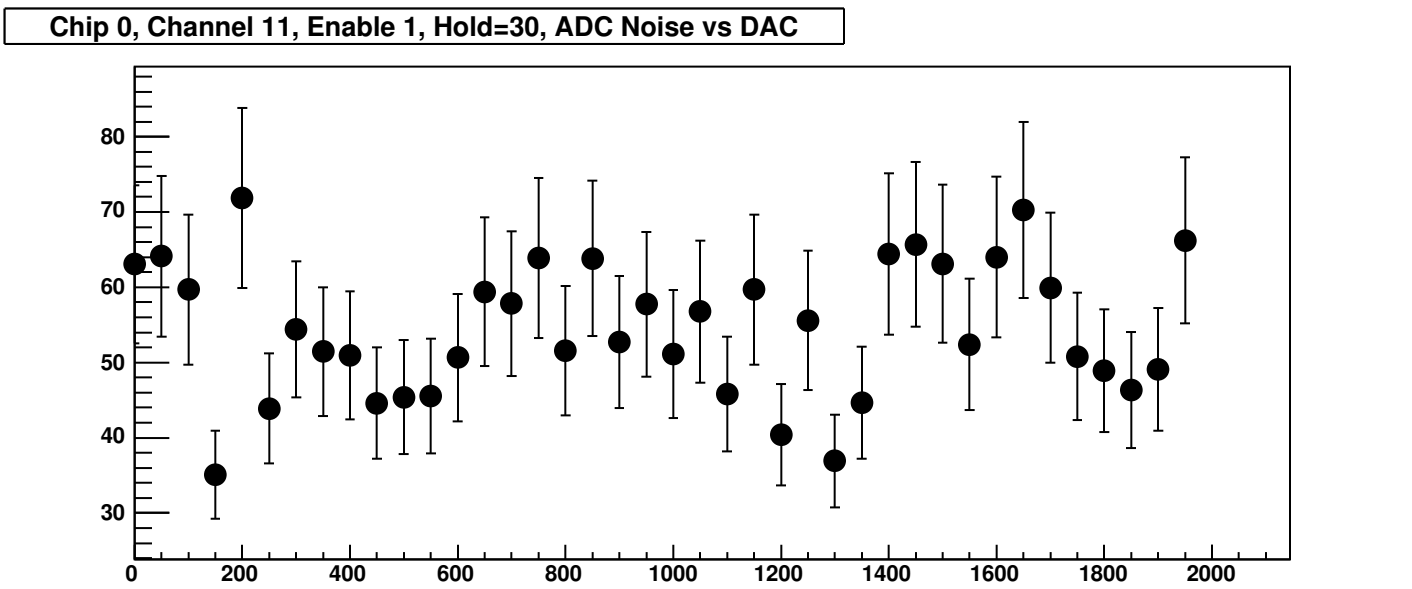

Chip 0, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

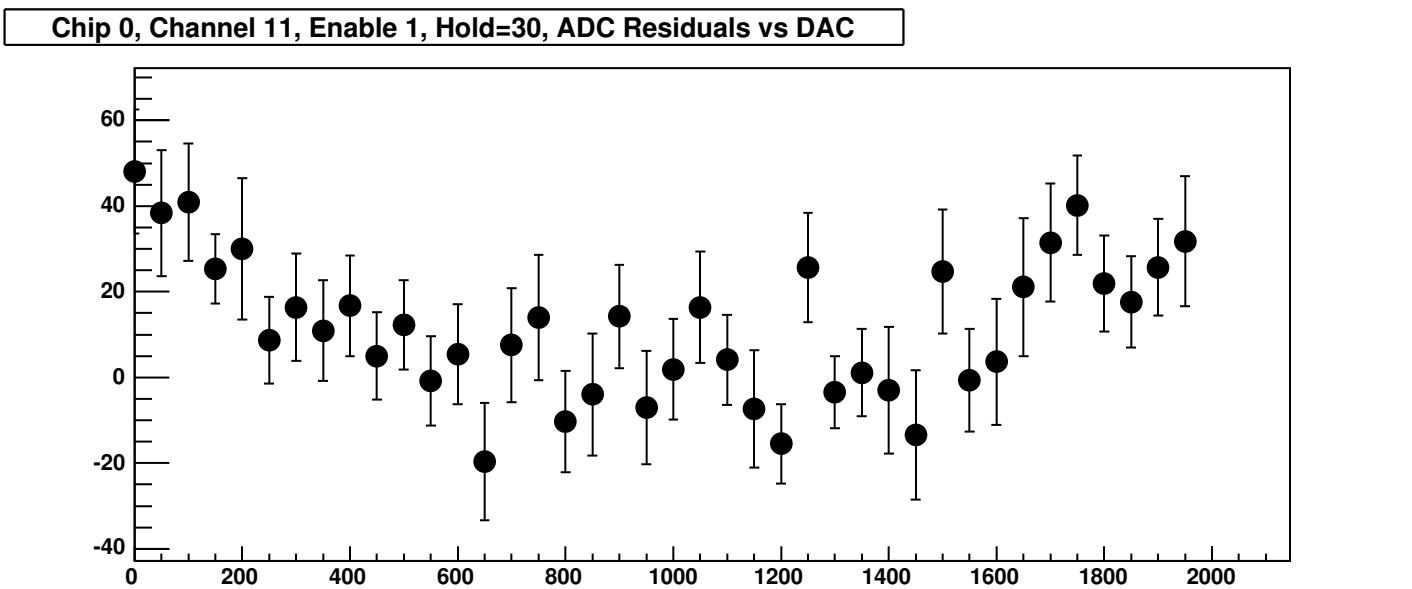

Chip 0, Channel 11, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 70.66 / 11
p0 -1065 ± 20.83
p1 18.61 ± 0.01919
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

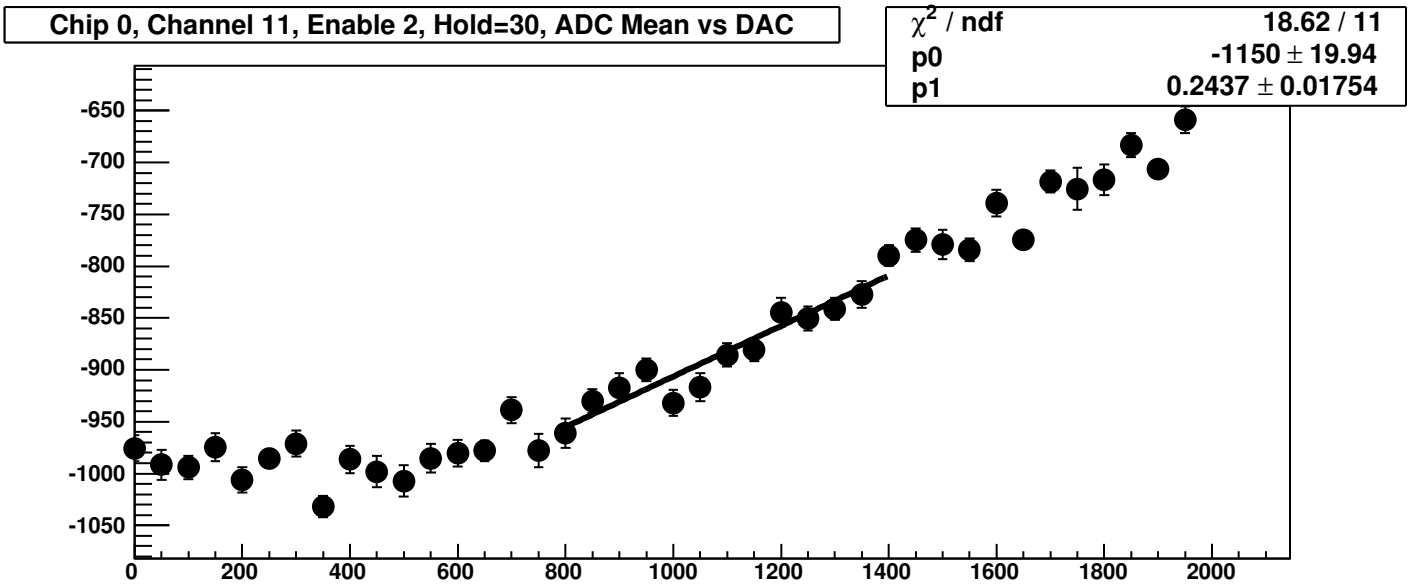

Chip 0, Channel 11, Enable 0!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

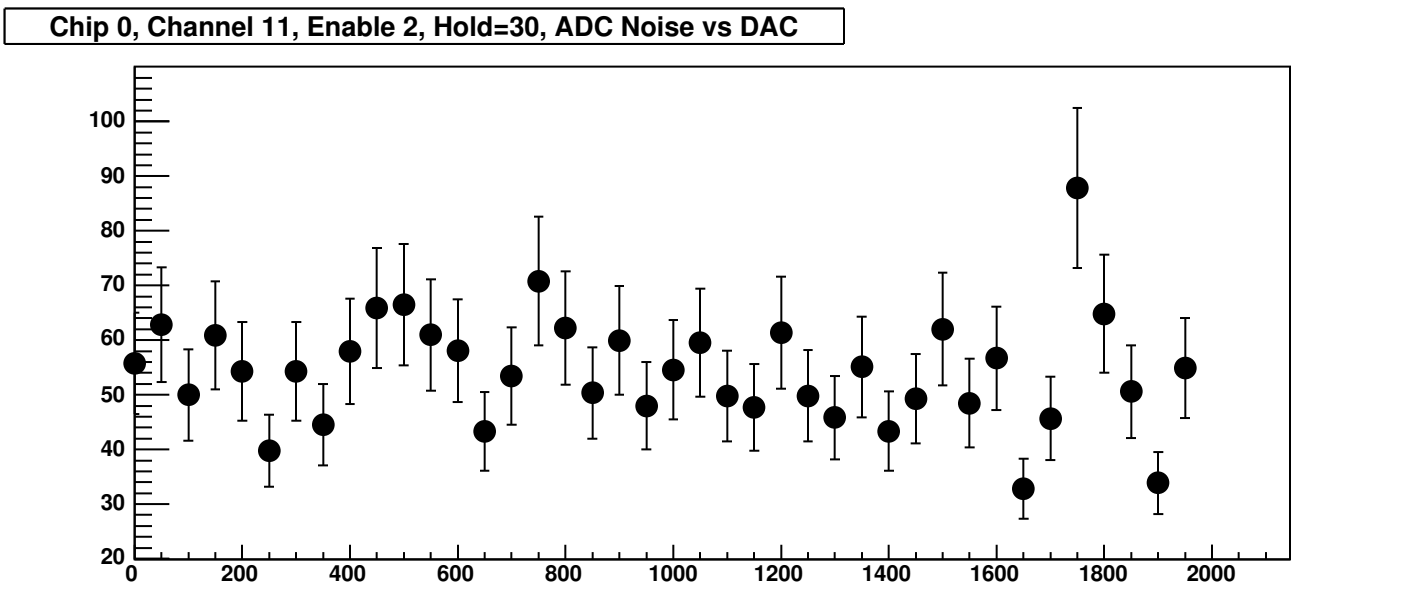

Chip 0, Channel 11, Enable 0!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

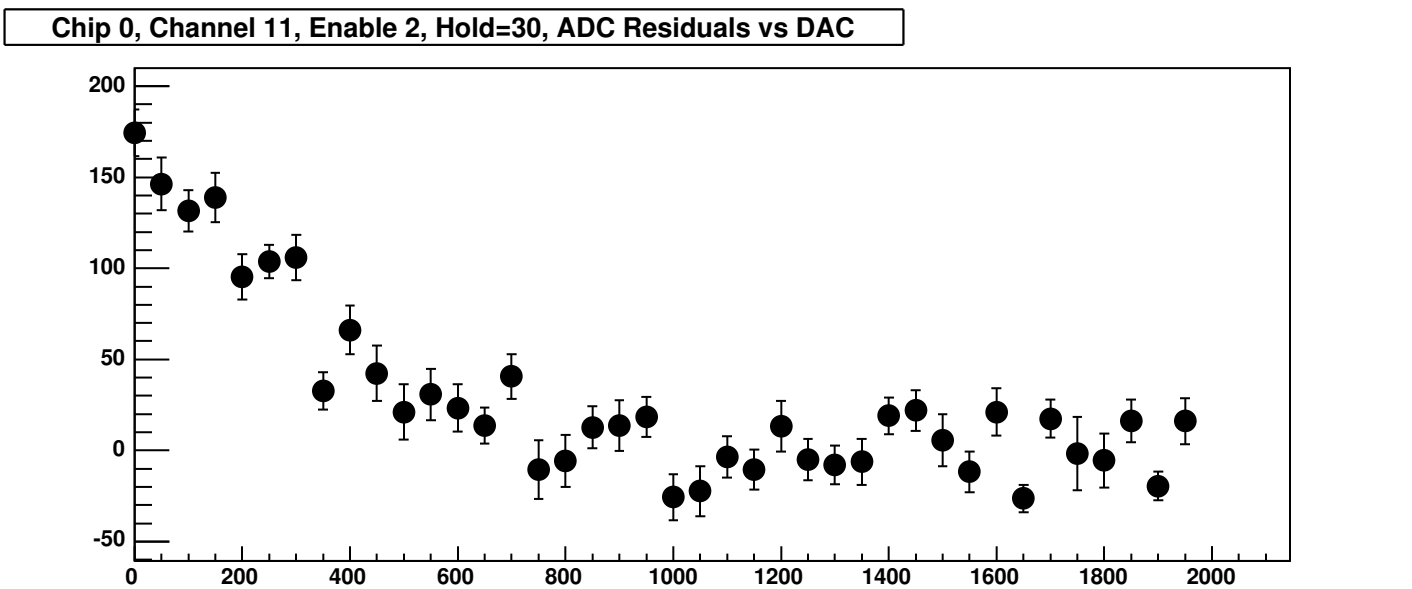

Chip 0, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 11.58 / 11
p0 -1128 ± 20.05
p1 0.08035 ± 0.01762
-920
-940
-960
-980
-1000
-1020
-1040
-1060
-1080
-1100
-1120
0
200
400
600
800
1000
1200
1400
1600
1800
2000

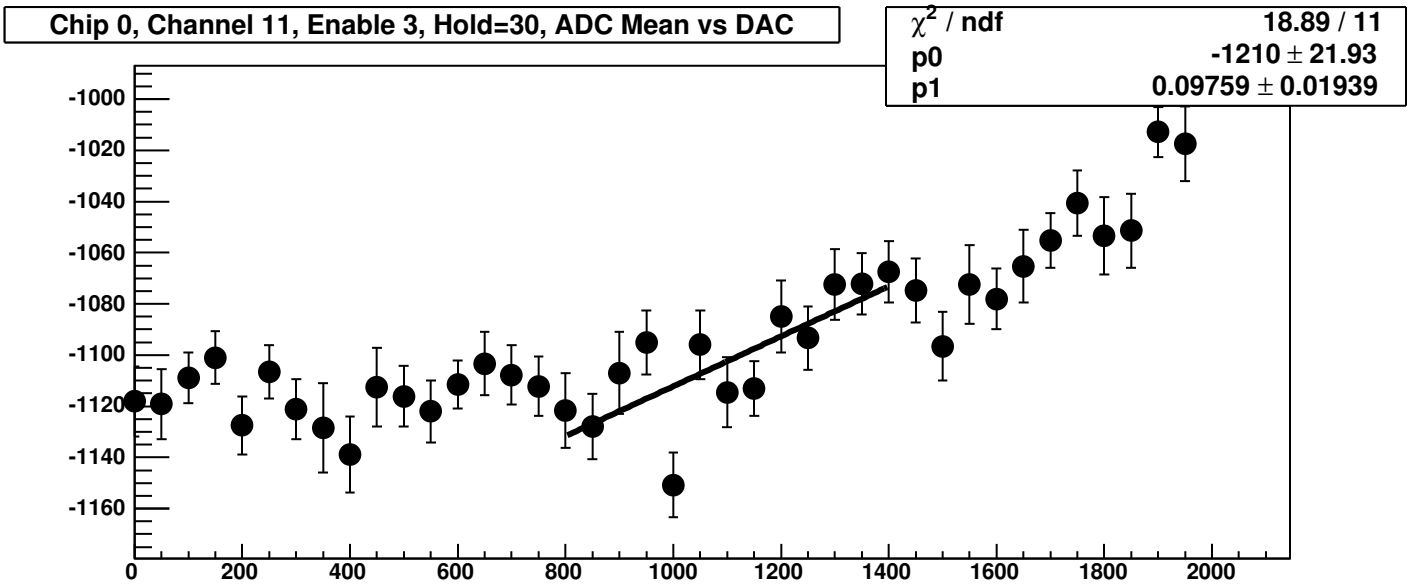

Chip 0, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

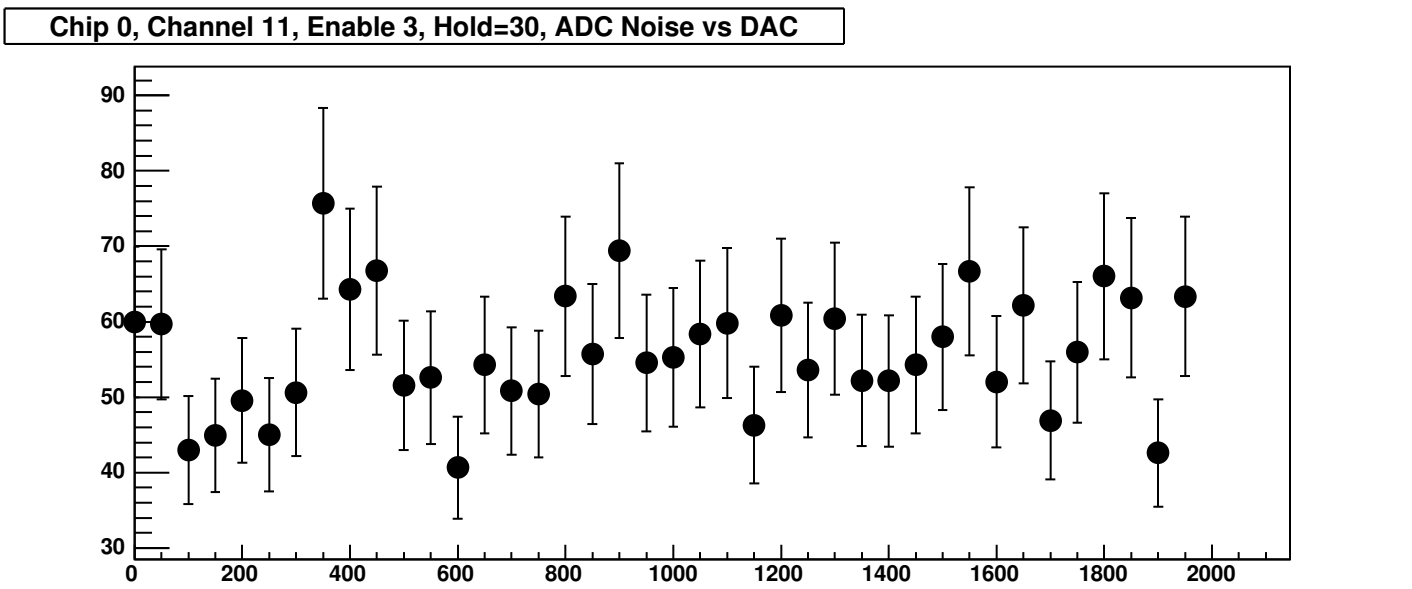

Chip 0, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

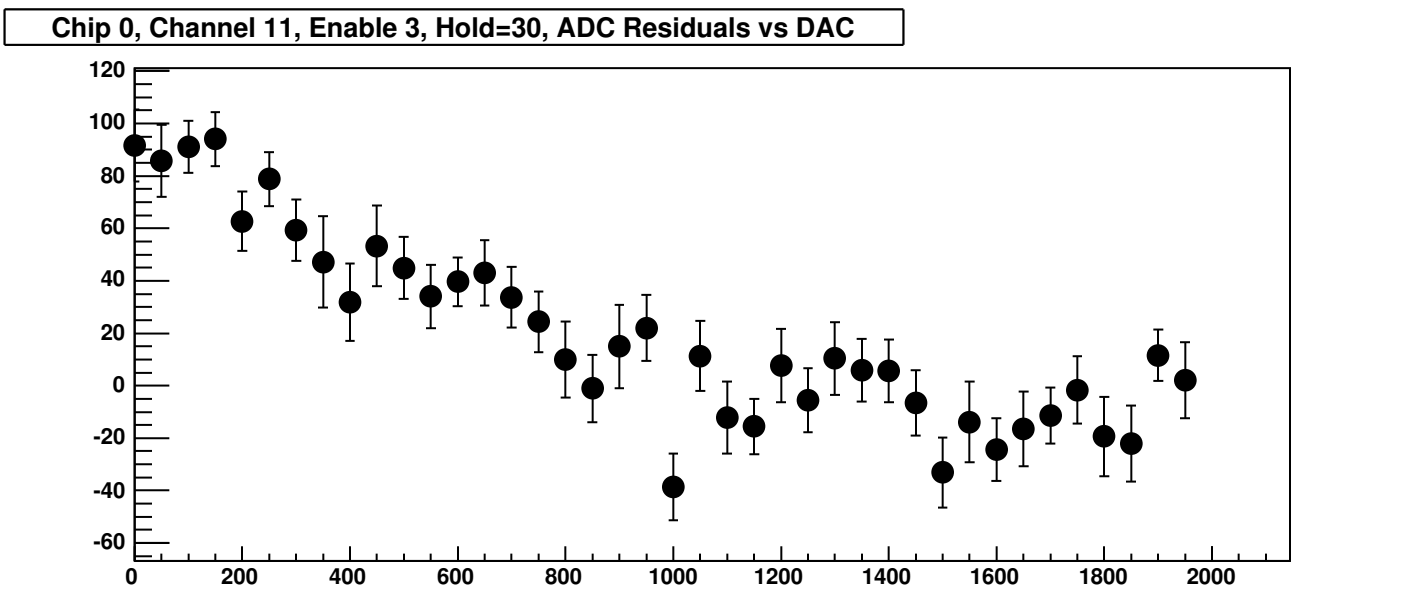

Chip 0, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 18.62 / 11
p0 -1150 ± 19.94
p1 0.2437 ± 0.01754
-650
-700
-750
-800
-850
-900
-950
-1000
-1050
0
200
400
600
800
1000
1200
1400
1600
1800
2000

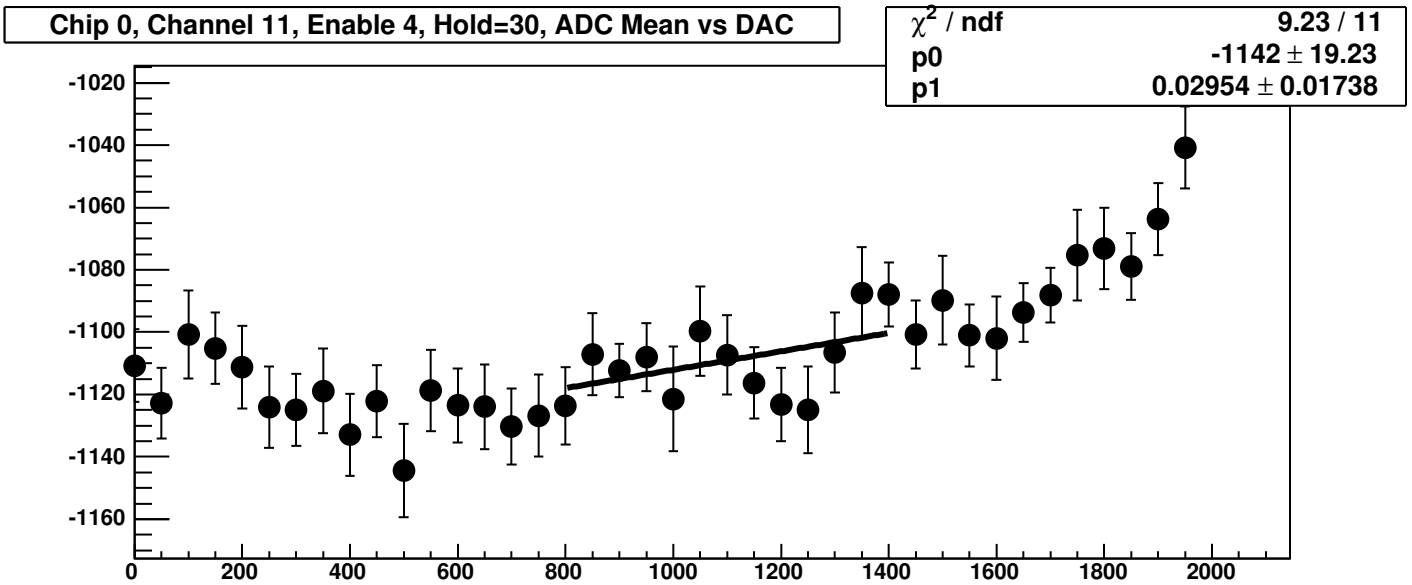

Chip 0, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

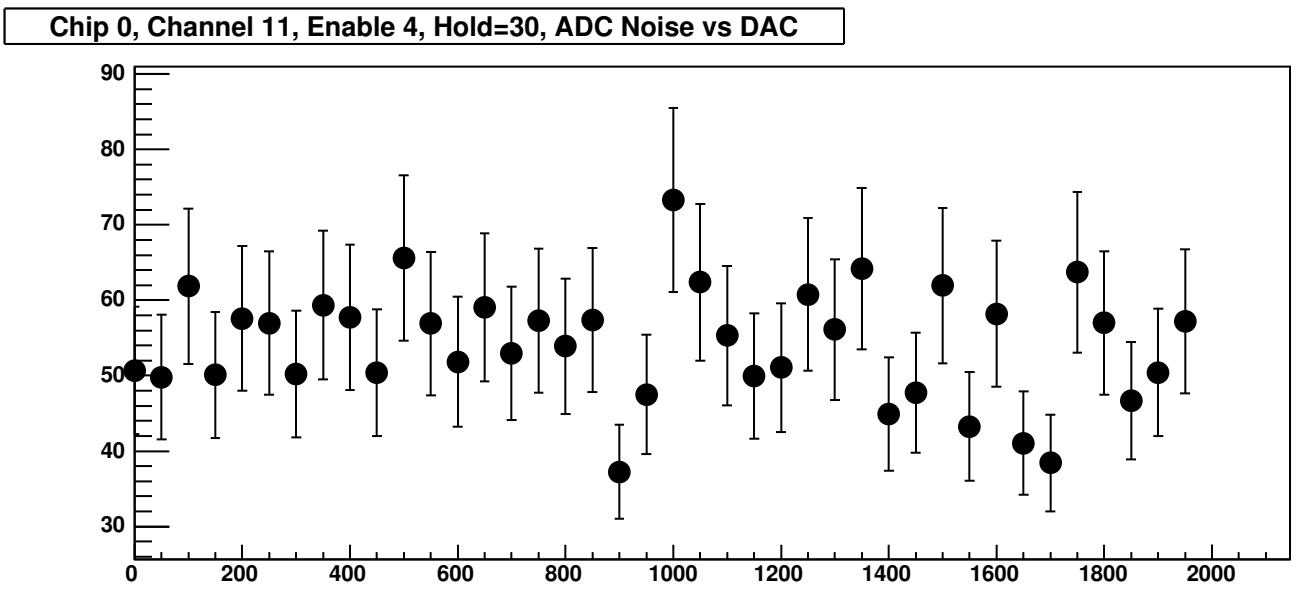

Chip 0, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

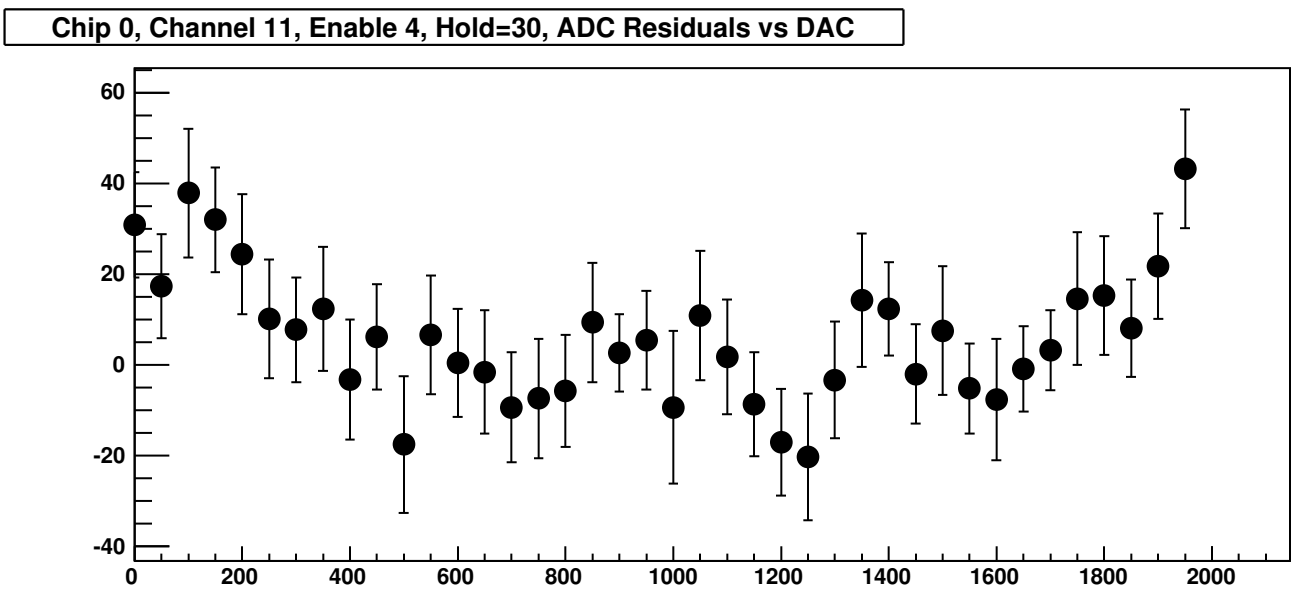

Chip 0, Channel 11, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 18.89 / 11
p0 -1210 ± 21.93
p1 0.09759 ± 0.01939
-1000
-1020
-1040
-1060
-1080
-1100
-1120
-1140
-1160
0
200
400
600
800
1000
1200
1400
1600
1800
2000

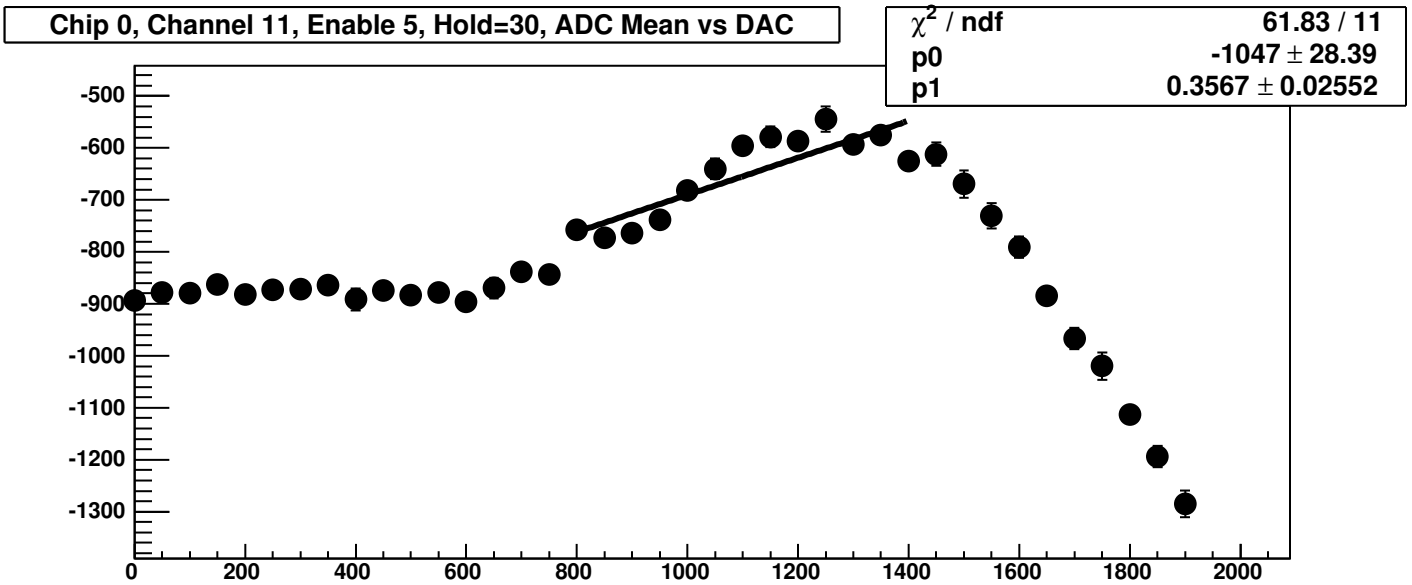

Chip 0, Channel 11, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

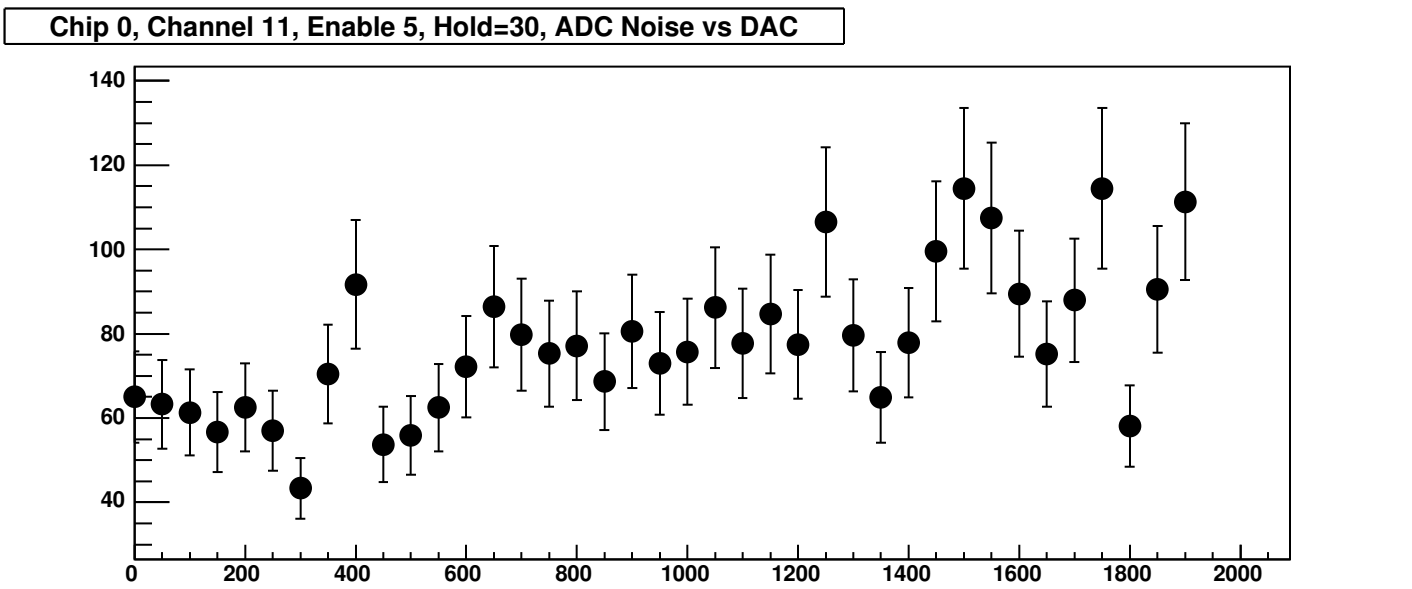

Chip 0, Channel 11, Enable 3, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

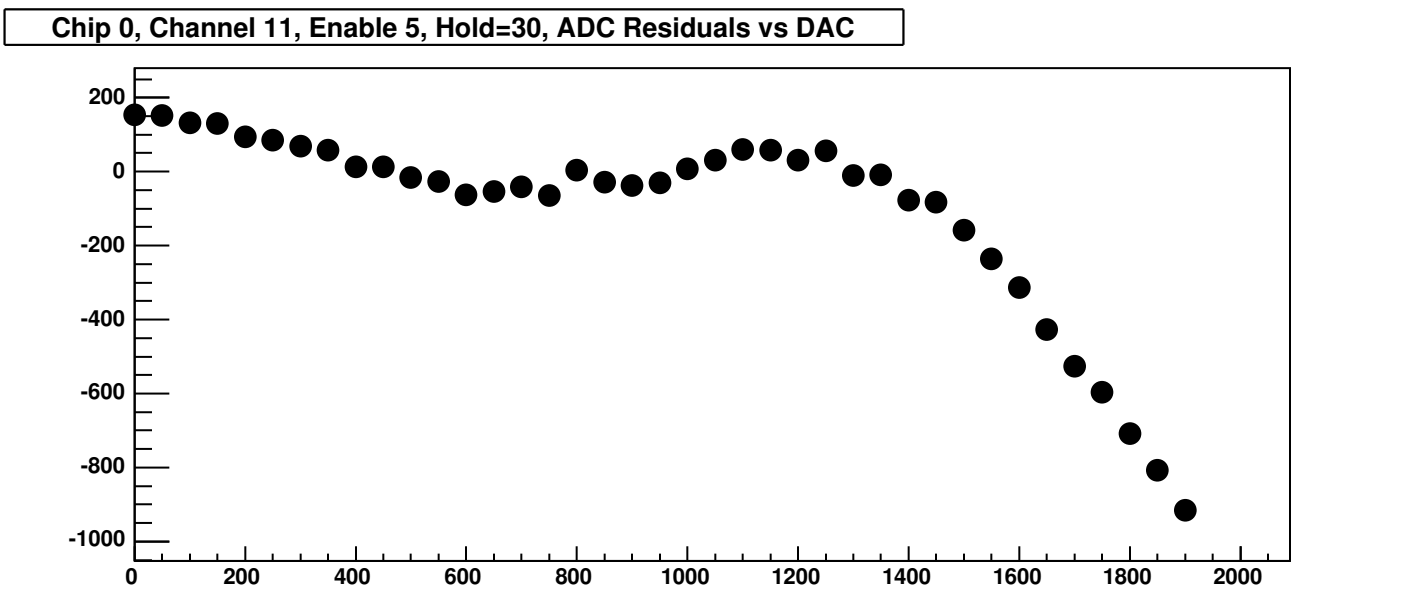

Chip 0, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 9.23 / 11
p0 -1142 ± 19.23
p1 0.02954 ± 0.01738
-1020
-1040
-1060
-1080
-1100
-1120
-1140
-1160
0
200
400
600
800
1000
1200
1400
1600
1800
2000

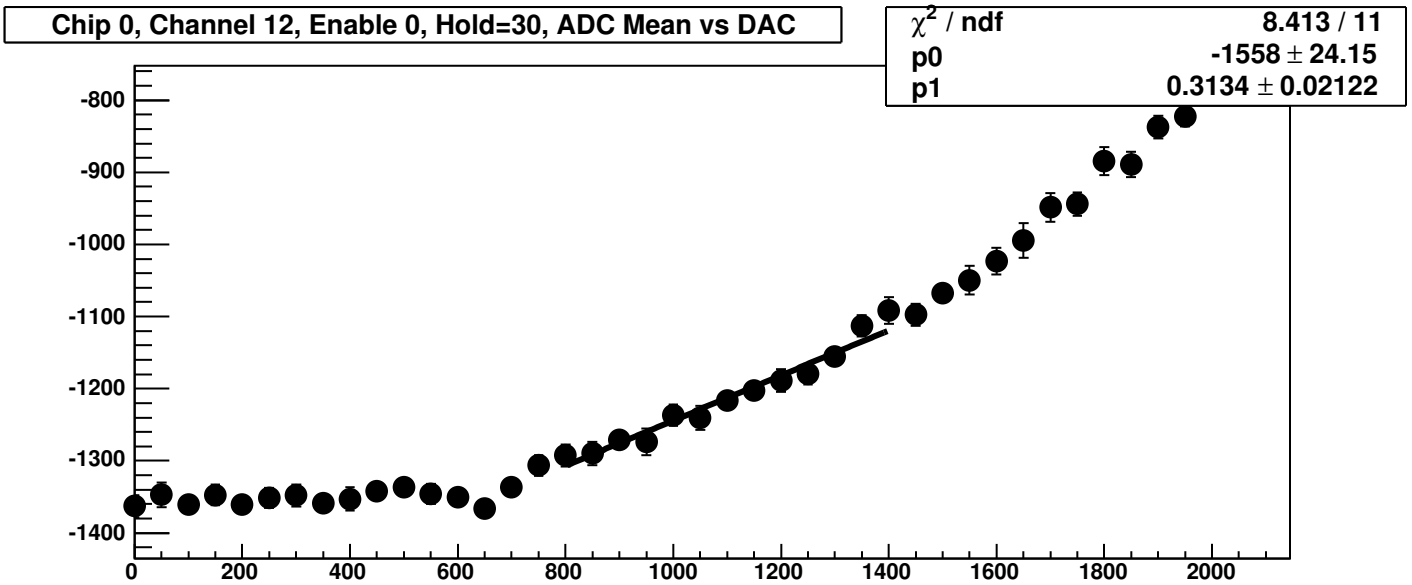

Chip 0, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

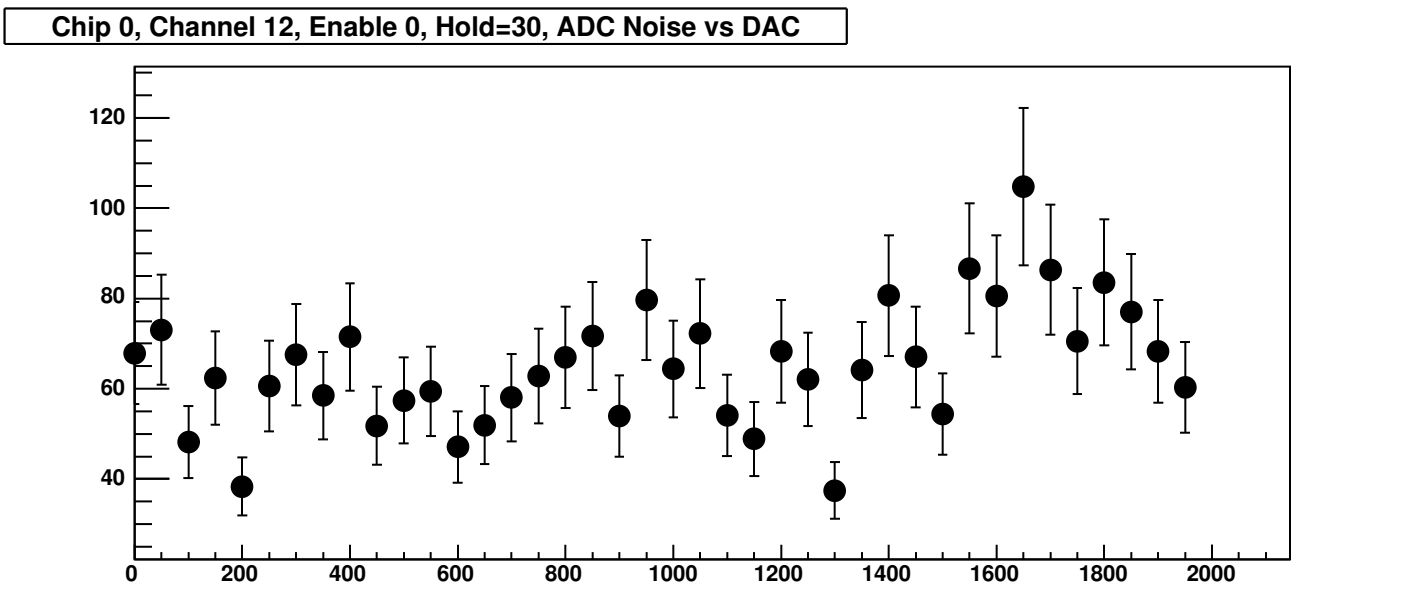

Chip 0, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

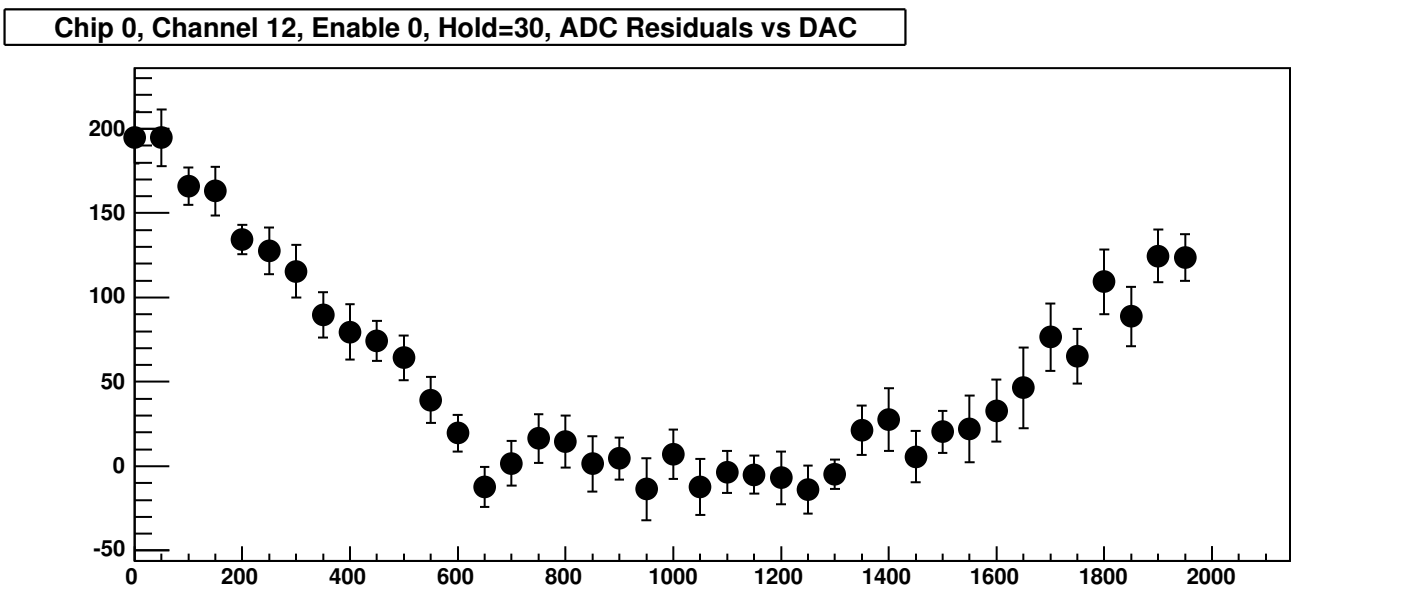

Chip 0, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 61.83 / 11
p0 -1047 ± 28.39
p1 0.3567 ± 0.02552
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

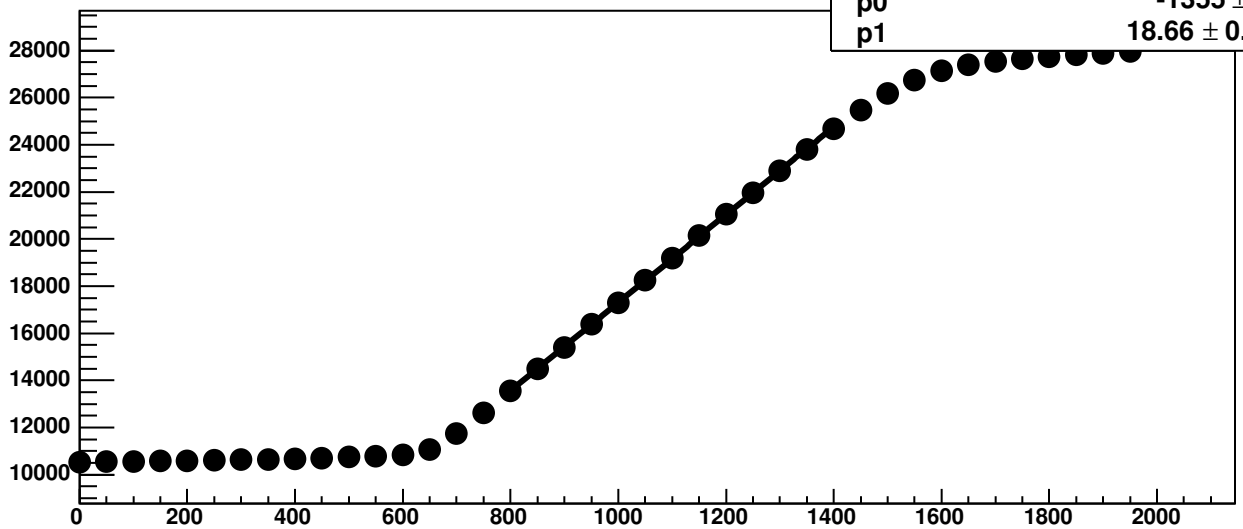

Chip 0, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 8.413 / 11
p0 -1558 ± 24.15
p1 0.3134 ± 0.02122
-800
-900
-1000
-1100
-1200
-1300
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

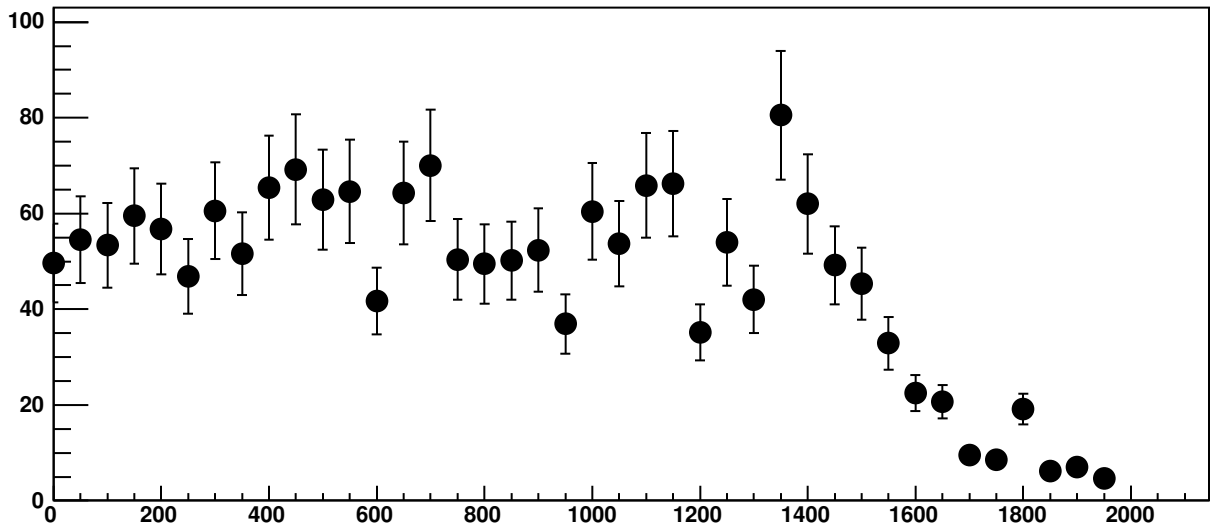

Chip 0, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

Chip 0, Channel 12, Enable 1!, Hold=30, ADC Mean vs DAC

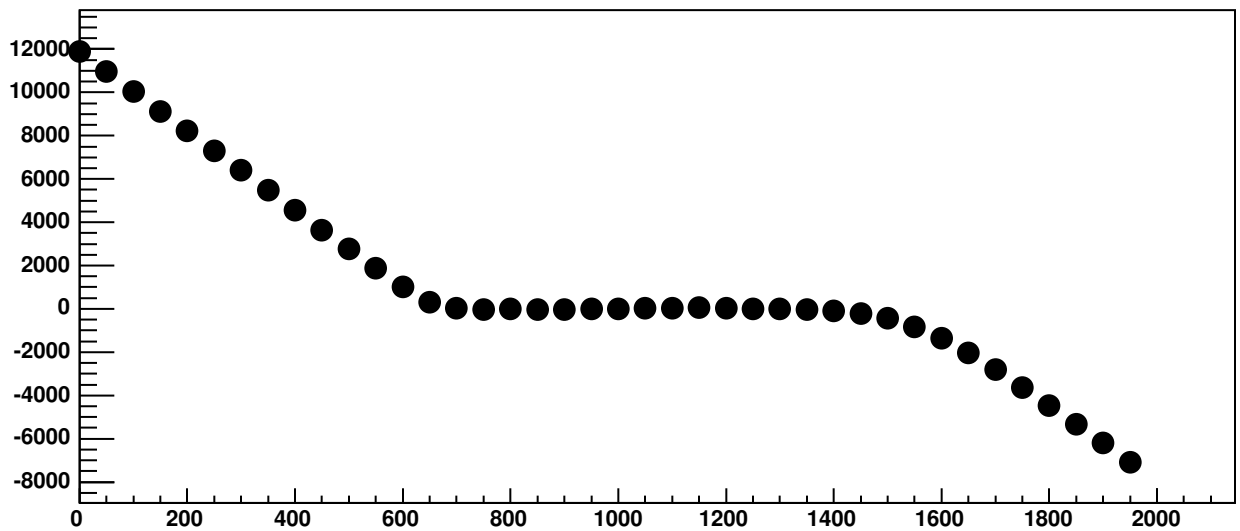
χ^2 / ndf	83.38 / 11
p0	-1355 ± 19.43
p1	18.66 ± 0.01768

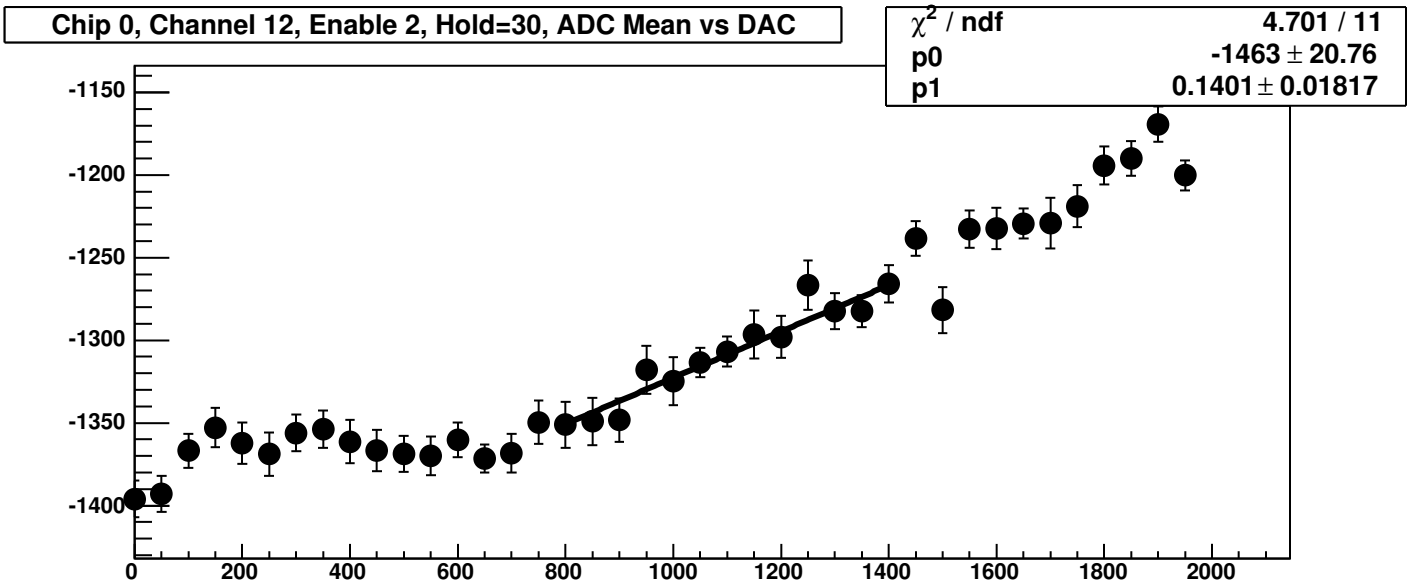


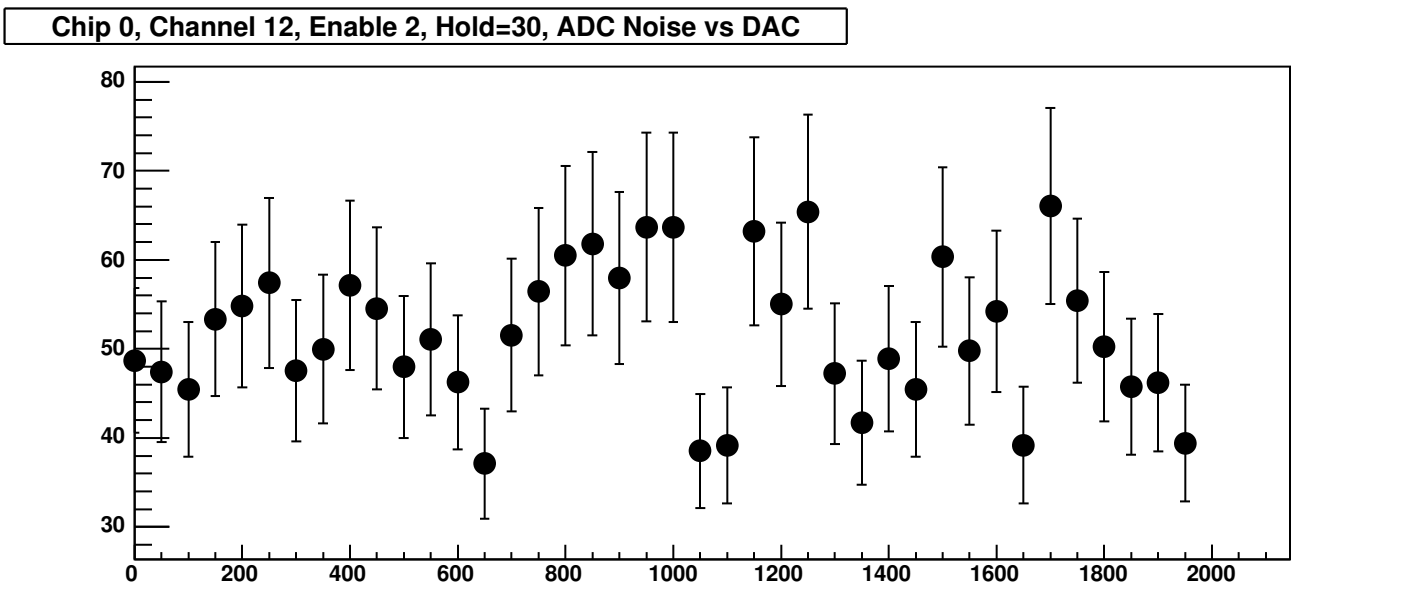
Chip 0, Channel 12, Enable 1!, Hold=30, ADC Noise vs DAC

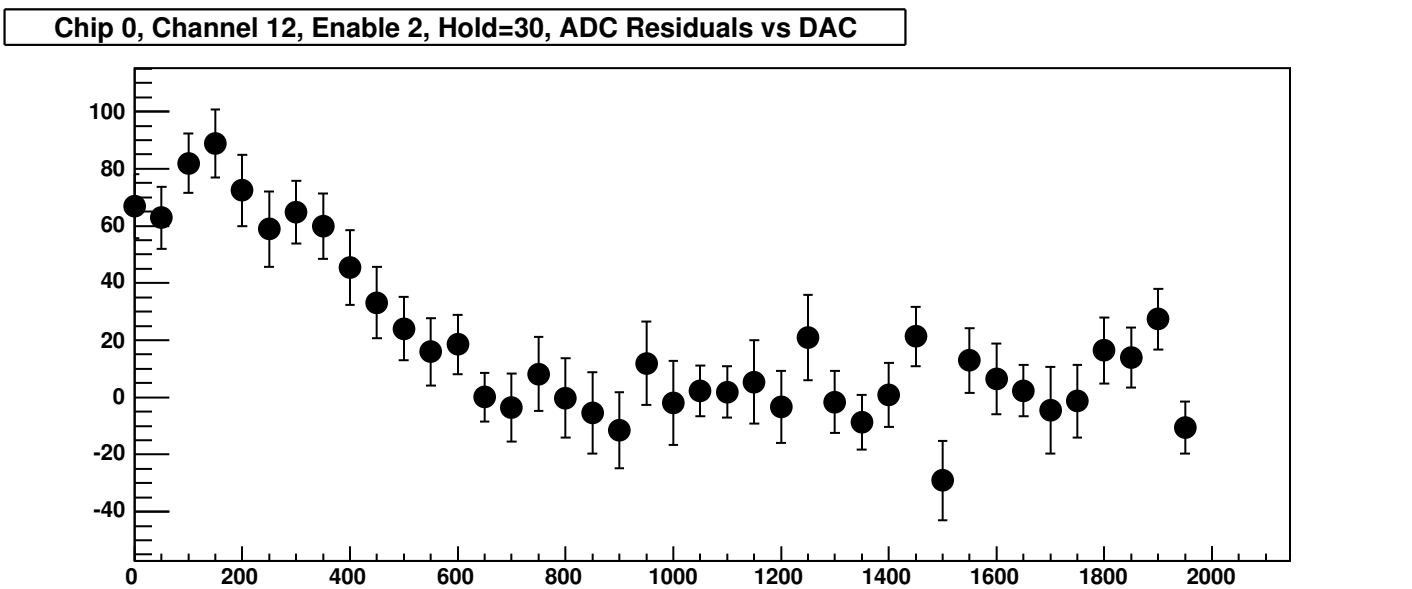


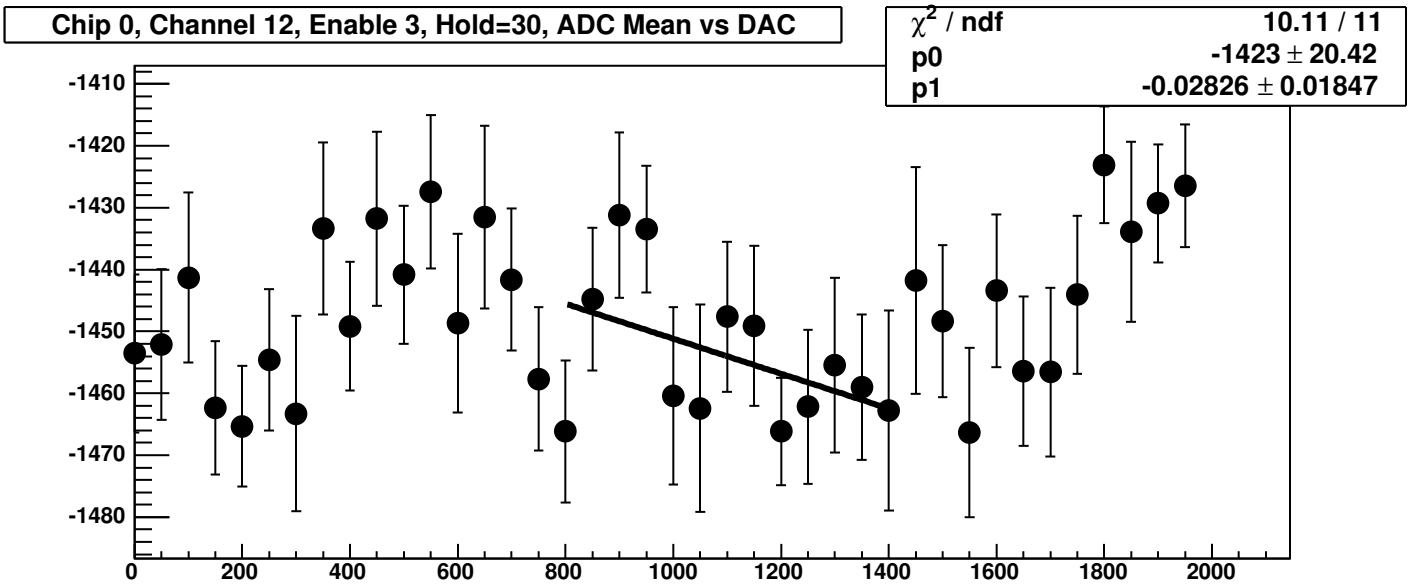
Chip 0, Channel 12, Enable 1!, Hold=30, ADC Residuals vs DAC

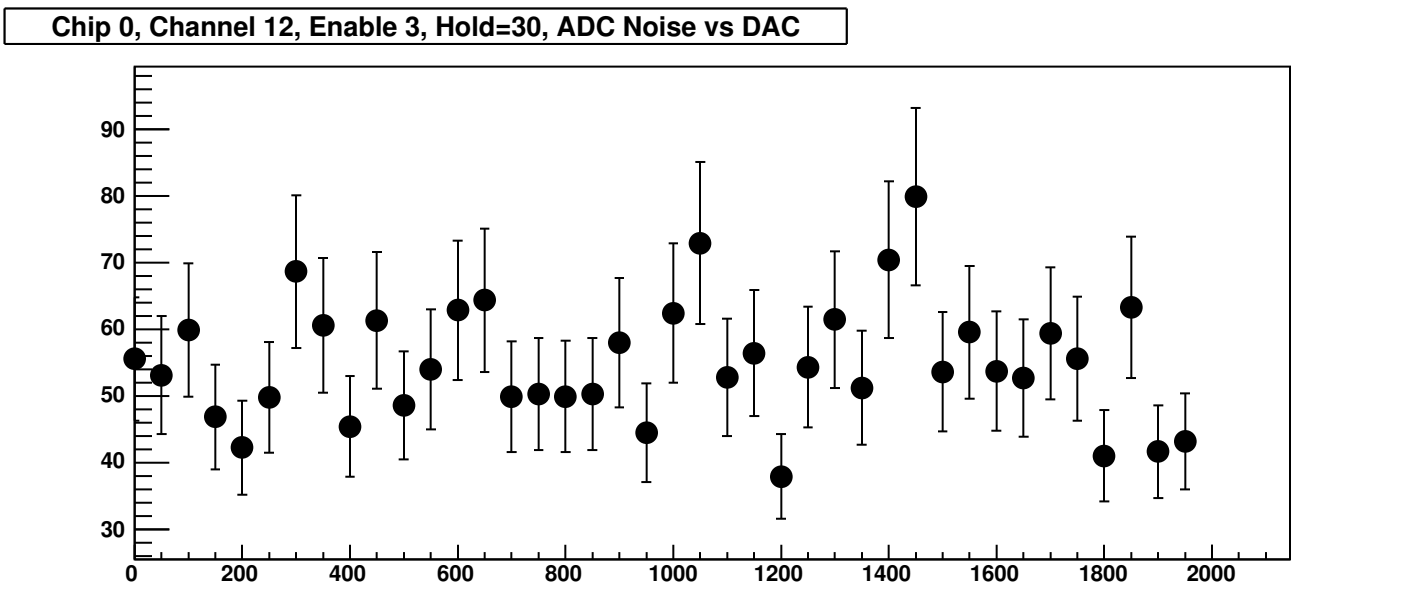


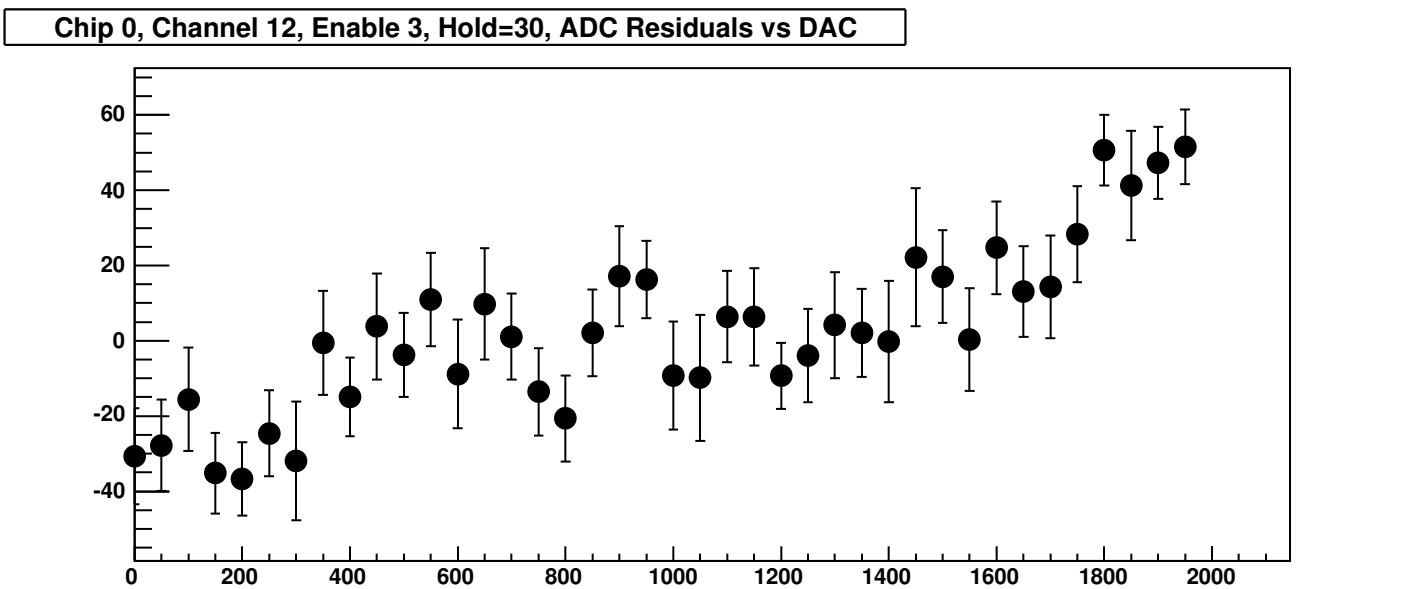

Chip 0, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4.701 / 11
p0 -1463 ± 20.76
p1 0.1401 ± 0.01817
-1150
-1200
-1250
-1300
-1350
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

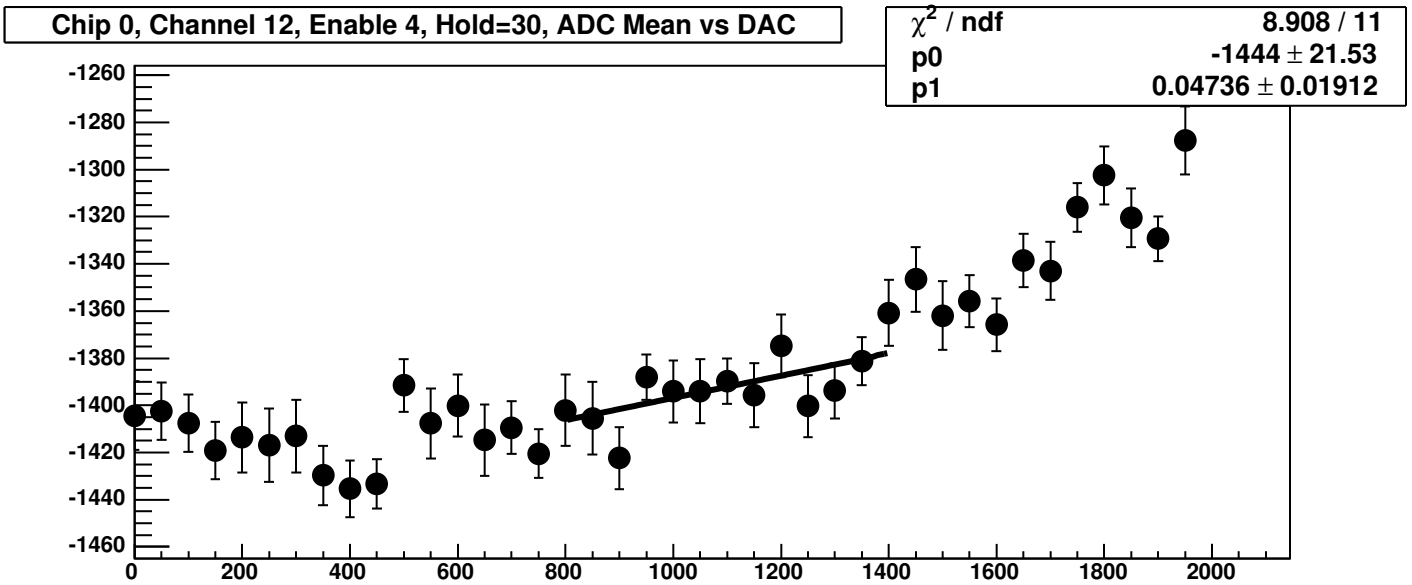

Chip 0, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

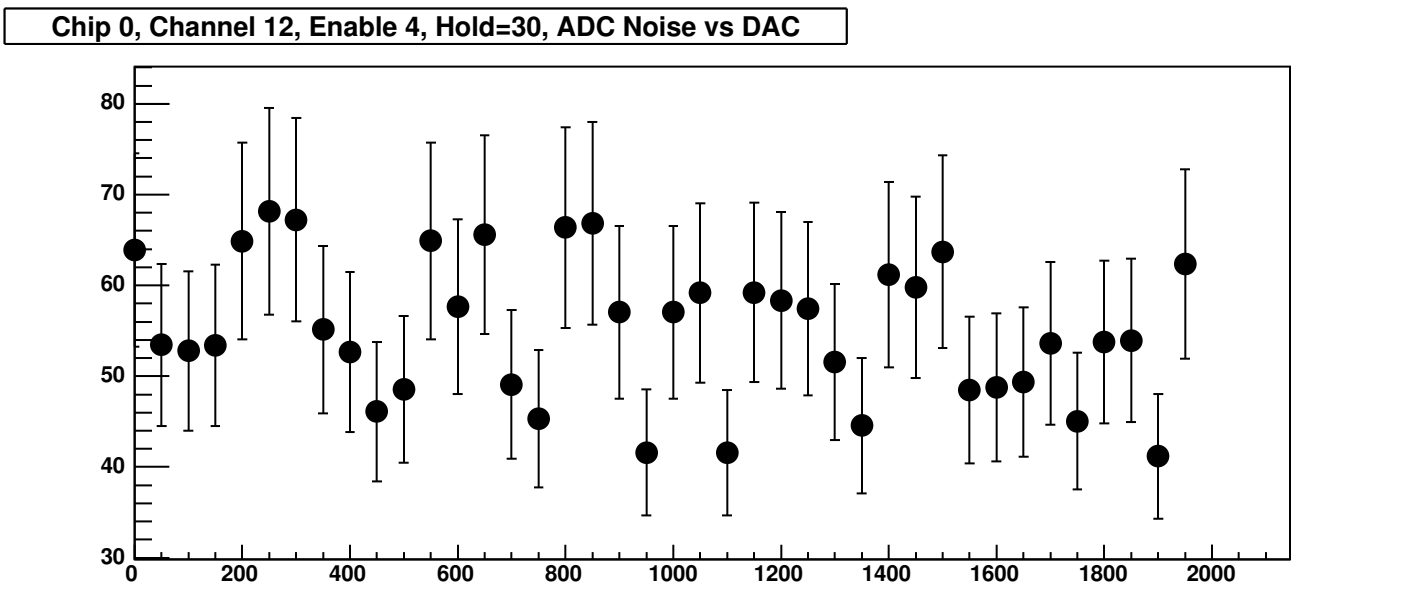

Chip 0, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

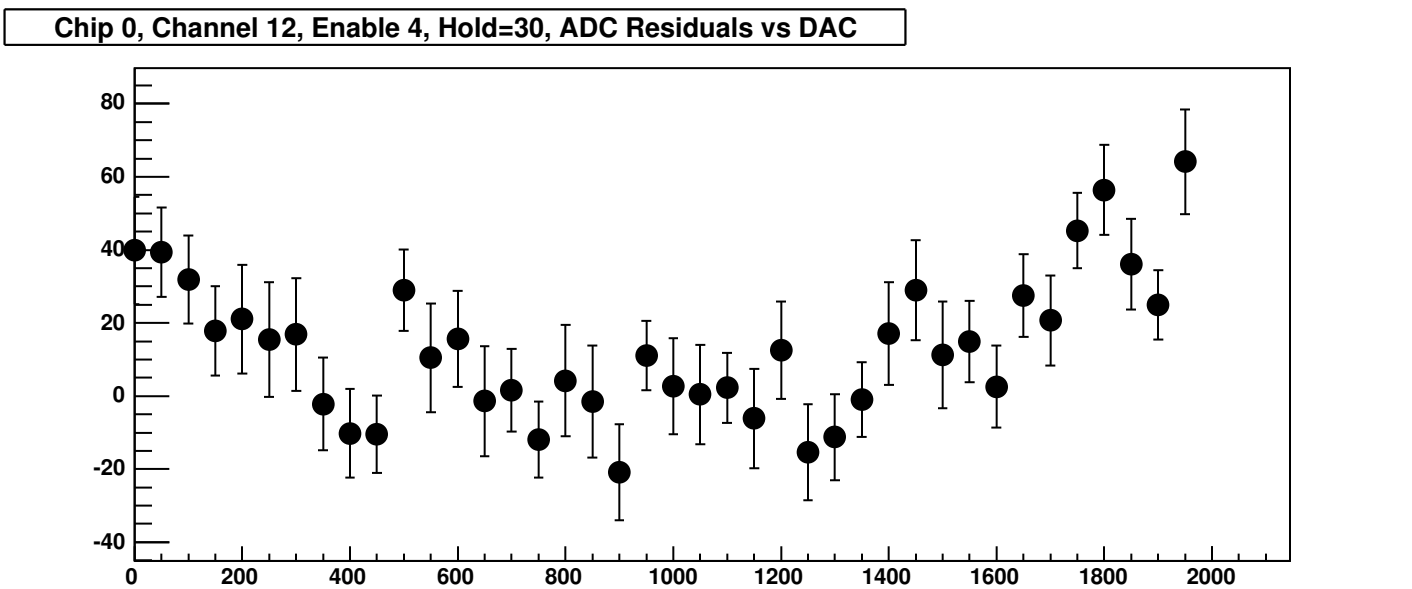

Chip 0, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 10.11 / 11
p0 -1423 ± 20.42
p1 -0.02826 ± 0.01847
-1410
-1420
-1430
-1440
-1450
-1460
-1470
-1480
0
200
400
600
800
1000
1200
1400
1600
1800
2000

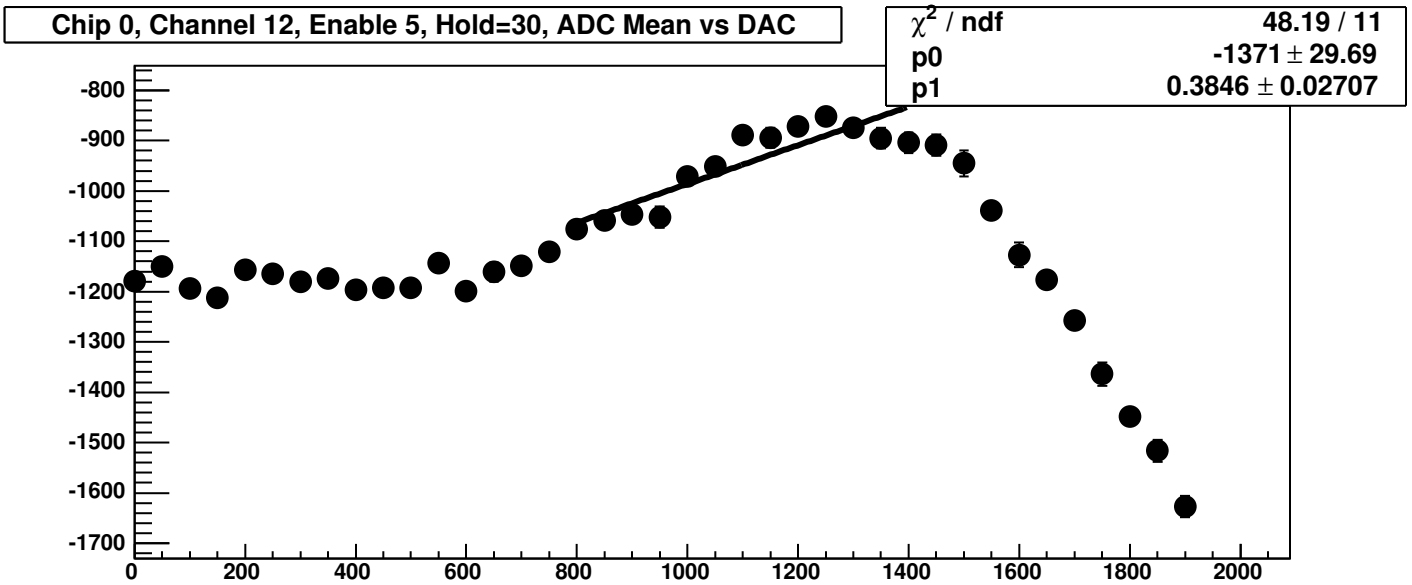

Chip 0, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

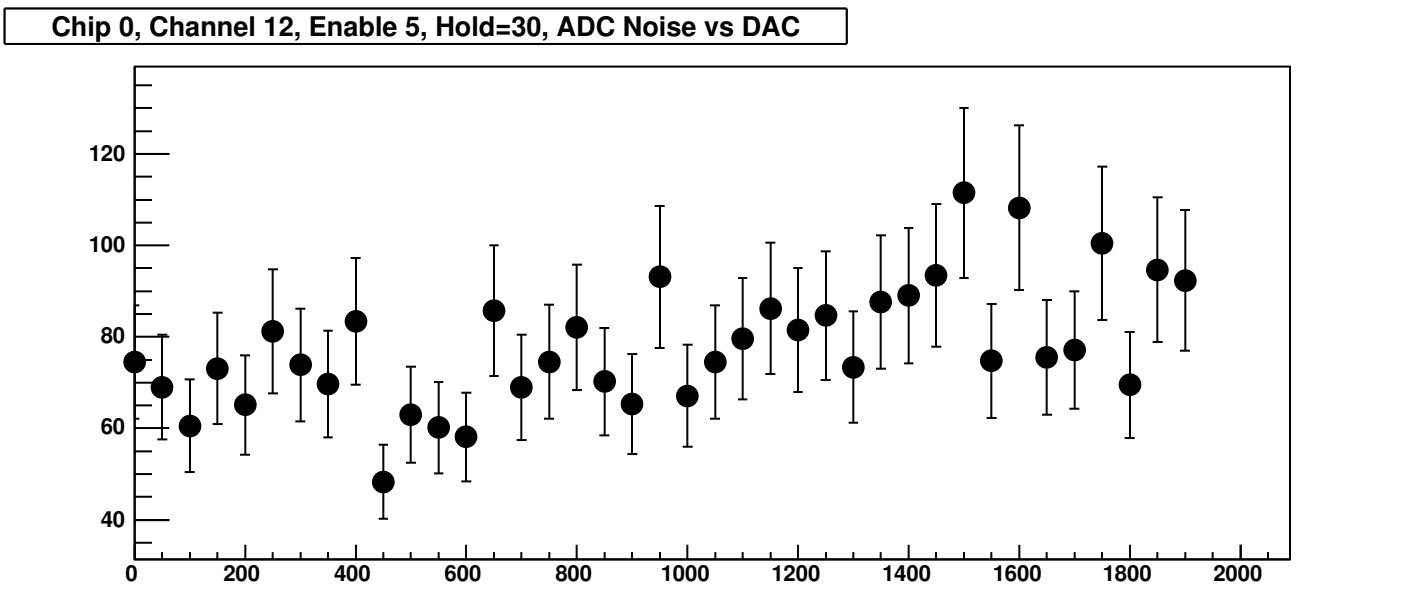

Chip 0, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

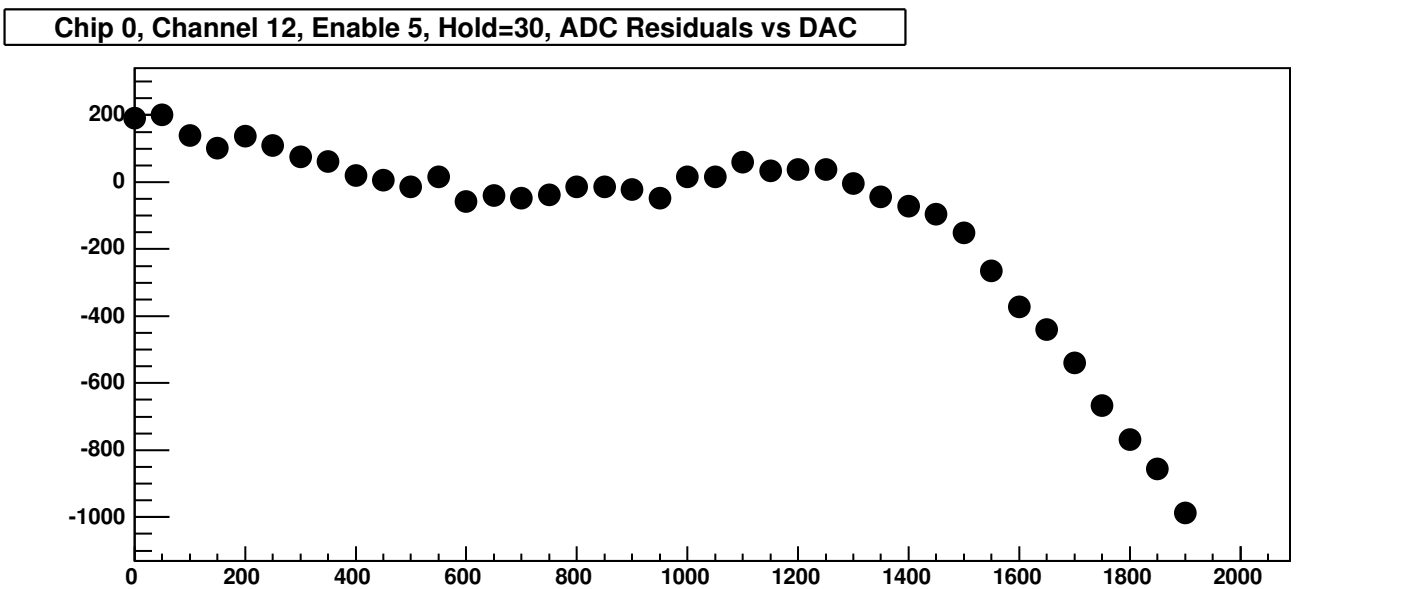

Chip 0, Channel 12, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 8.908 / 11
p0 -1444 ± 21.53
p1 0.04736 ± 0.01912
-1260
-1280
-1300
-1320
-1340
-1360
-1380
-1400
-1420
-1440
-1460
0
200
400
600
800
1000
1200
1400
1600
1800
2000

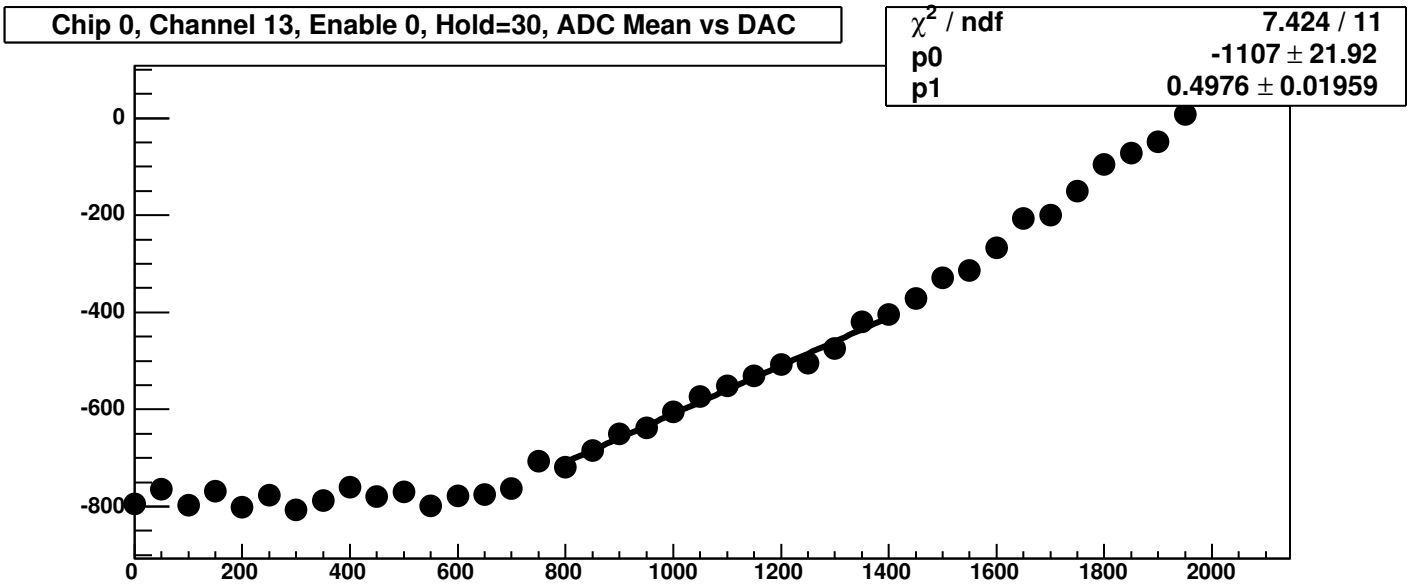

Chip 0, Channel 12, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

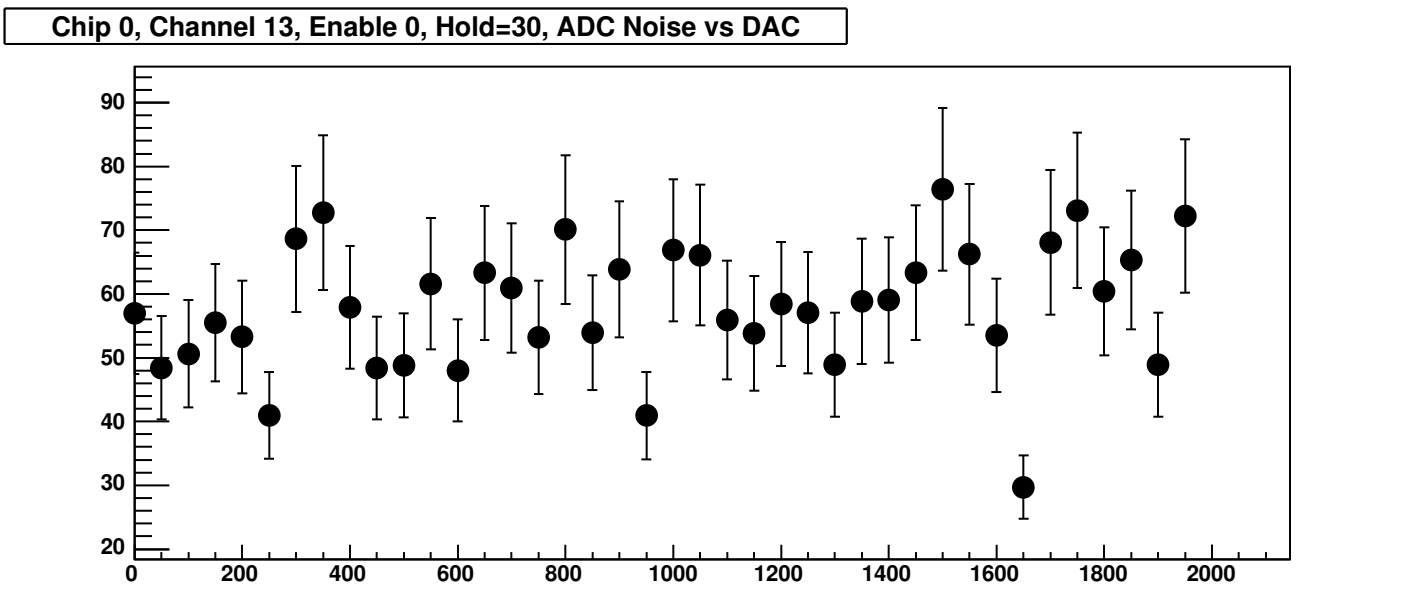

Chip 0, Channel 12, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

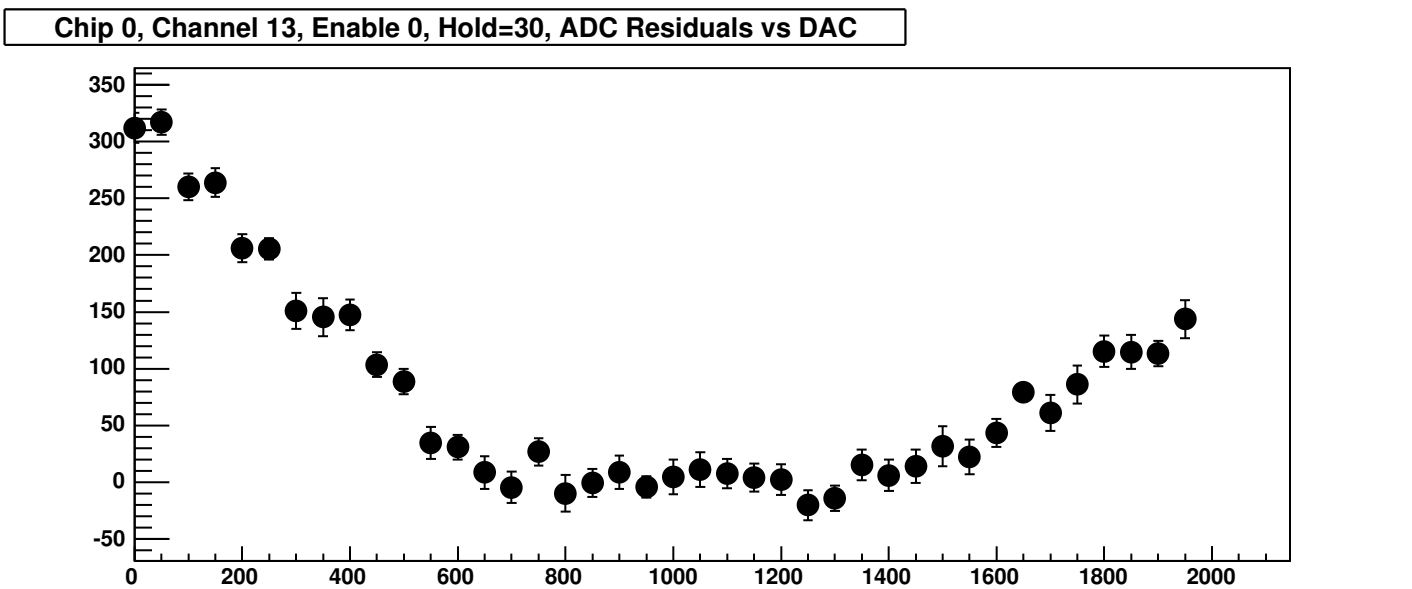

Chip 0, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 48.19 / 11
p0 -1371 ± 29.69
p1 0.3846 ± 0.02707
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

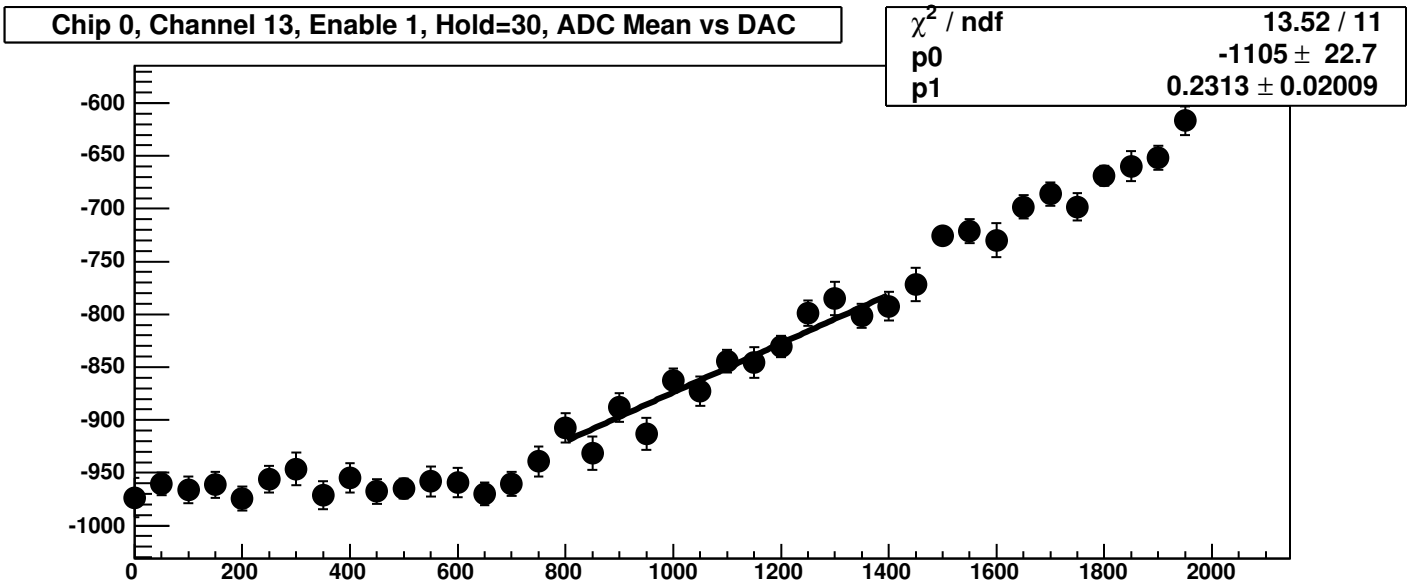

Chip 0, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

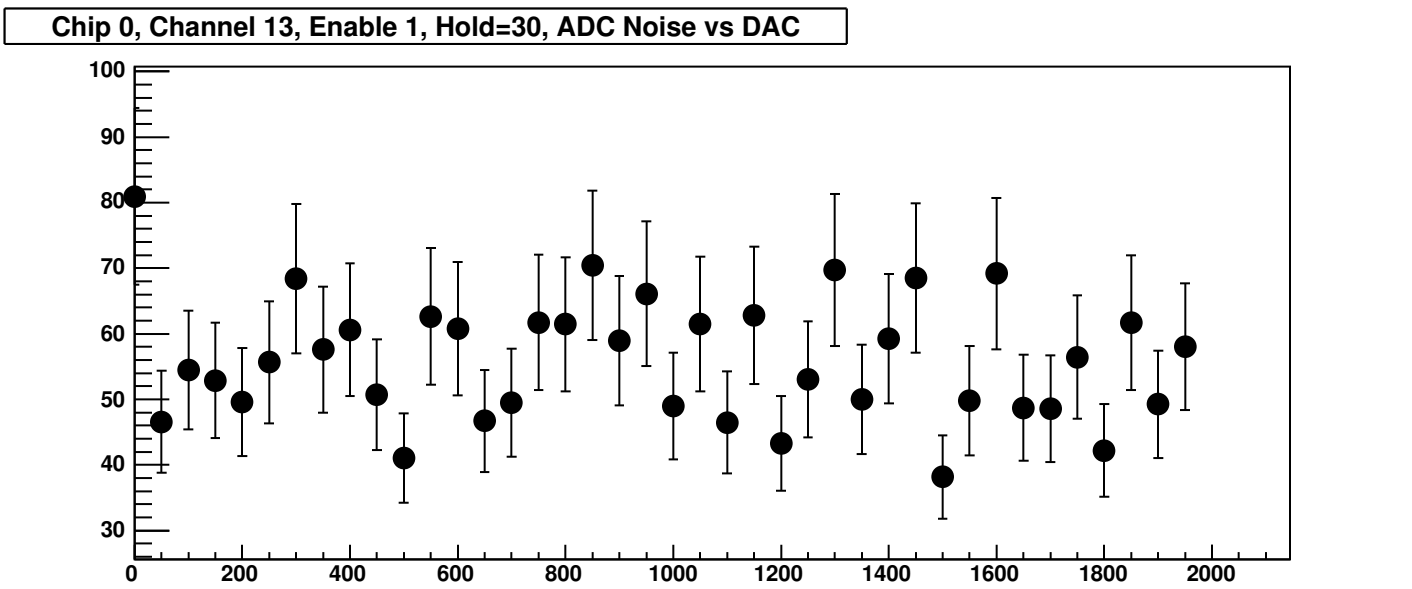

Chip 0, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

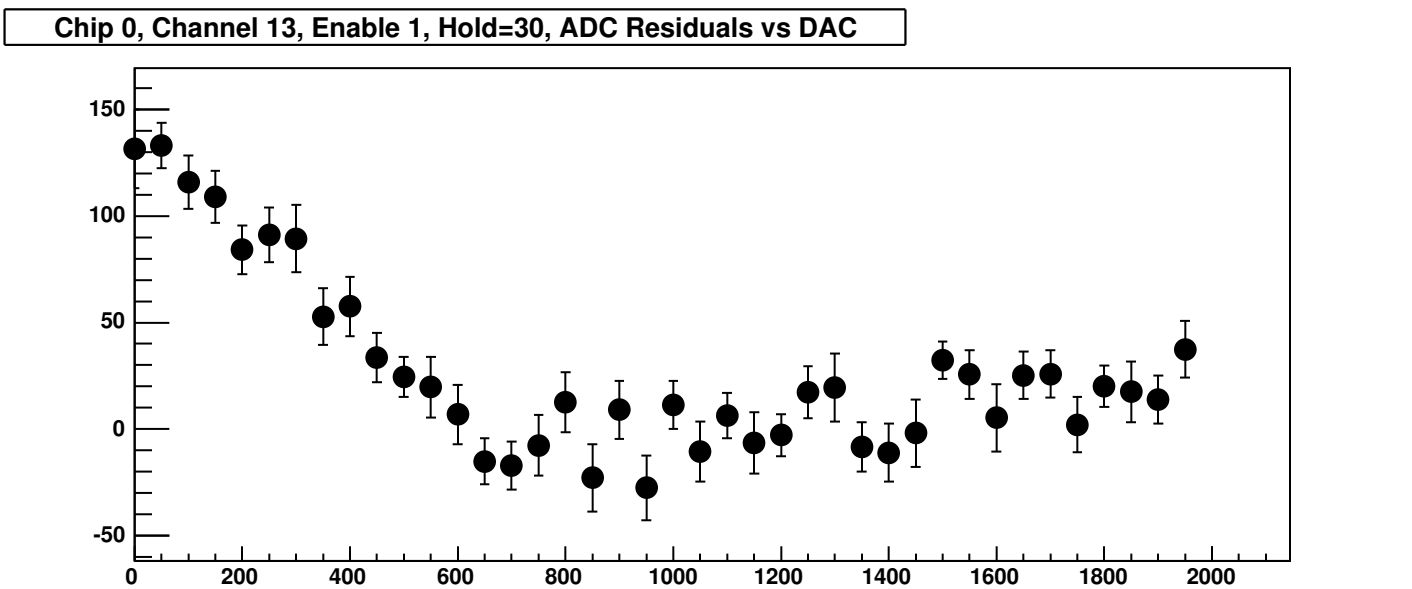

Chip 0, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 7.424 / 11
p0 -1107 ± 21.92
p1 0.4976 ± 0.01959
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

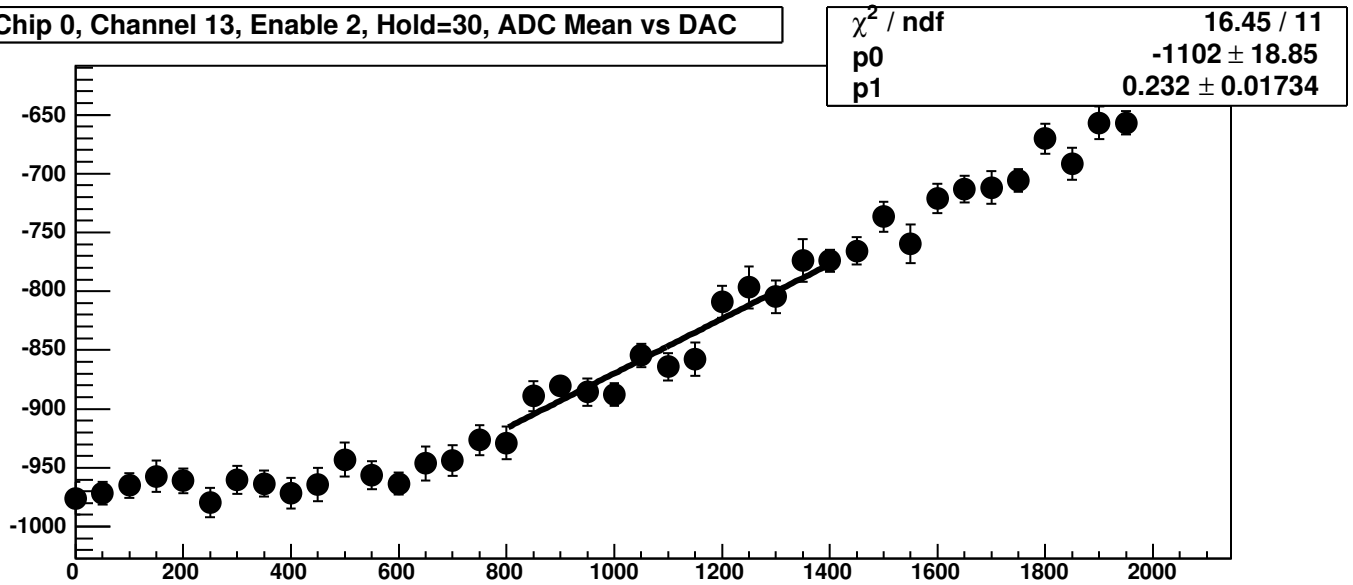

Chip 0, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 13, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 13.52 / 11
p0 -1105 ± 22.7
p1 0.2313 ± 0.02009
-600
-650
-700
-750
-800
-850
-900
-950
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

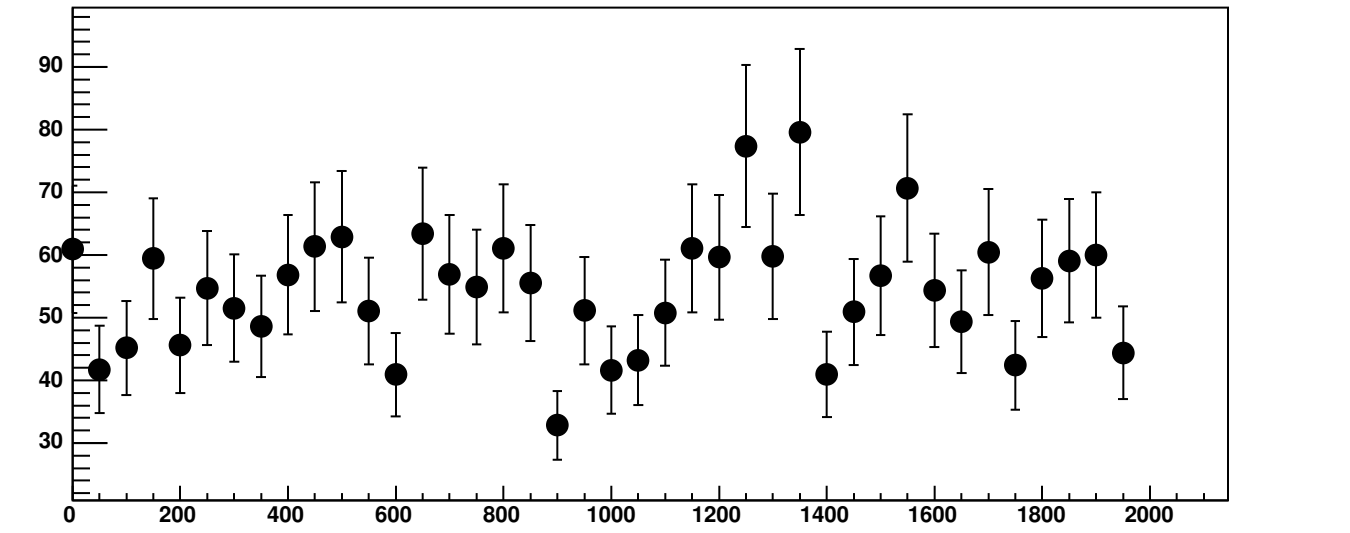

Chip 0, Channel 13, Enable 1, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 13, Enable 1, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

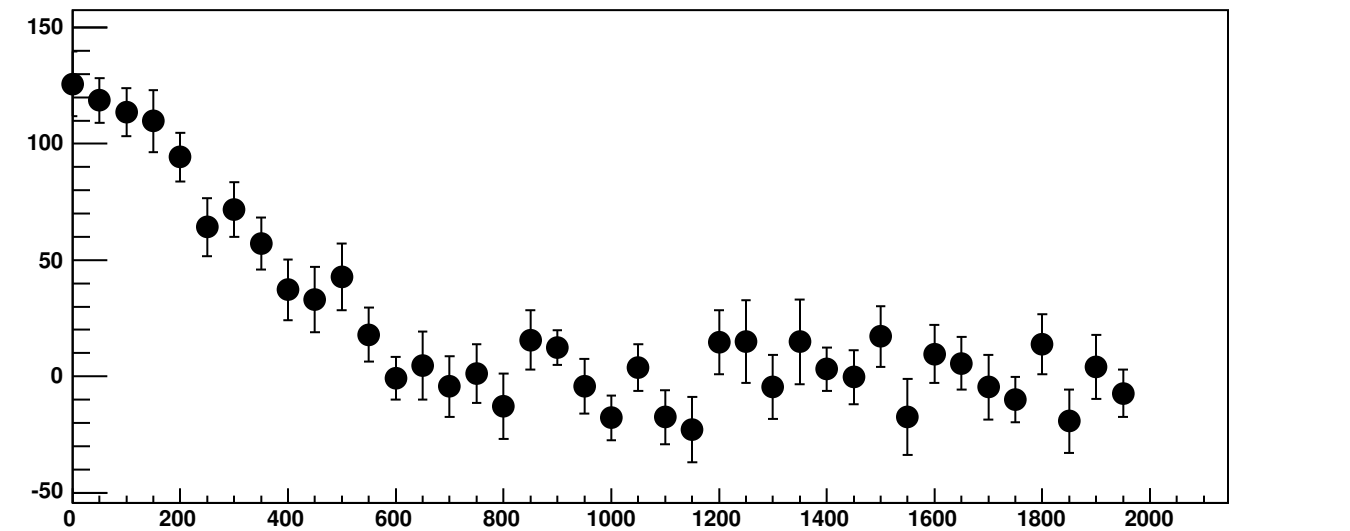
Chip 0, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC

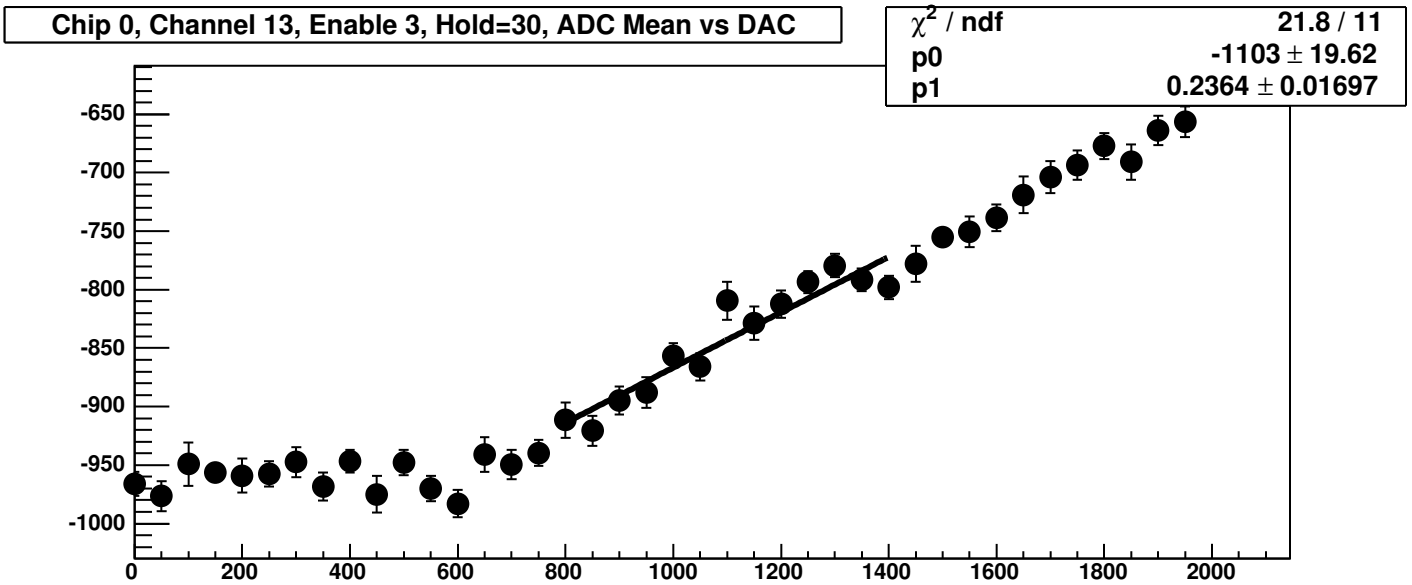


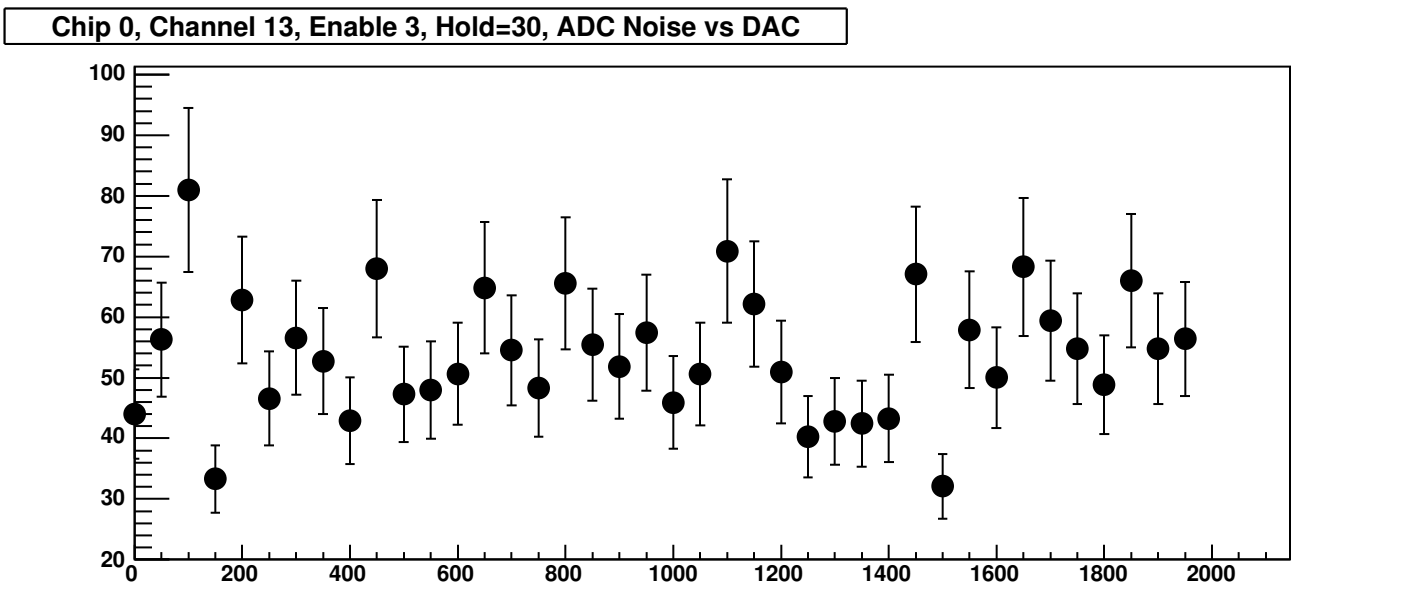
Chip 0, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC

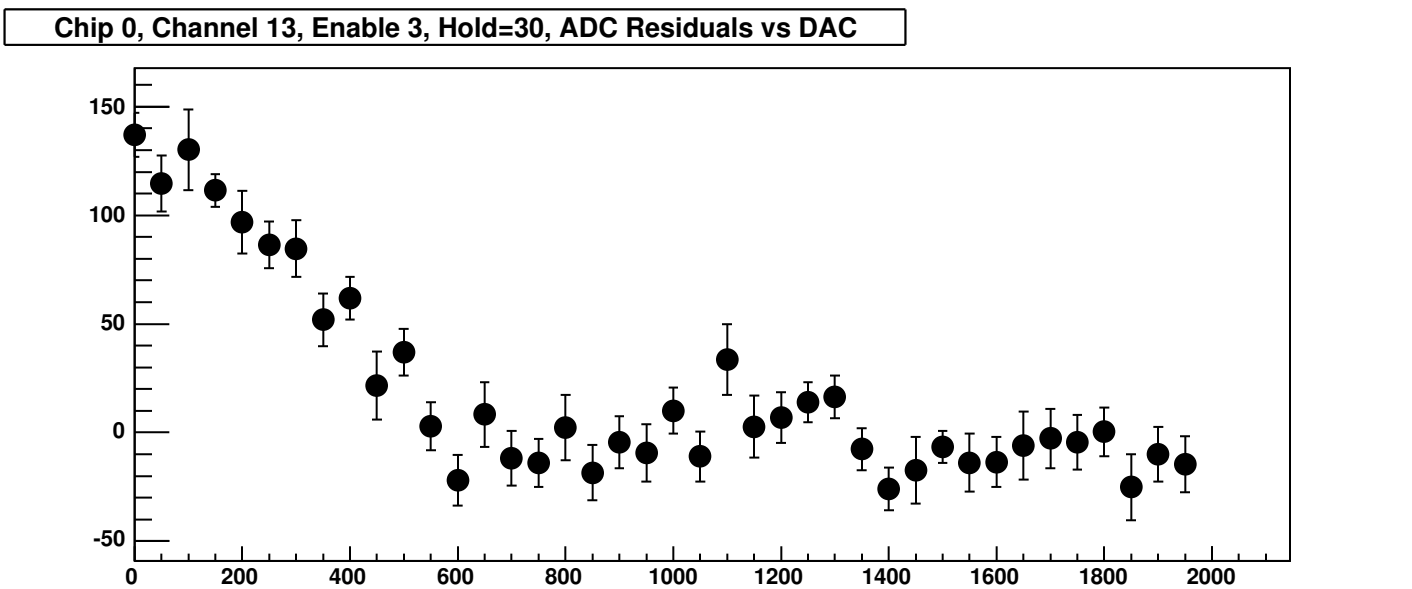


Chip 0, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC

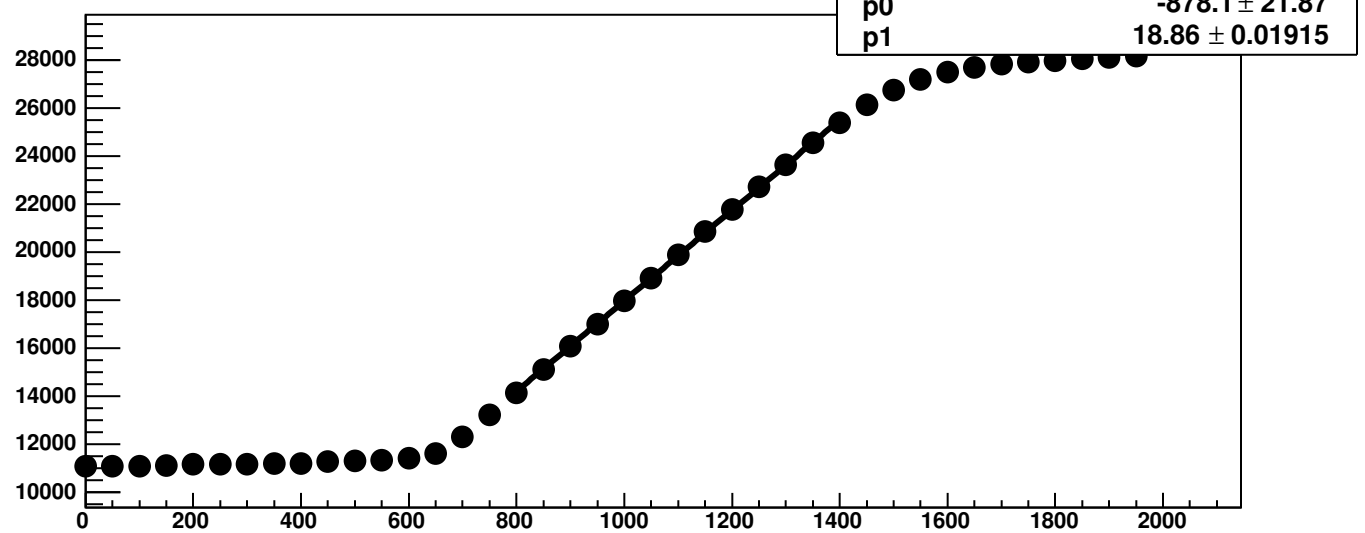



Chip 0, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 21.8 / 11
p0 -1103 ± 19.62
p1 0.2364 ± 0.01697
-650
-700
-750
-800
-850
-900
-950
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

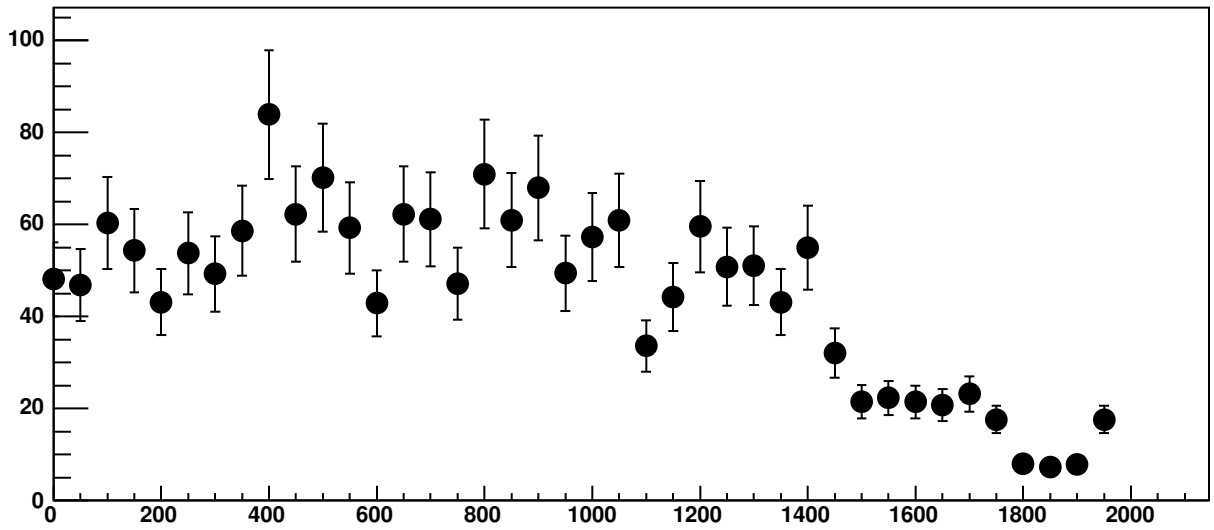

Chip 0, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

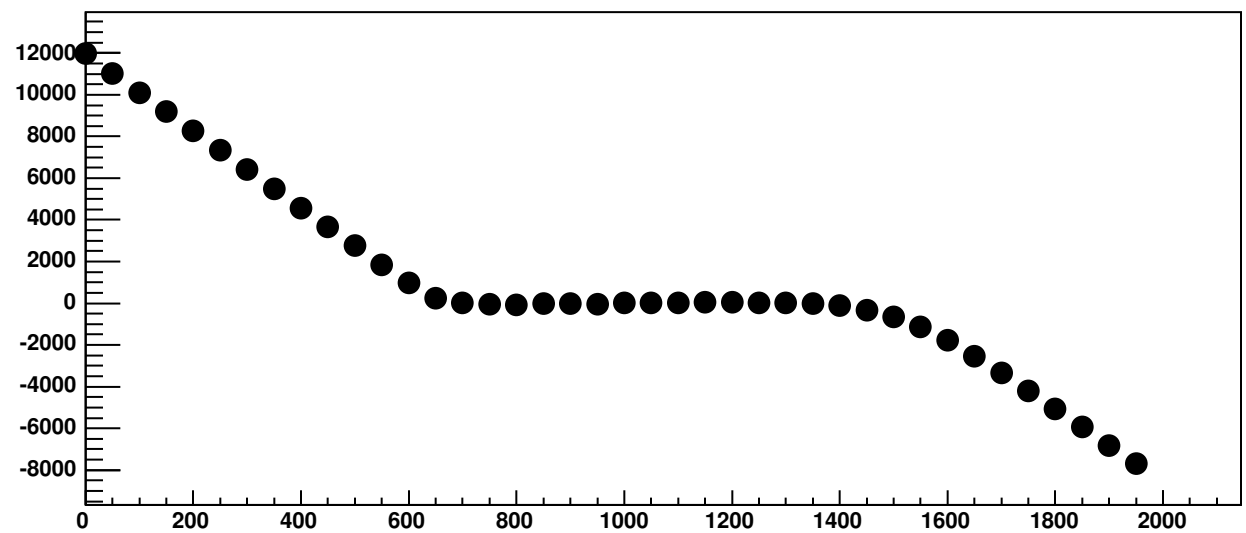
Chip 0, Channel 13, Enable 4!, Hold=30, ADC Mean vs DAC

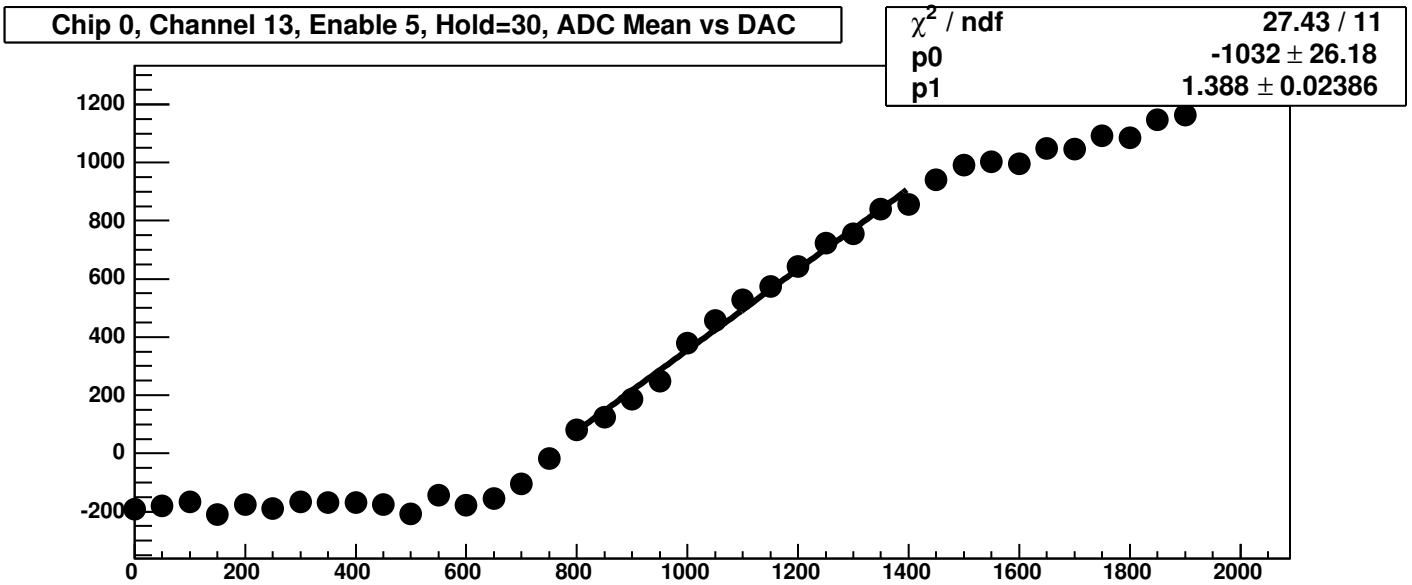


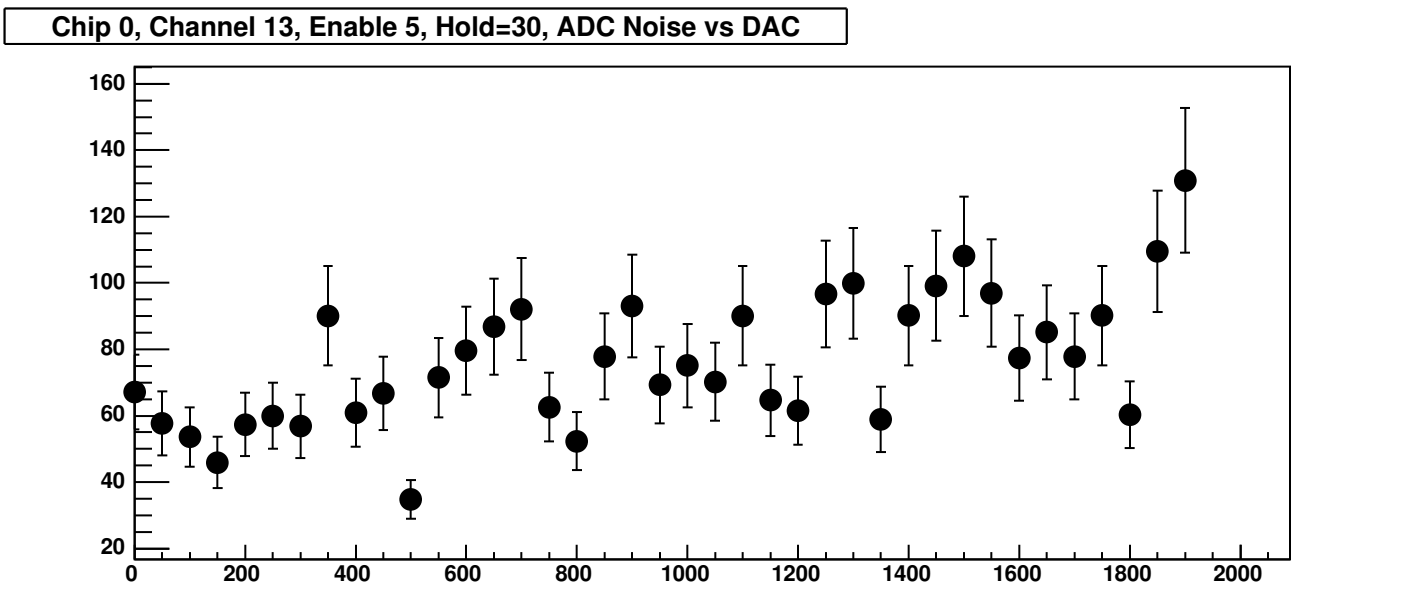
Chip 0, Channel 13, Enable 4!, Hold=30, ADC Noise vs DAC

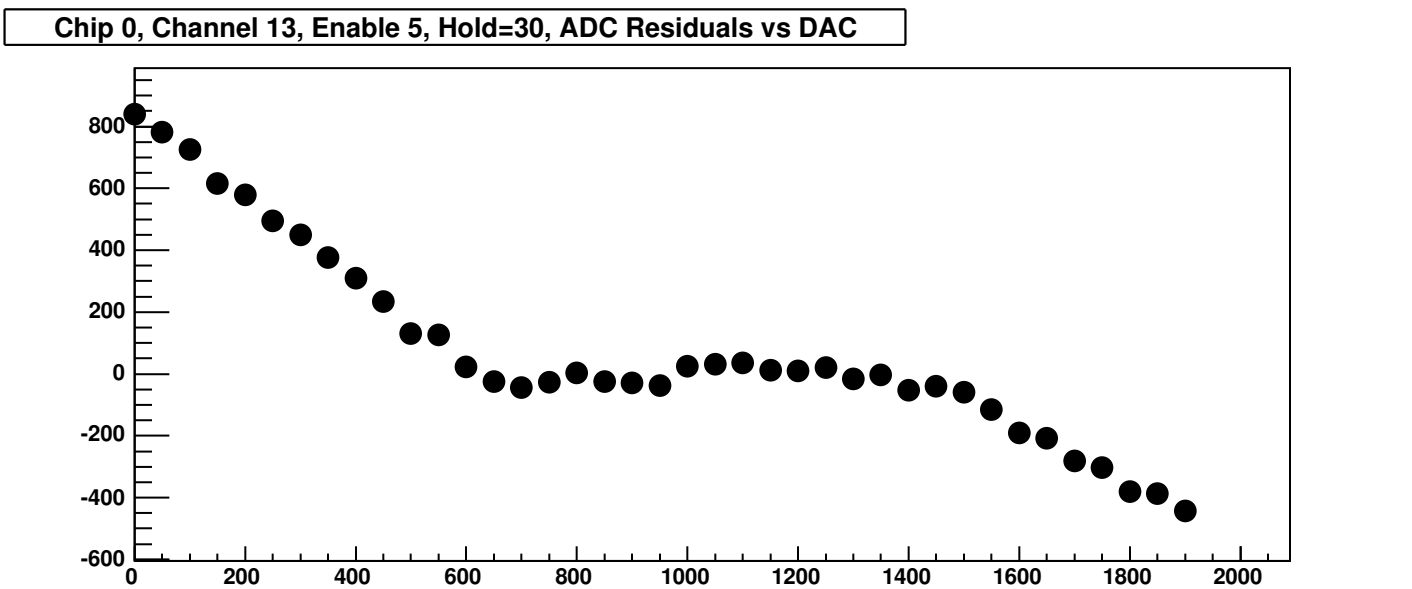


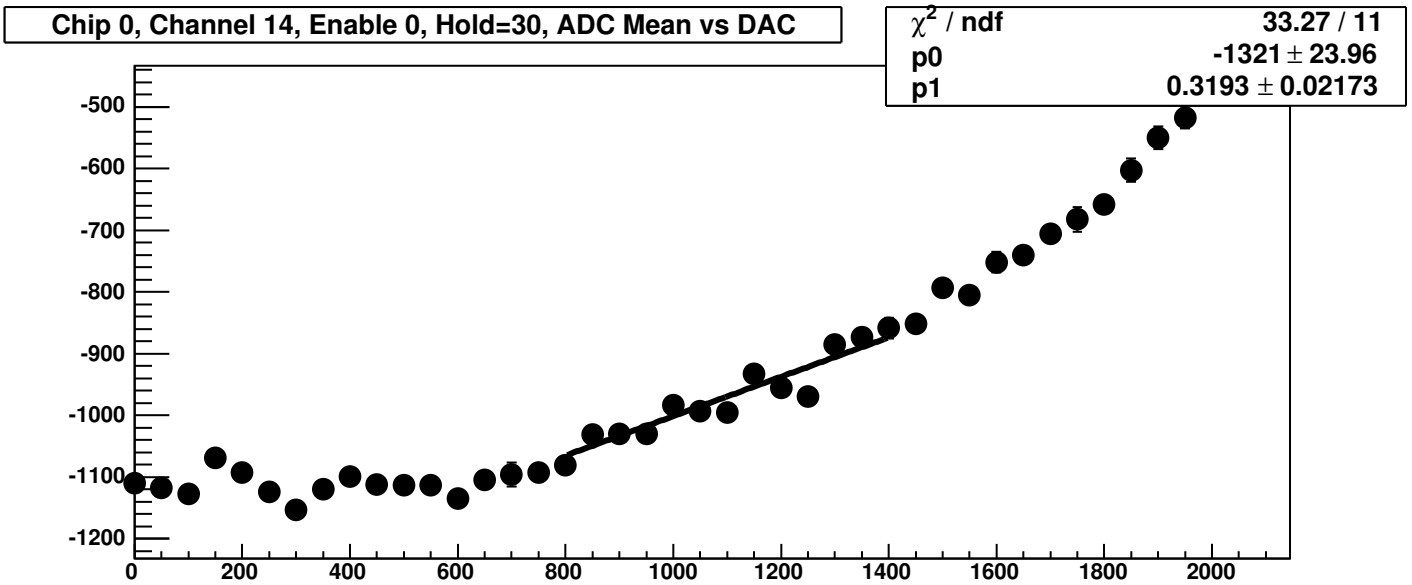
Chip 0, Channel 13, Enable 4!, Hold=30, ADC Residuals vs DAC

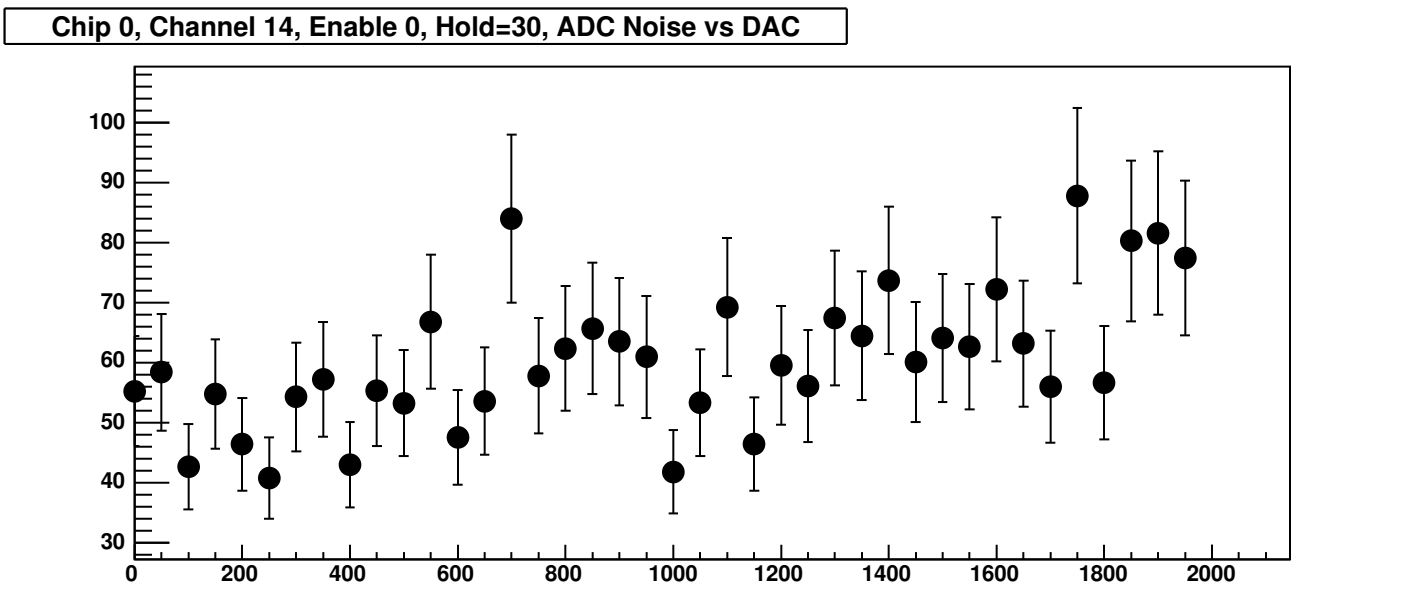


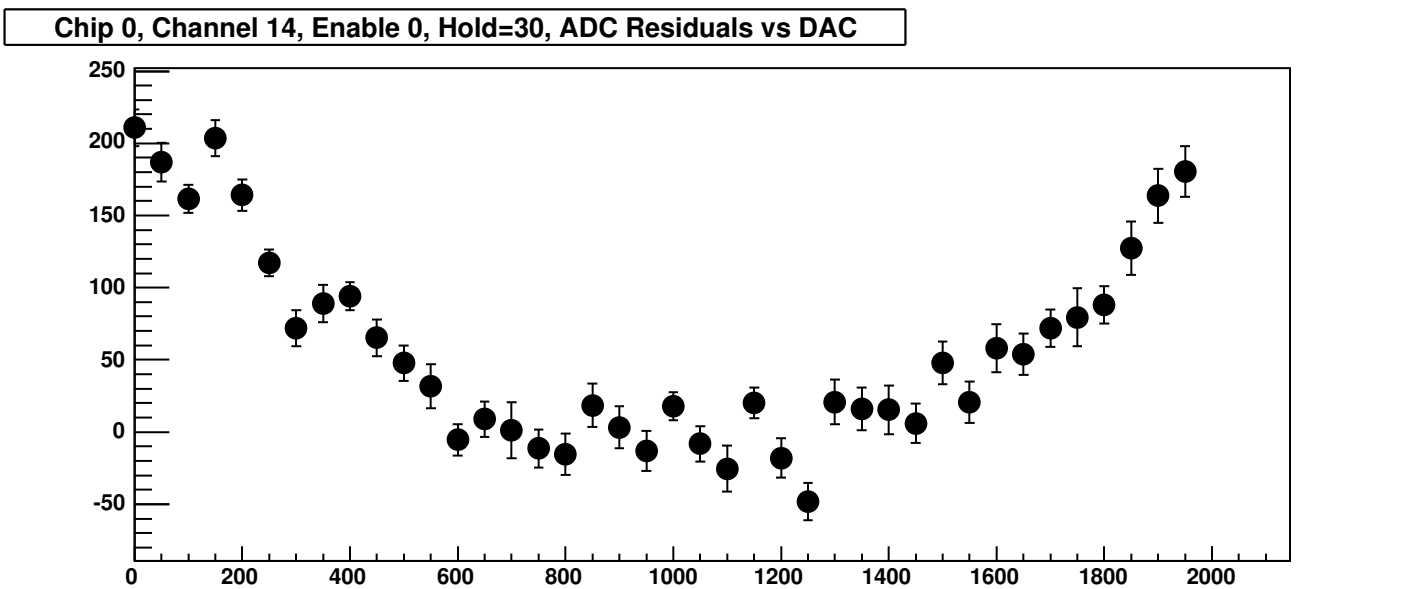

Chip 0, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 27.43 / 11
p0 -1032 ± 26.18
p1 1.388 ± 0.02386
1200
1000
800
600
400
200
0
-200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

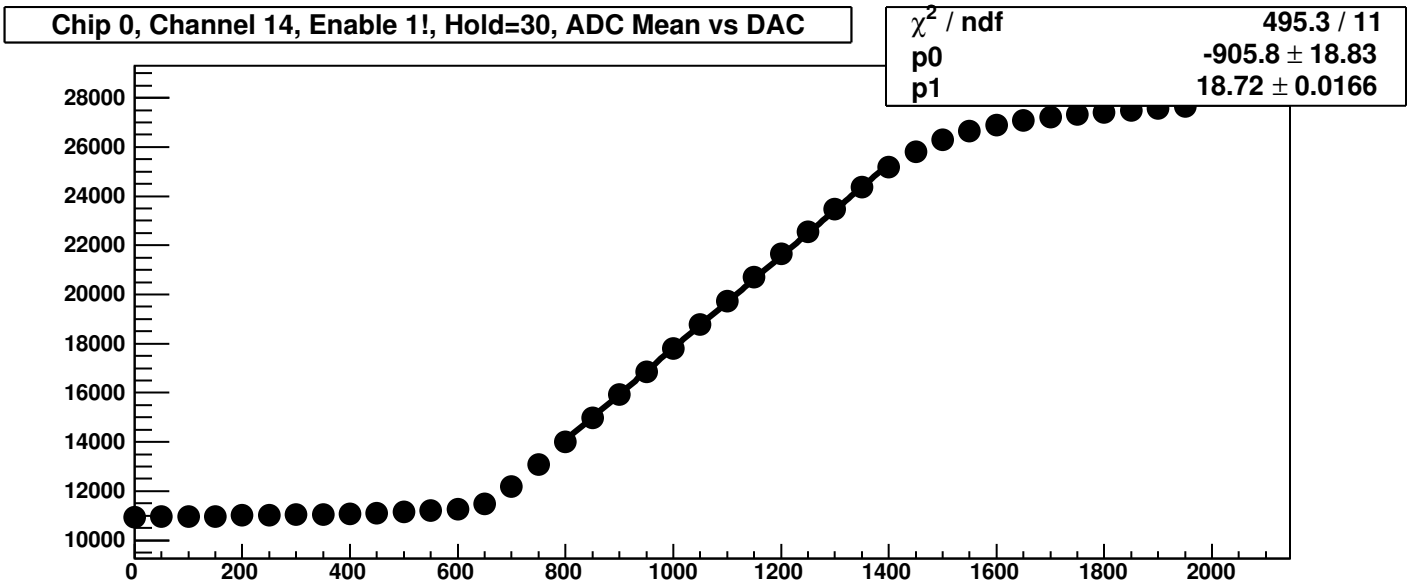

Chip 0, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

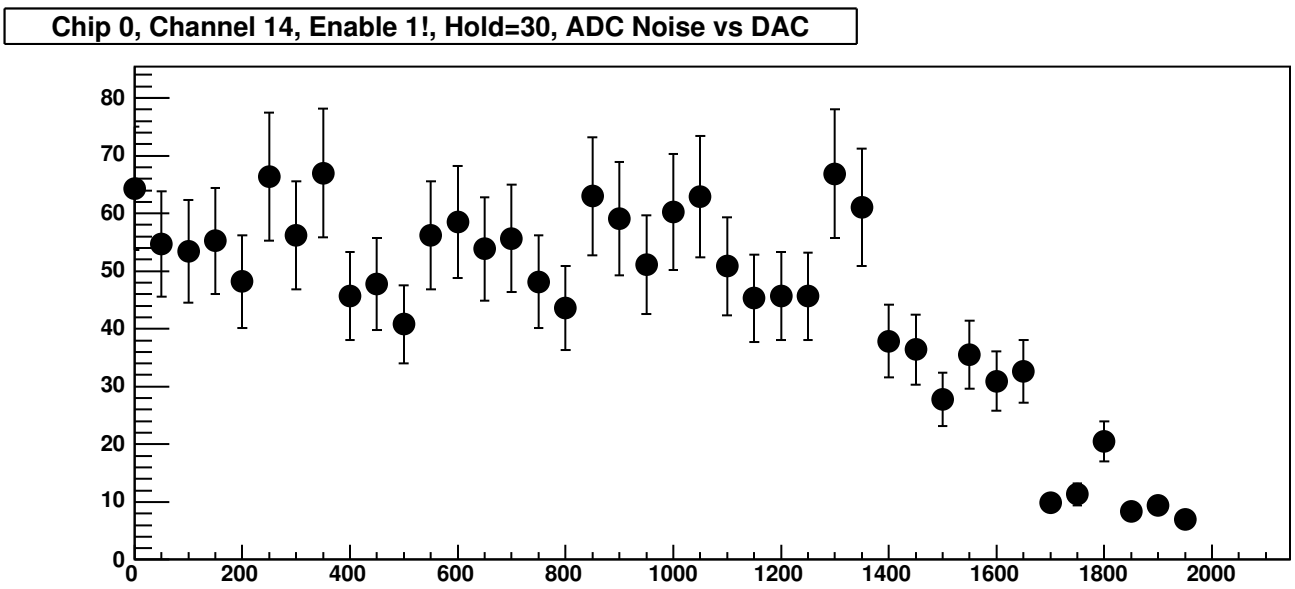

Chip 0, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

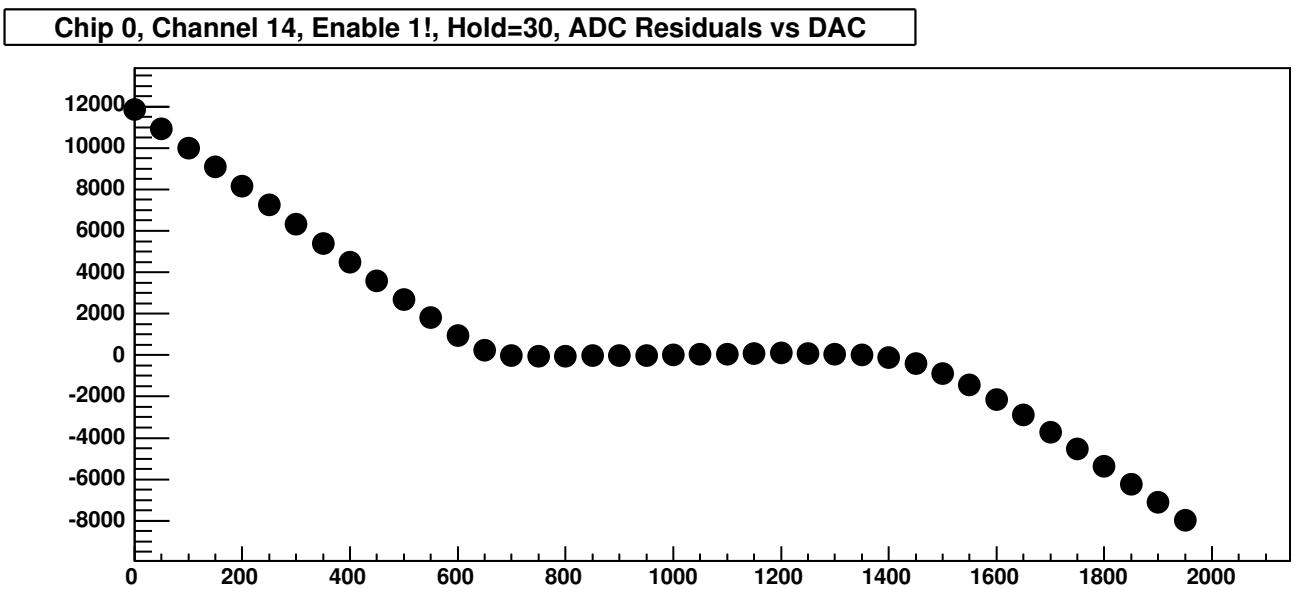

Chip 0, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 33.27 / 11
p0 -1321 ± 23.96
p1 0.3193 ± 0.02173
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

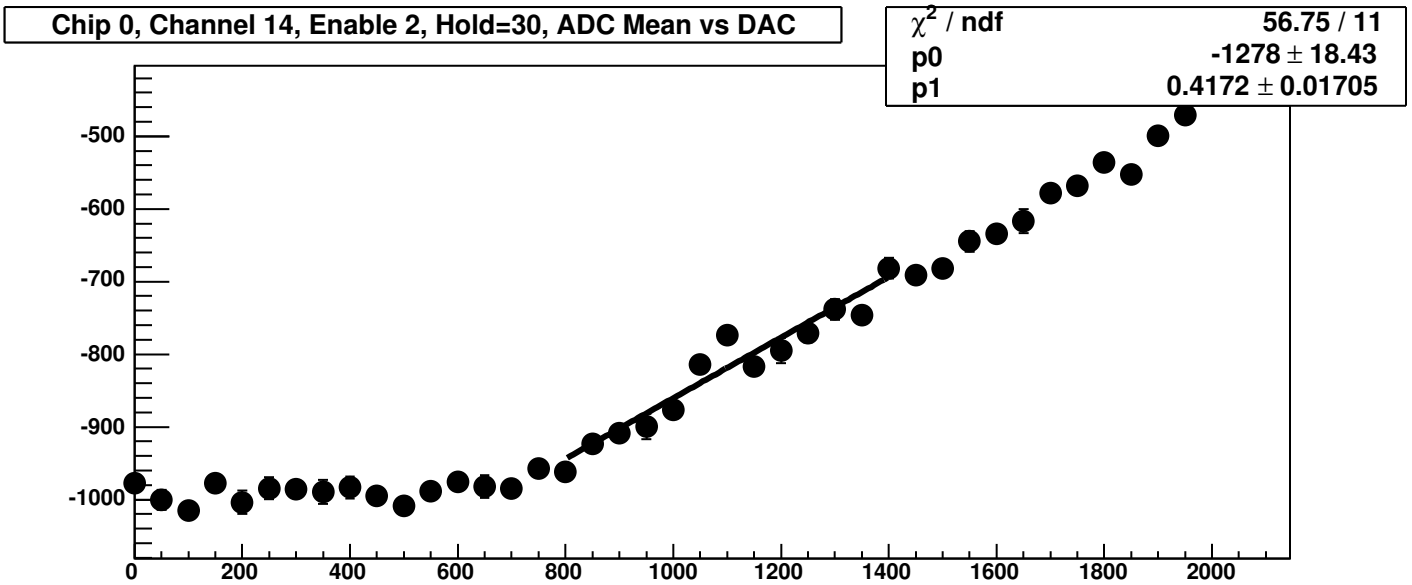

Chip 0, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

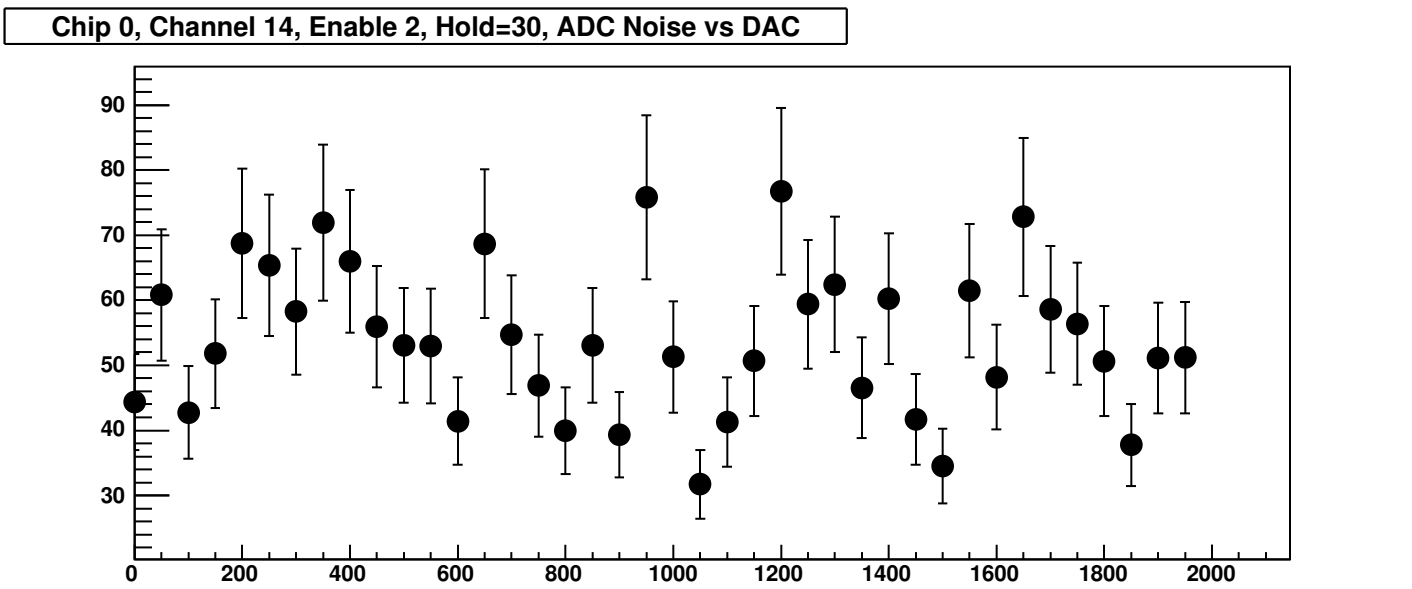

Chip 0, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

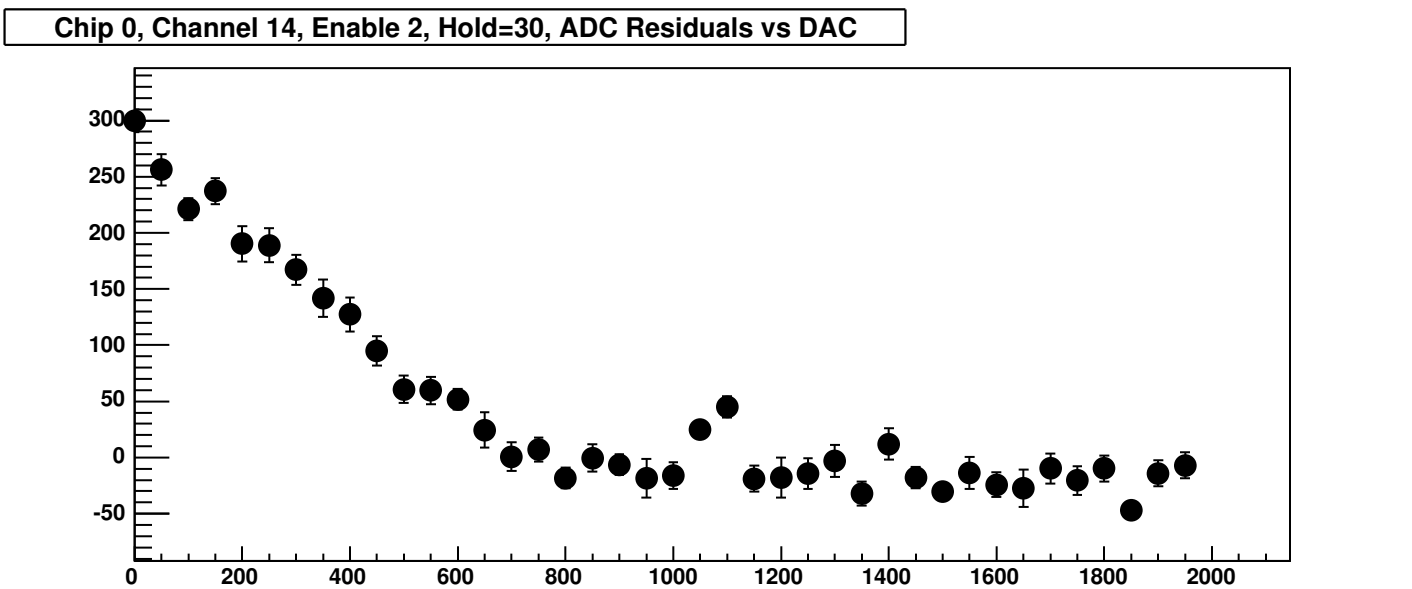

Chip 0, Channel 14, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 495.3 / 11
p0 -905.8 ± 18.83
p1 18.72 ± 0.0166
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

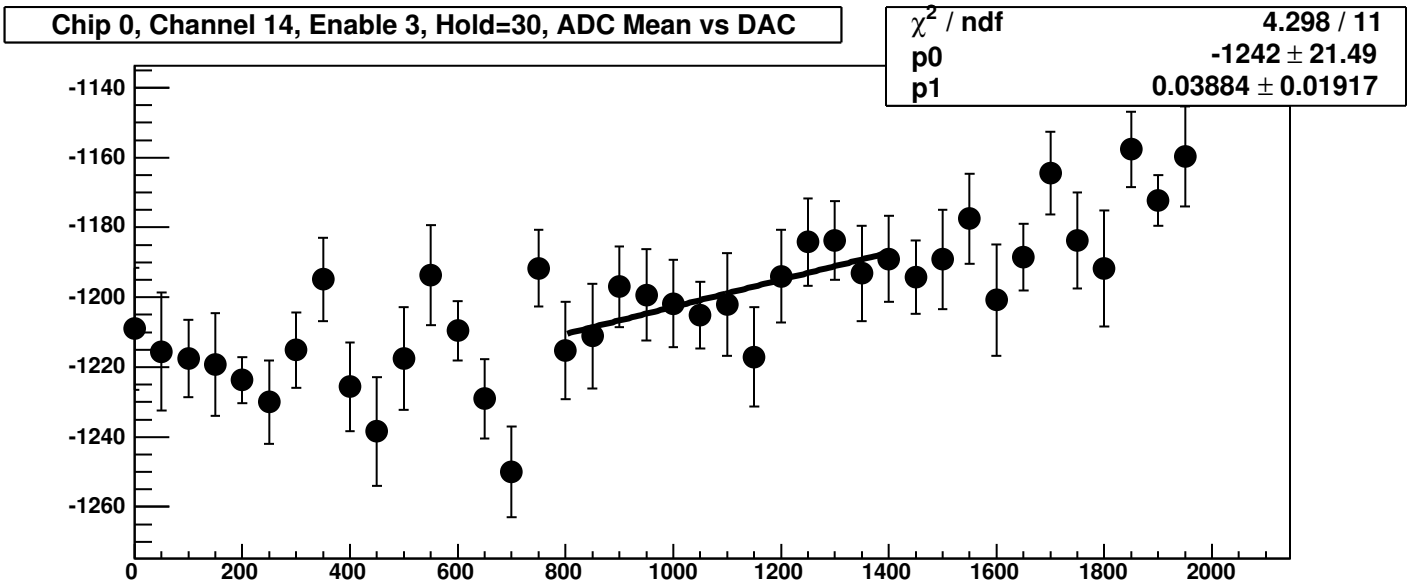

Chip 0, Channel 14, Enable 1!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

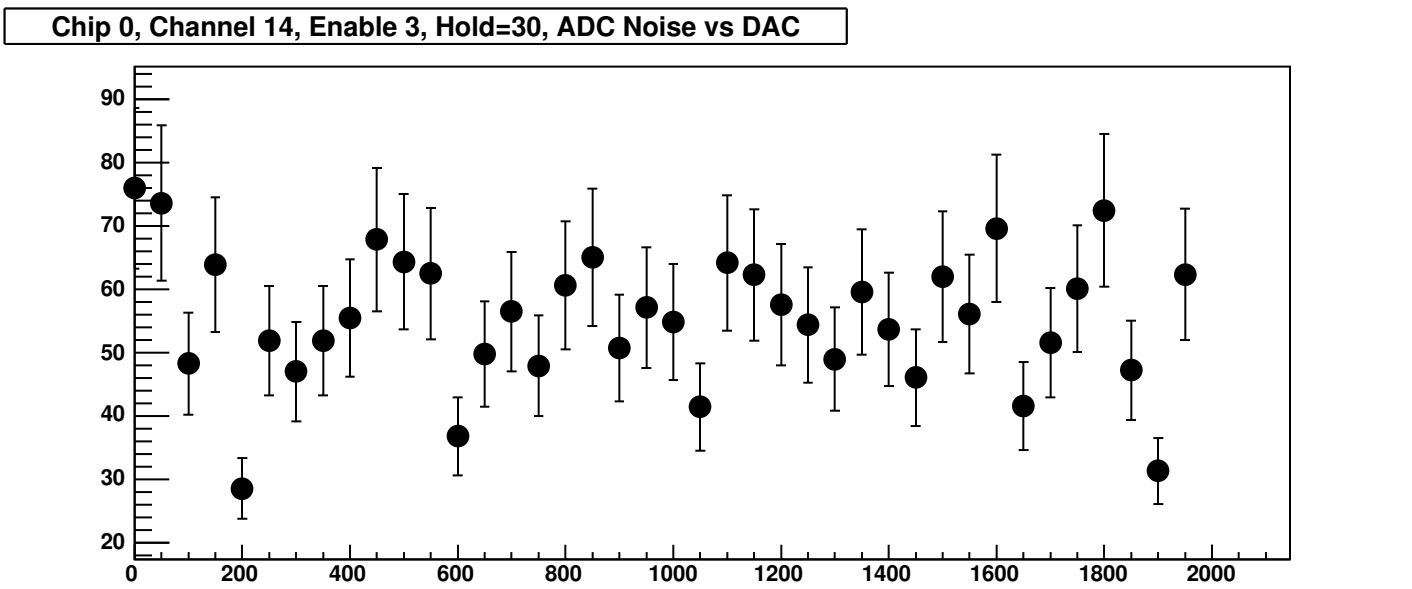

Chip 0, Channel 14, Enable 1!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

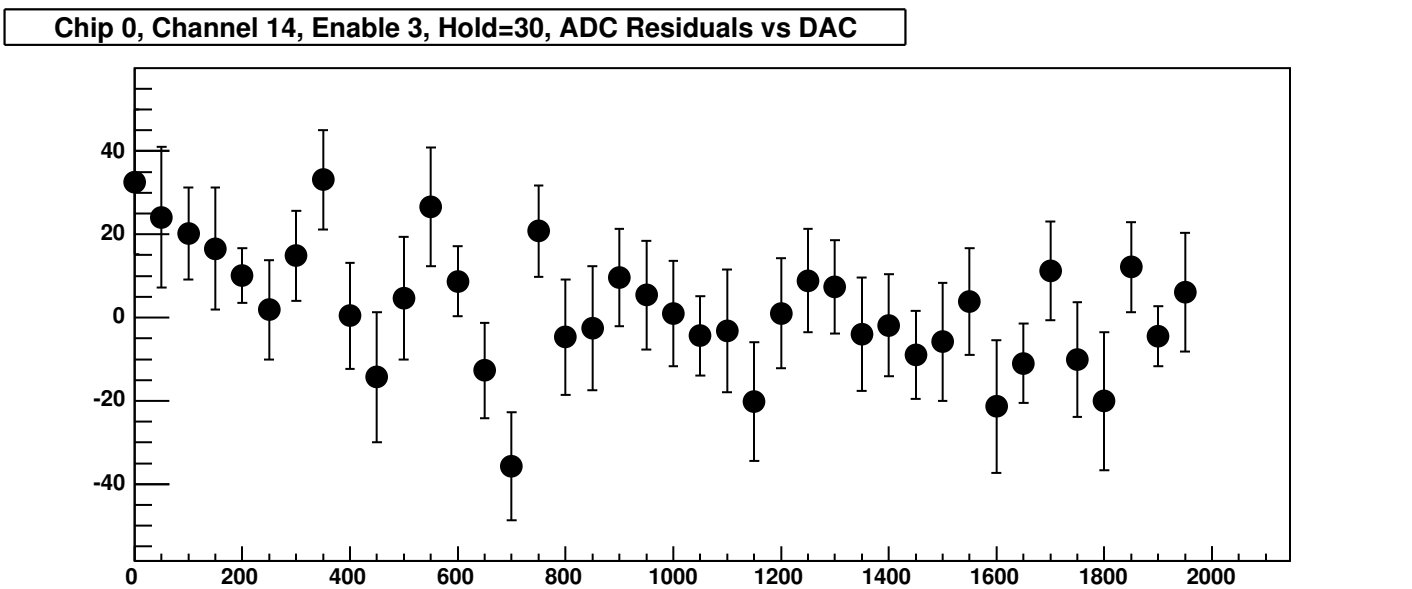

Chip 0, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 56.75 / 11
p0 -1278 ± 18.43
p1 0.4172 ± 0.01705
-500
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

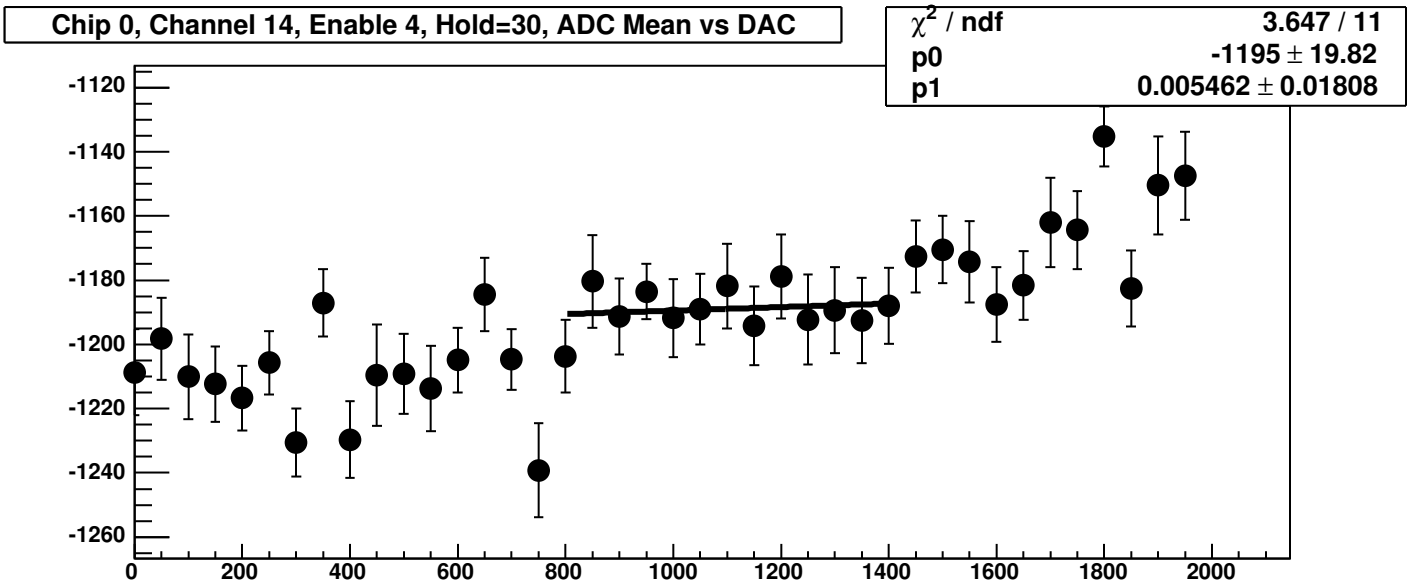

Chip 0, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

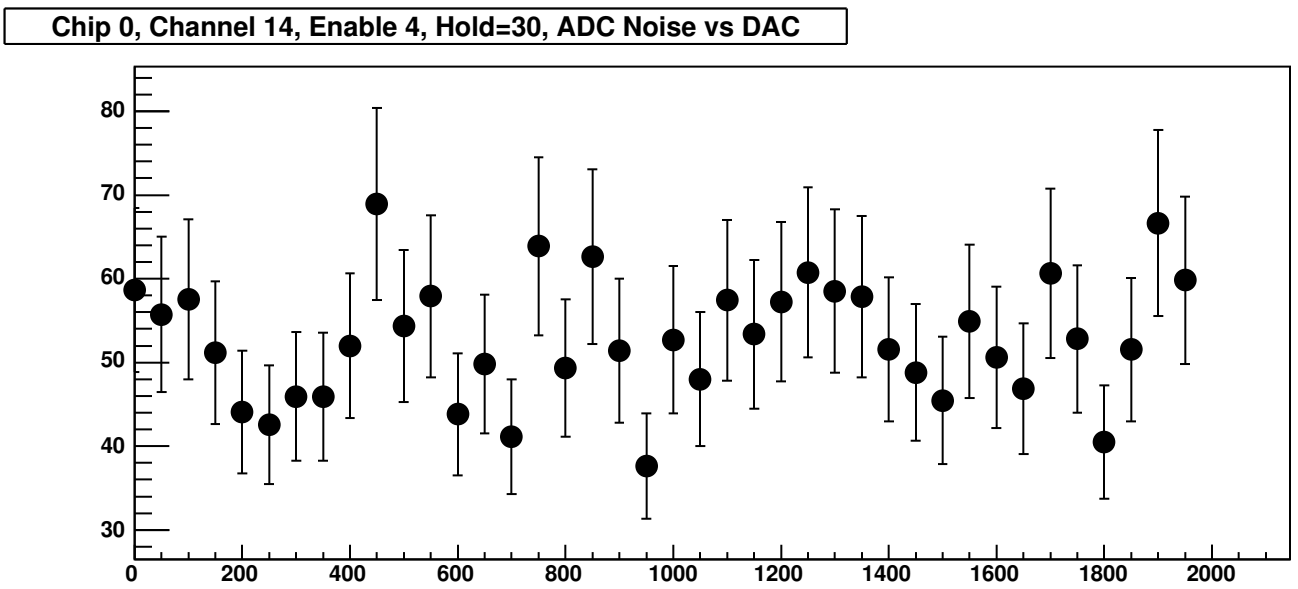

Chip 0, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

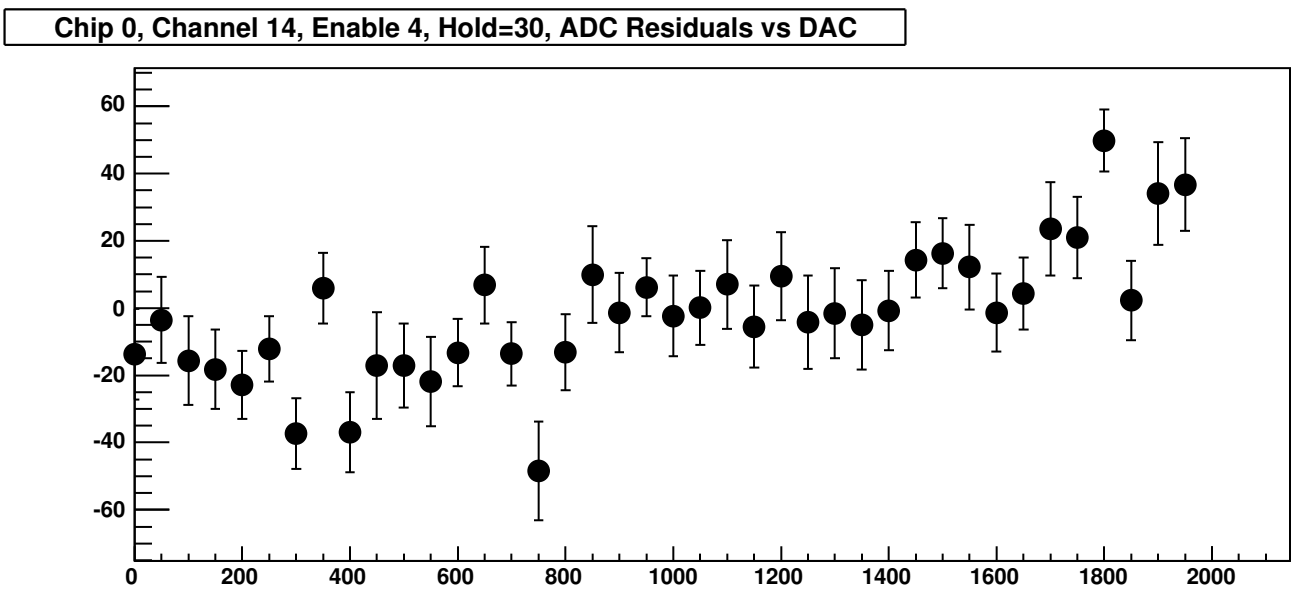

Chip 0, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4.298 / 11
p0 -1242 ± 21.49
p1 0.03884 ± 0.01917
-1140
-1160
-1180
-1200
-1220
-1240
-1260
0
200
400
600
800
1000
1200
1400
1600
1800
2000

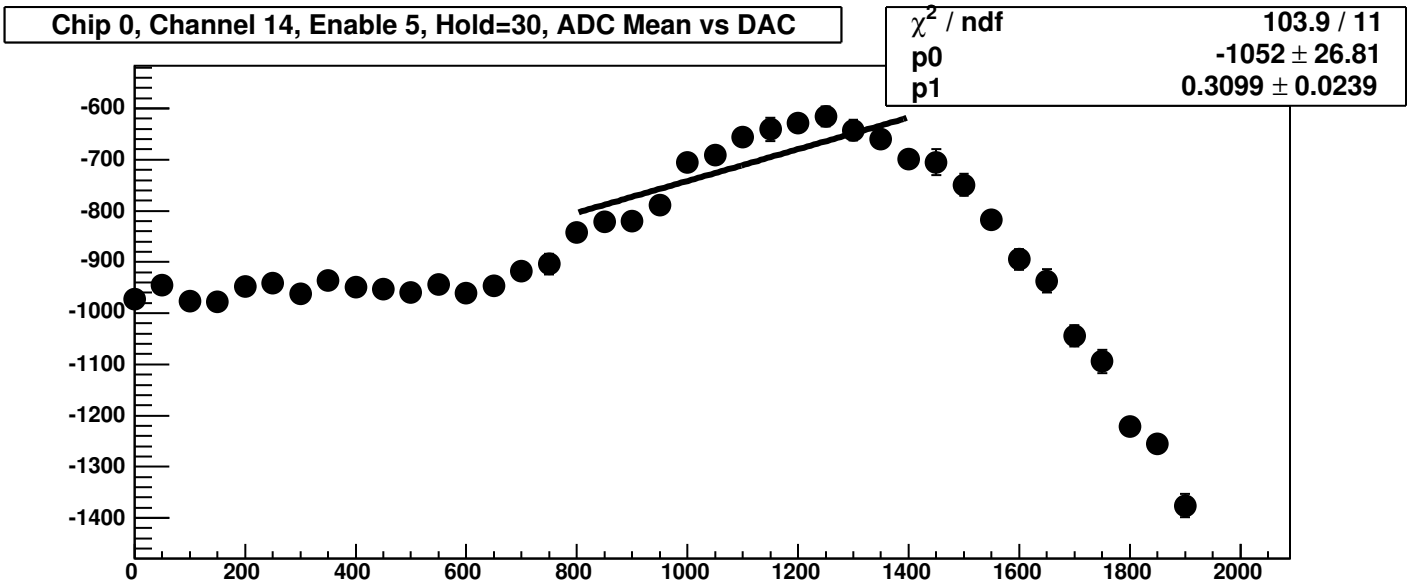

Chip 0, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

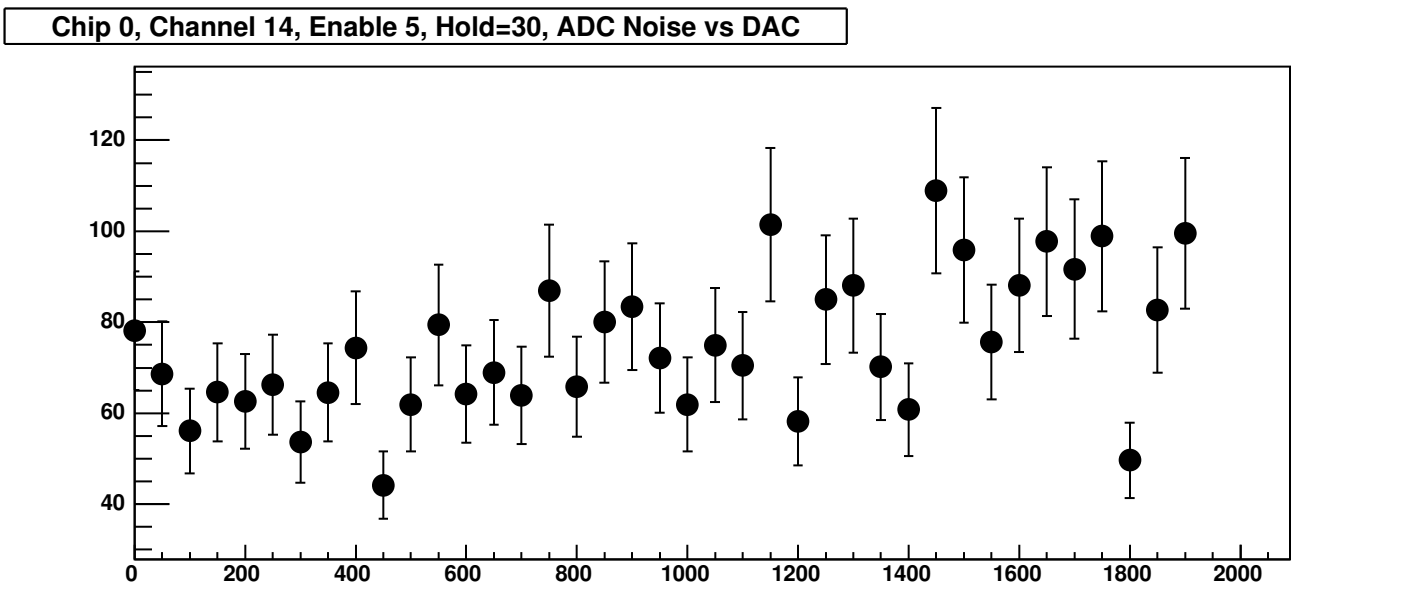

Chip 0, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

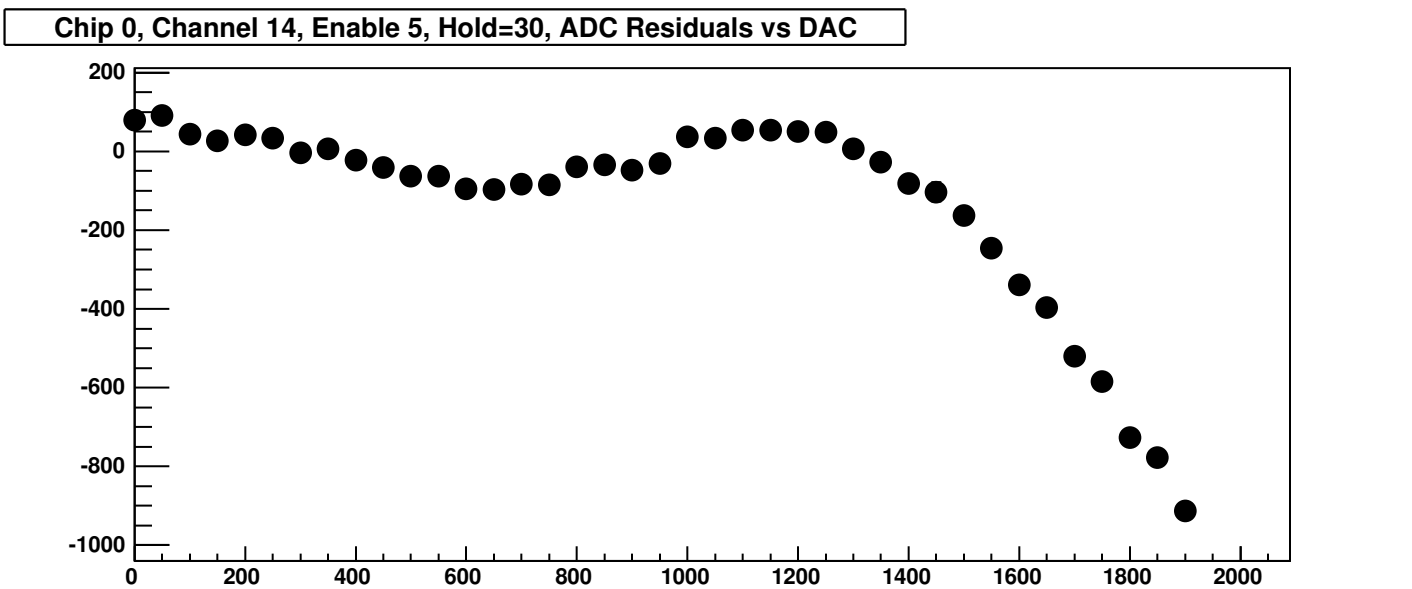

Chip 0, Channel 14, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 3.647 / 11
p0 -1195 ± 19.82
p1 0.005462 ± 0.01808
-1120
-1140
-1160
-1180
-1200
-1220
-1240
-1260
0
200
400
600
800
1000
1200
1400
1600
1800
2000

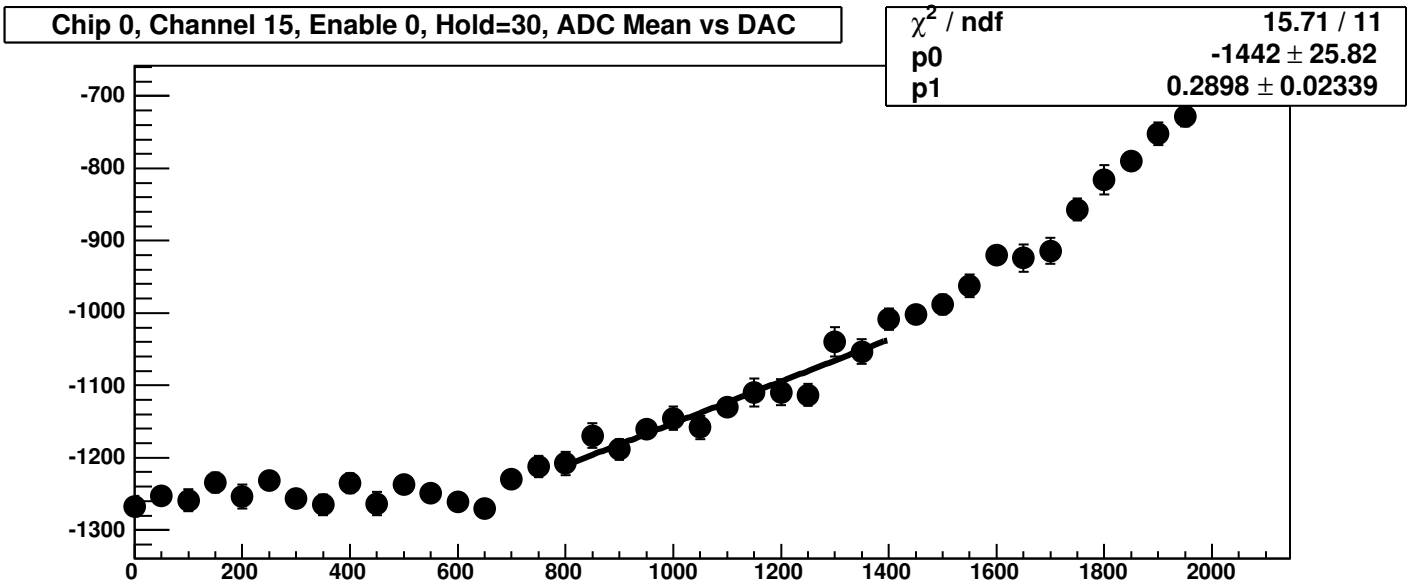

Chip 0, Channel 14, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

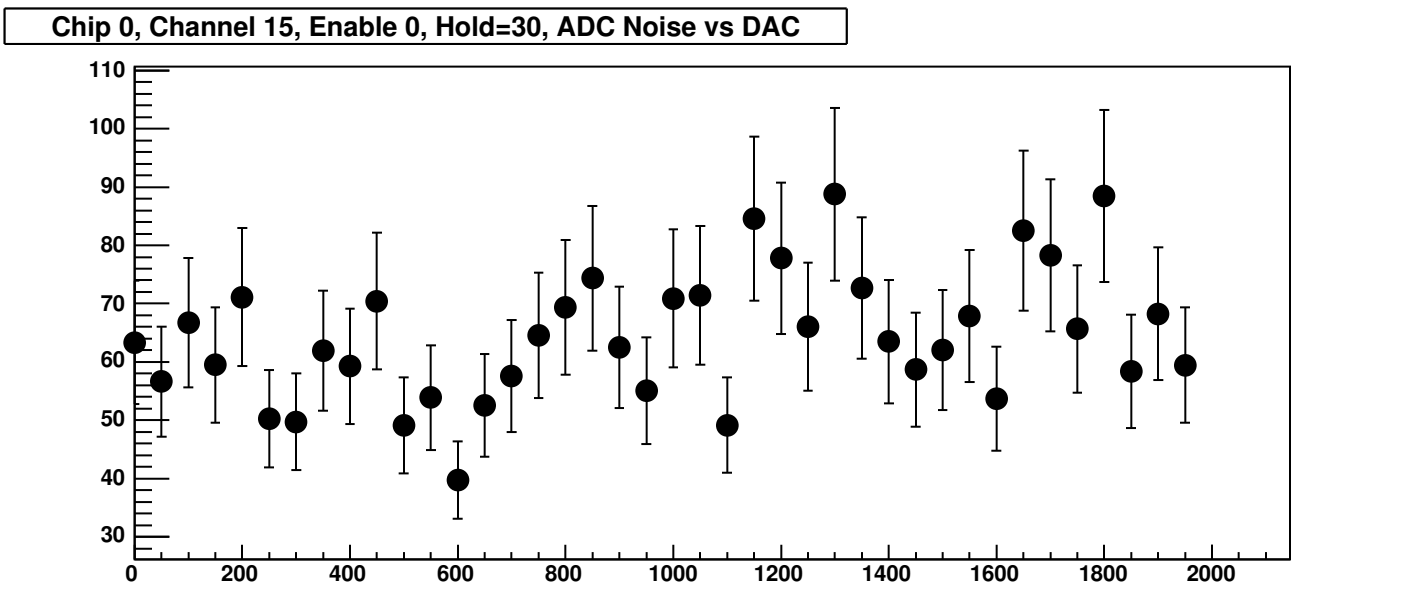

Chip 0, Channel 14, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

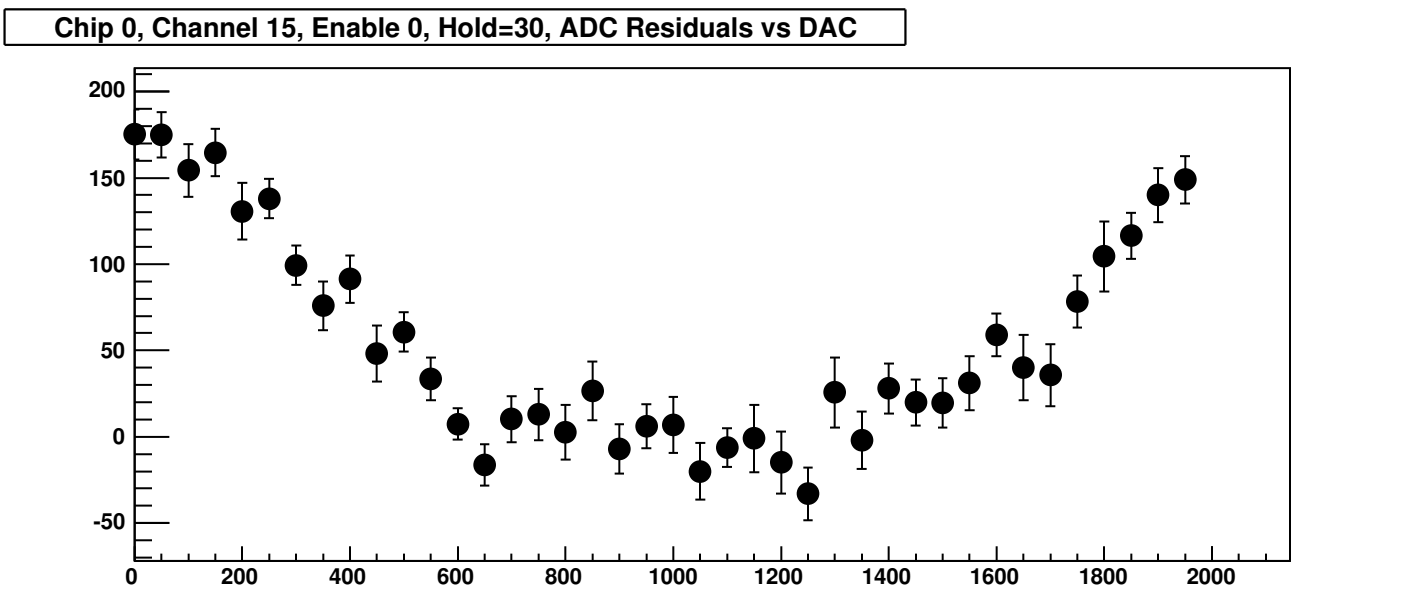

Chip 0, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 103.9 / 11
p0 -1052 ± 26.81
p1 0.3099 ± 0.0239
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

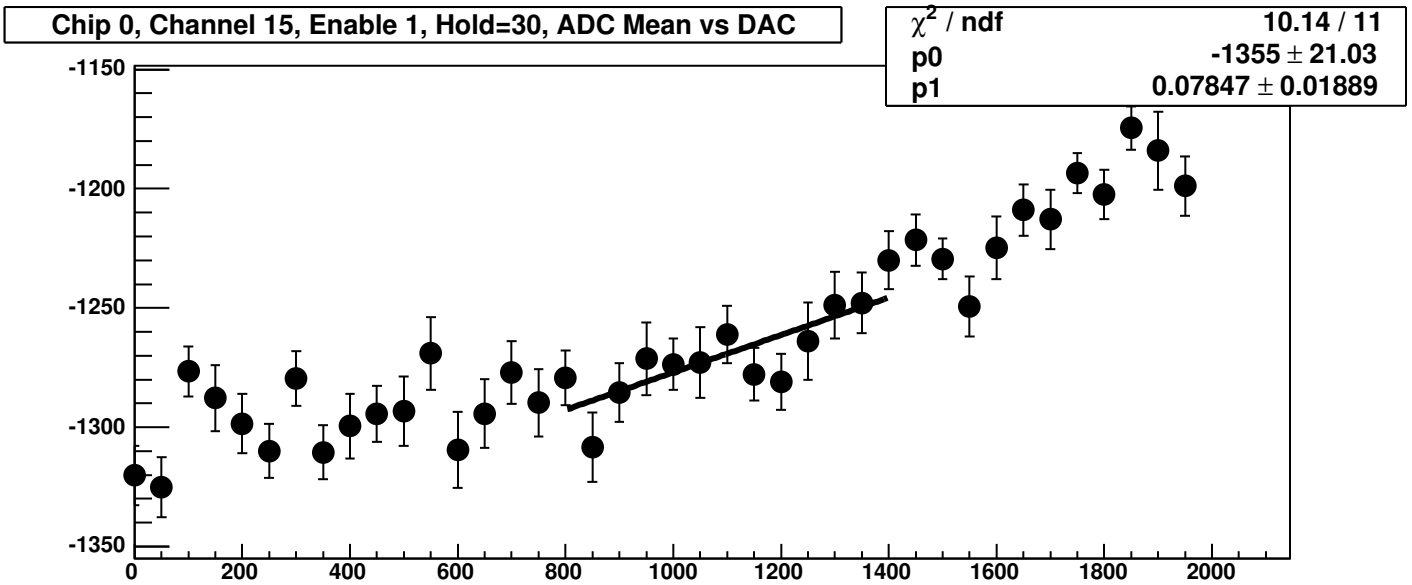

Chip 0, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

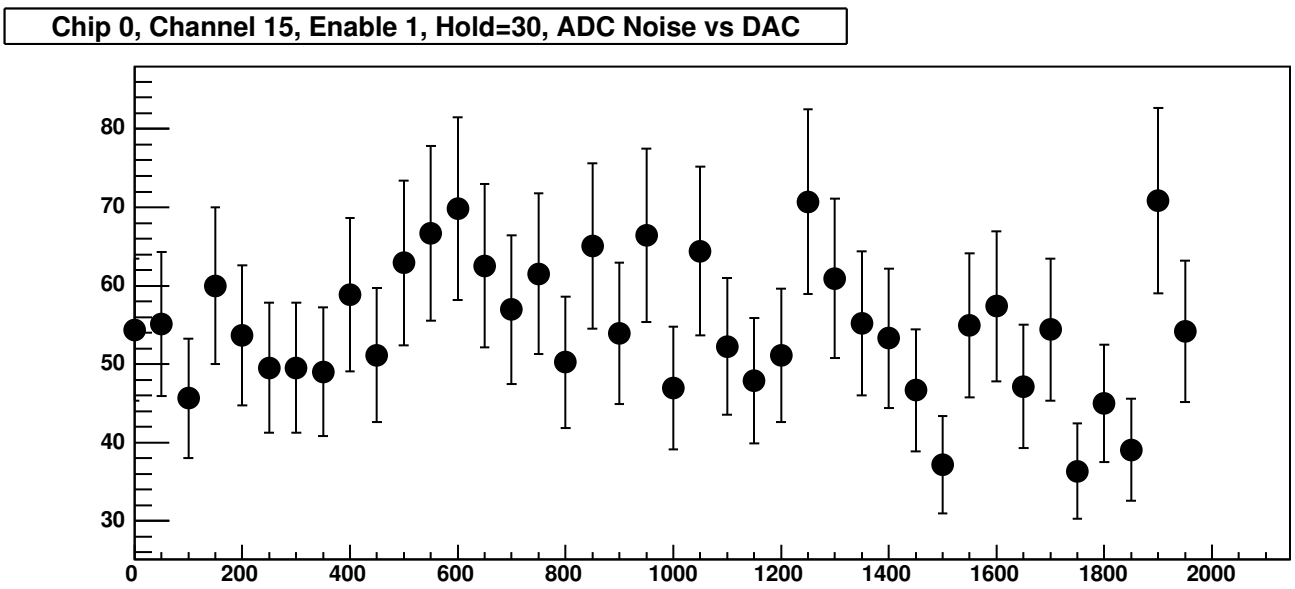

Chip 0, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

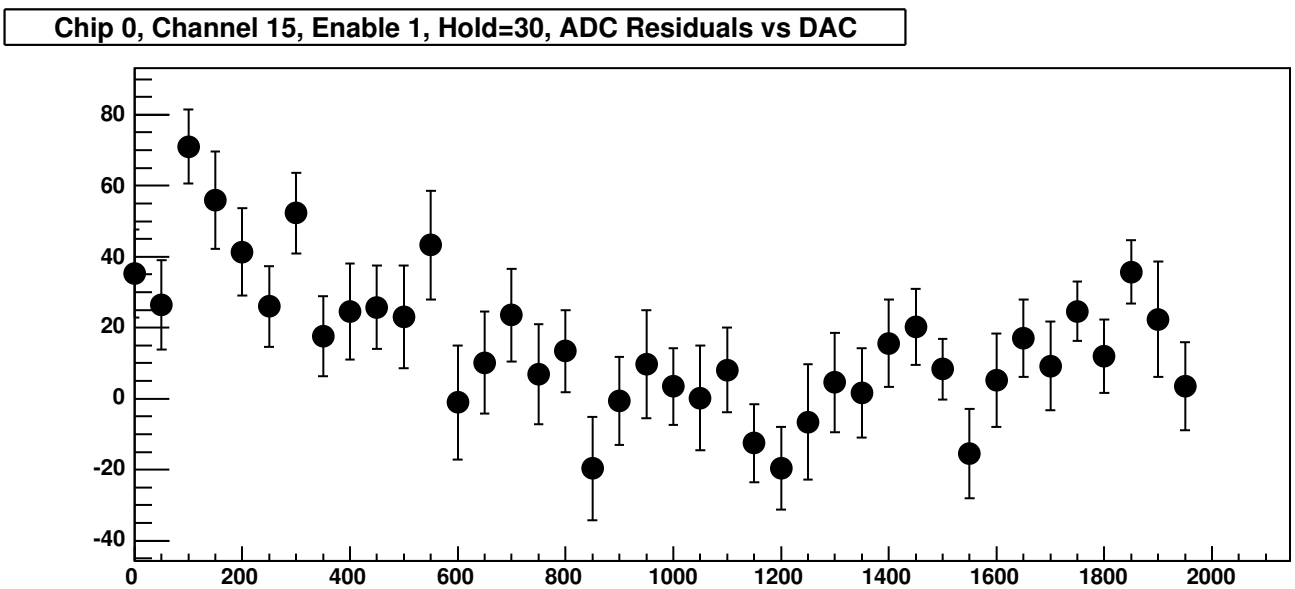

Chip 0, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 15.71 / 11
p0 -1442 ± 25.82
p1 0.2898 ± 0.02339
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

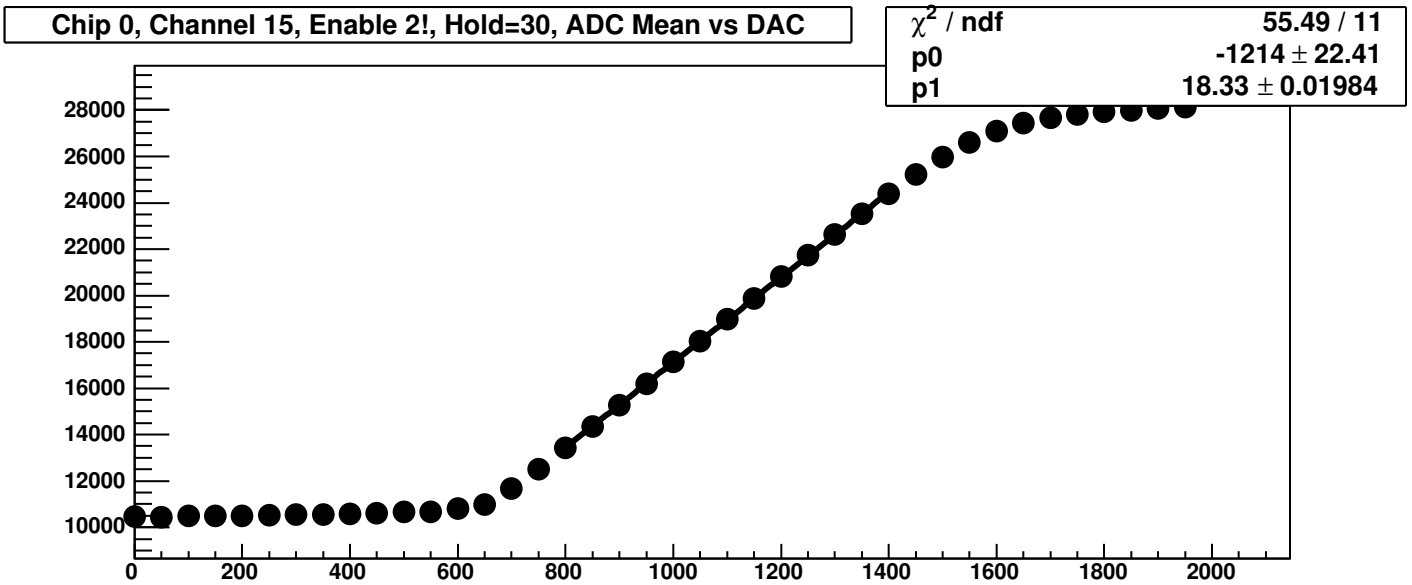

Chip 0, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

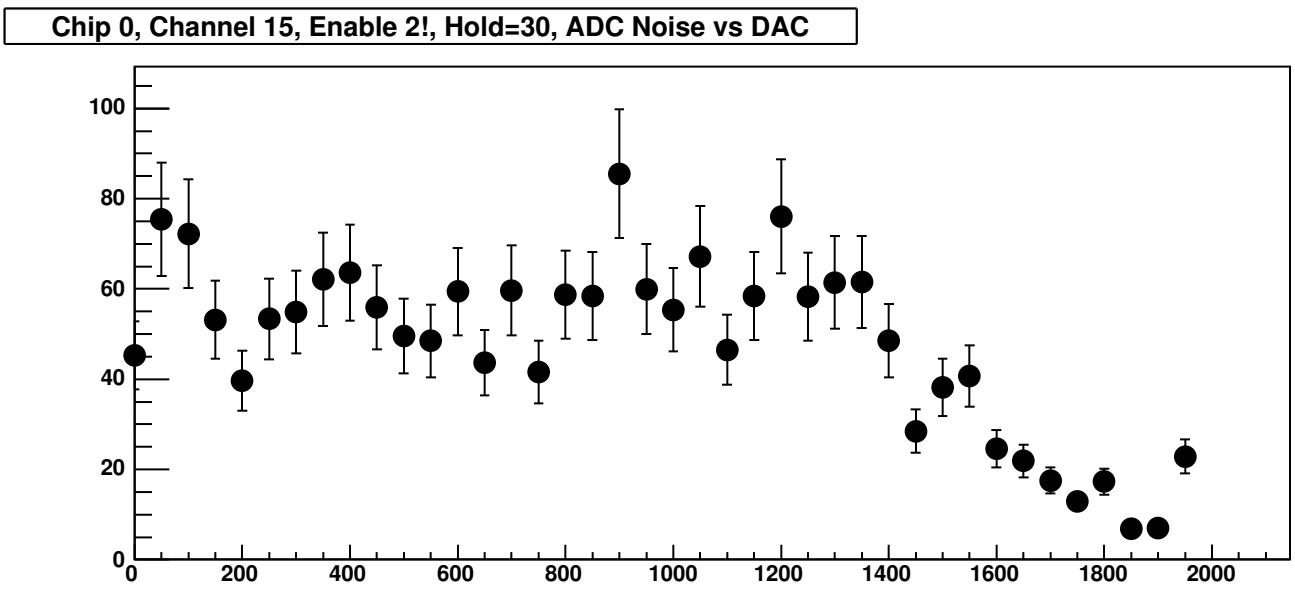

Chip 0, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

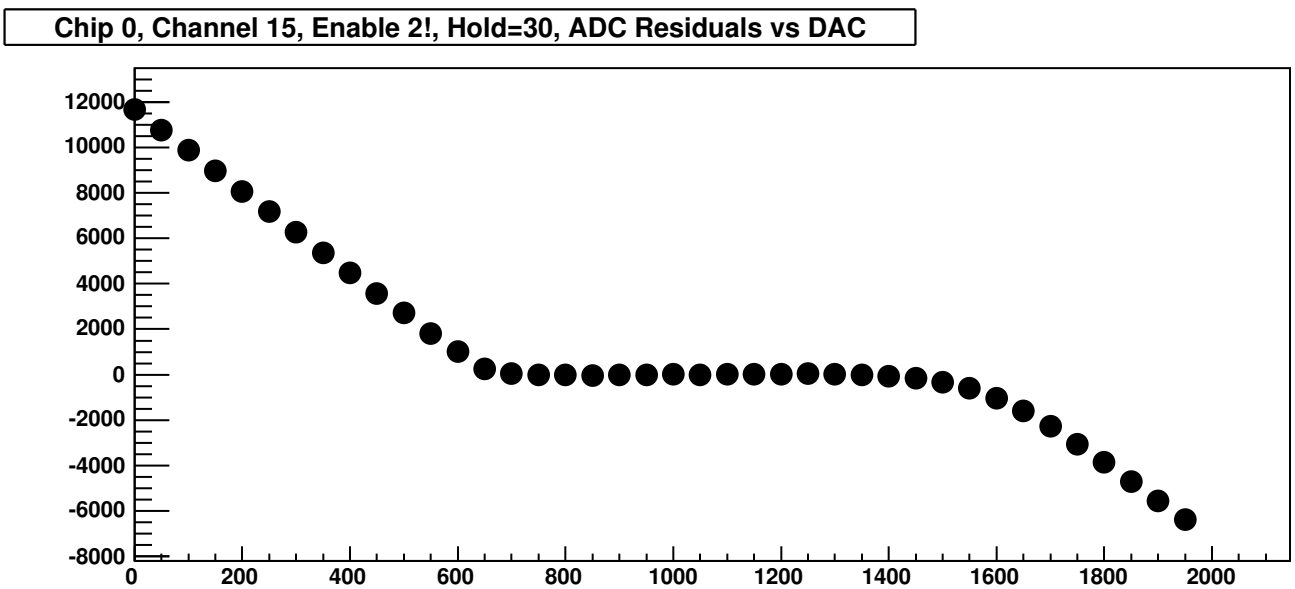

Chip 0, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 10.14 / 11
p0 -1355 ± 21.03
p1 0.07847 ± 0.01889
-1150
-1200
-1250
-1300
-1350
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

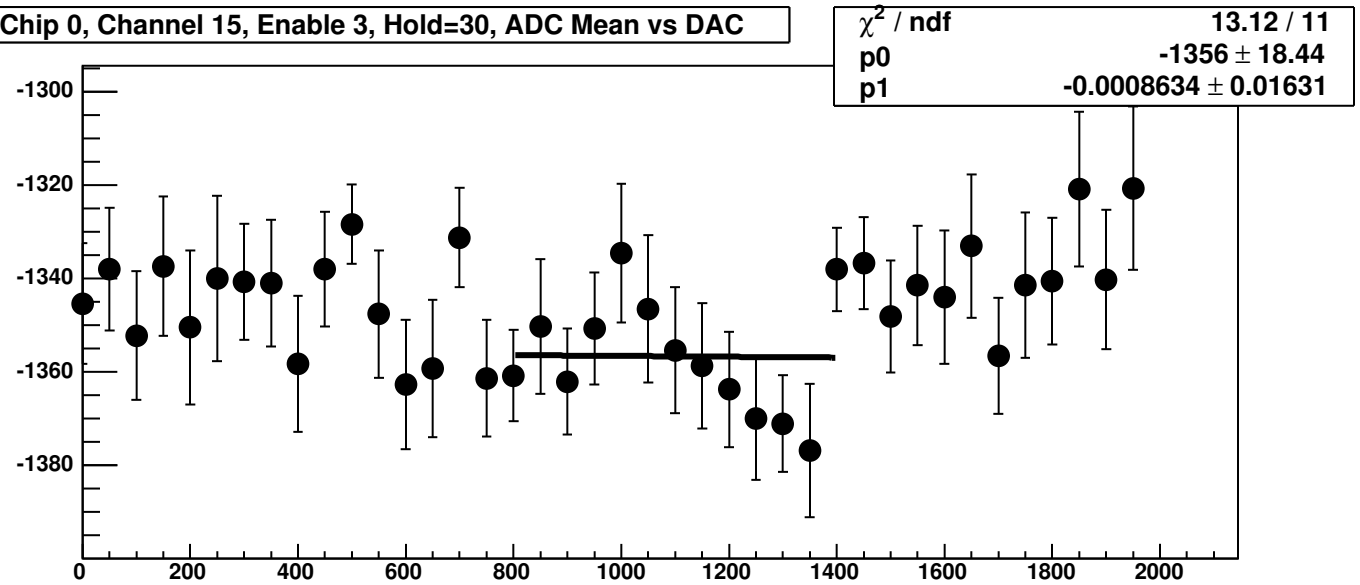

Chip 0, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 15, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 55.49 / 11
p0 -1214 ± 22.41
p1 18.33 ± 0.01984
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

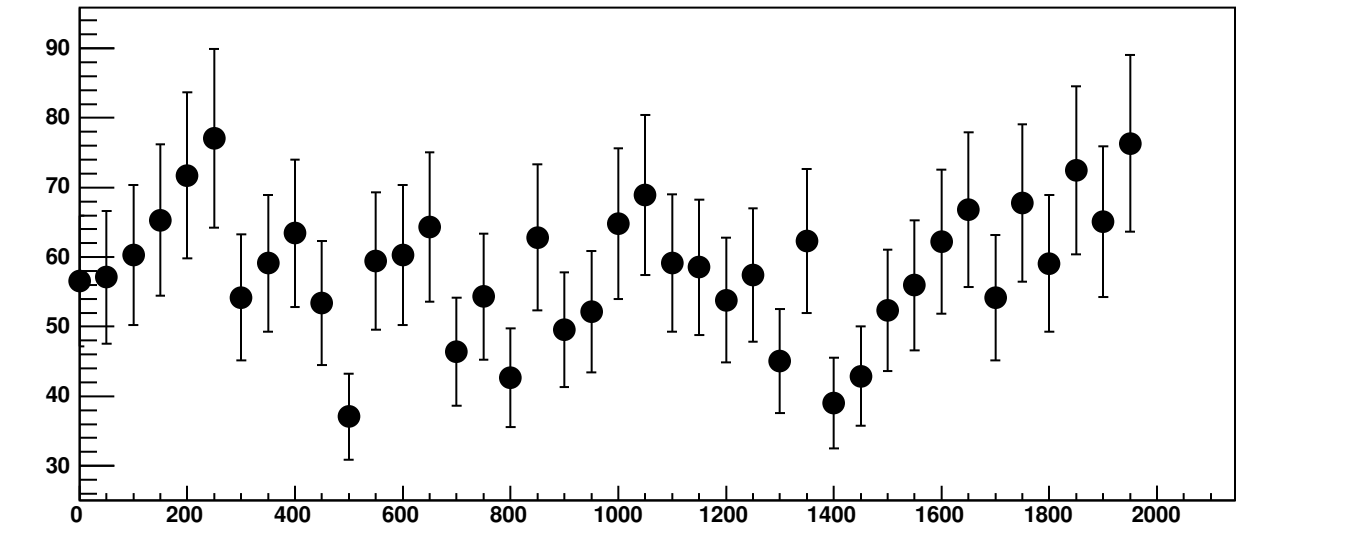

Chip 0, Channel 15, Enable 2!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 15, Enable 2!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

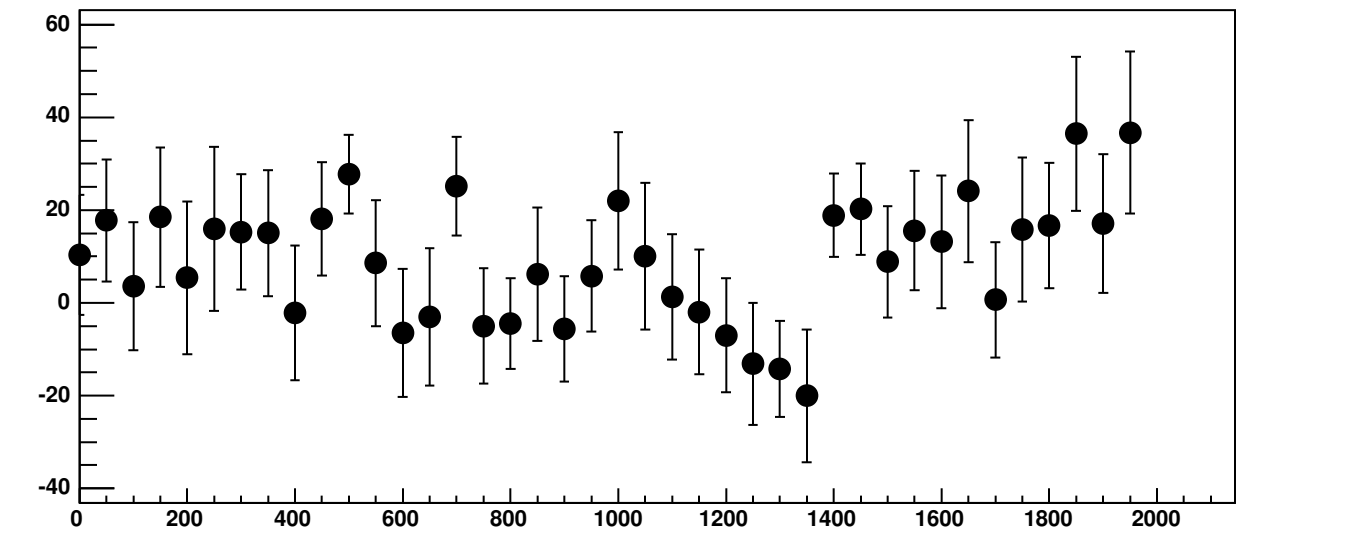
Chip 0, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC

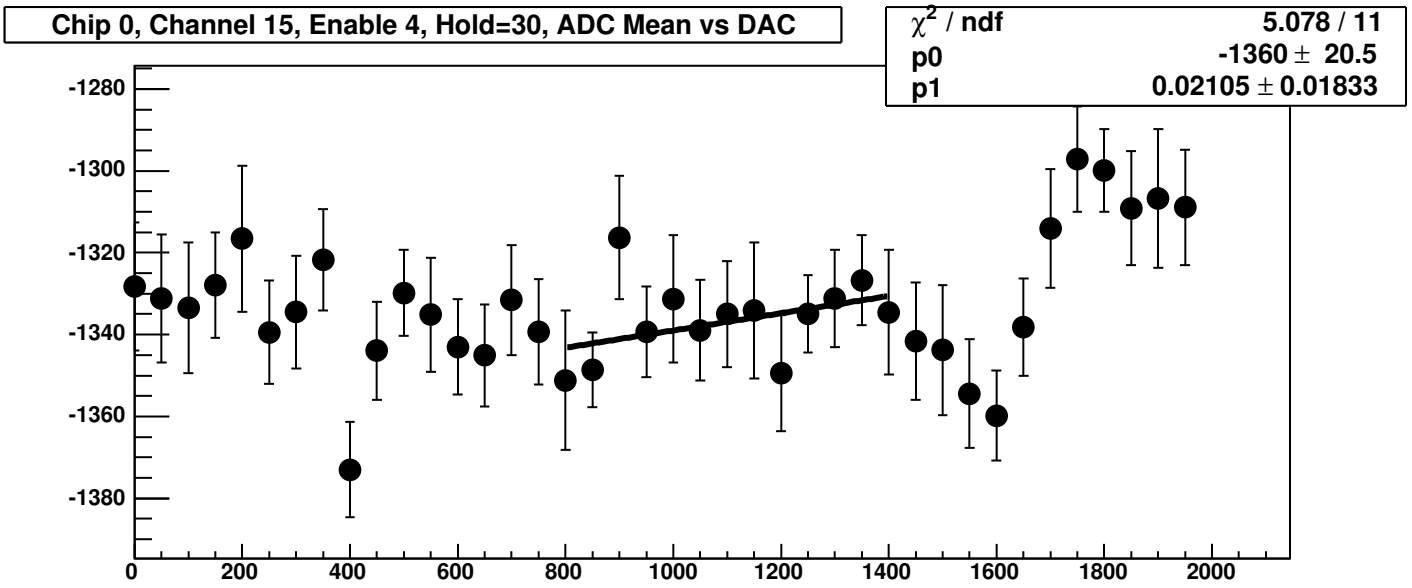


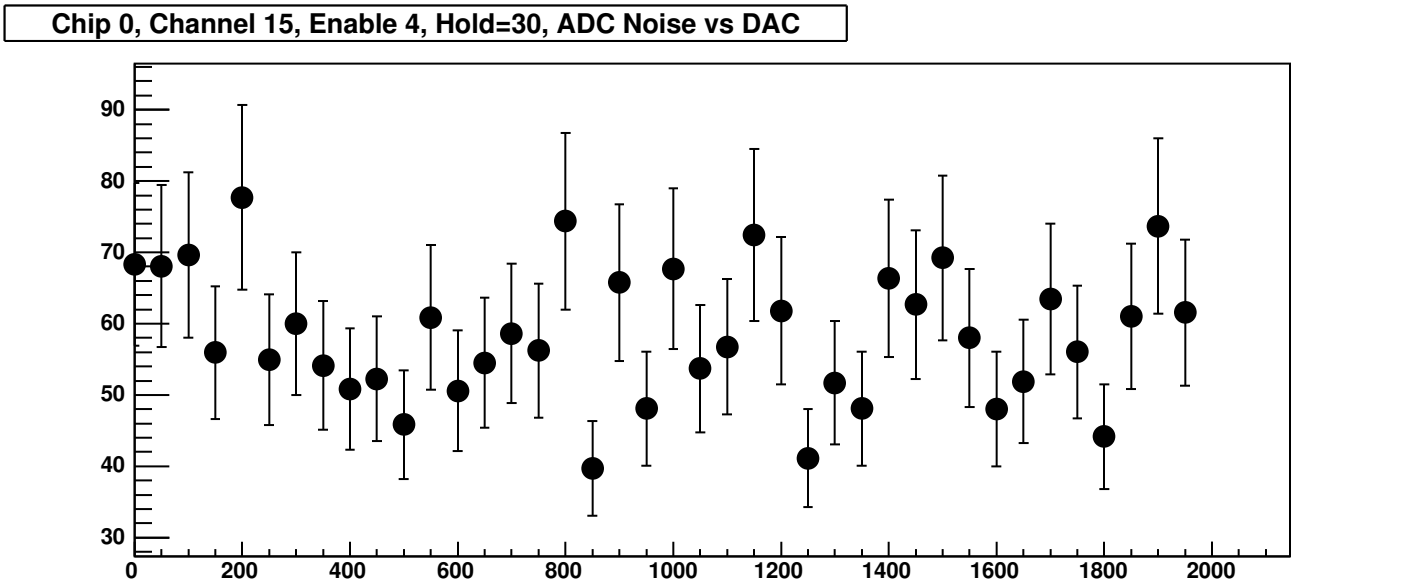
Chip 0, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC

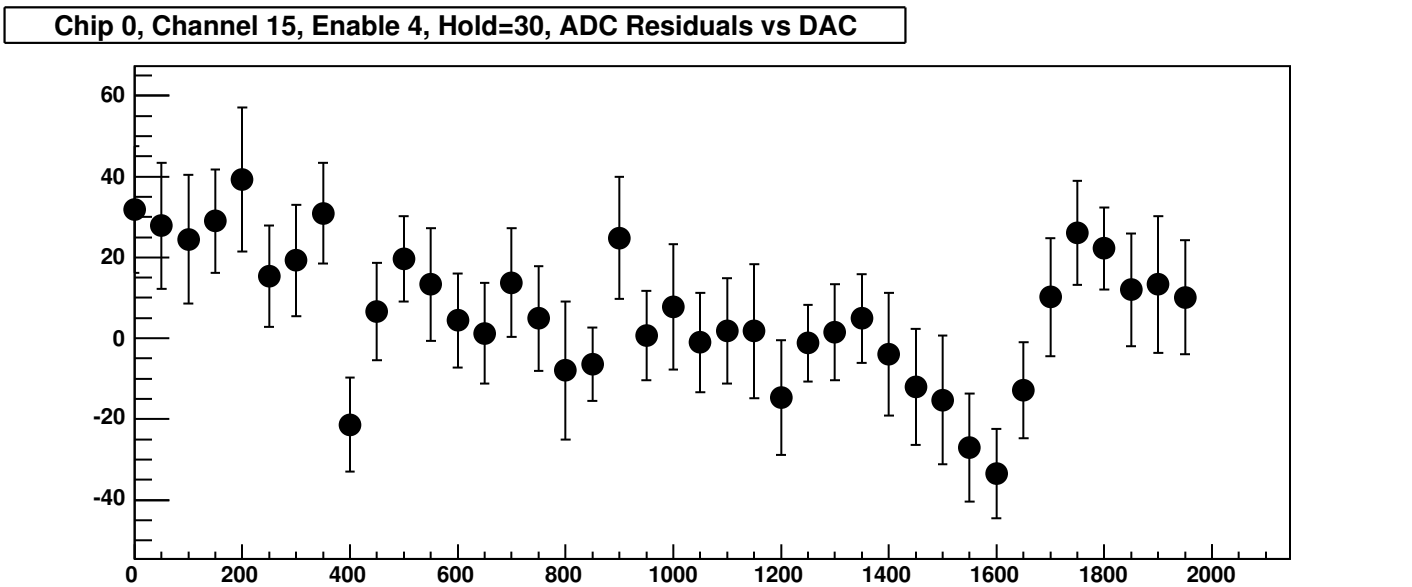


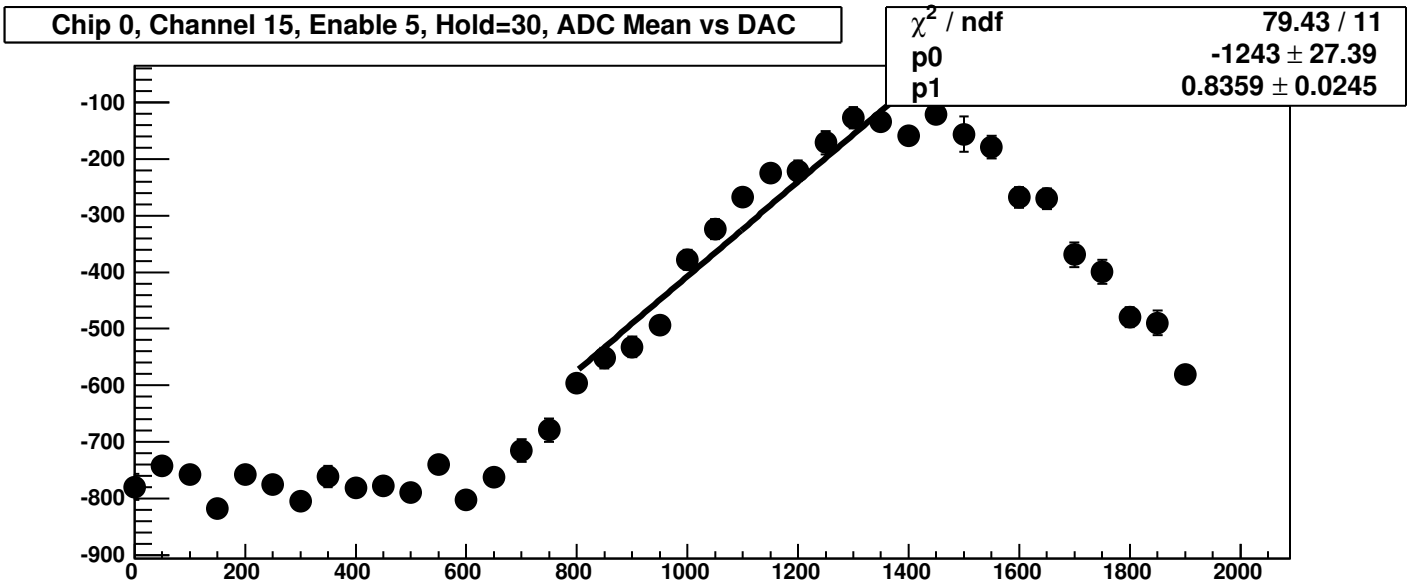
Chip 0, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC

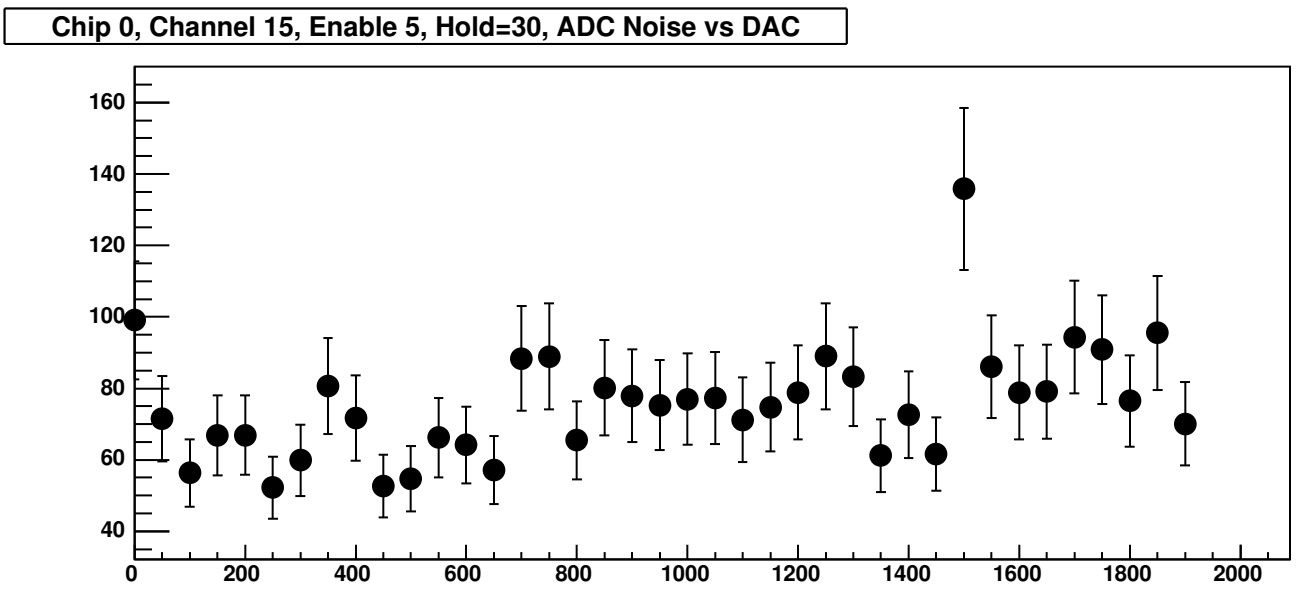


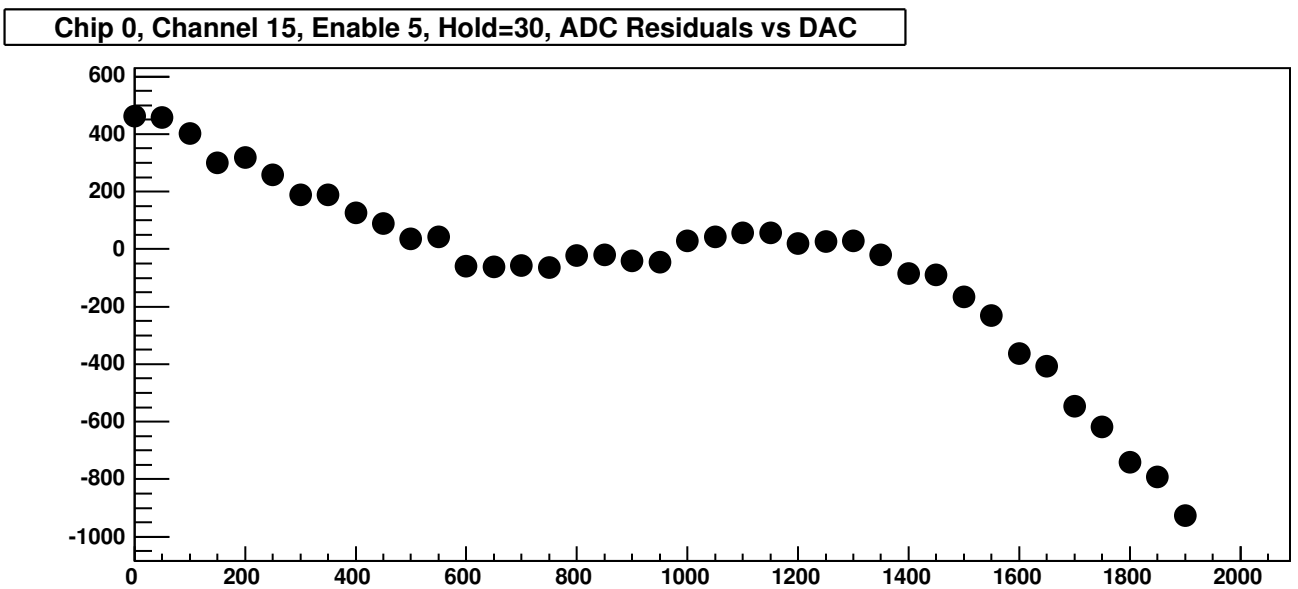

Chip 0, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5.078 / 11
p0 -1360 ± 20.5
p1 0.02105 ± 0.01833
-1280
-1300
-1320
-1340
-1360
-1380
0
200
400
600
800
1000
1200
1400
1600
1800
2000

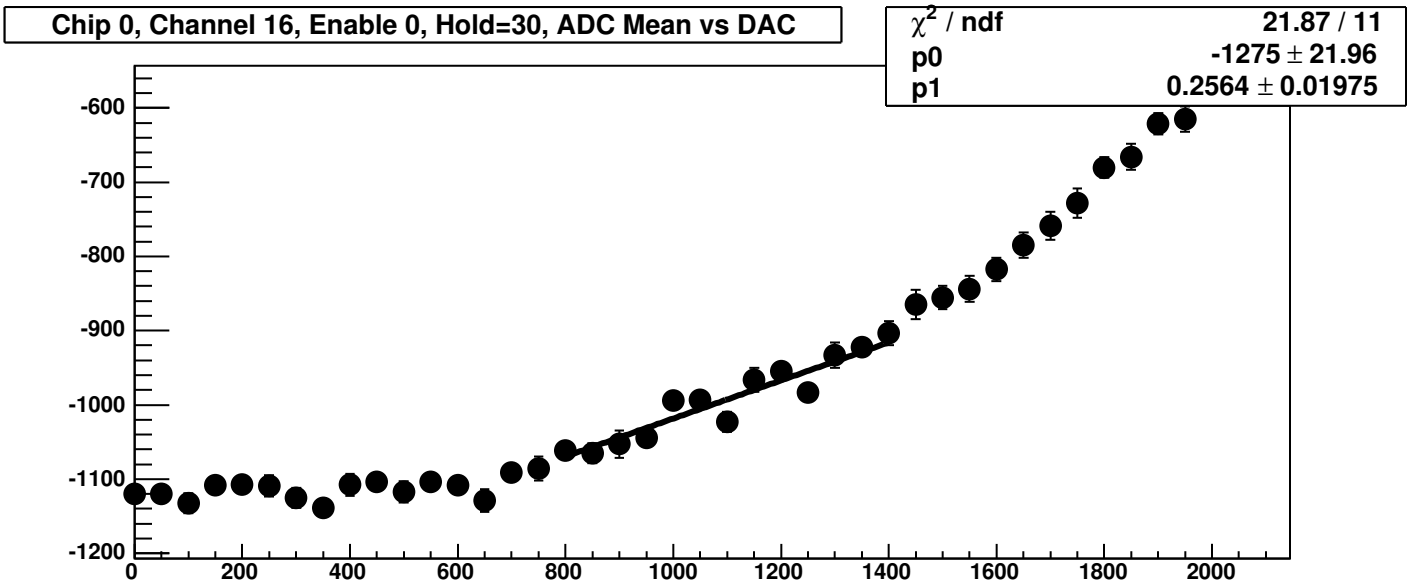

Chip 0, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

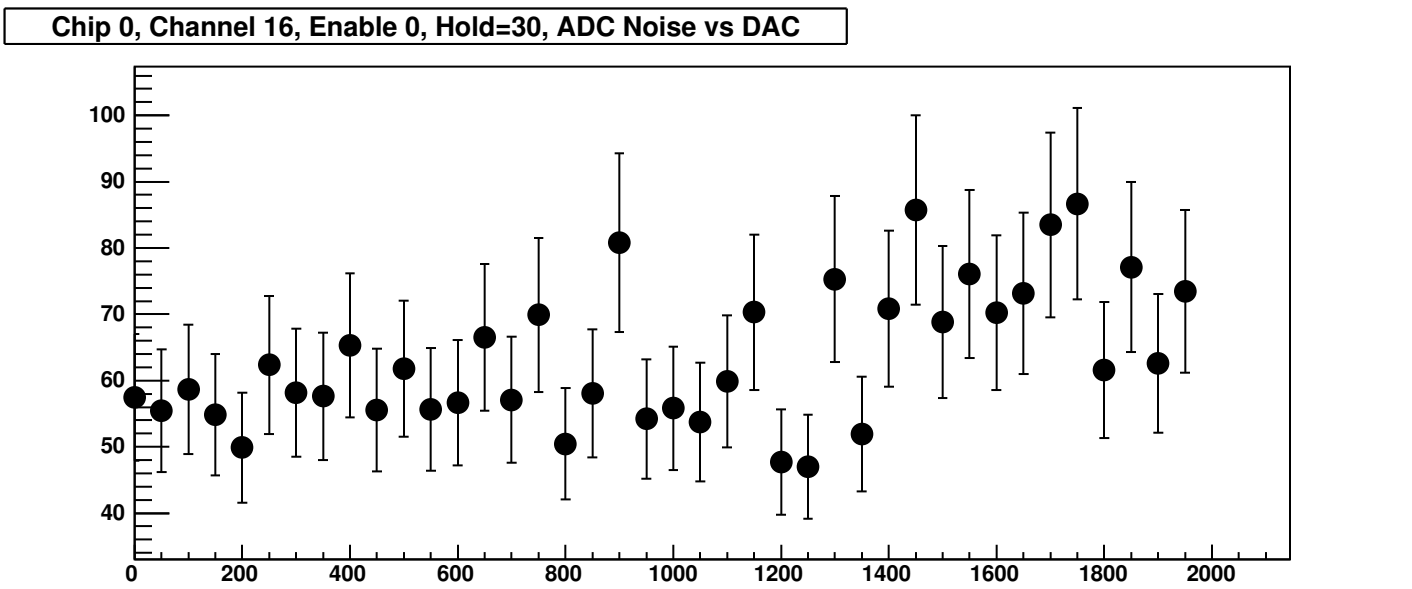

Chip 0, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

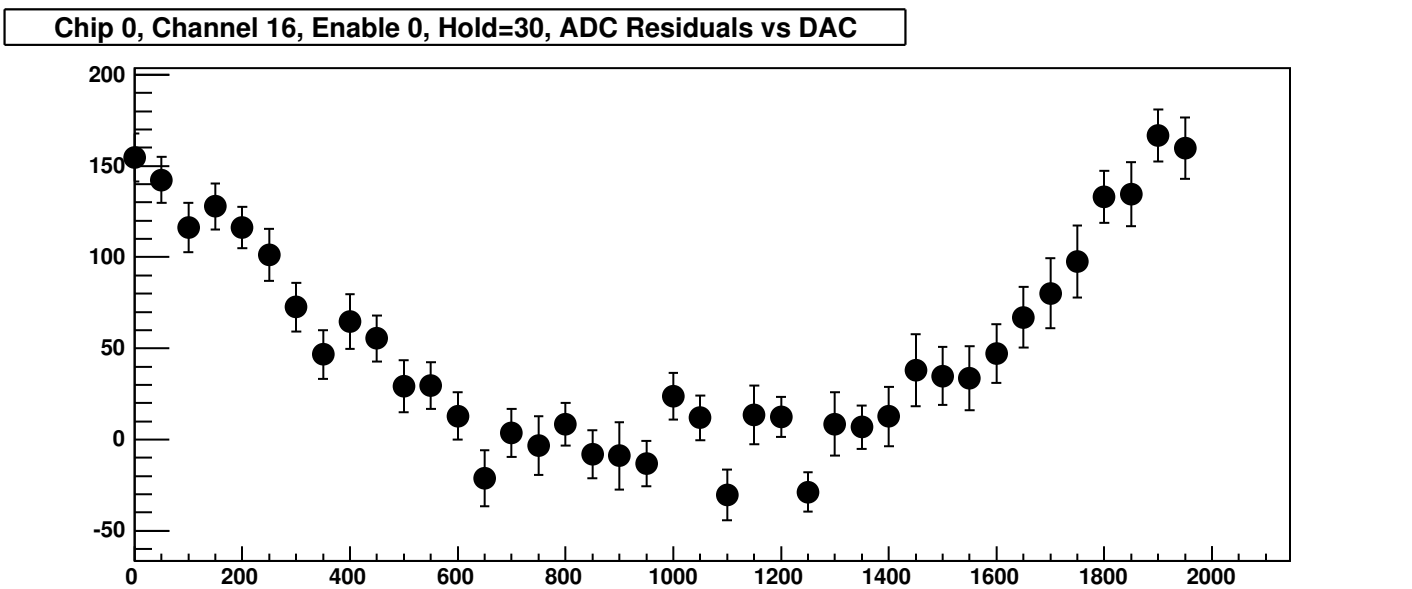

Chip 0, Channel 15, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 79.43 / 11
p0 -1243 ± 27.39
p1 0.8359 ± 0.0245
-100
-200
-300
-400
-500
-600
-700
-800
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

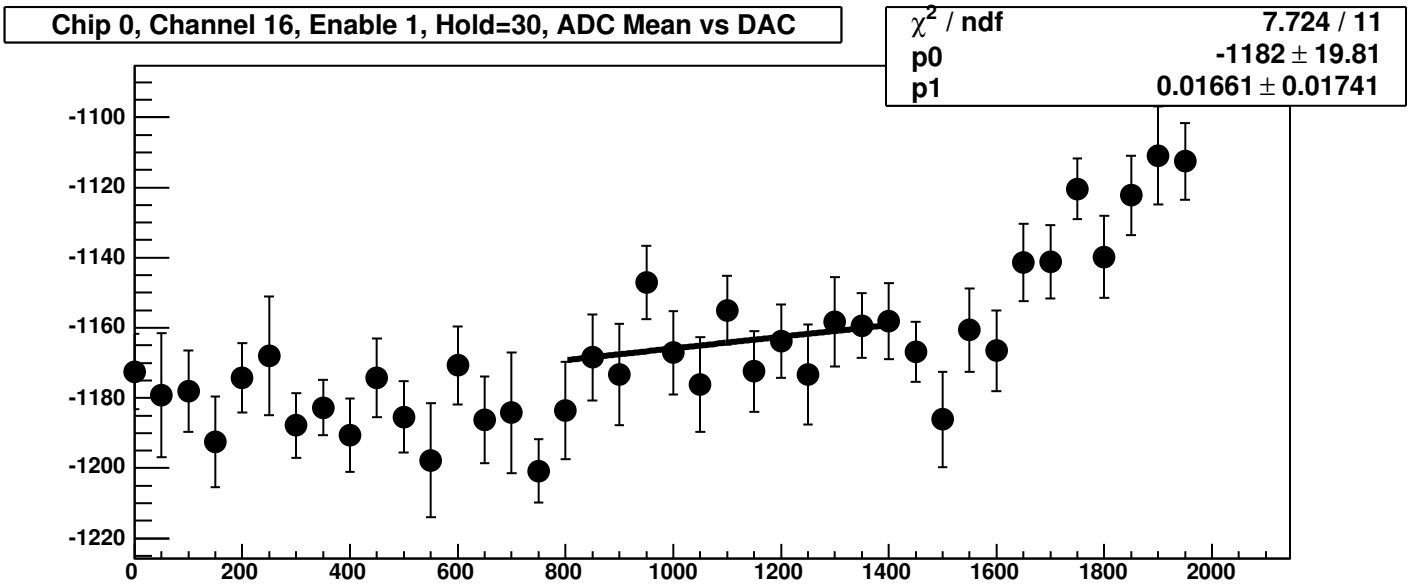

Chip 0, Channel 15, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

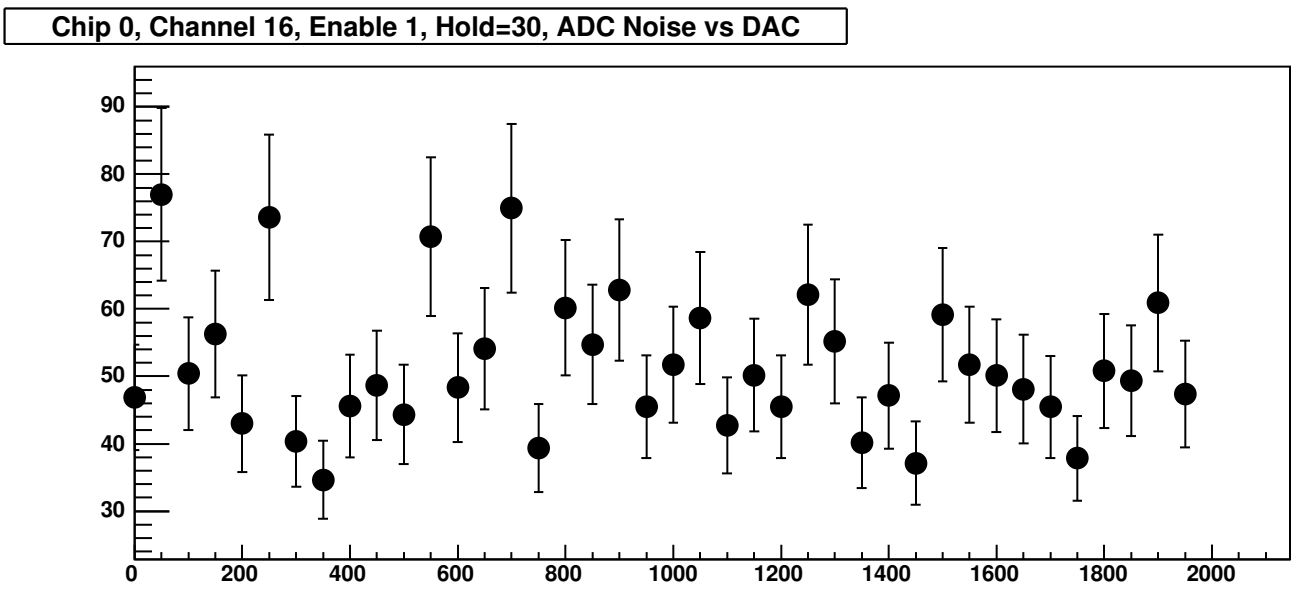

Chip 0, Channel 15, Enable 5, Hold=30, ADC Residuals vs DAC
600
400
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

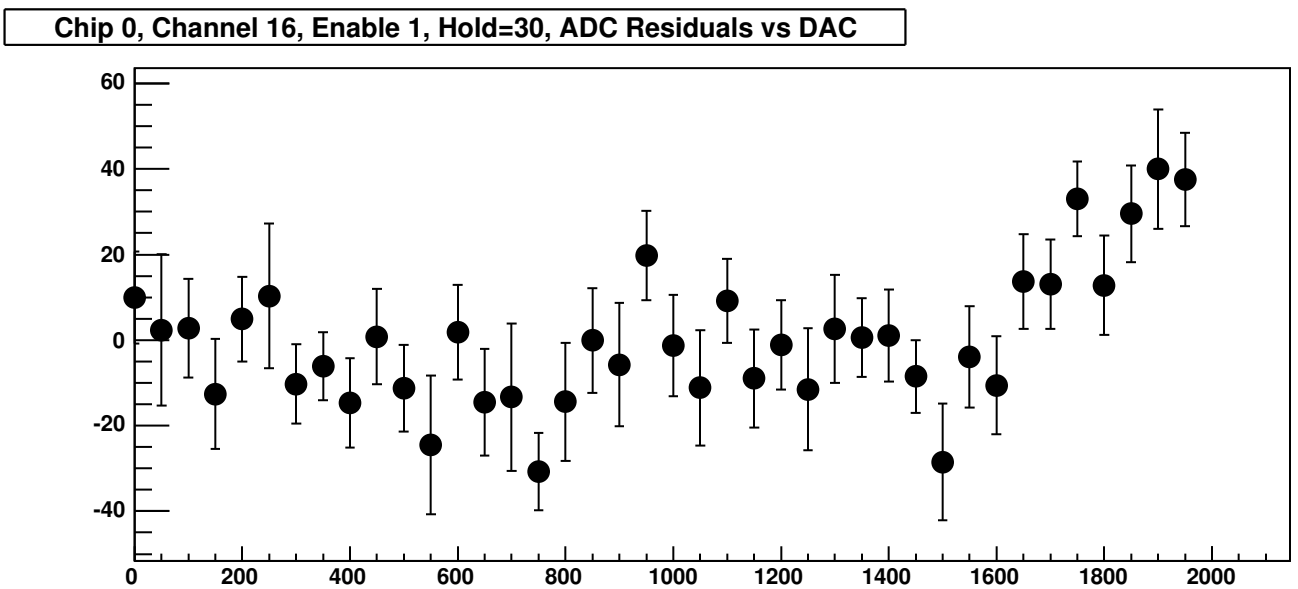

Chip 0, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 21.87 / 11
p0 -1275 ± 21.96
p1 0.2564 ± 0.01975
-600
-700
-800
-900
-1000
-1100
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

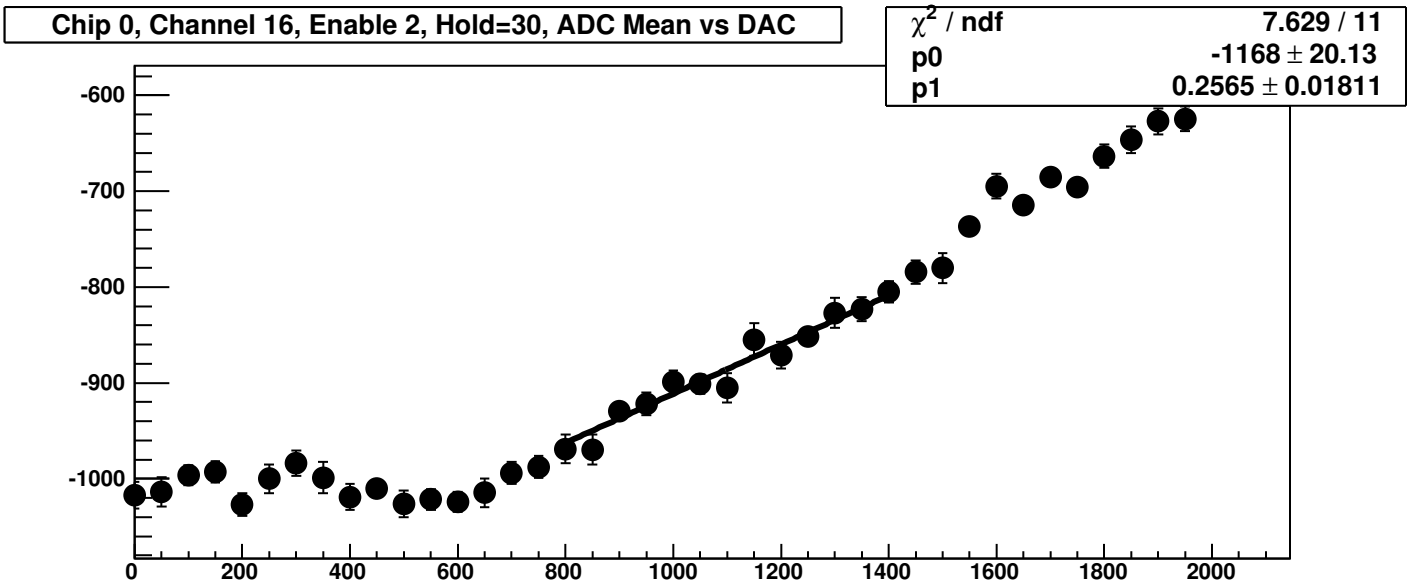

Chip 0, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

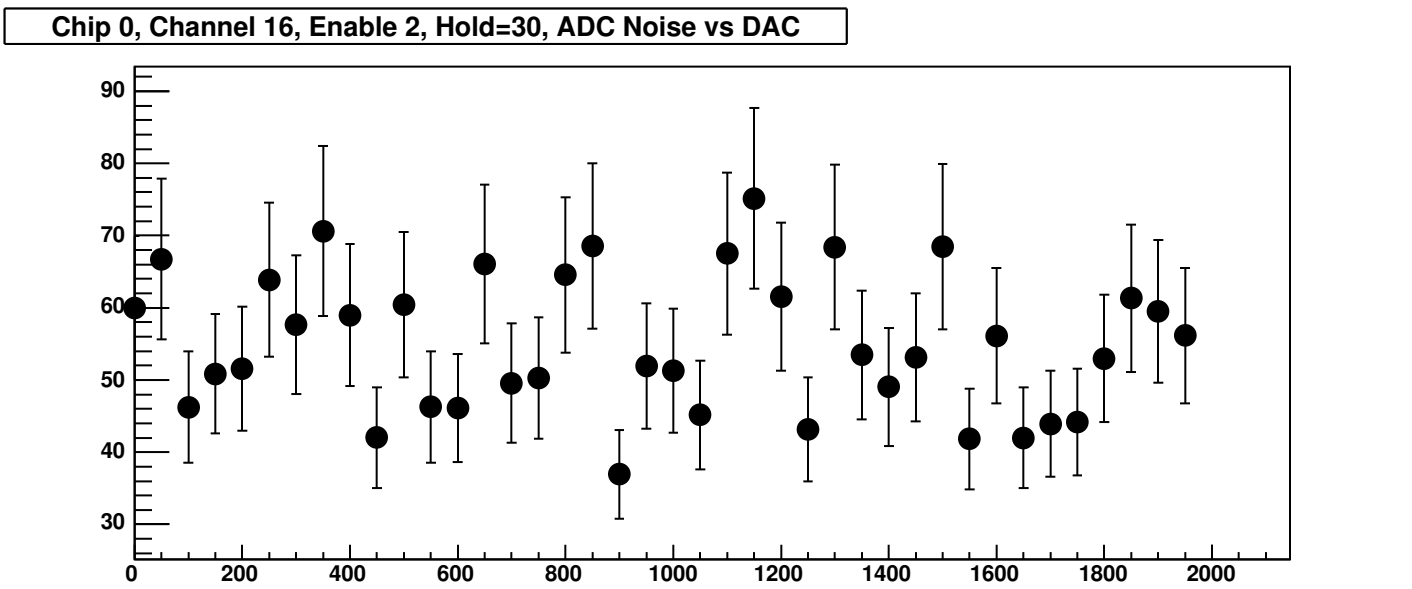

Chip 0, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

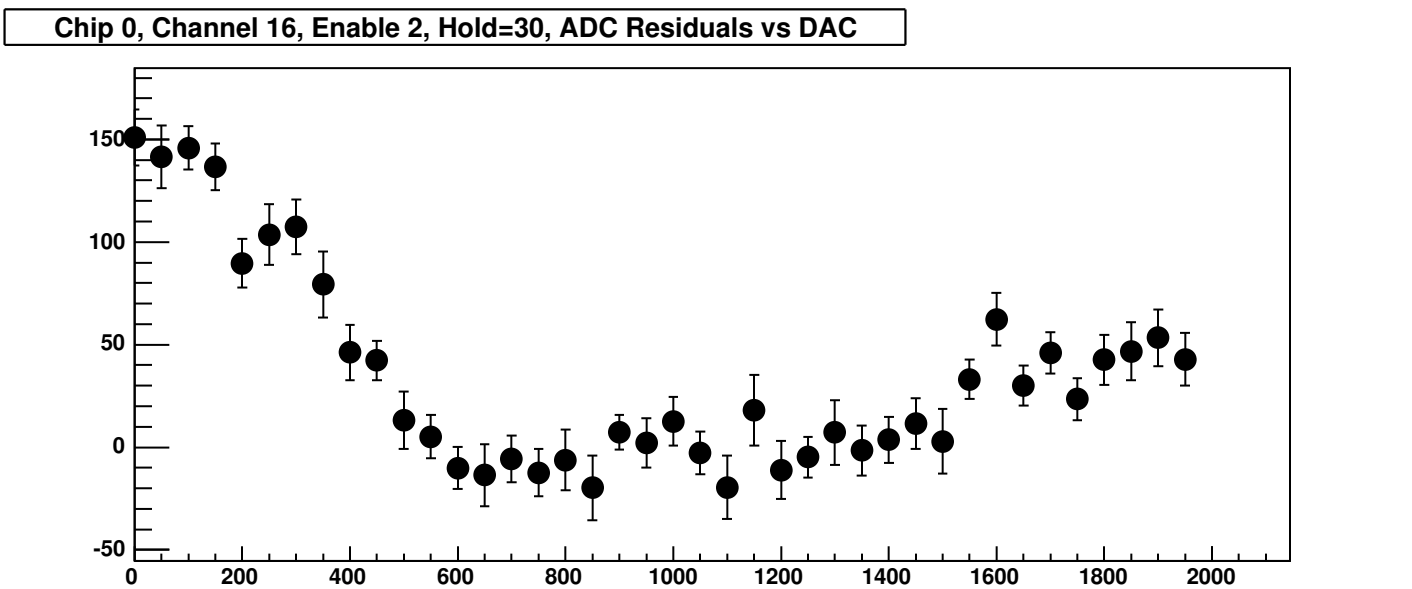

Chip 0, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7.724 / 11
p0 -1182 ± 19.81
p1 0.01661 ± 0.01741
-1100
-1120
-1140
-1160
-1180
-1200
-1220
0
200
400
600
800
1000
1200
1400
1600
1800
2000

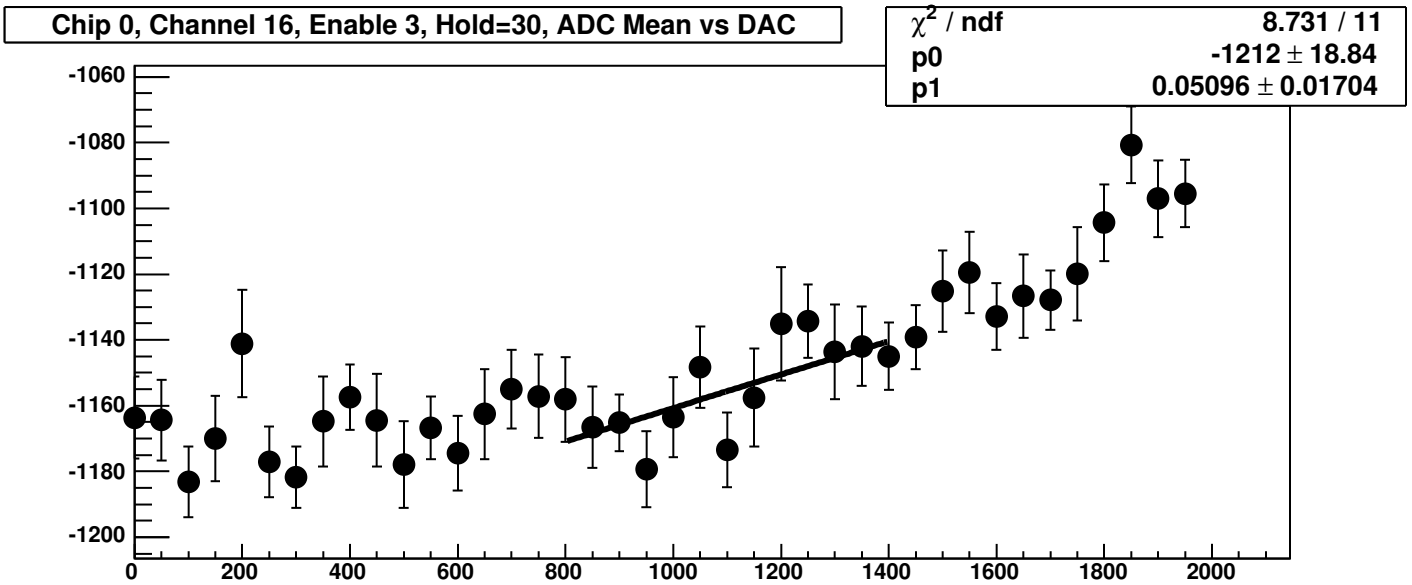

Chip 0, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

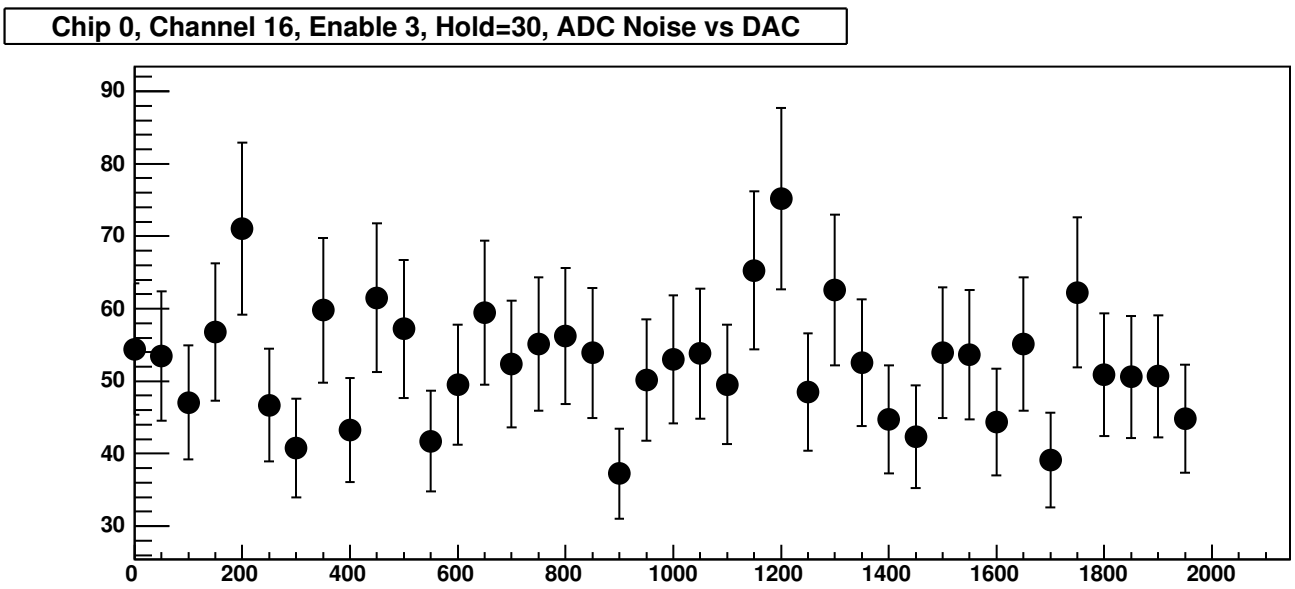

Chip 0, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

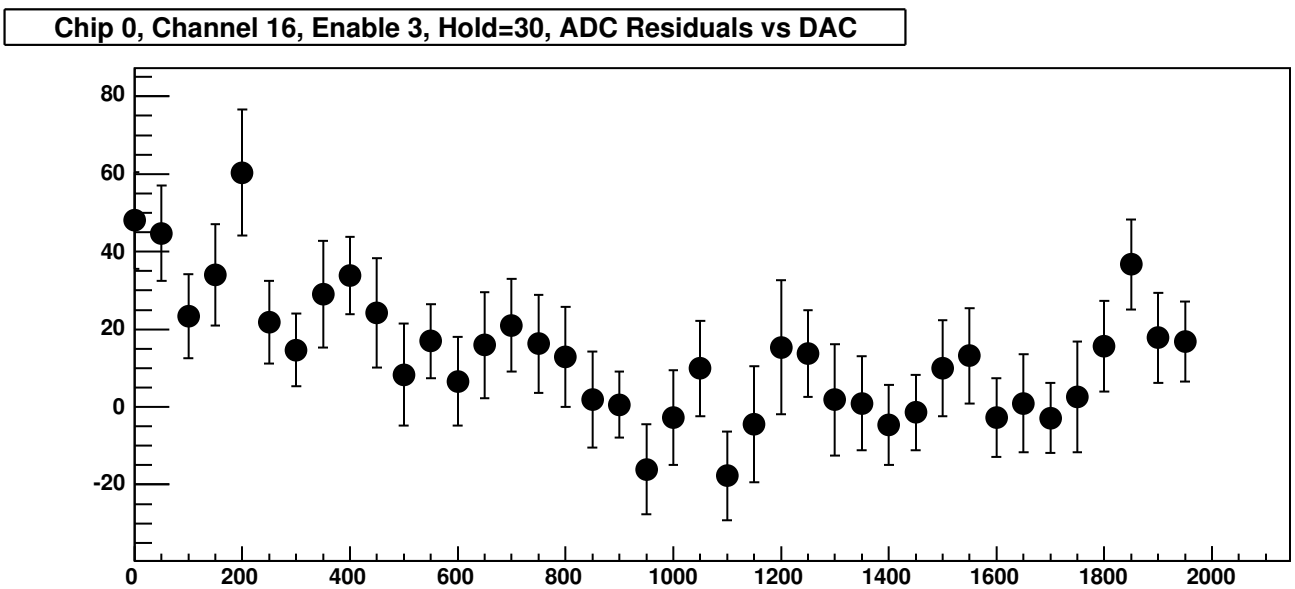

Chip 0, Channel 16, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 7.629 / 11
p0 -1168 ± 20.13
p1 0.2565 ± 0.01811
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

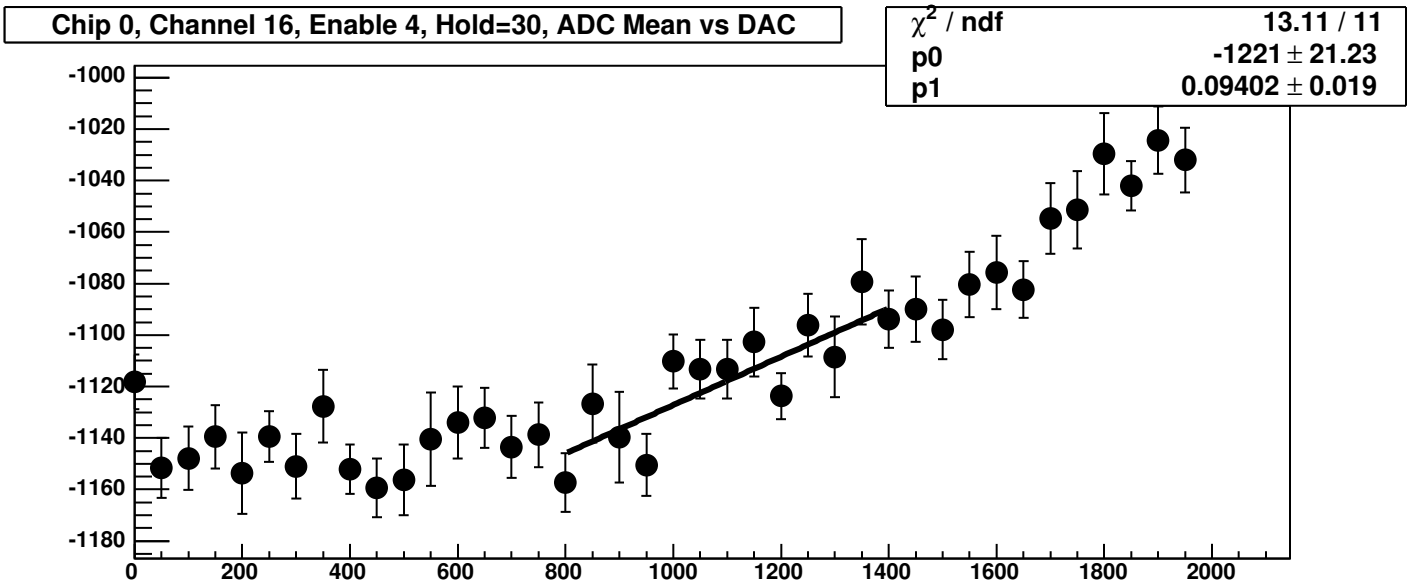

Chip 0, Channel 16, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

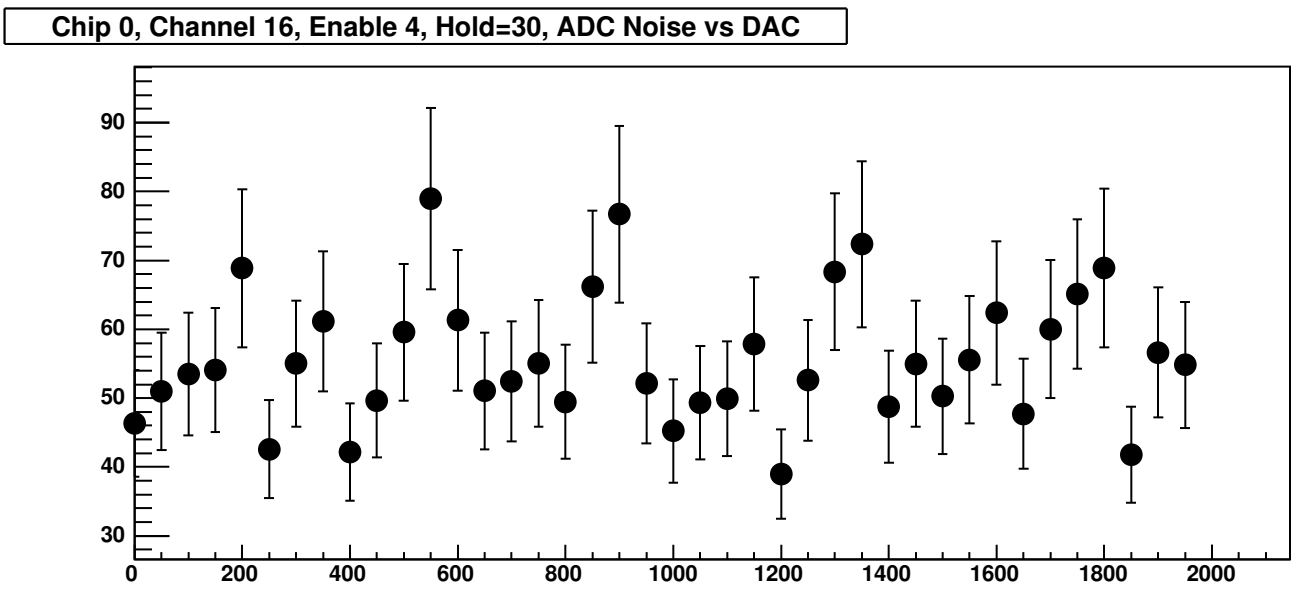

Chip 0, Channel 16, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

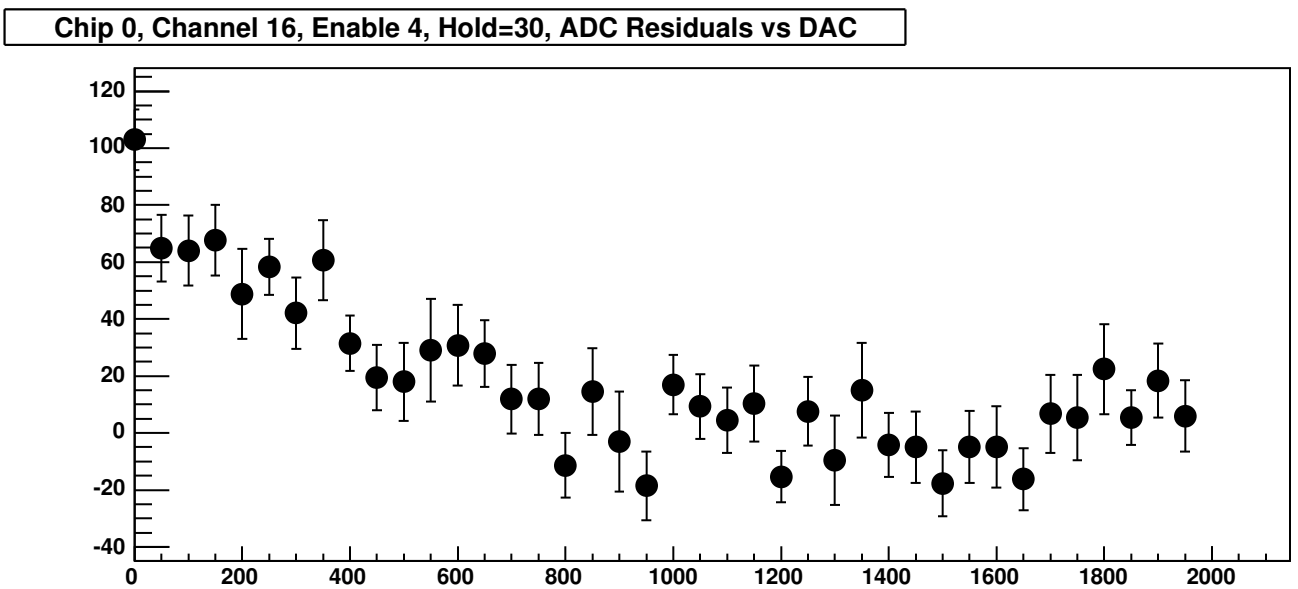

Chip 0, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 8.731 / 11
p0 -1212 ± 18.84
p1 0.05096 ± 0.01704
-1060
-1080
-1100
-1120
-1140
-1160
-1180
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

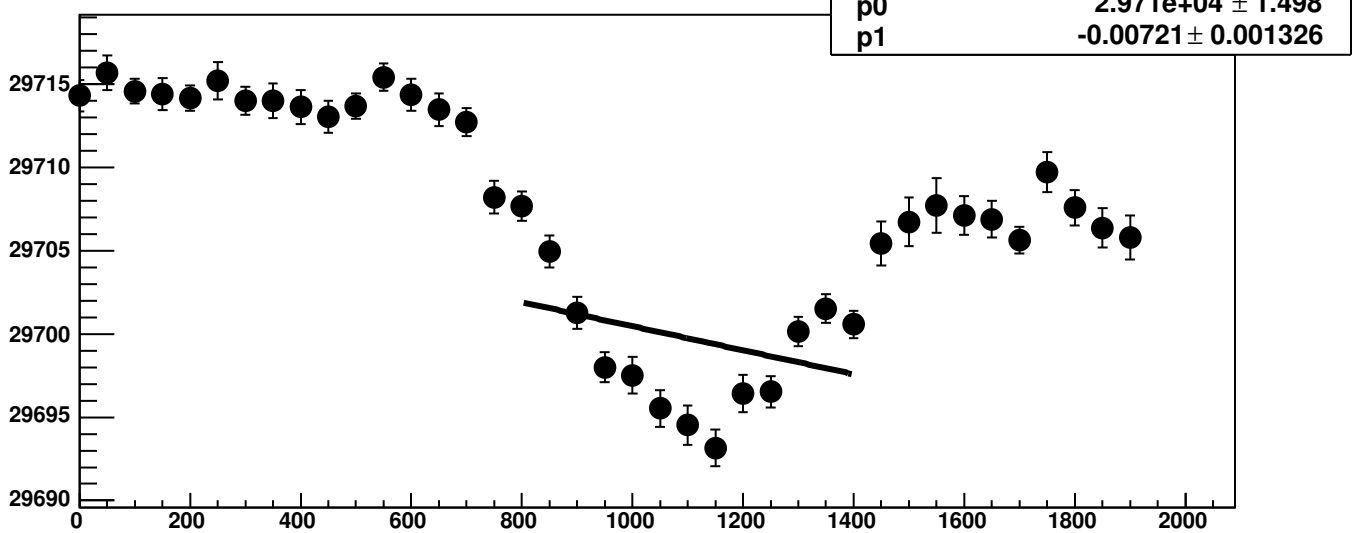

Chip 0, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 13.11 / 11
p0 -1221 ± 21.23
p1 0.09402 ± 0.019
-1000
-1020
-1040
-1060
-1080
-1100
-1120
-1140
-1160
-1180
0
200
400
600
800
1000
1200
1400
1600
1800
2000

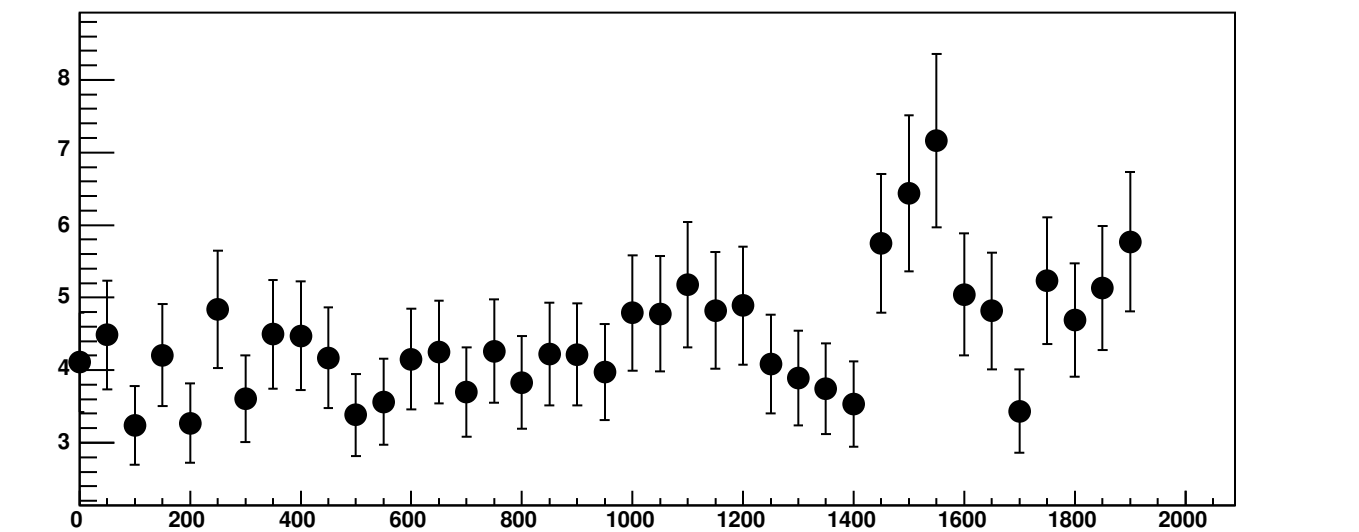

Chip 0, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 0, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

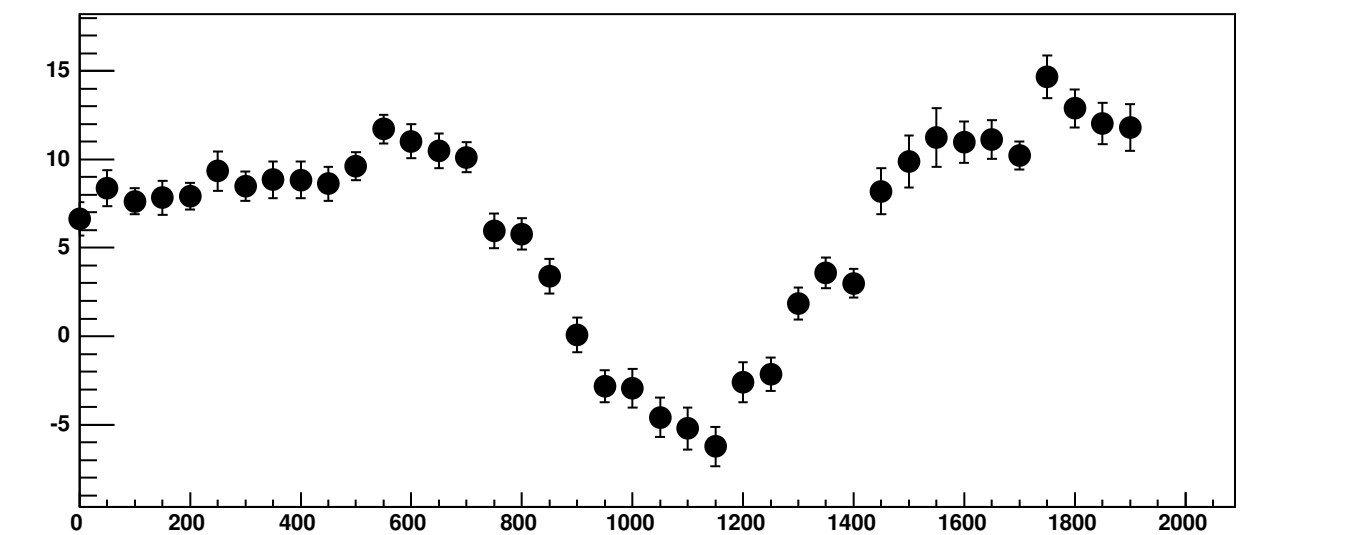
Chip 0, Channel 16, Enable 5!, Hold=30, ADC Mean vs DAC

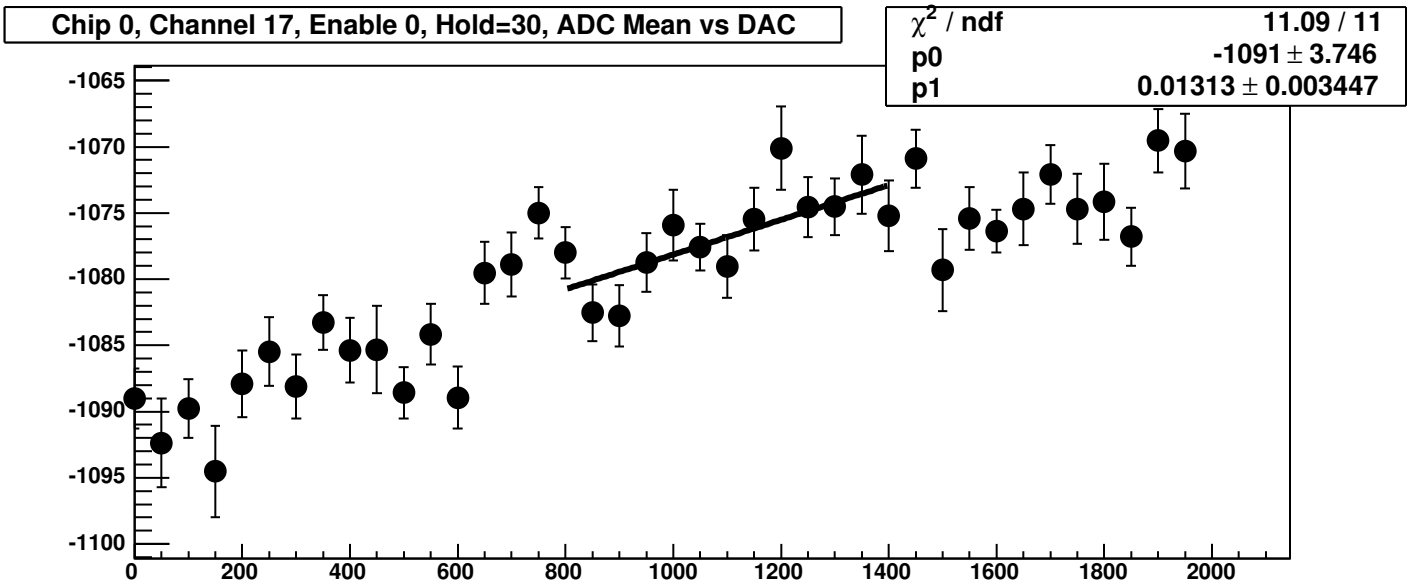


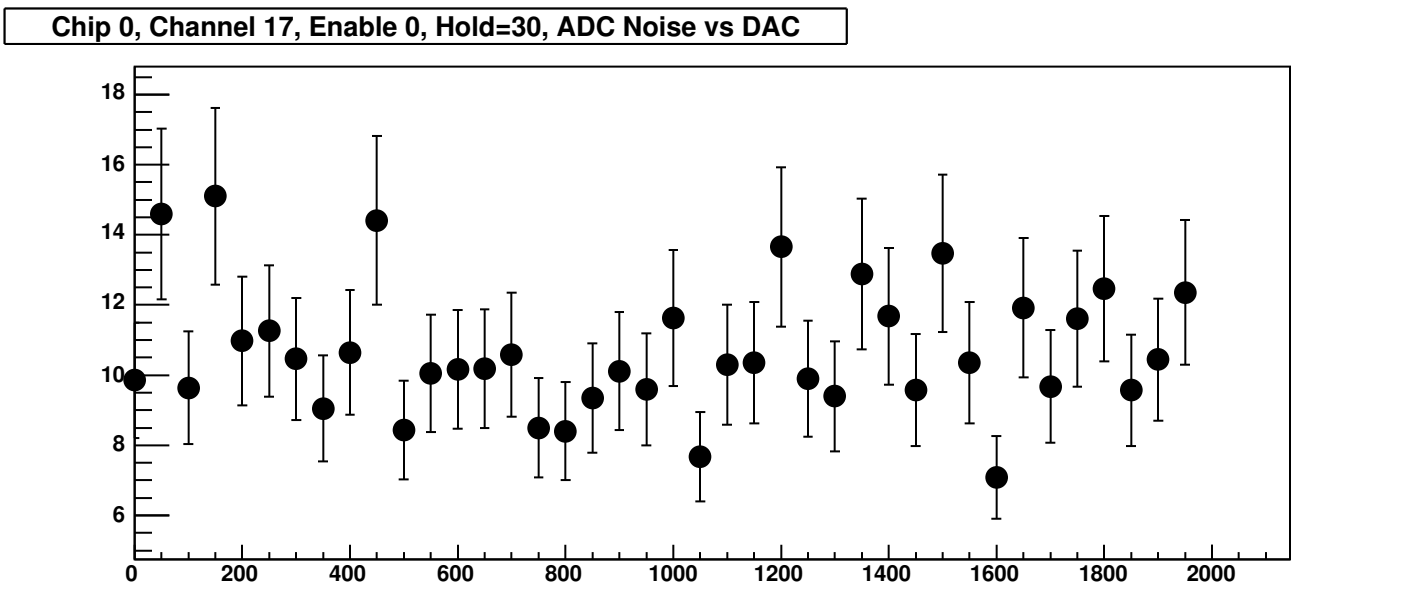
Chip 0, Channel 16, Enable 5!, Hold=30, ADC Noise vs DAC

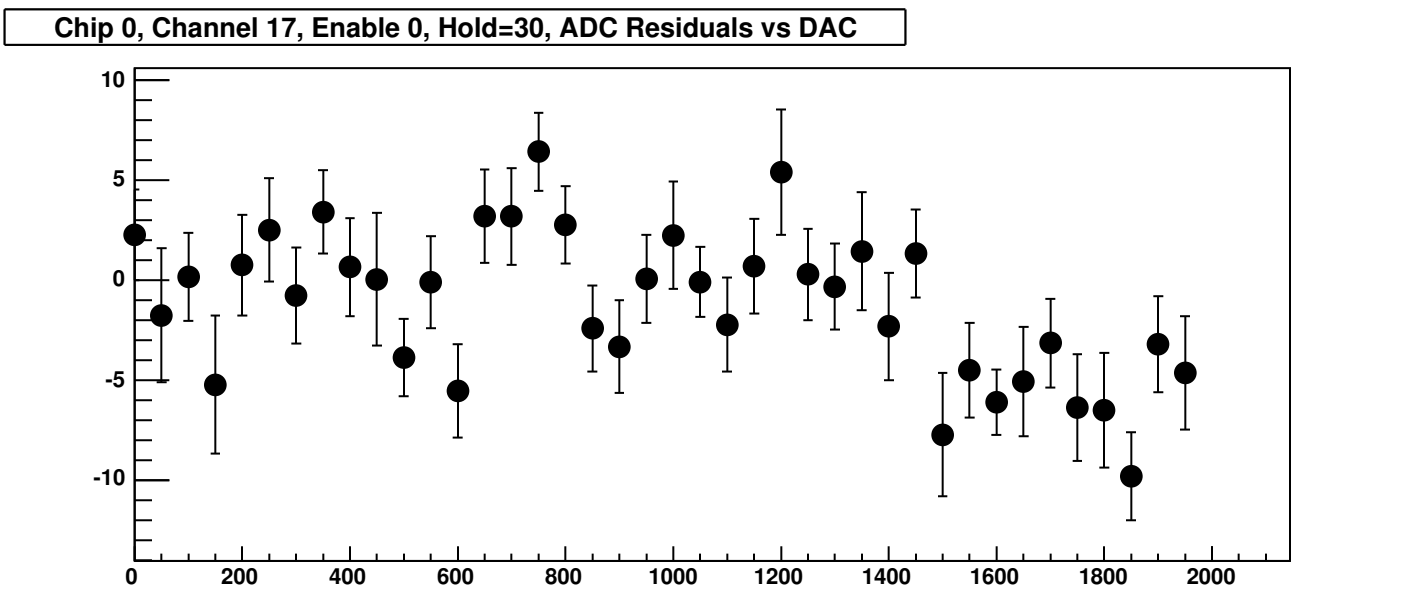


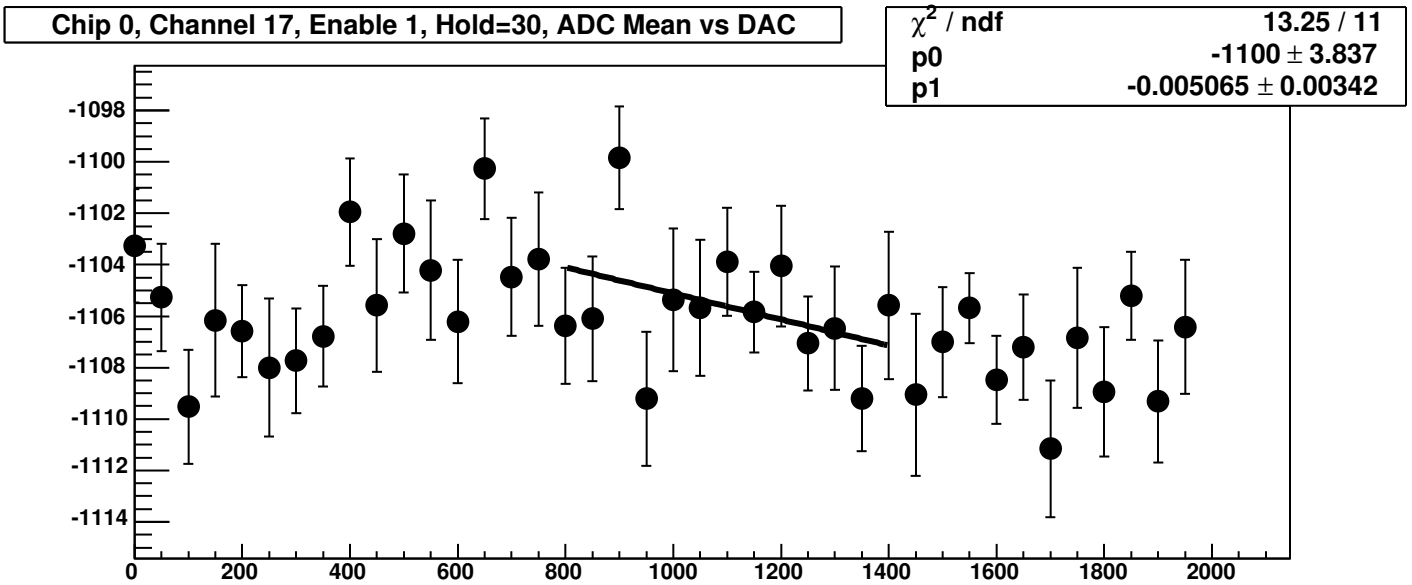
Chip 0, Channel 16, Enable 5!, Hold=30, ADC Residuals vs DAC

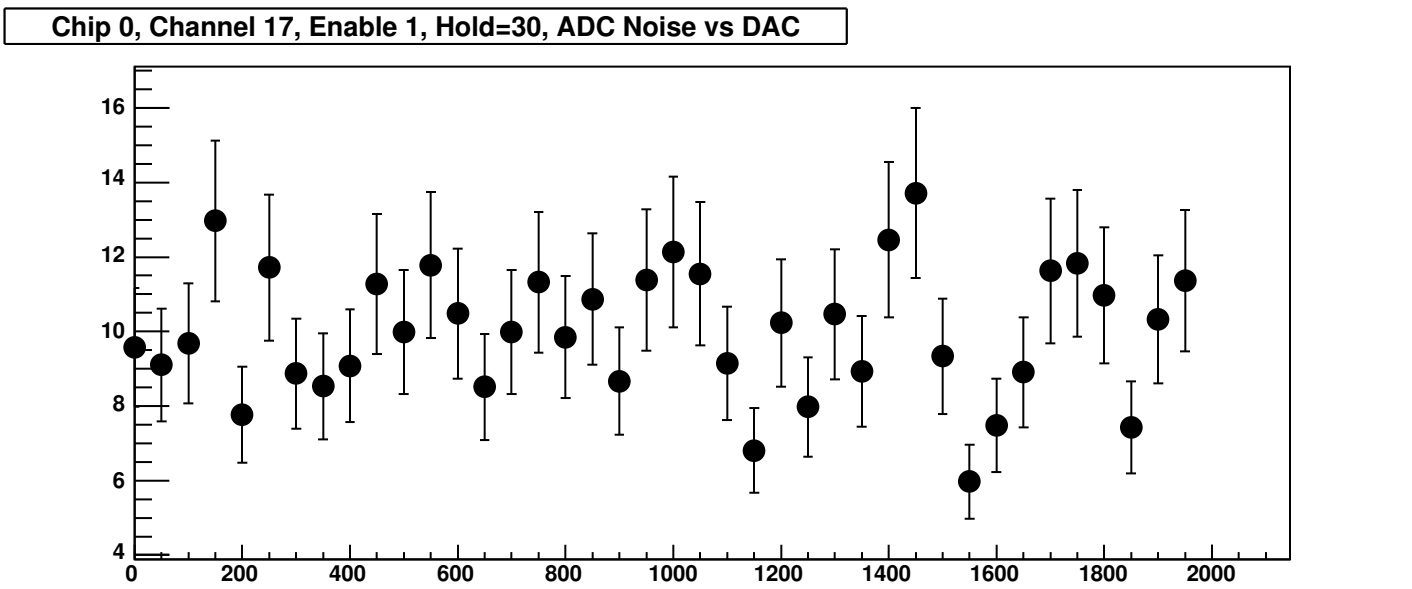


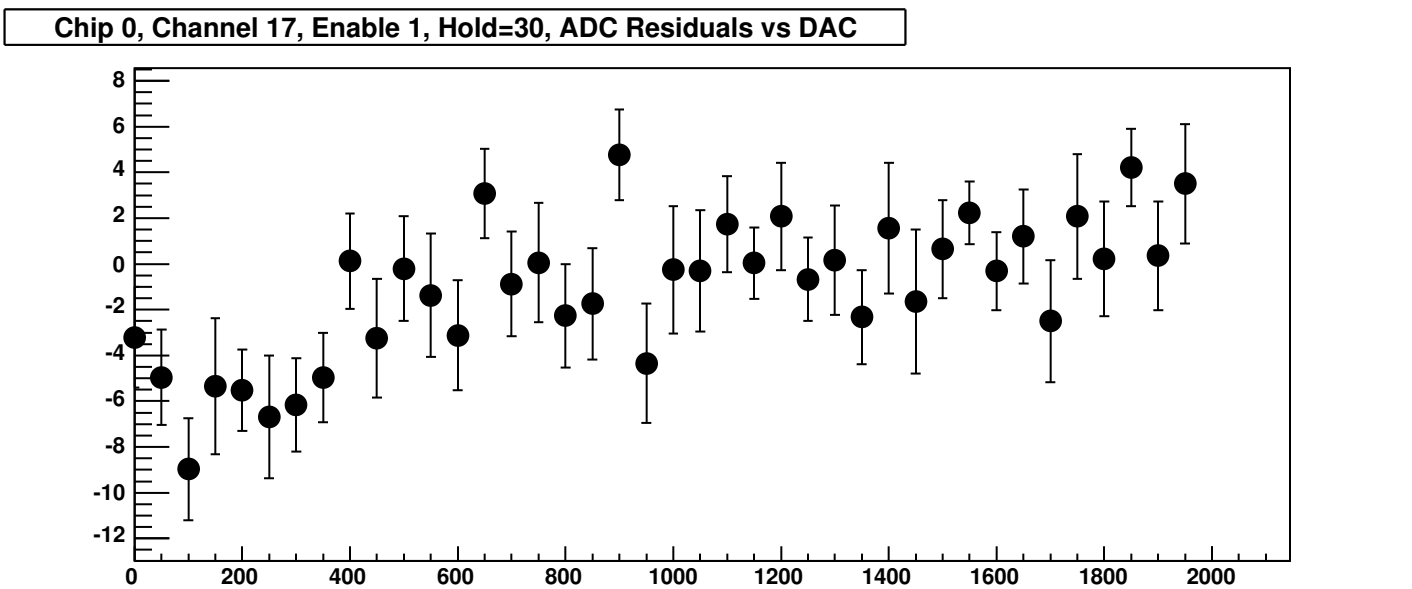

Chip 0, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 11.09 / 11
p0 -1091 ± 3.746
p1 0.01313 ± 0.003447
-1065
-1070
-1075
-1080
-1085
-1090
-1095
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

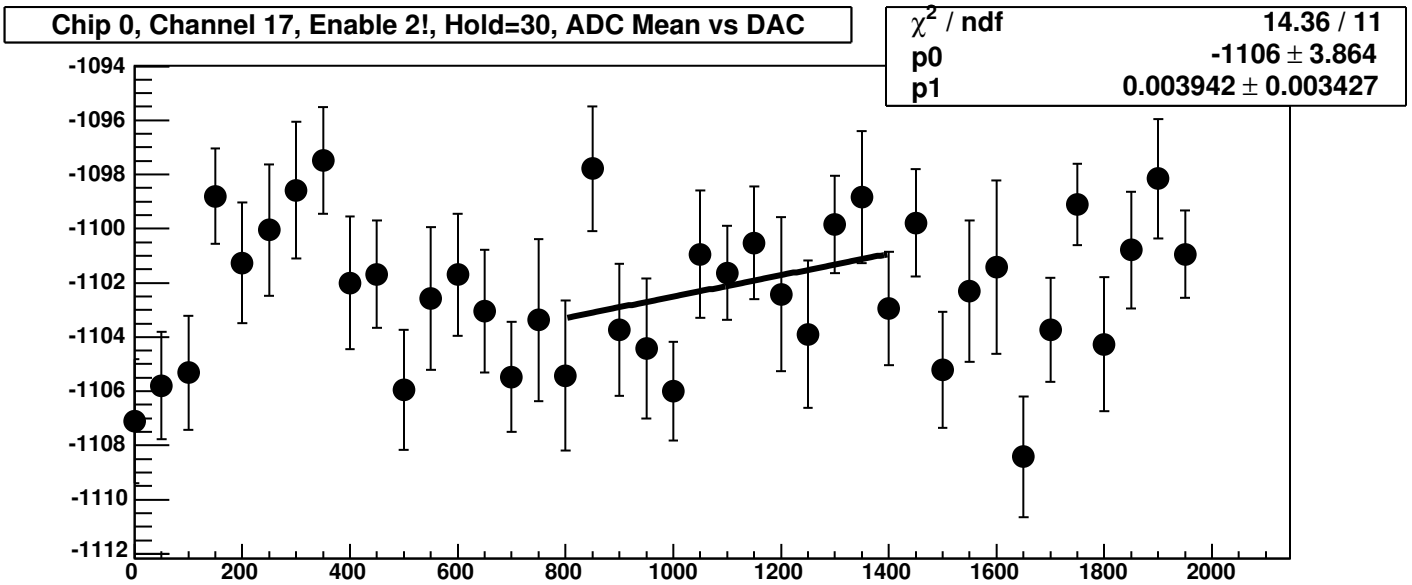

Chip 0, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
18
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

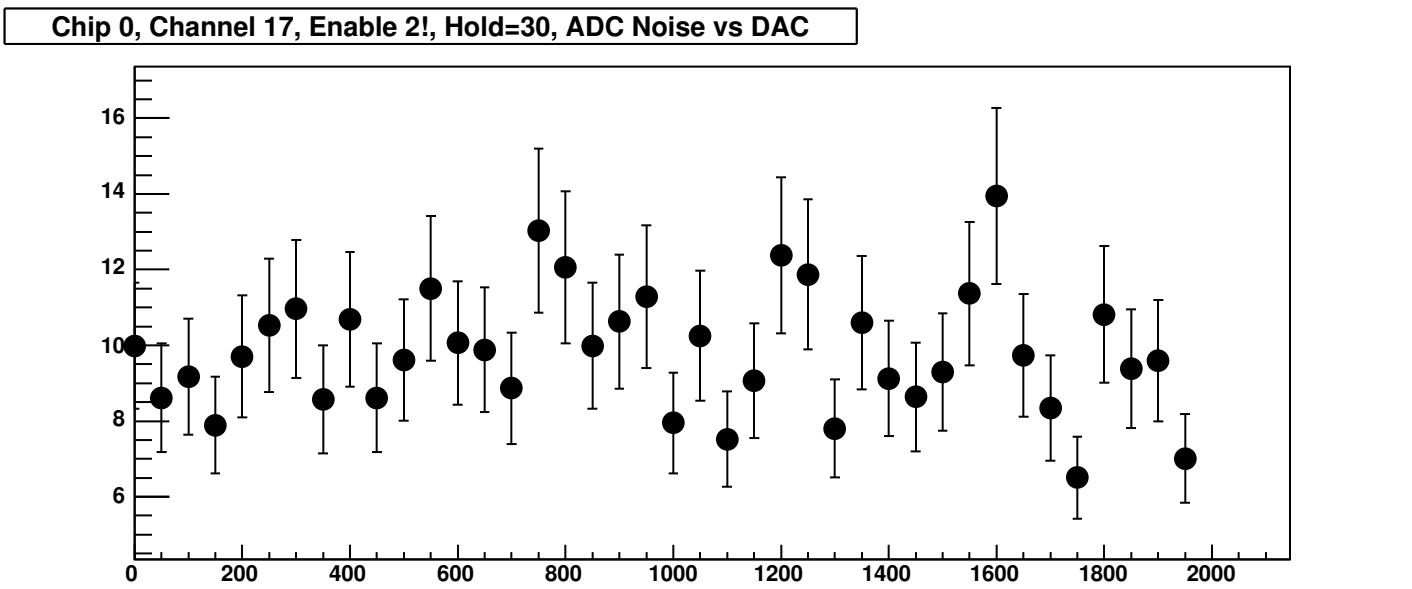

Chip 0, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

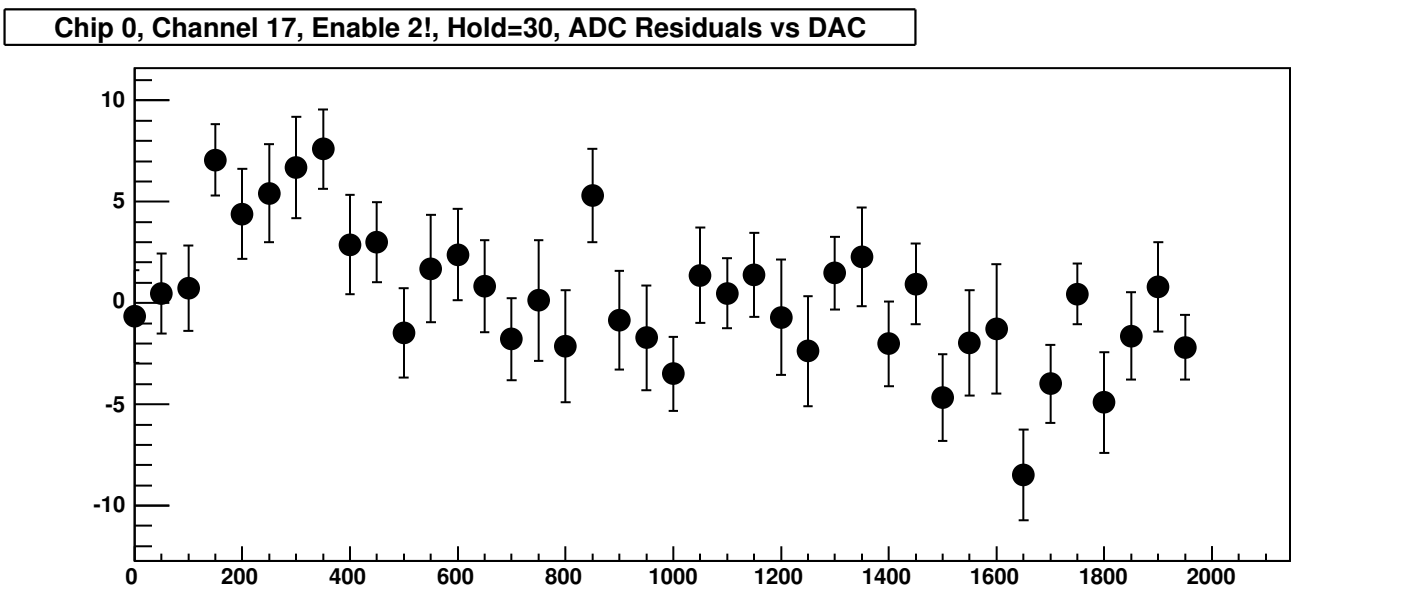

Chip 0, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 13.25 / 11
p0 -1100 ± 3.837
p1 -0.005065 ± 0.00342
-1098
-1100
-1102
-1104
-1106
-1108
-1110
-1112
-1114
0
200
400
600
800
1000
1200
1400
1600
1800
2000

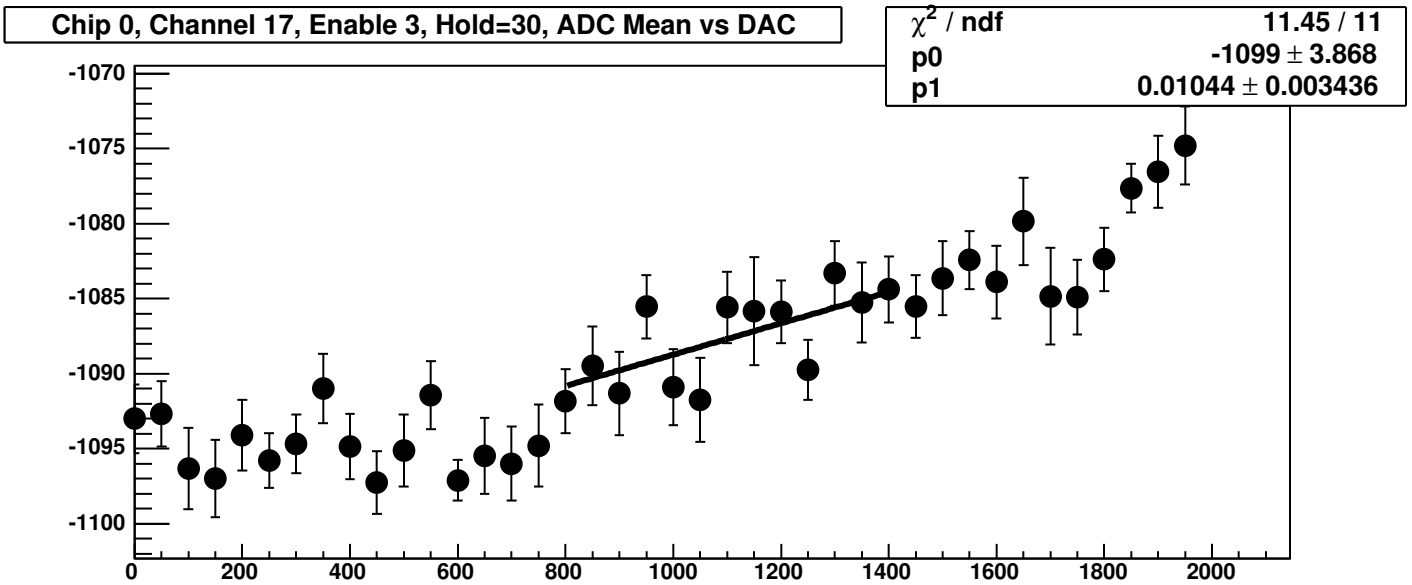

Chip 0, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
4
0
200
400
600
800
1000
1200
1400
1600
1800
2000

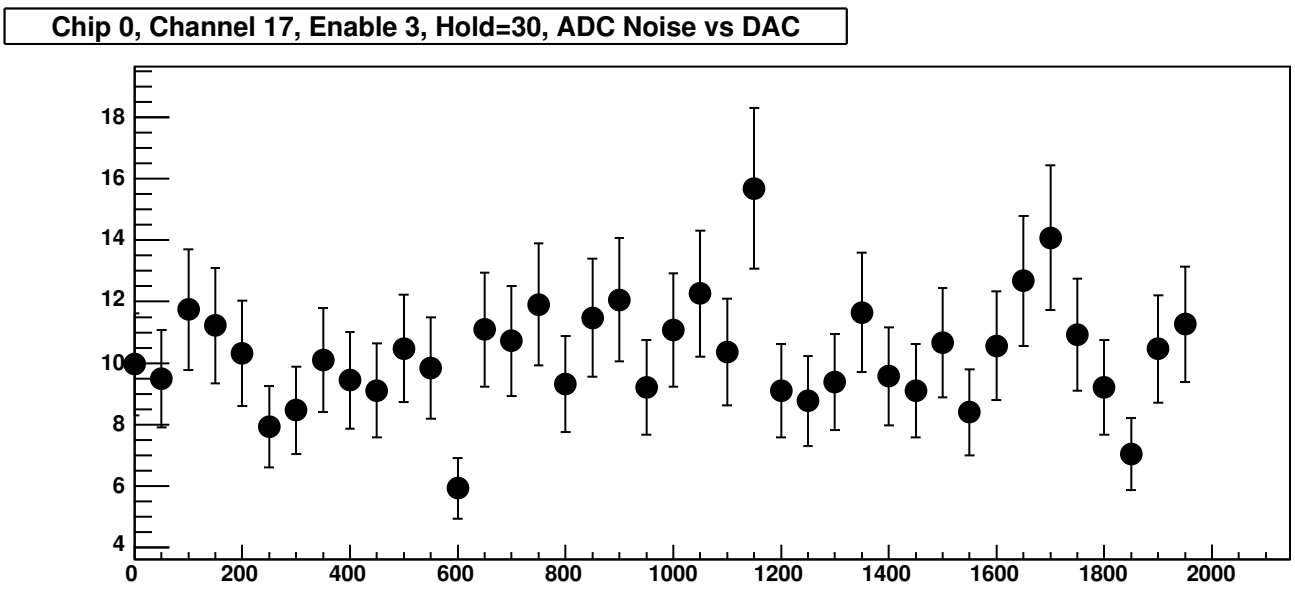

Chip 0, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
0
200
400
600
800
1000
1200
1400
1600
1800
2000

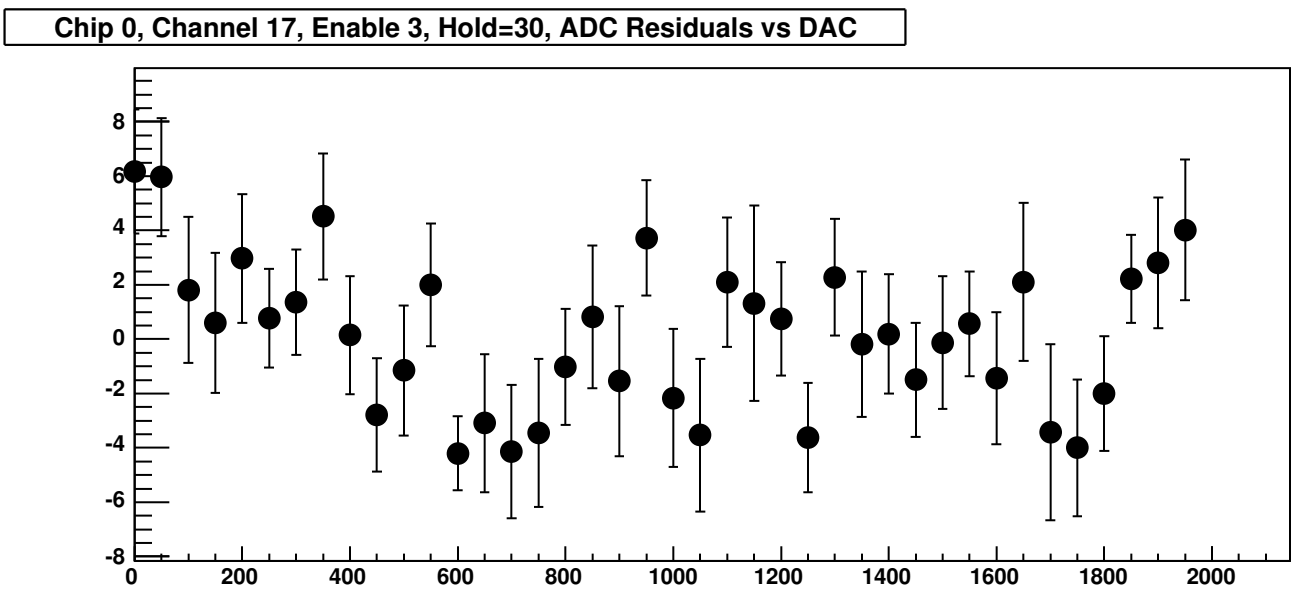

Chip 0, Channel 17, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 14.36 / 11
p0 -1106 ± 3.864
p1 0.003942 ± 0.003427
-1094
-1096
-1098
-1100
-1102
-1104
-1106
-1108
-1110
-1112
0
200
400
600
800
1000
1200
1400
1600
1800
2000

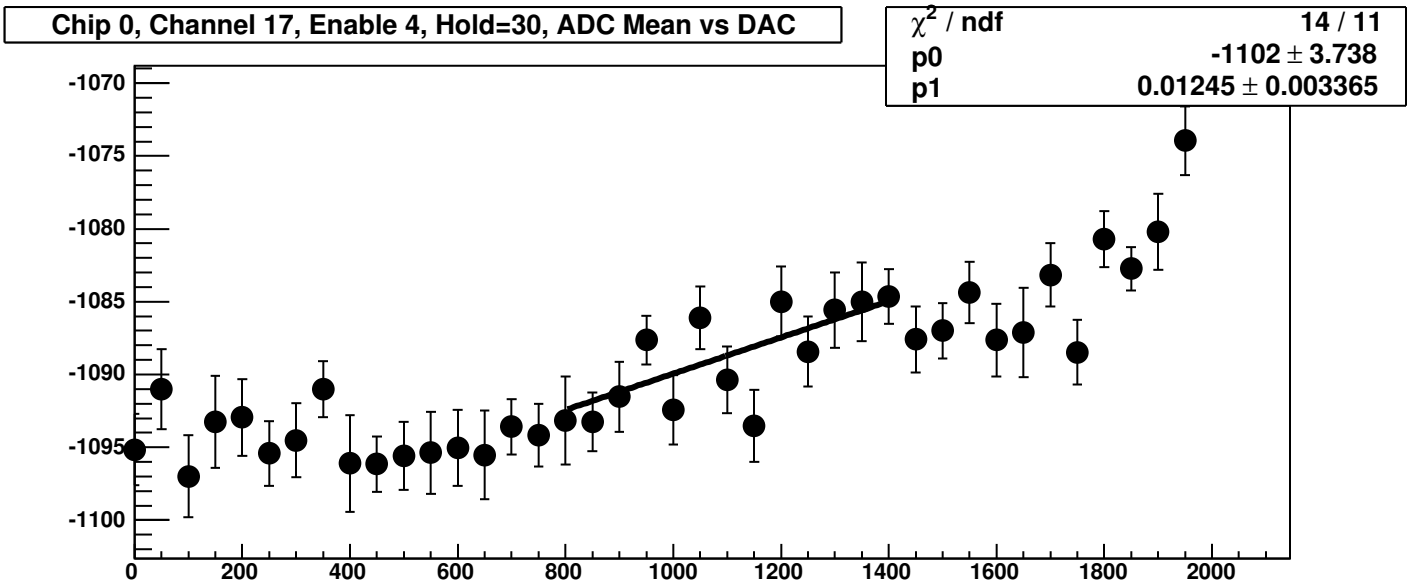

Chip 0, Channel 17, Enable 2!, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

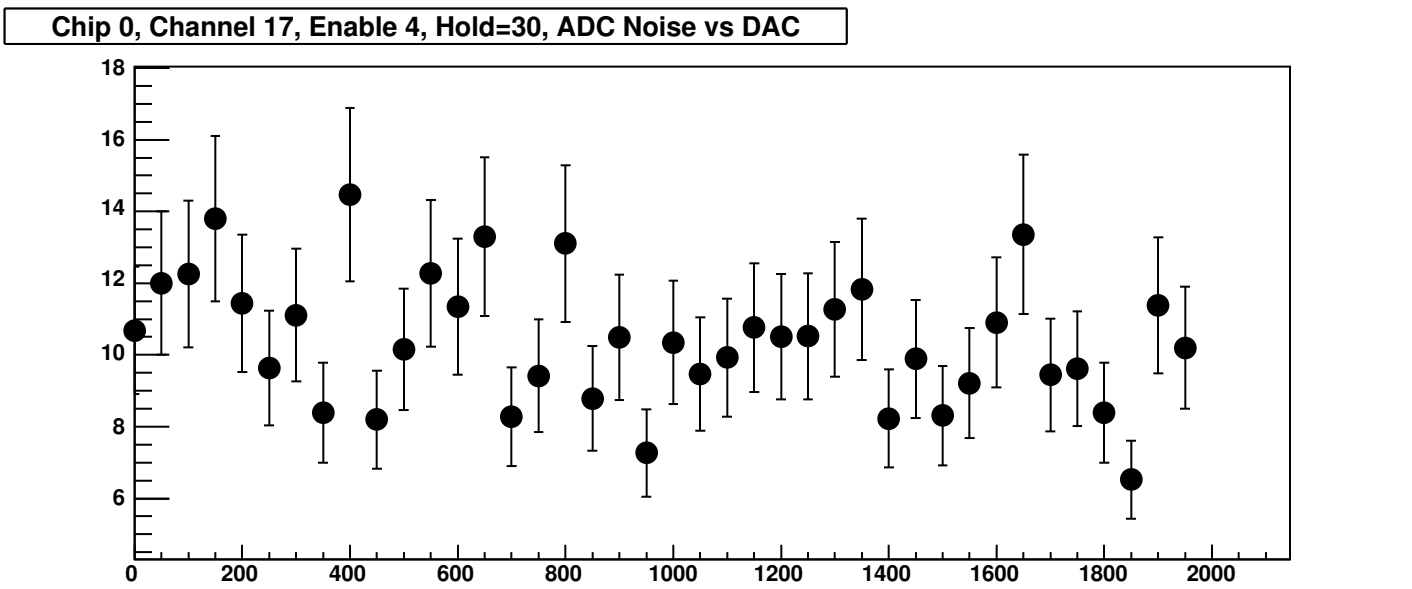

Chip 0, Channel 17, Enable 2!, Hold=30, ADC Residuals vs DAC
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

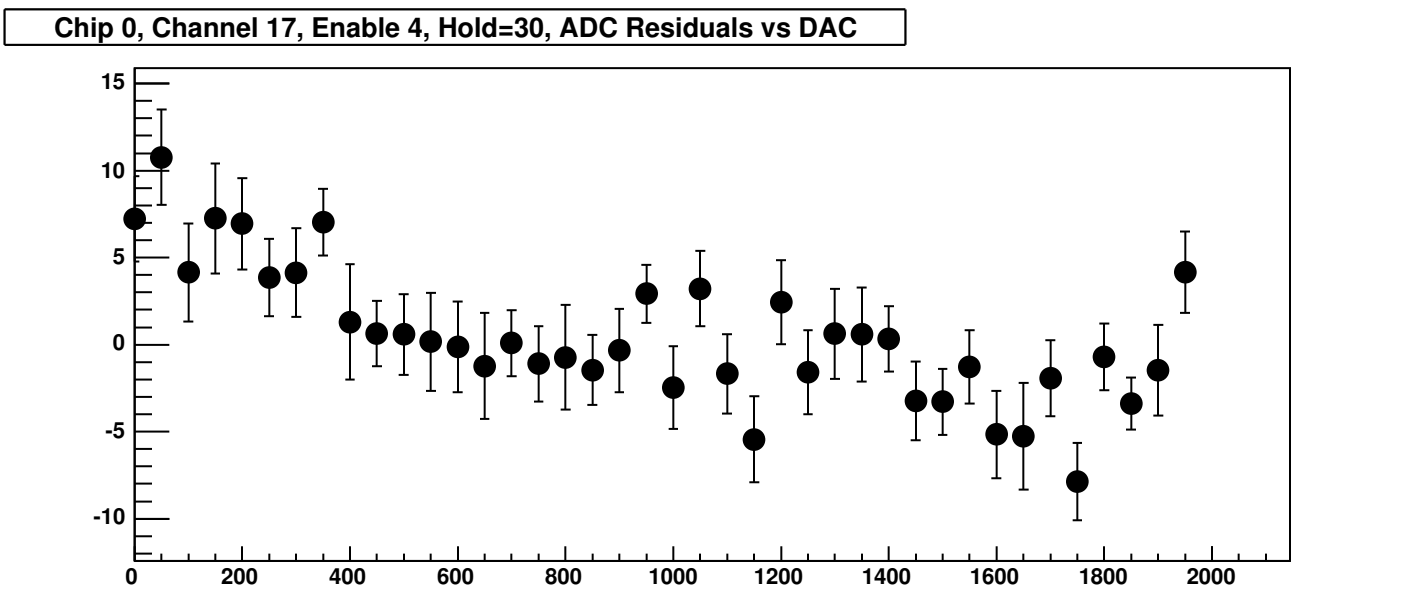

Chip 0, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 11.45 / 11
p0 -1099 ± 3.868
p1 0.01044 ± 0.003436
-1070
-1075
-1080
-1085
-1090
-1095
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

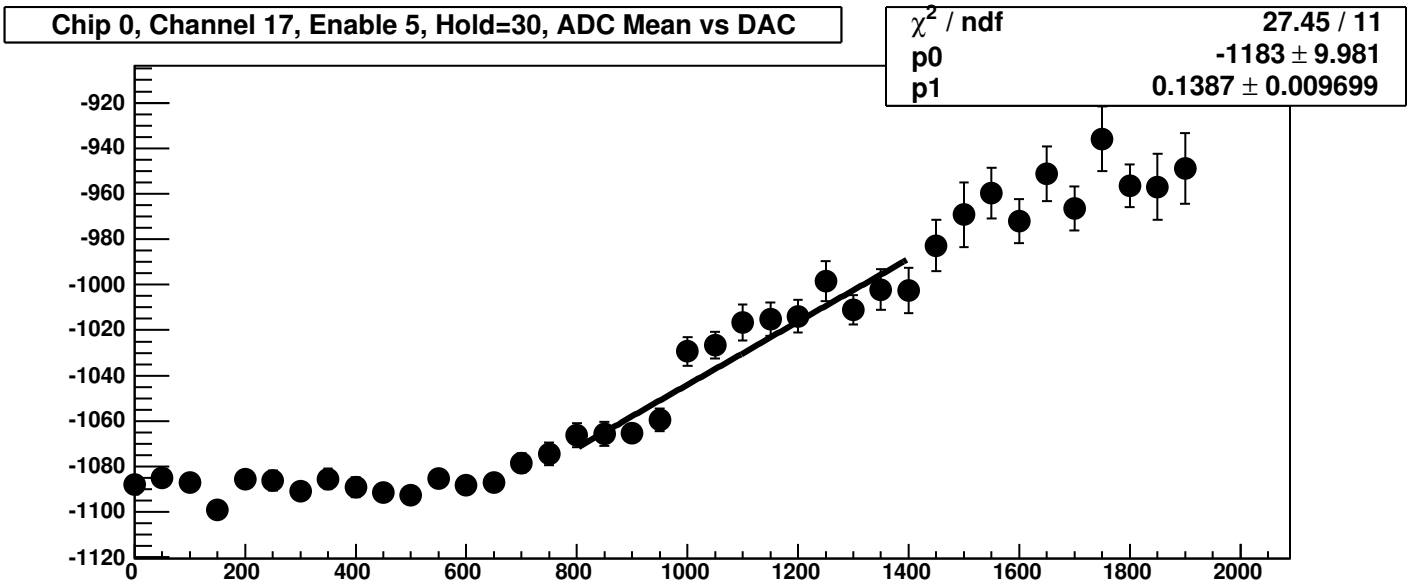

Chip 0, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
200
400
600
800
1000
1200
1400
1600
1800
2000

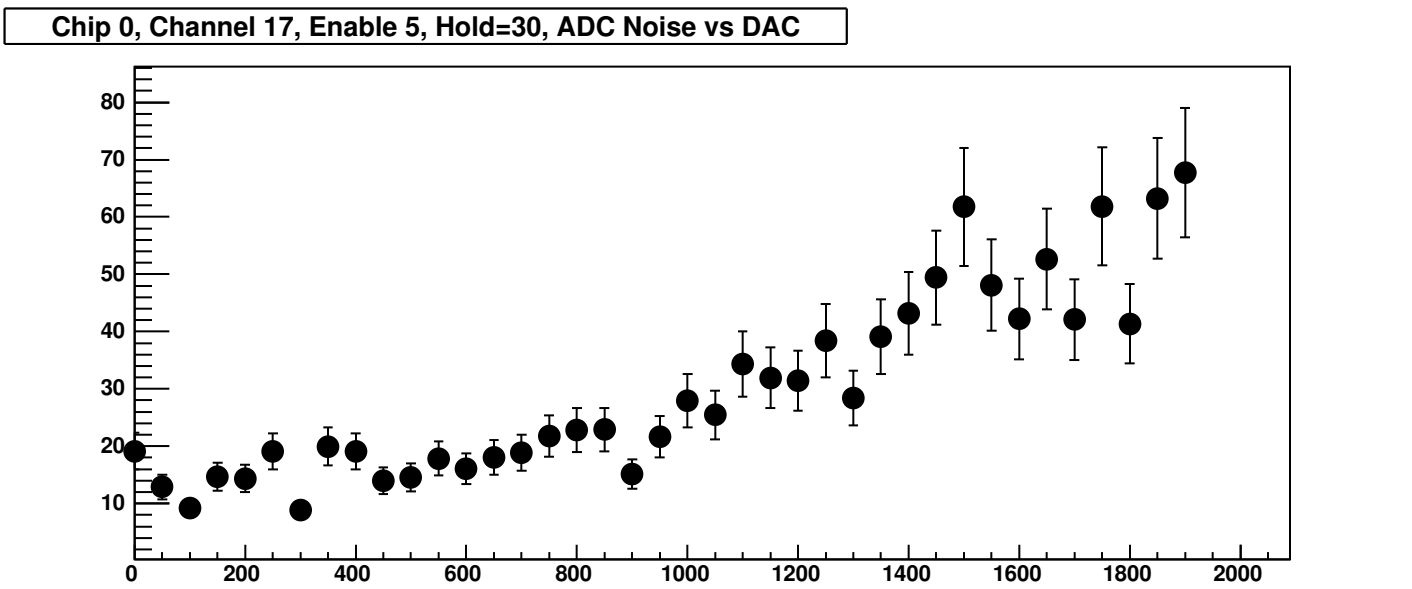

Chip 0, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
0
200
400
600
800
1000
1200
1400
1600
1800
2000

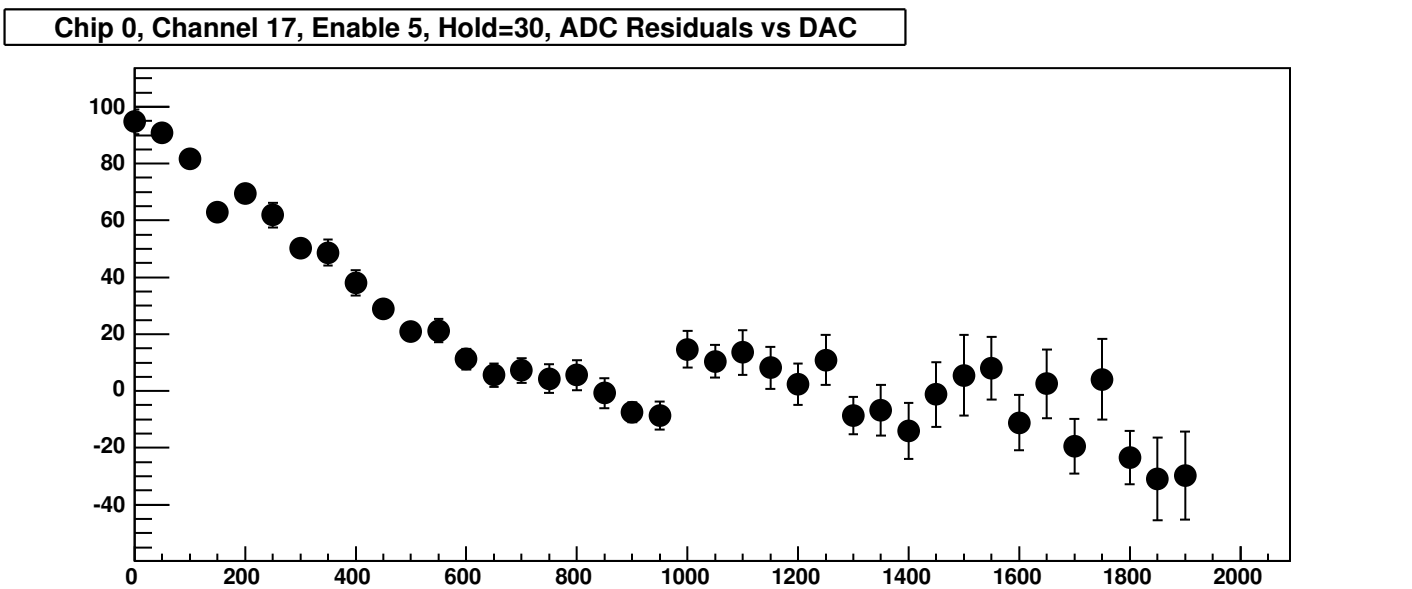

Chip 0, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 14 / 11
p0 -1102 ± 3.738
p1 0.01245 ± 0.003365
-1070
-1075
-1080
-1085
-1090
-1095
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

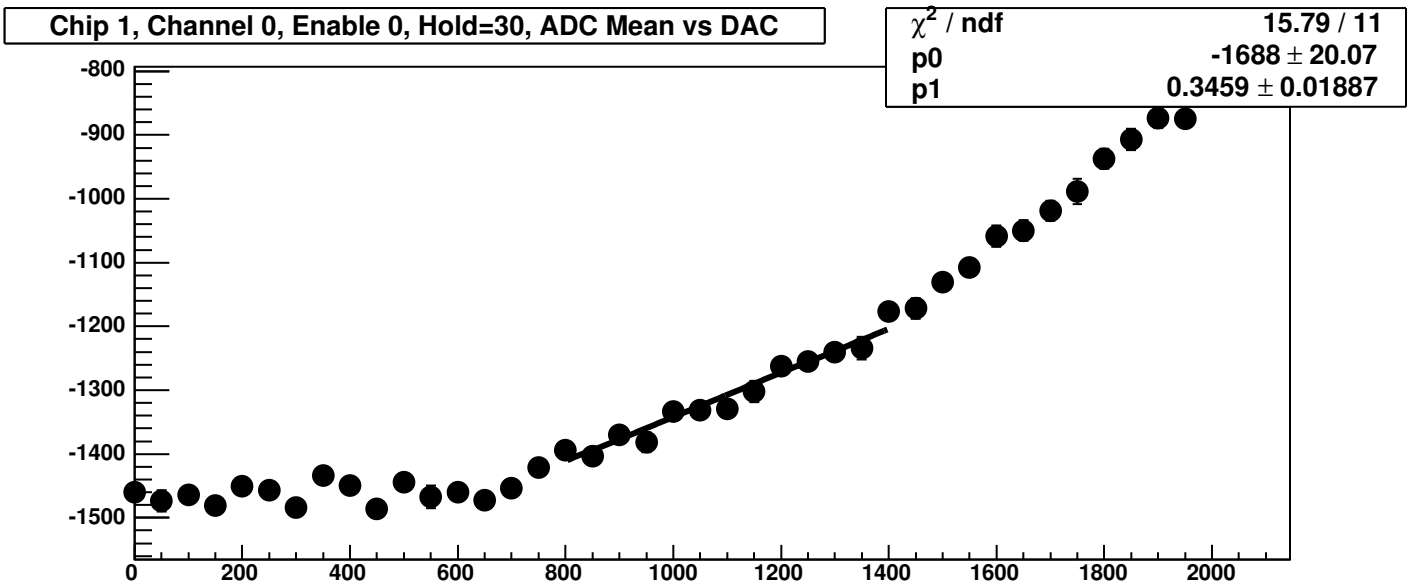

Chip 0, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC
18
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

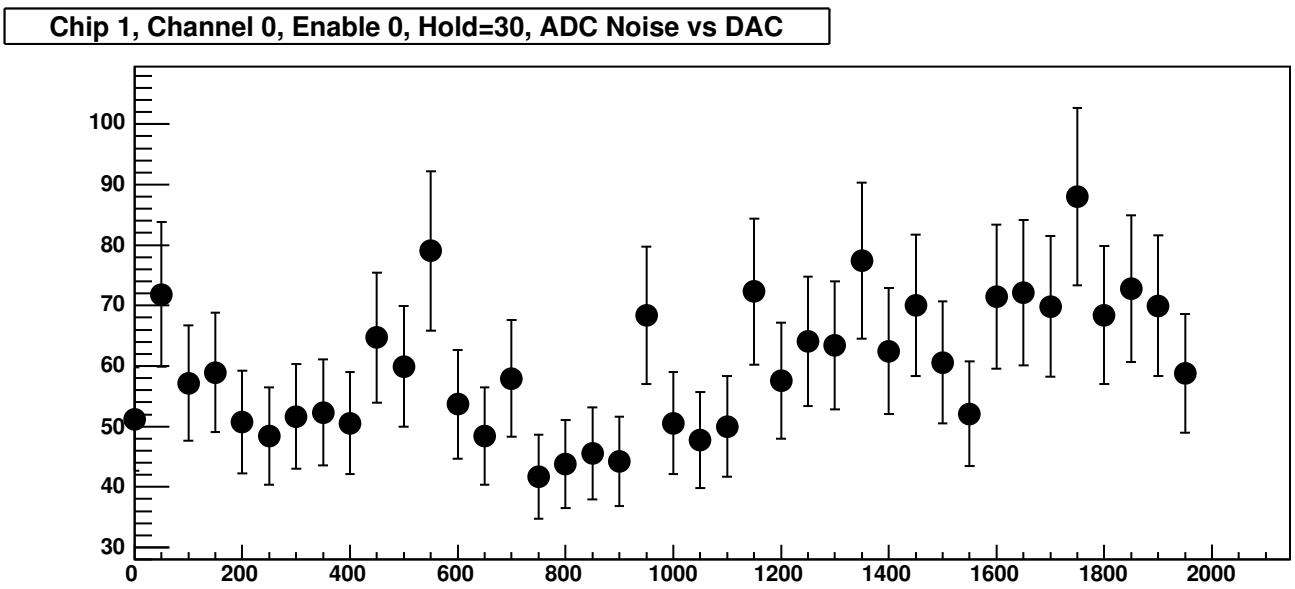

Chip 0, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

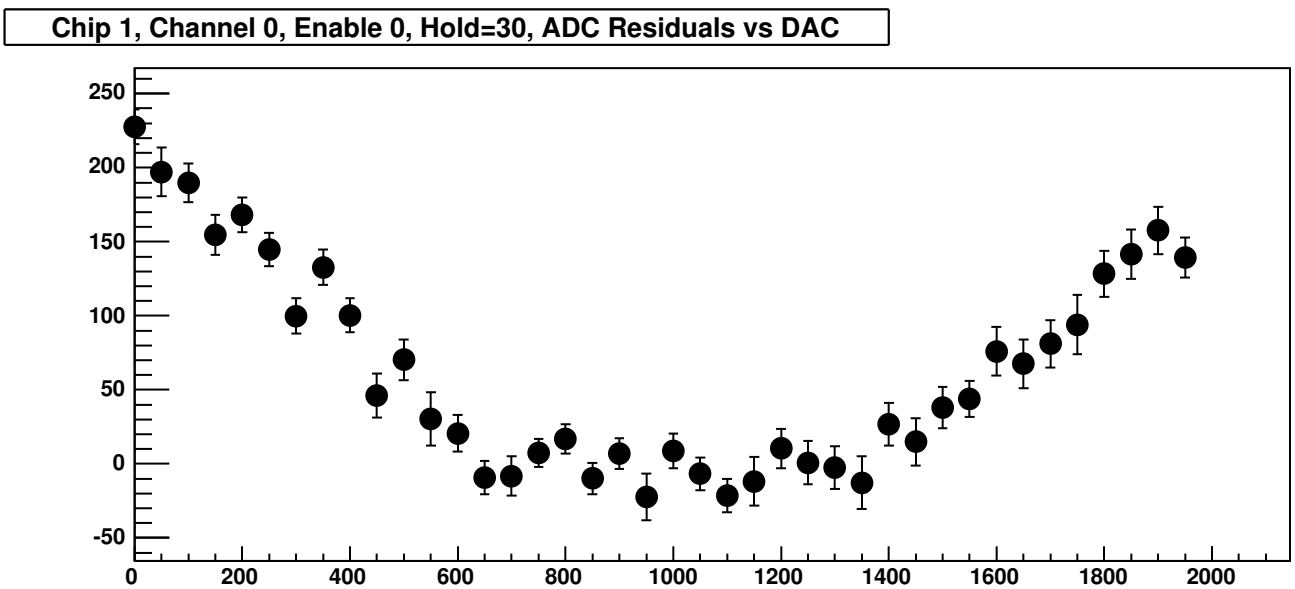

Chip 0, Channel 17, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 27.45 / 11
p0 -1183 ± 9.981
p1 0.1387 ± 0.009699
-920
-940
-960
-980
-1000
-1020
-1040
-1060
-1080
-1100
-1120
0
200
400
600
800
1000
1200
1400
1600
1800
2000

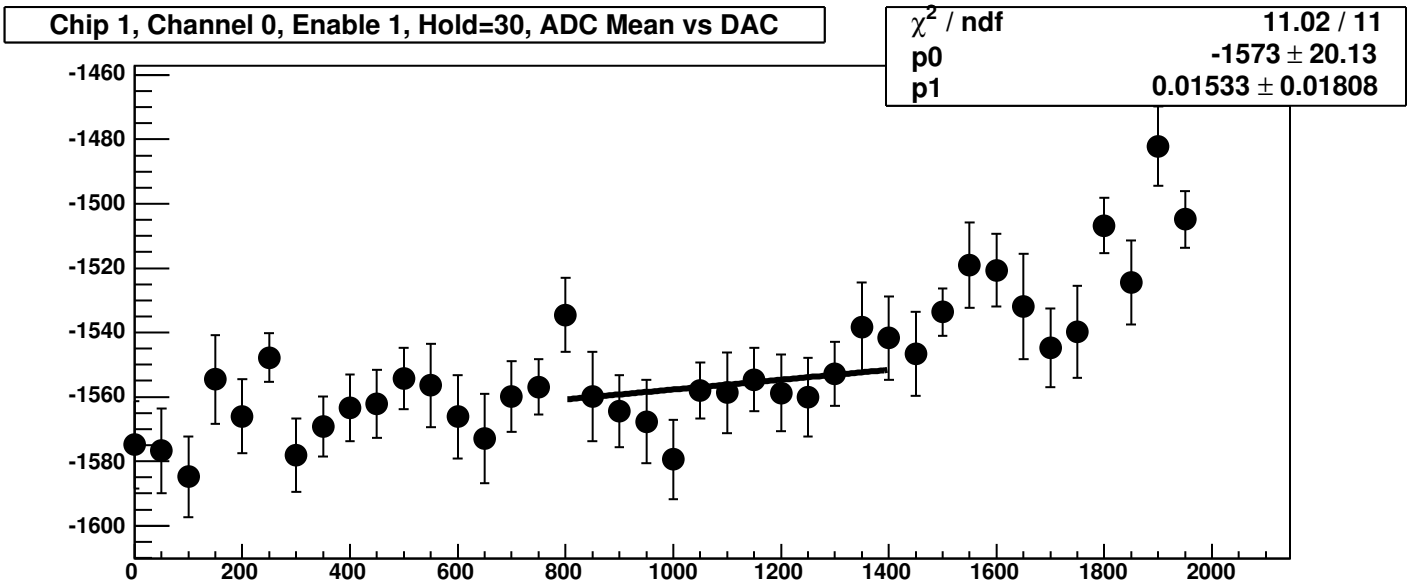

Chip 0, Channel 17, Enable 5, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

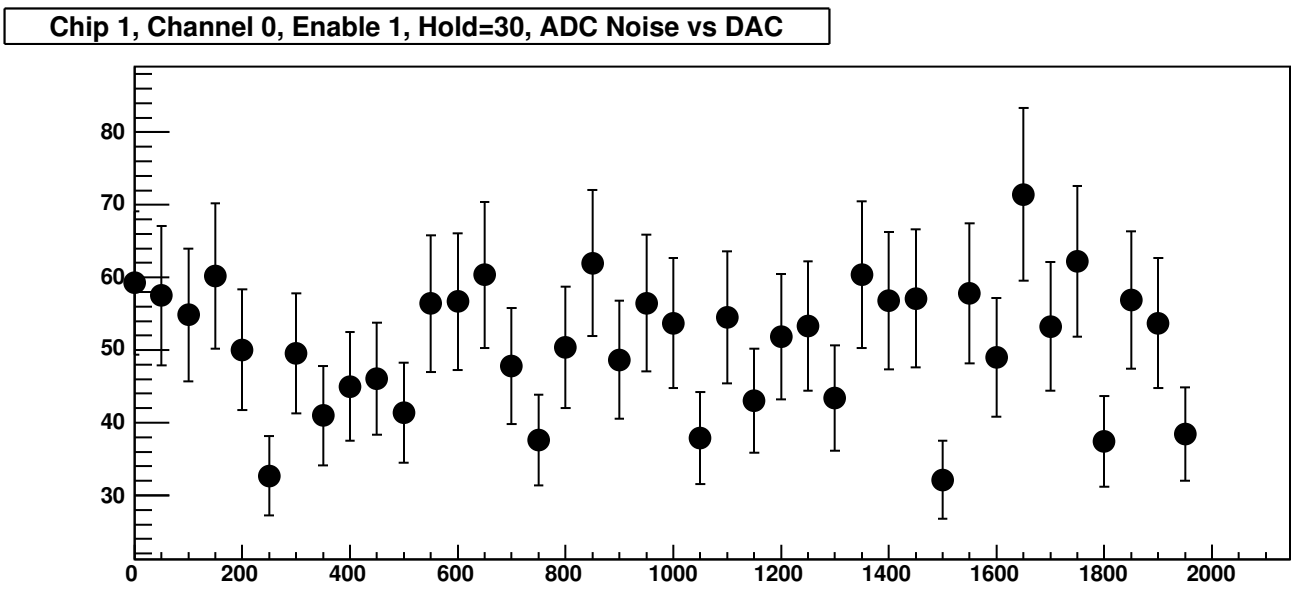

Chip 0, Channel 17, Enable 5, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

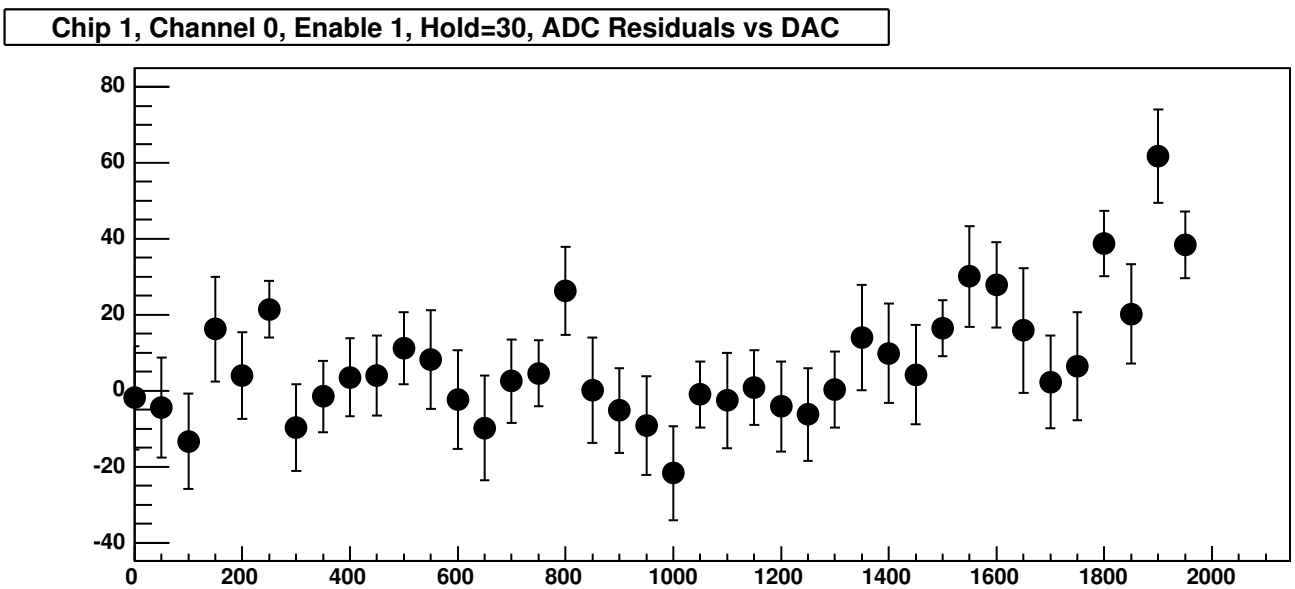

Chip 1, Channel 0, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 15.79 / 11
p0 -1688 ± 20.07
p1 0.3459 ± 0.01887
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

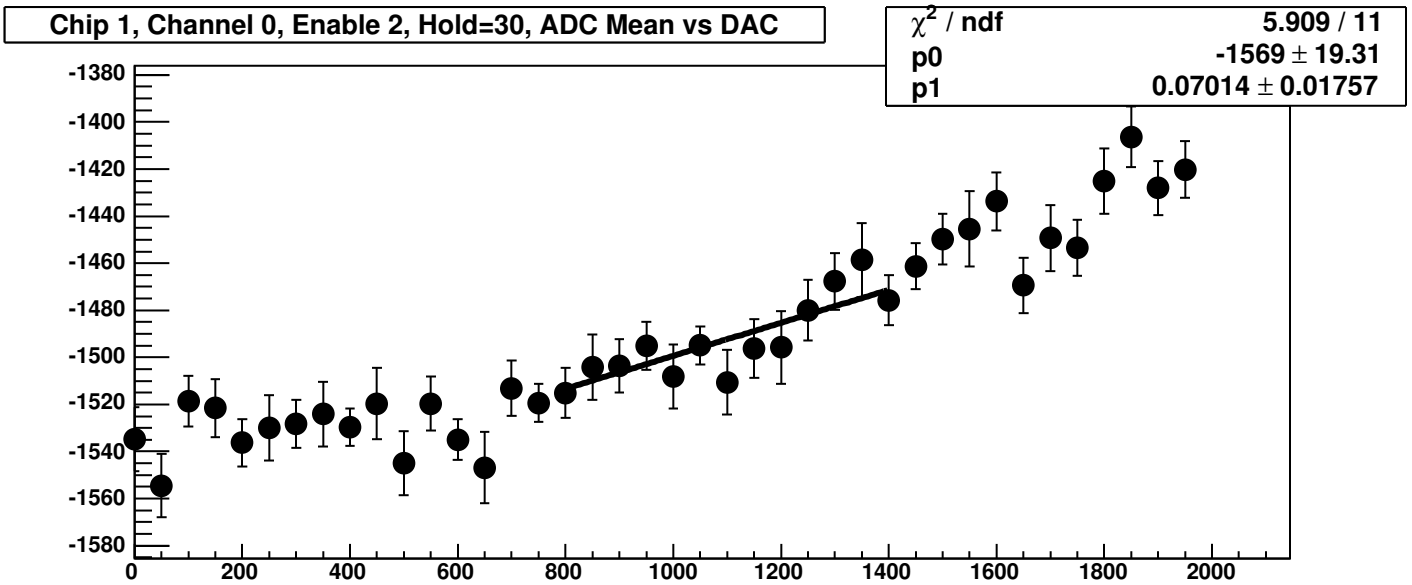

Chip 1, Channel 0, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

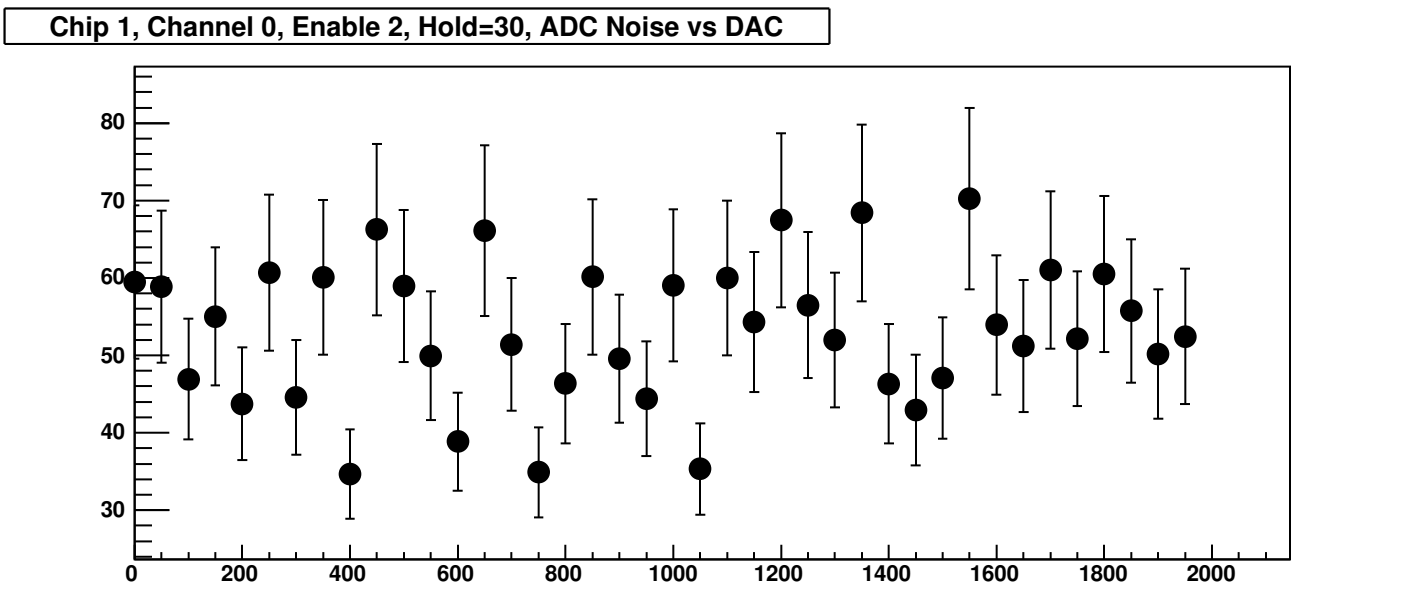

Chip 1, Channel 0, Enable 0, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

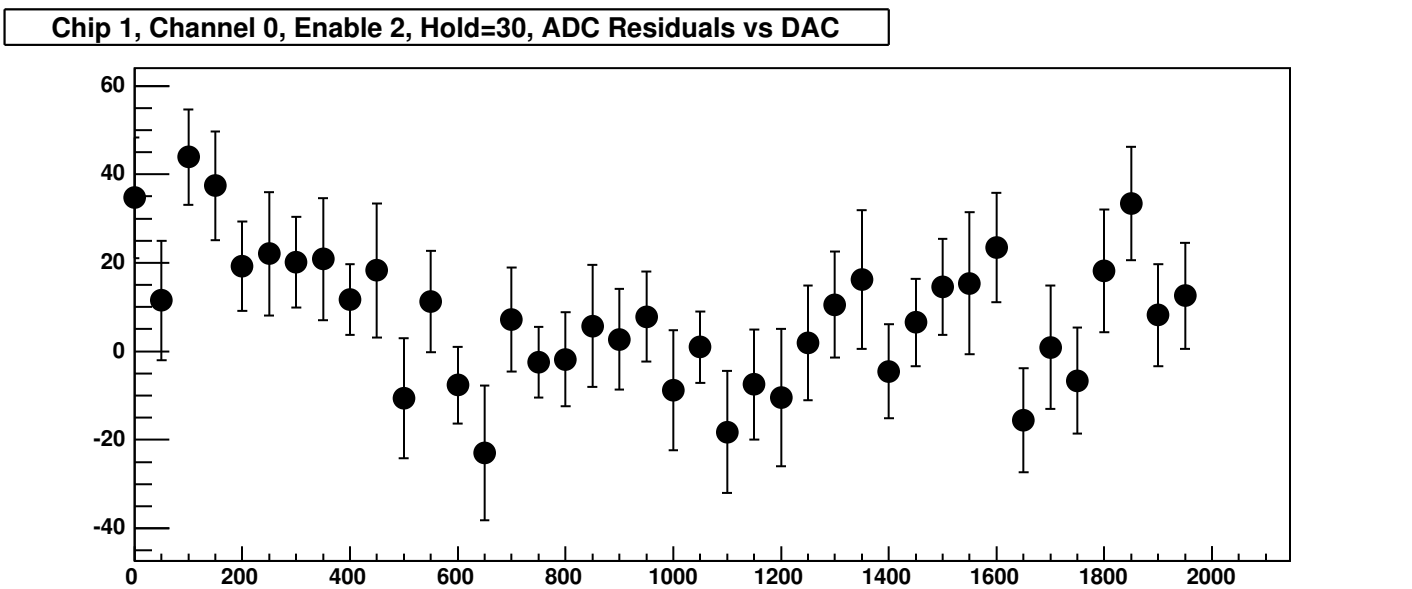

Chip 1, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 11.02 / 11
p0 -1573 ± 20.13
p1 0.01533 ± 0.01808
-1460
-1480
-1500
-1520
-1540
-1560
-1580
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

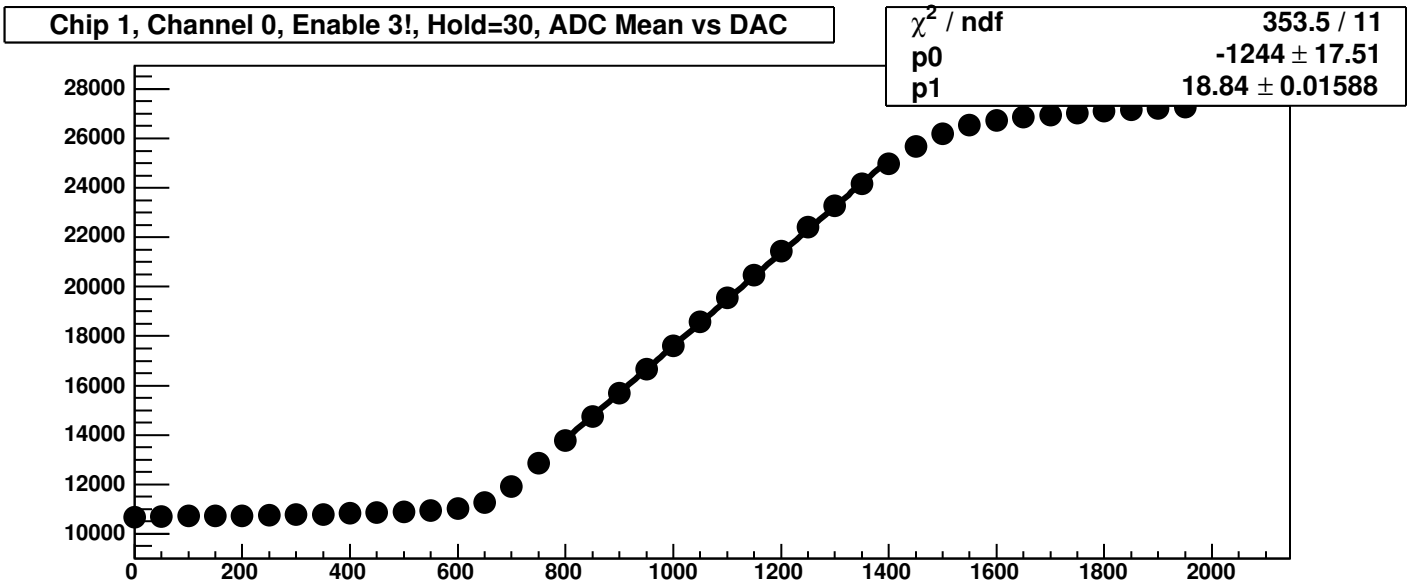

Chip 1, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

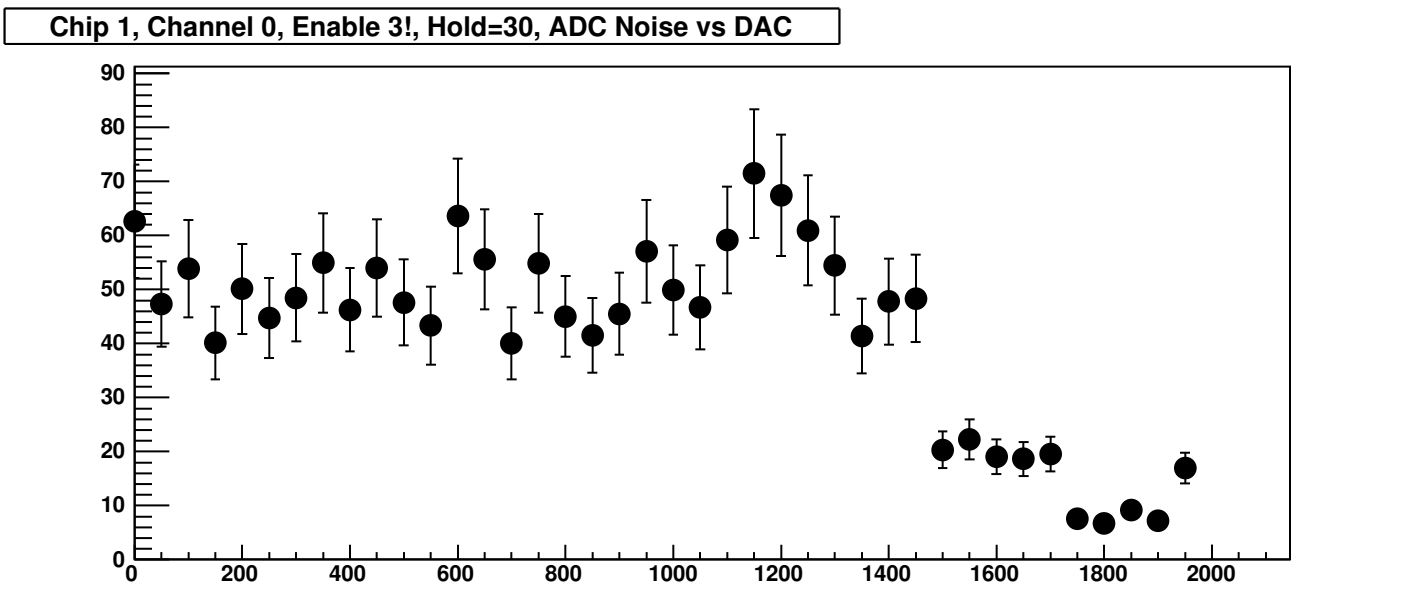

Chip 1, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

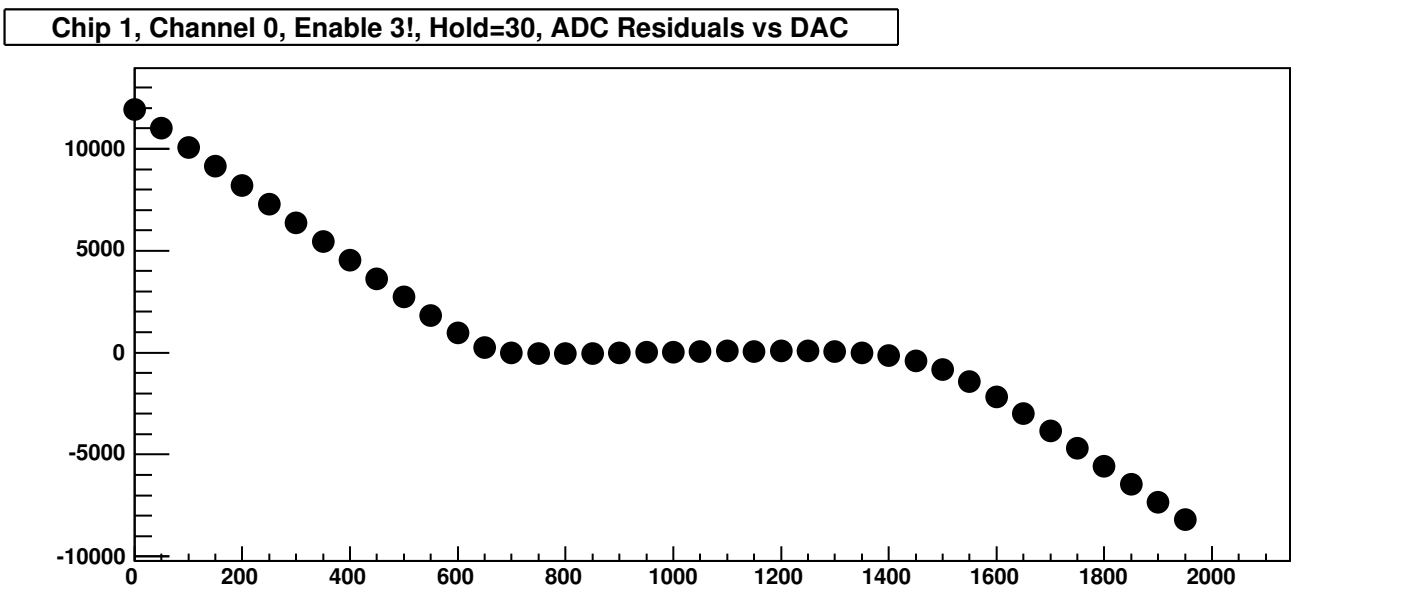

Chip 1, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 5.909 / 11
p0 -1569 ± 19.31
p1 0.07014 ± 0.01757
-1380
-1400
-1420
-1440
-1460
-1480
-1500
-1520
-1540
-1560
-1580
0
200
400
600
800
1000
1200
1400
1600
1800
2000

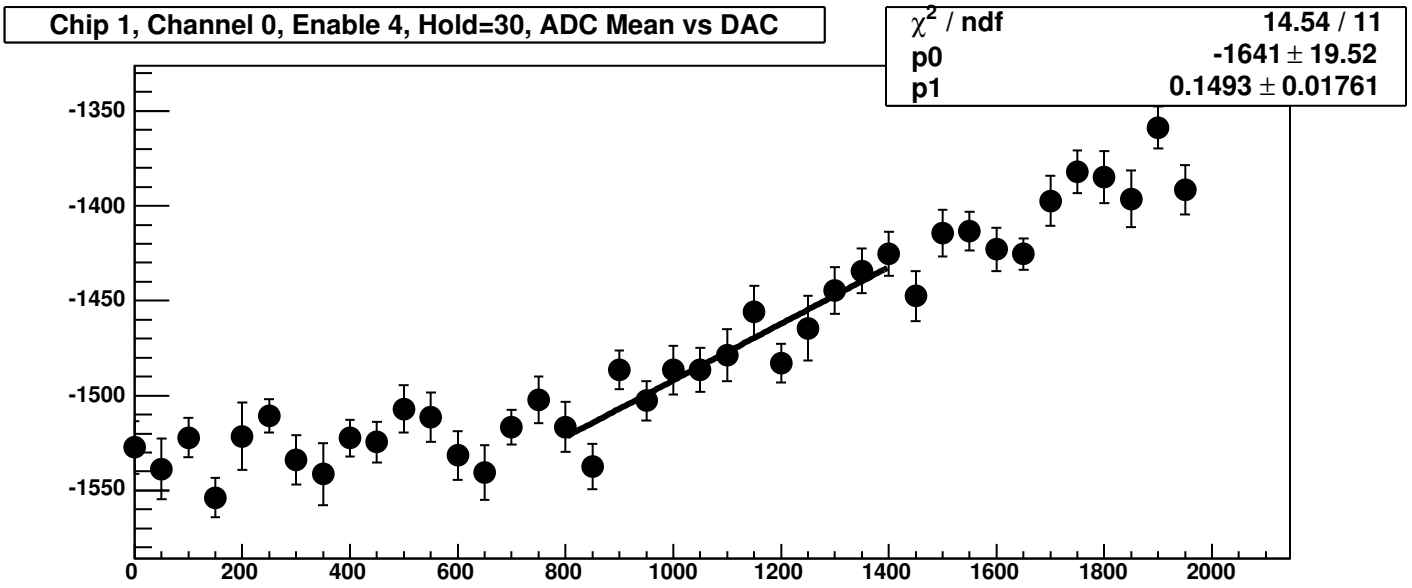

Chip 1, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

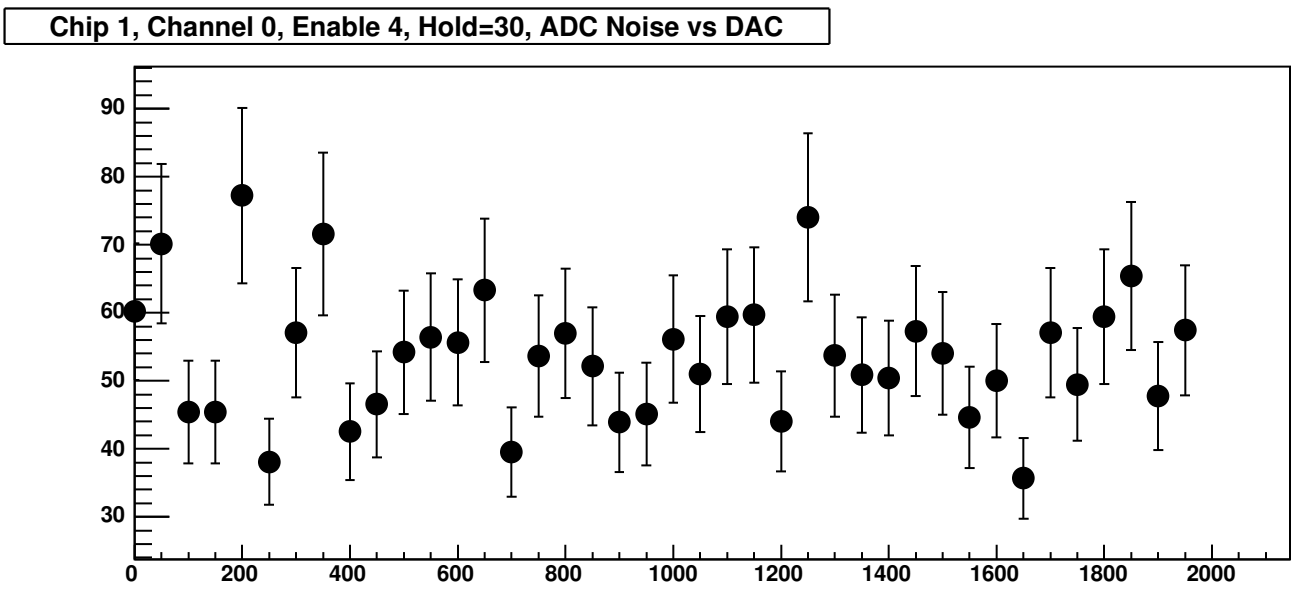

Chip 1, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

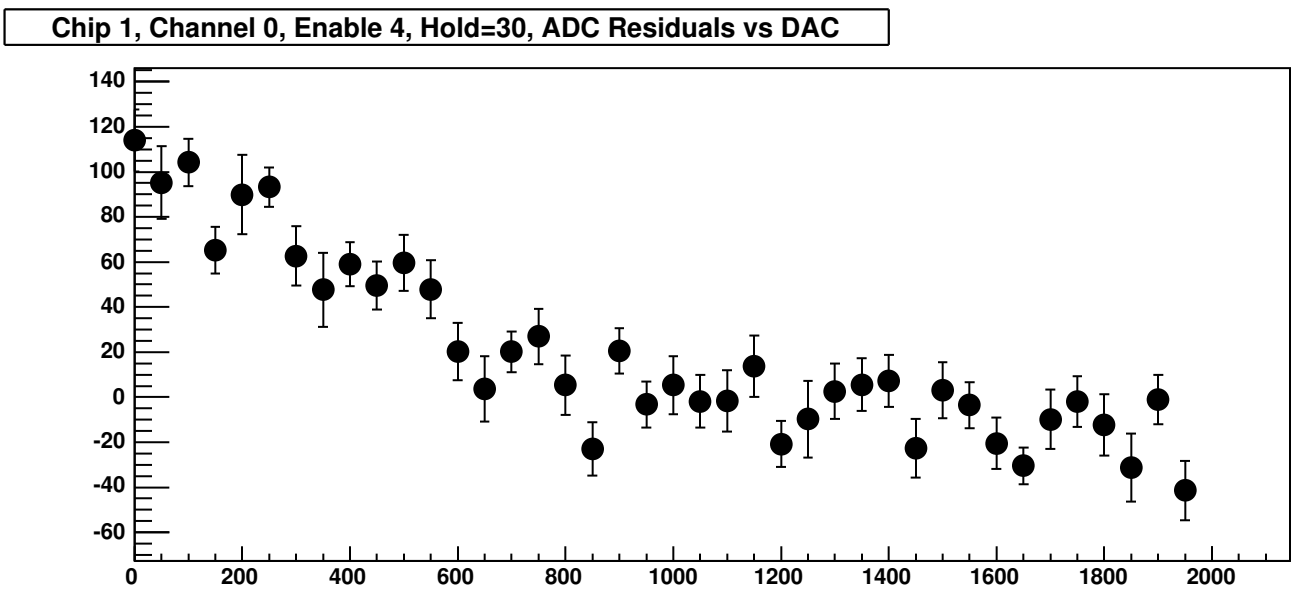

Chip 1, Channel 0, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 353.5 / 11
p0 -1244 ± 17.51
p1 18.84 ± 0.01588
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

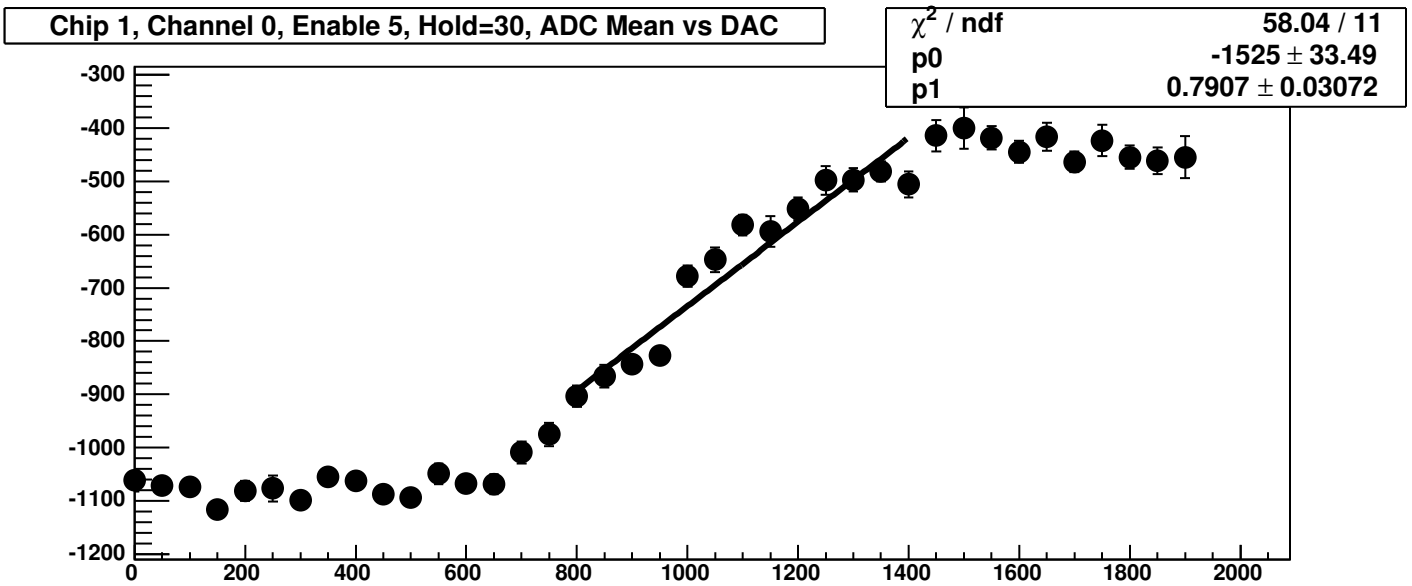

Chip 1, Channel 0, Enable 3!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

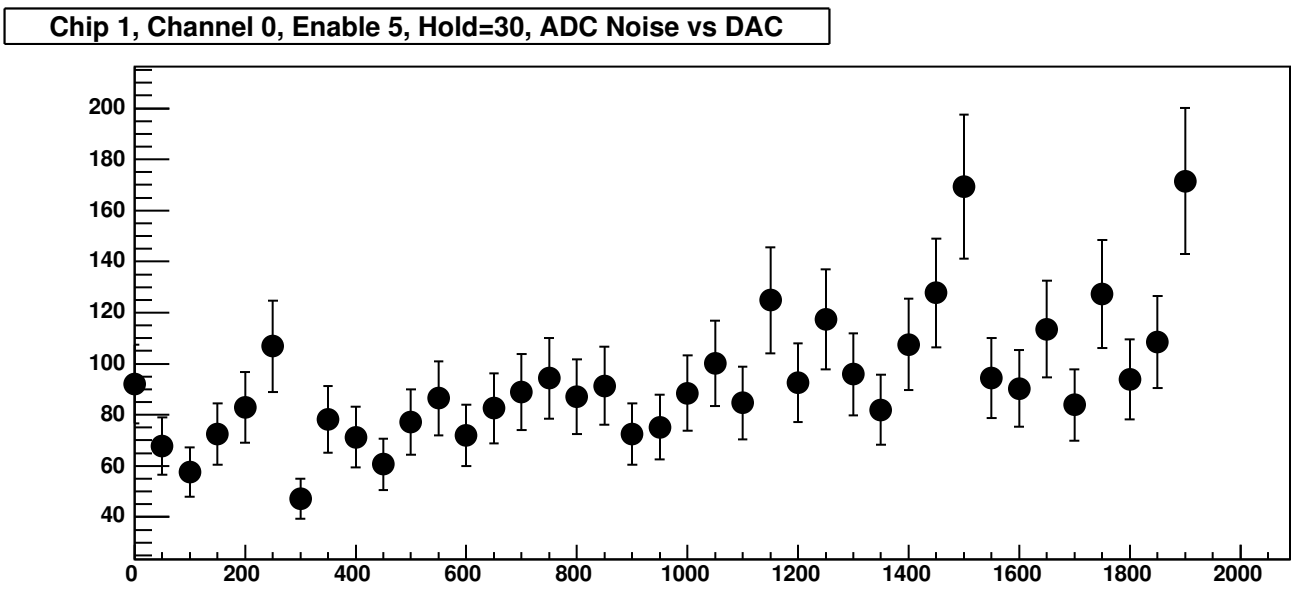

Chip 1, Channel 0, Enable 3!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

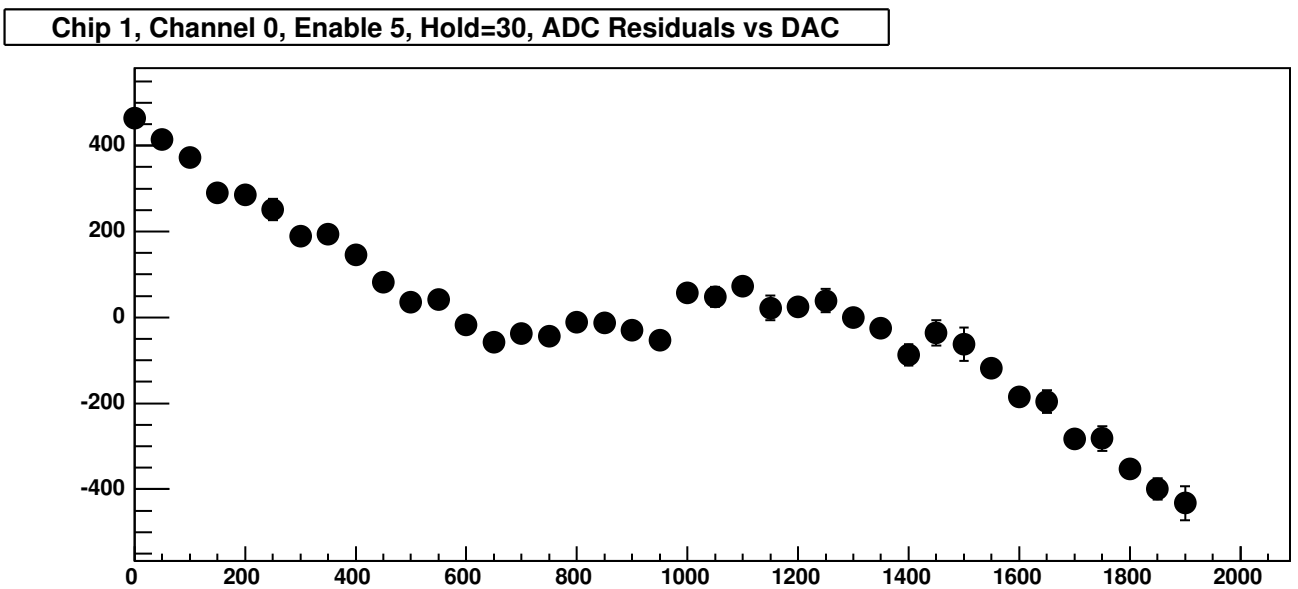

Chip 1, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 14.54 / 11
p0 -1641 ± 19.52
p1 0.1493 ± 0.01761
-1350
-1400
-1450
-1500
-1550
0
200
400
600
800
1000
1200
1400
1600
1800
2000

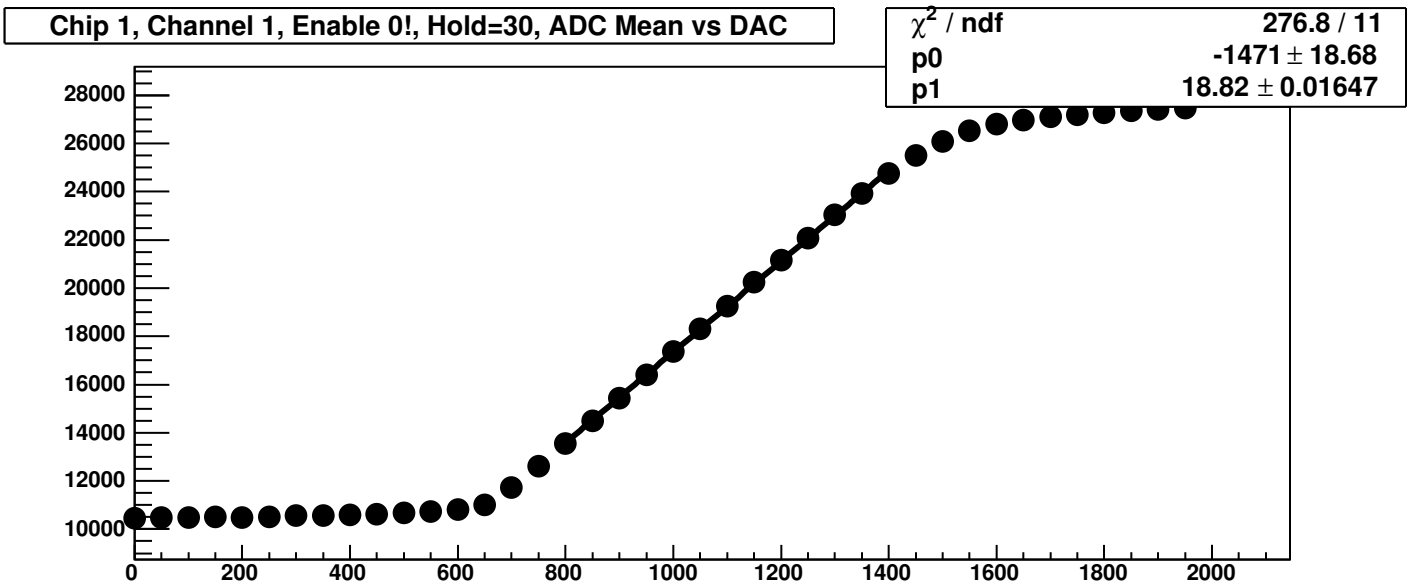

Chip 1, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

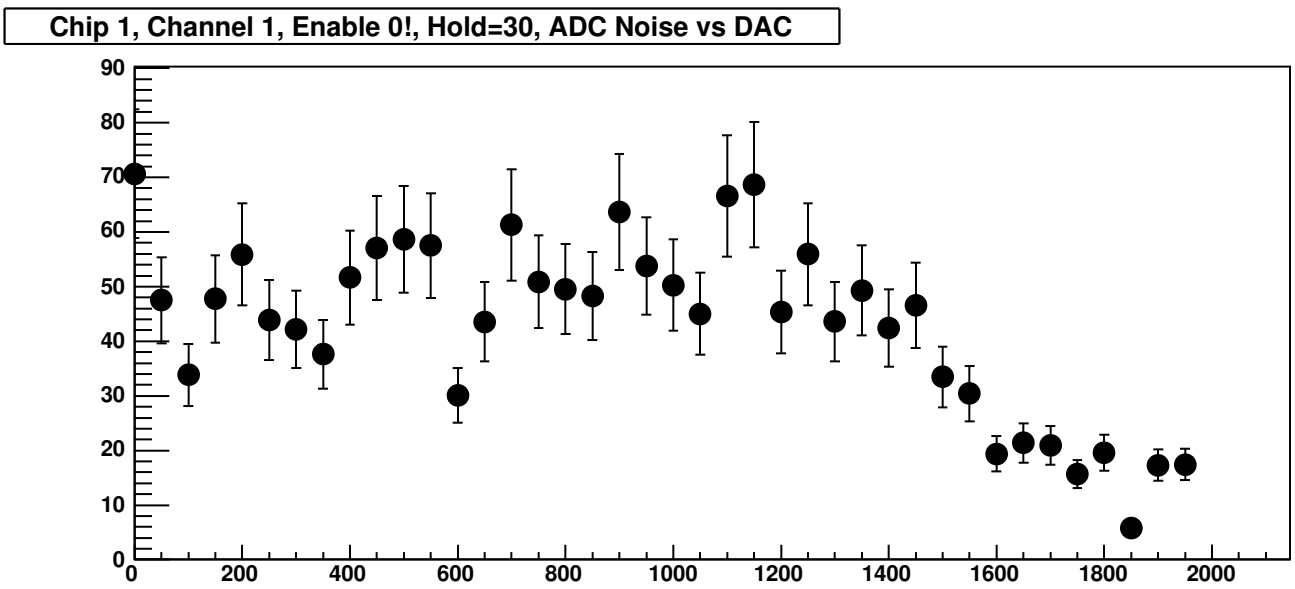

Chip 1, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

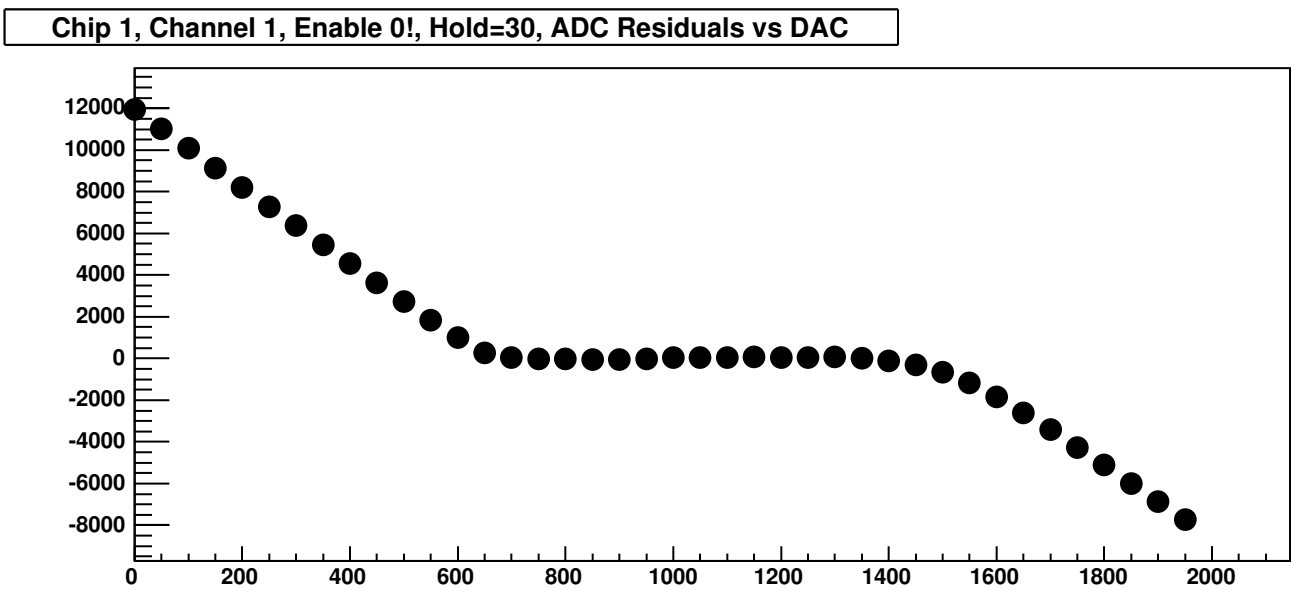

Chip 1, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 58.04 / 11
p0 -1525 ± 33.49
p1 0.7907 ± 0.03072
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

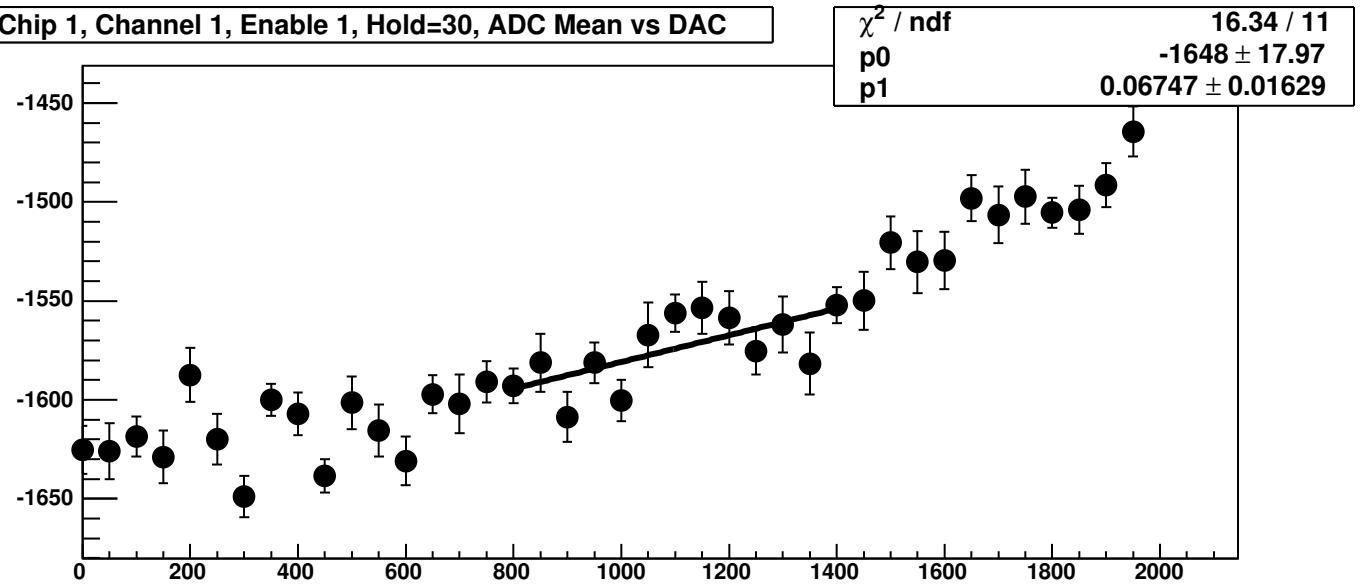

Chip 1, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 1, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 276.8 / 11
p0 -1471 ± 18.68
p1 18.82 ± 0.01647
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

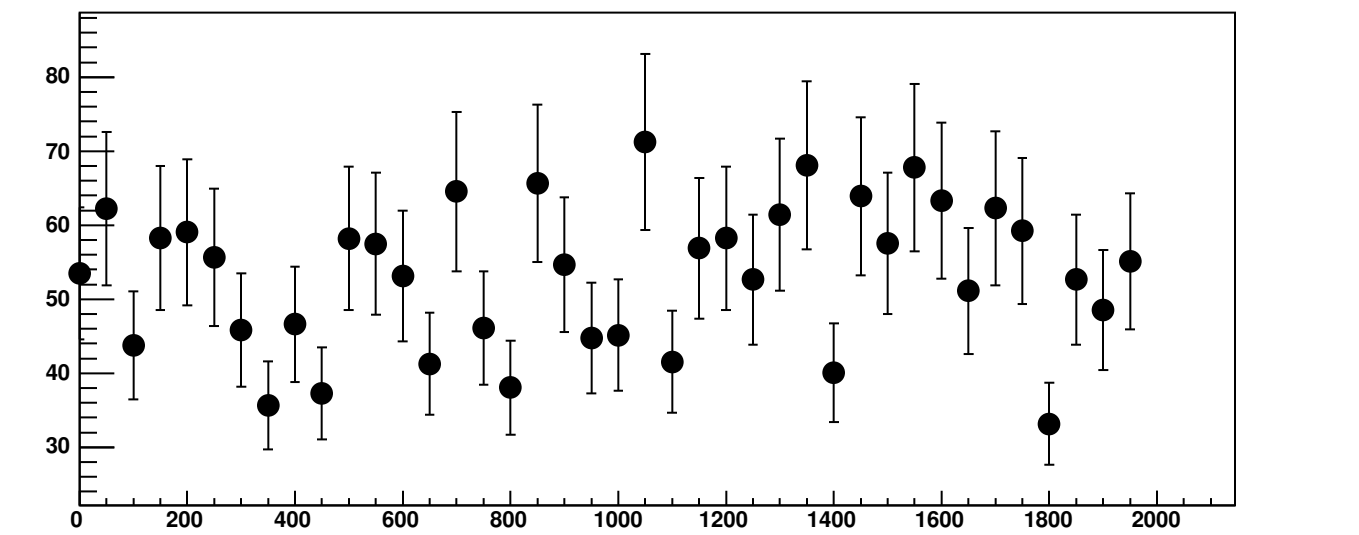

Chip 1, Channel 1, Enable 0!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 1, Enable 0!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

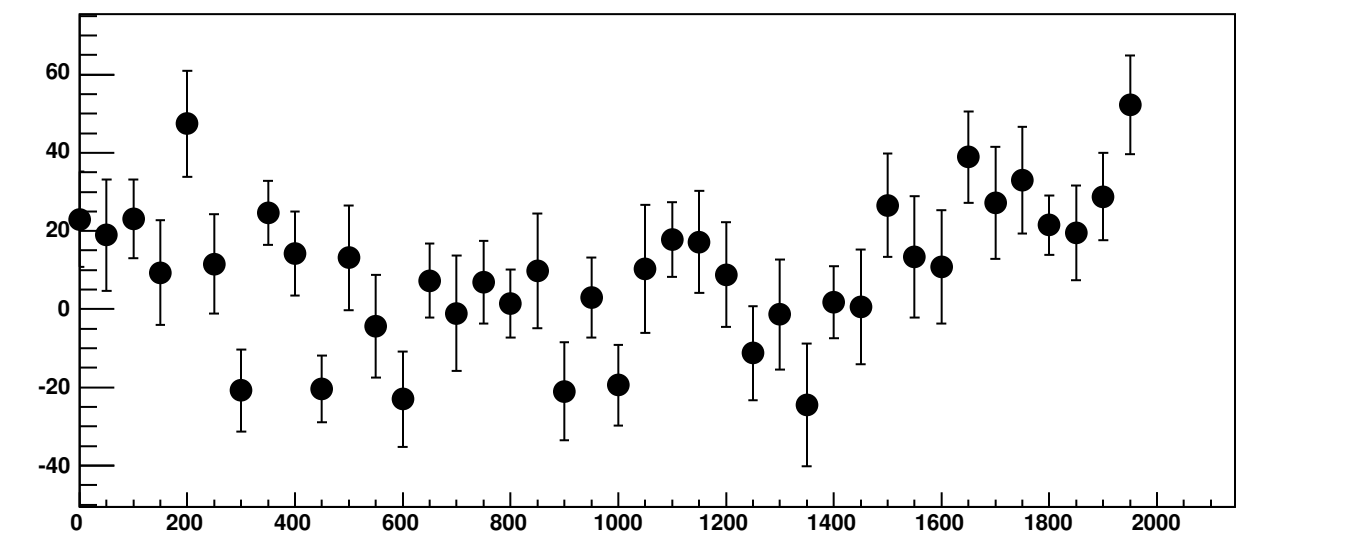
Chip 1, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC

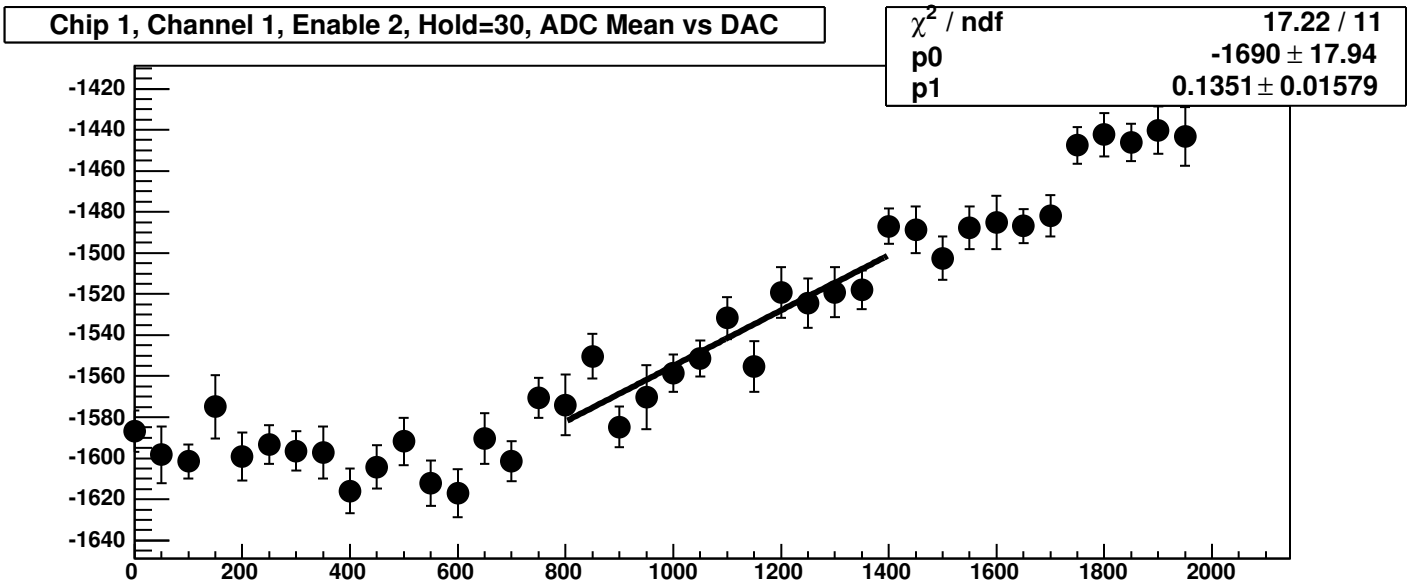


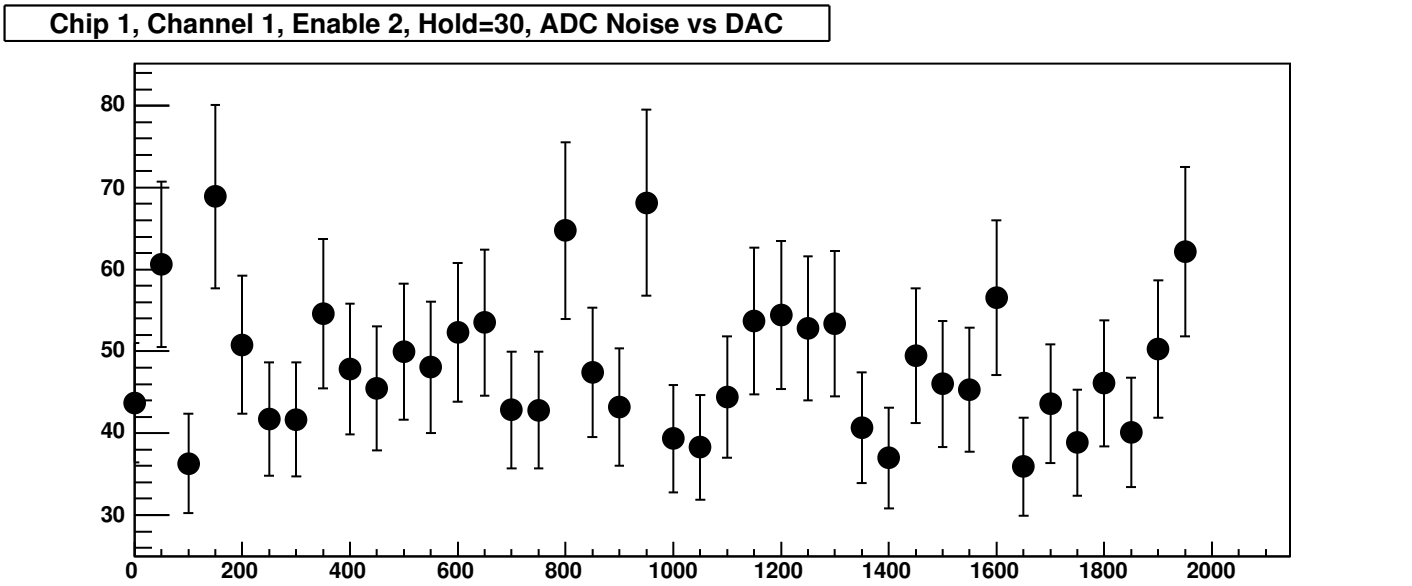
Chip 1, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC

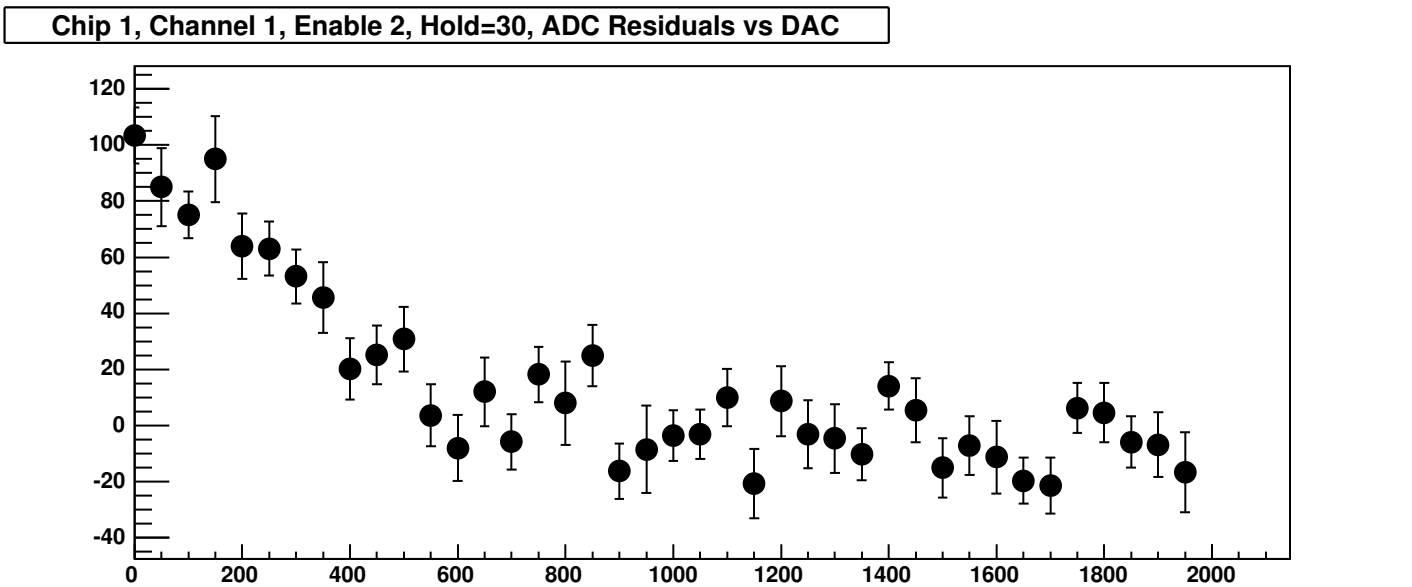


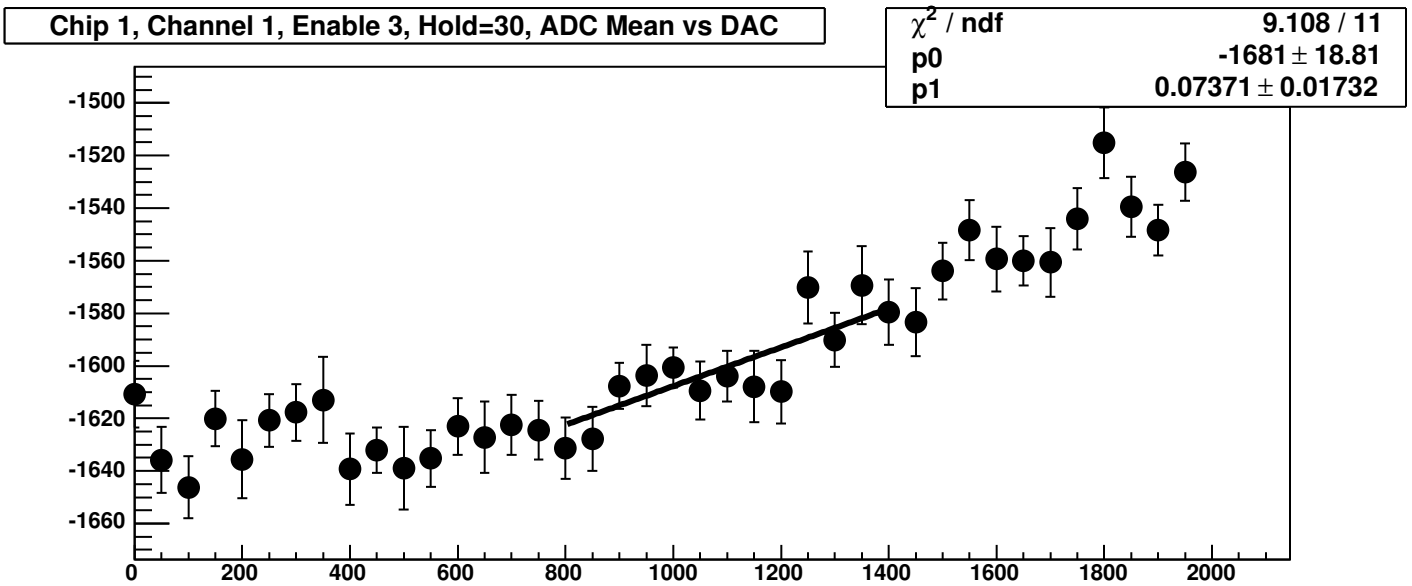
Chip 1, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC

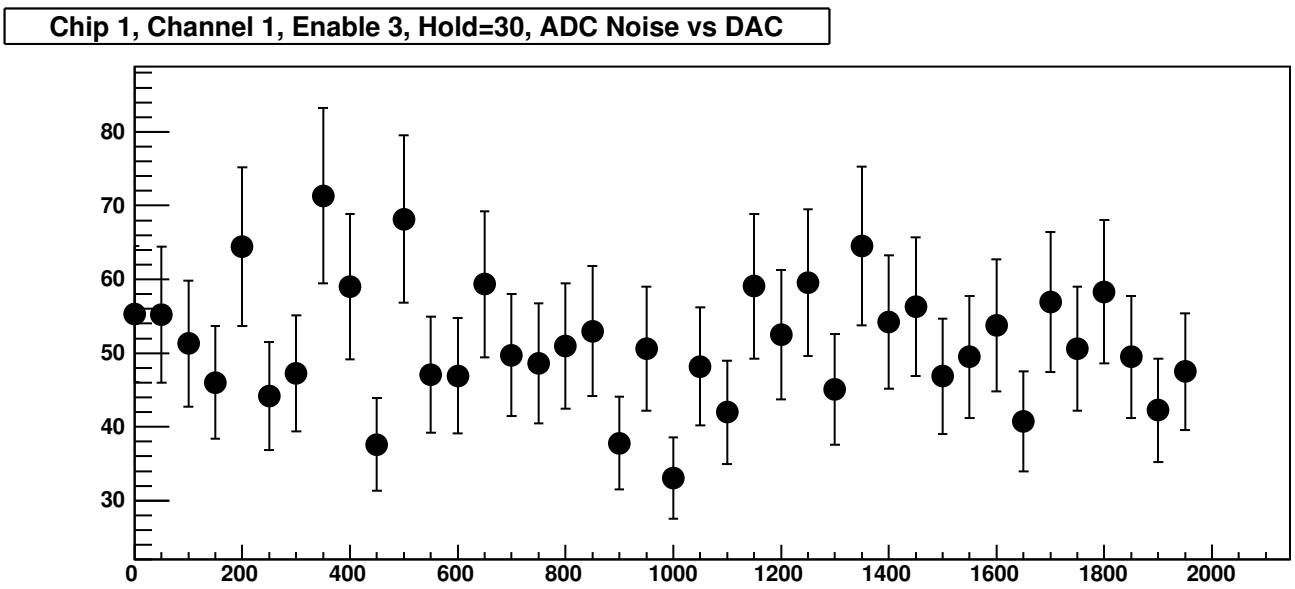


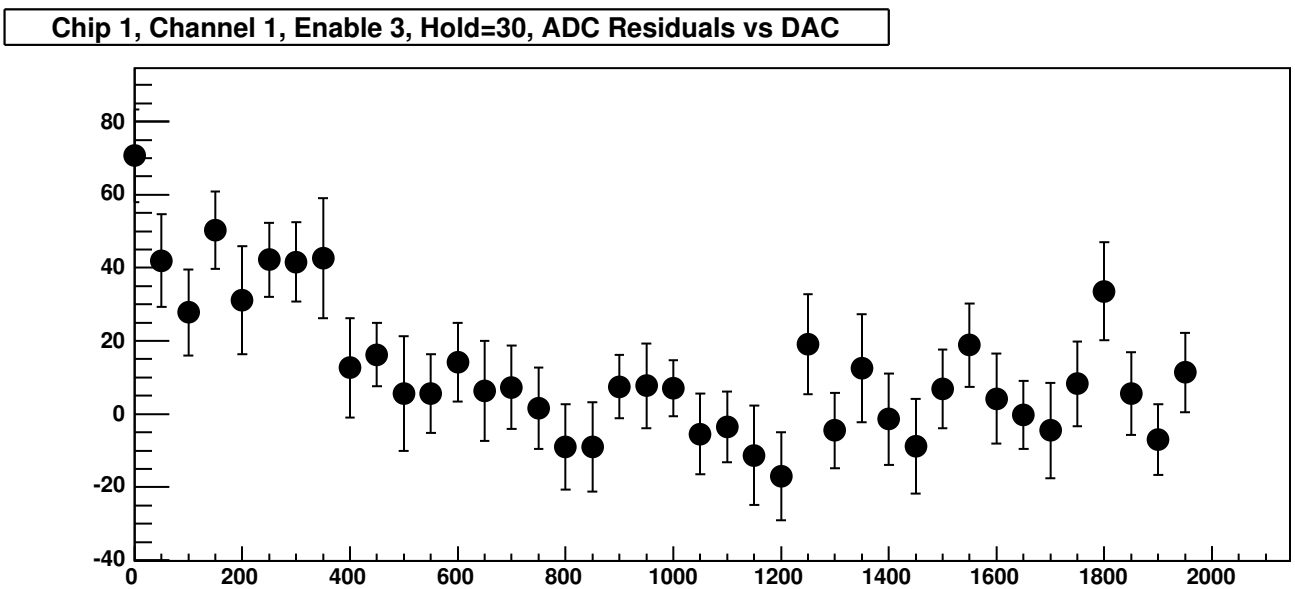

Chip 1, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 17.22 / 11
p0 -1690 ± 17.94
p1 0.1351 ± 0.01579
-1420
-1440
-1460
-1480
-1500
-1520
-1540
-1560
-1580
-1600
-1620
-1640
0
200
400
600
800
1000
1200
1400
1600
1800
2000

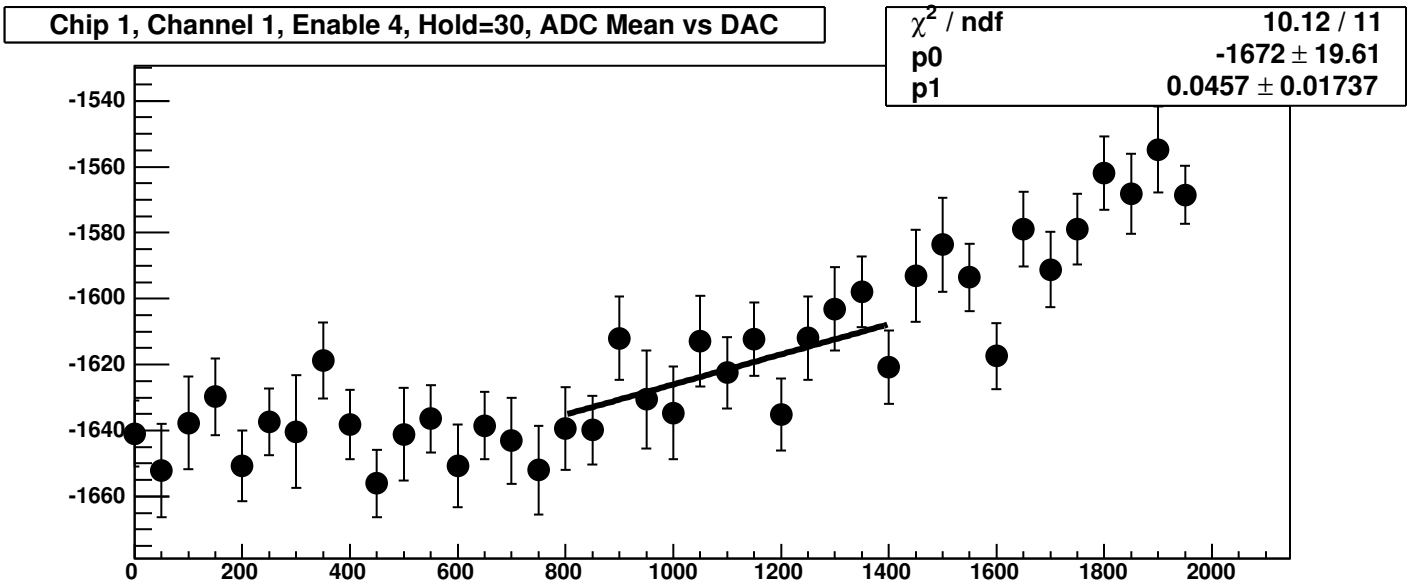

Chip 1, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

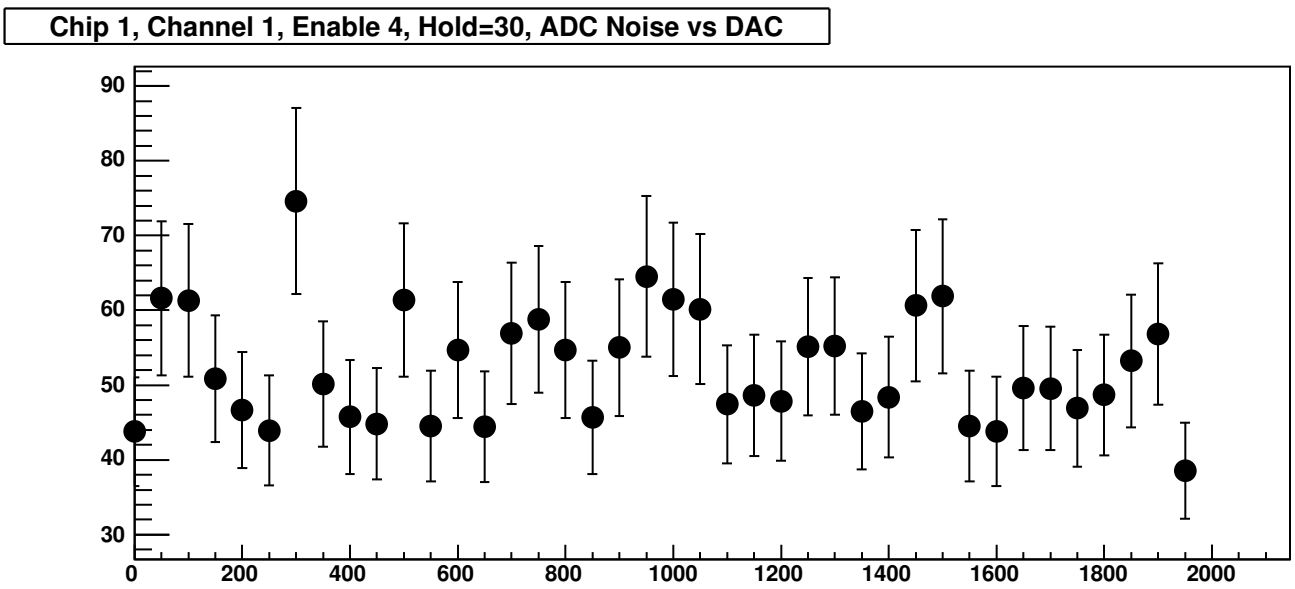

Chip 1, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

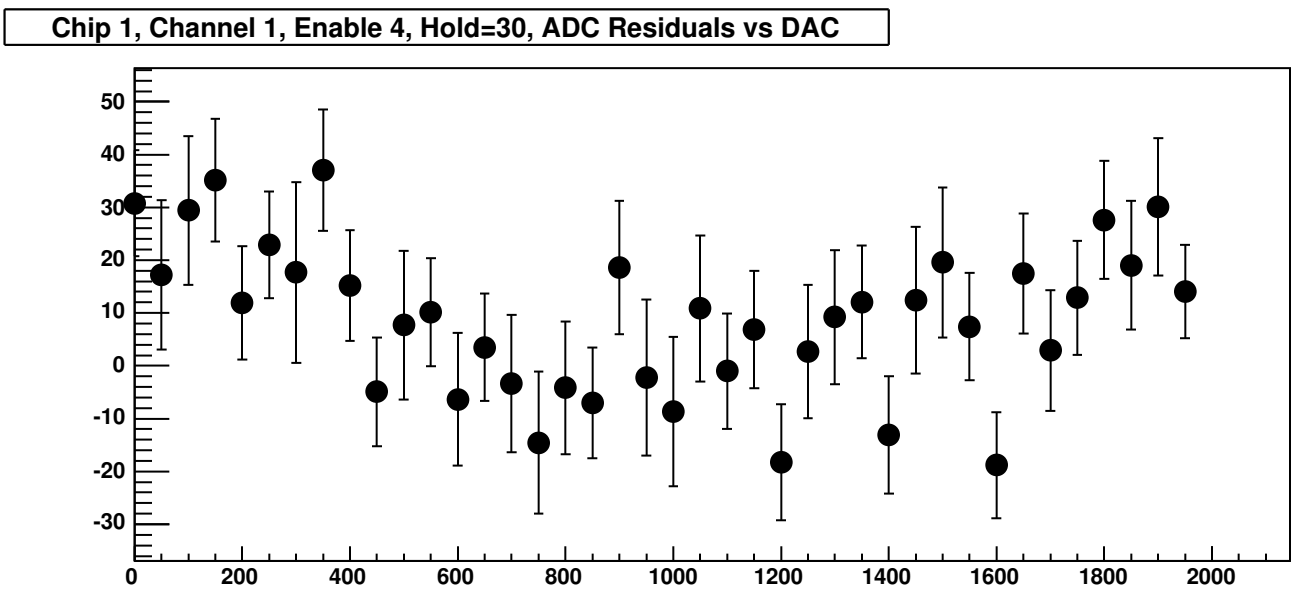

Chip 1, Channel 1, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 9.108 / 11
p0 -1681 ± 18.81
p1 0.07371 ± 0.01732
-1500
-1520
-1540
-1560
-1580
-1600
-1620
-1640
-1660
0
200
400
600
800
1000
1200
1400
1600
1800
2000

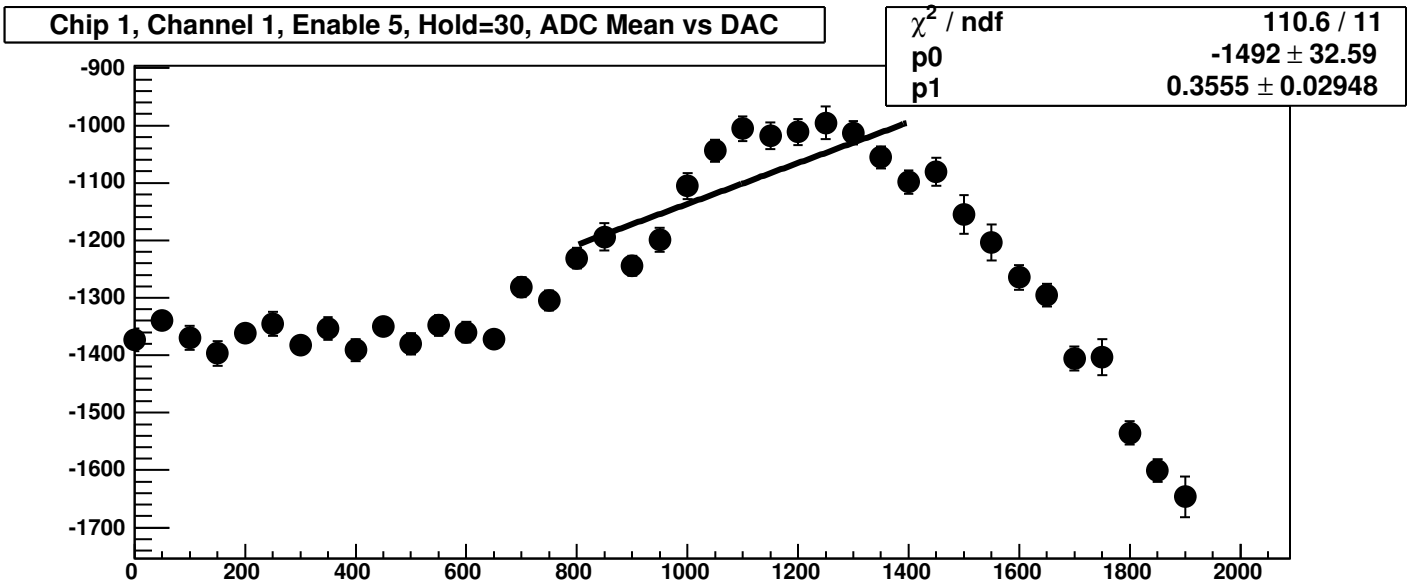

Chip 1, Channel 1, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

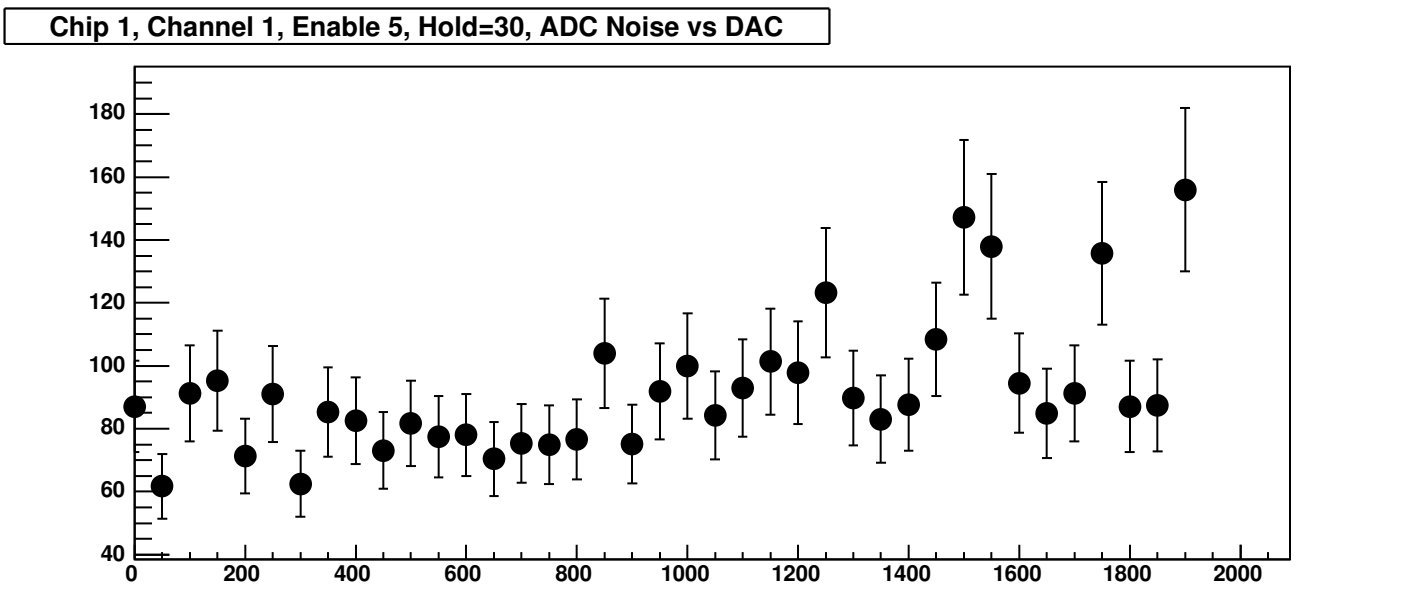

Chip 1, Channel 1, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

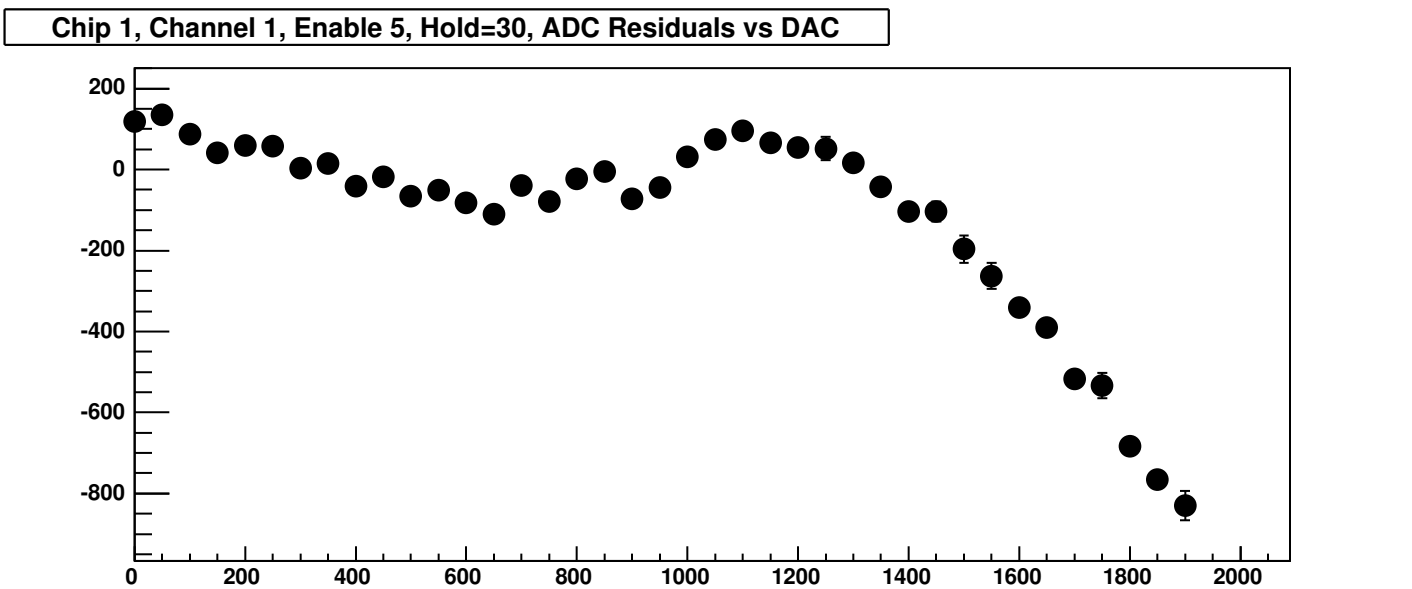

Chip 1, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 10.12 / 11
p0 -1672 ± 19.61
p1 0.0457 ± 0.01737
-1540
-1560
-1580
-1600
-1620
-1640
-1660
0
200
400
600
800
1000
1200
1400
1600
1800
2000

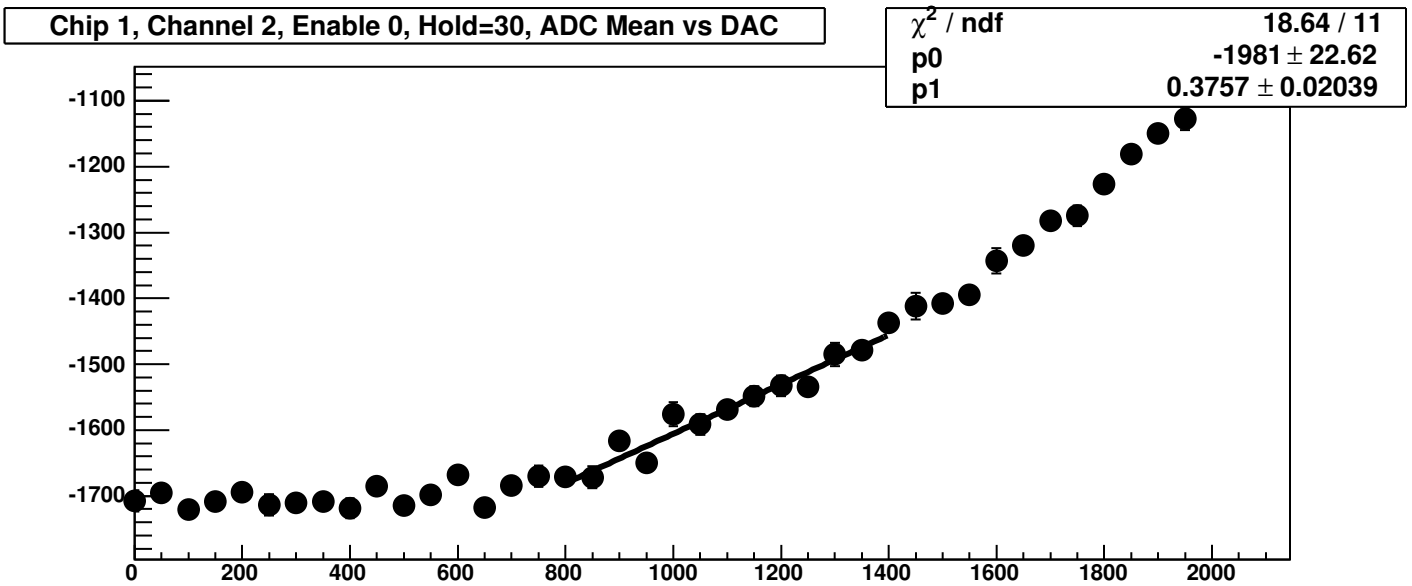

Chip 1, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

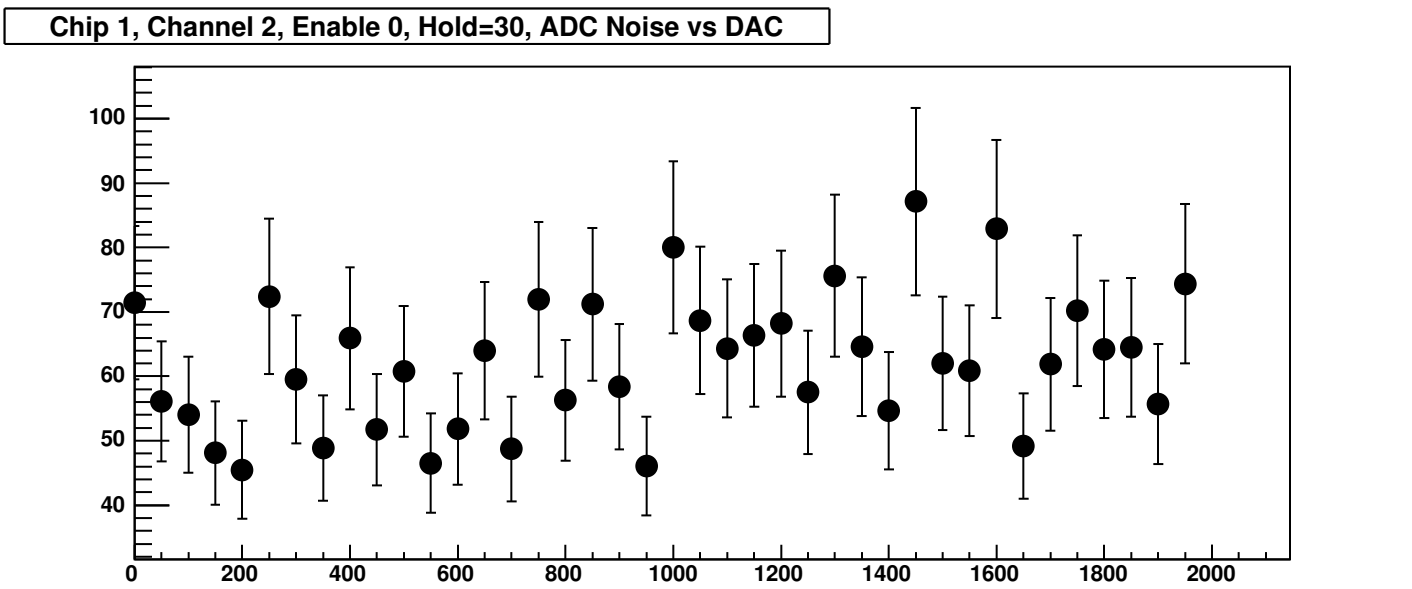

Chip 1, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
50
40
30
20
10
0
-10
-20
-30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

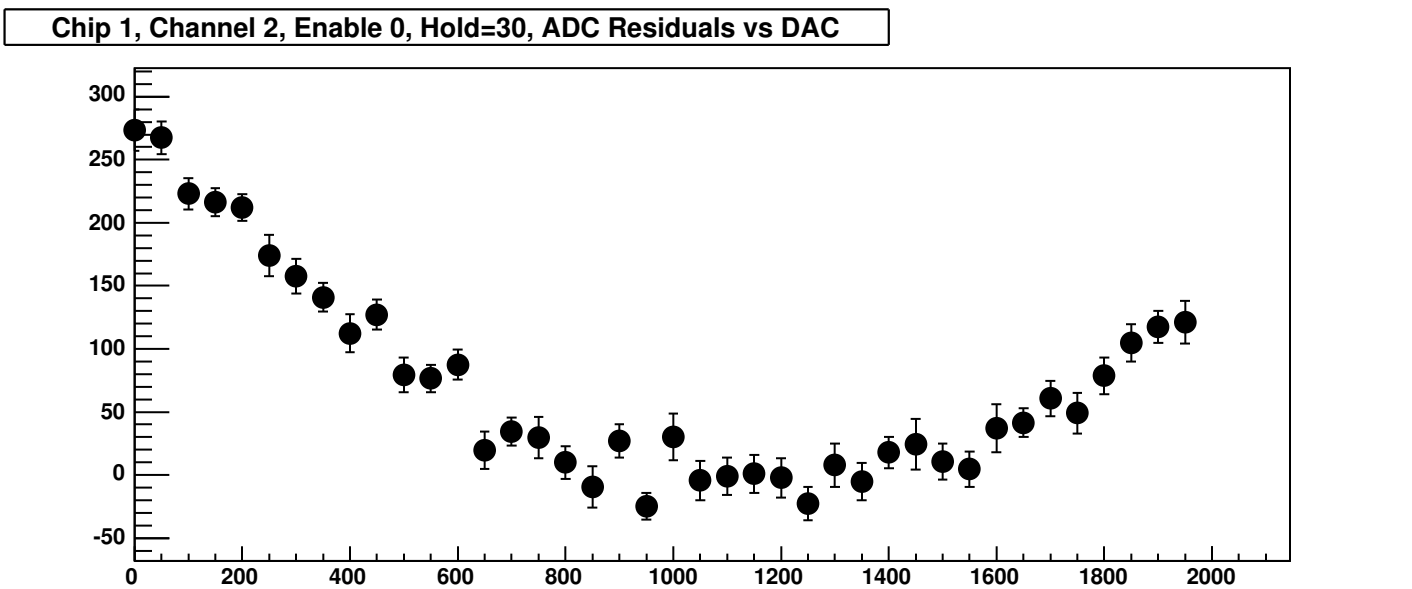

Chip 1, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 110.6 / 11
p0 -1492 ± 32.59
p1 0.3555 ± 0.02948
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

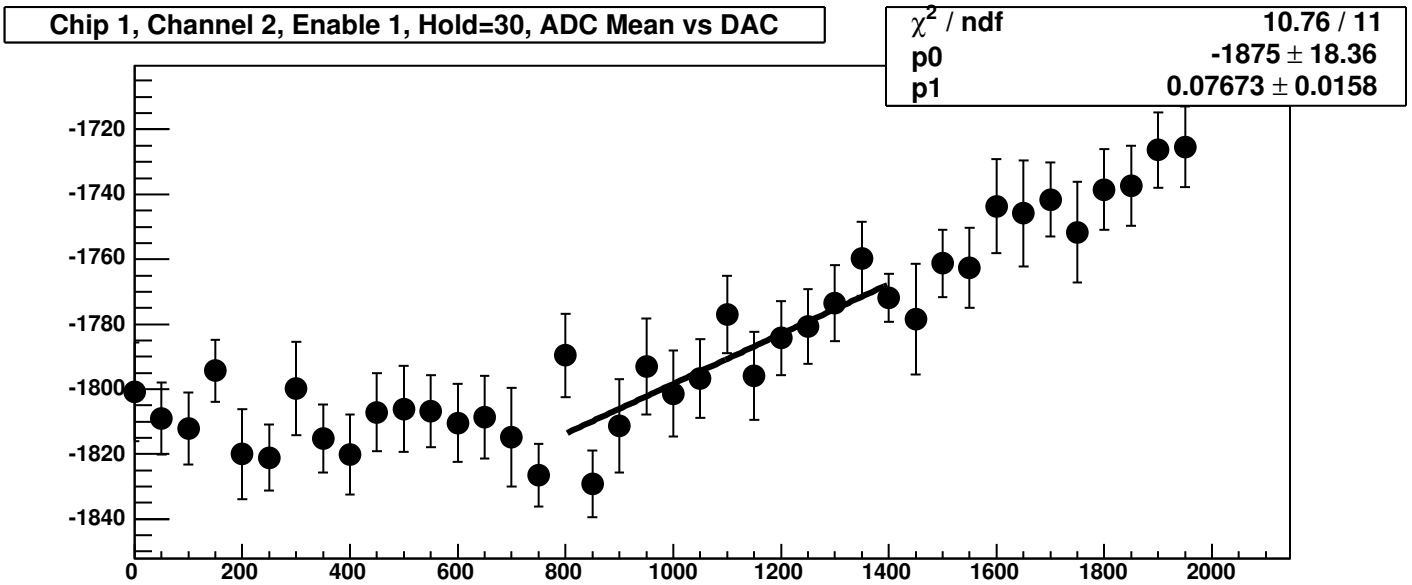

Chip 1, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

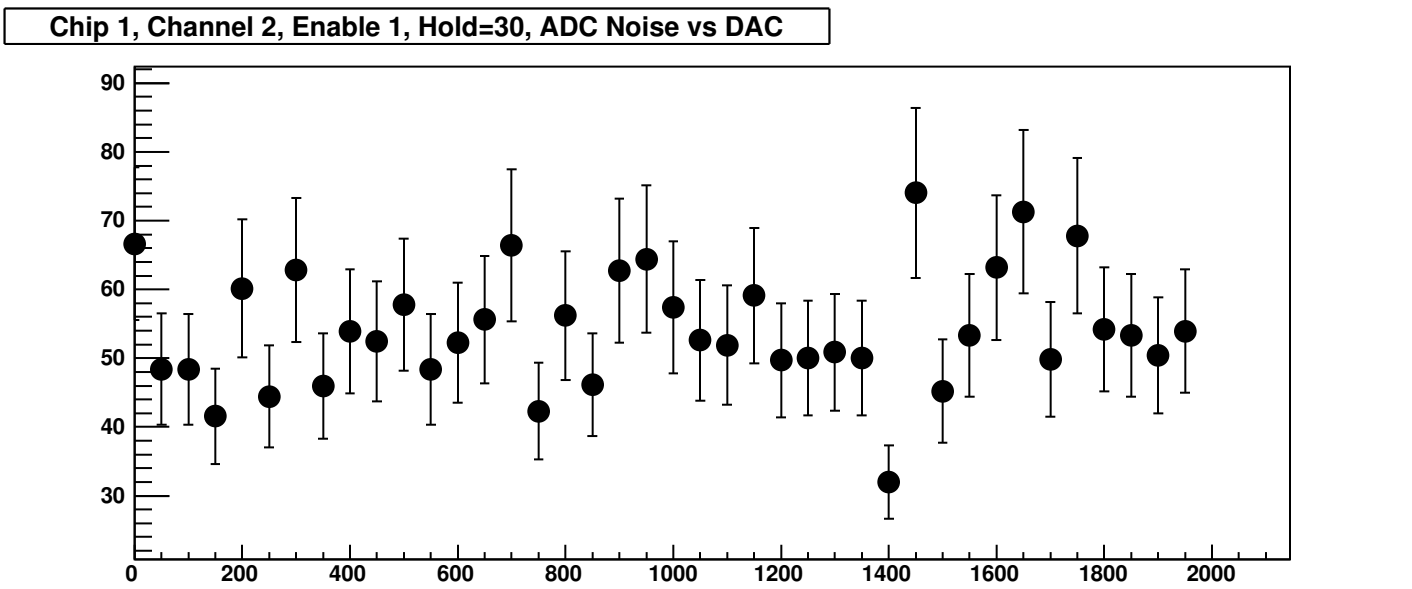

Chip 1, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

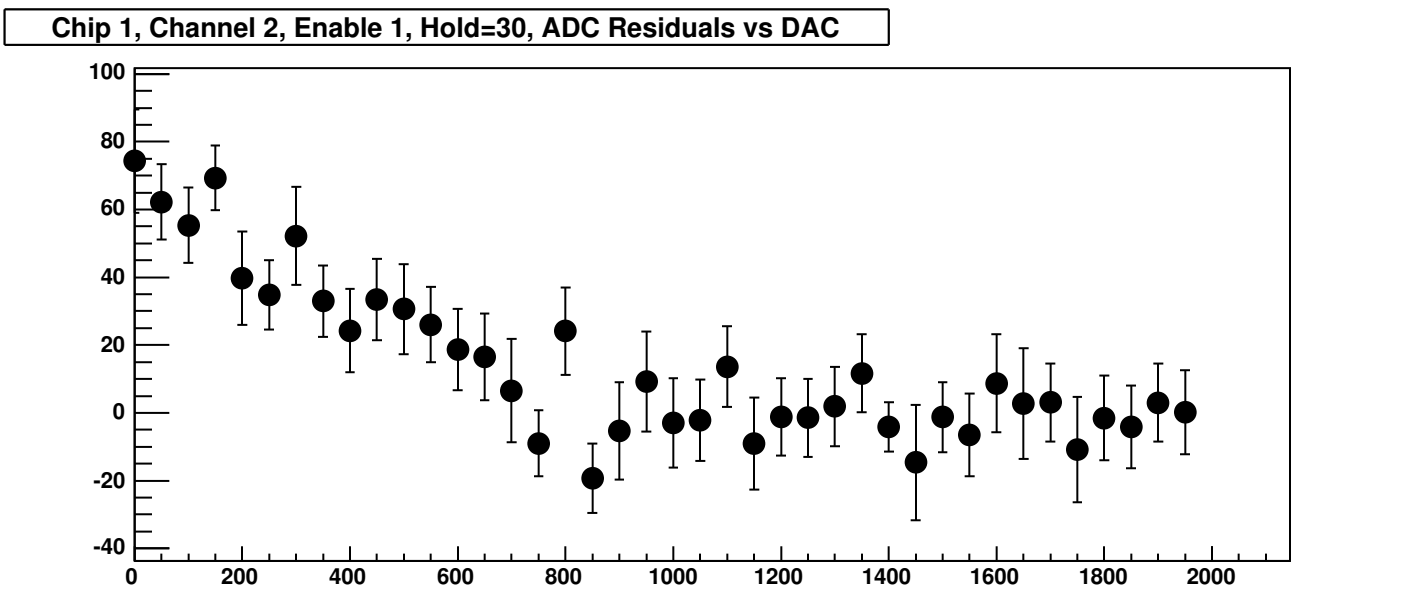

Chip 1, Channel 2, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 18.64 / 11
p0 -1981 ± 22.62
p1 0.3757 ± 0.02039
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

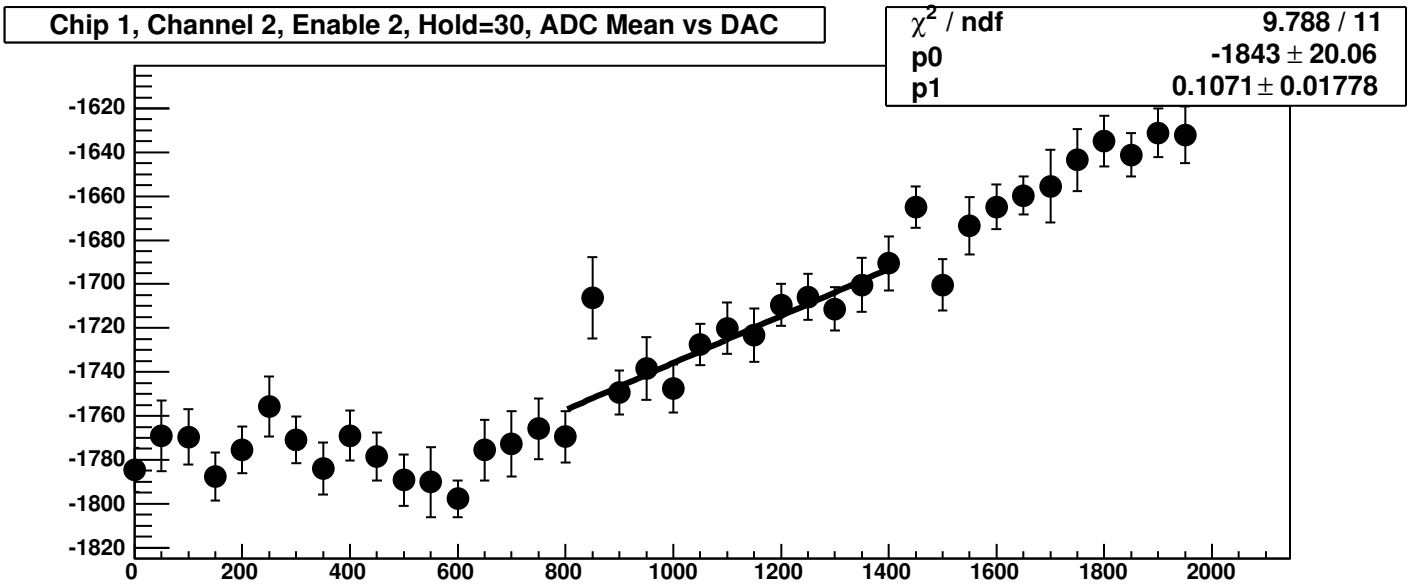

Chip 1, Channel 2, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

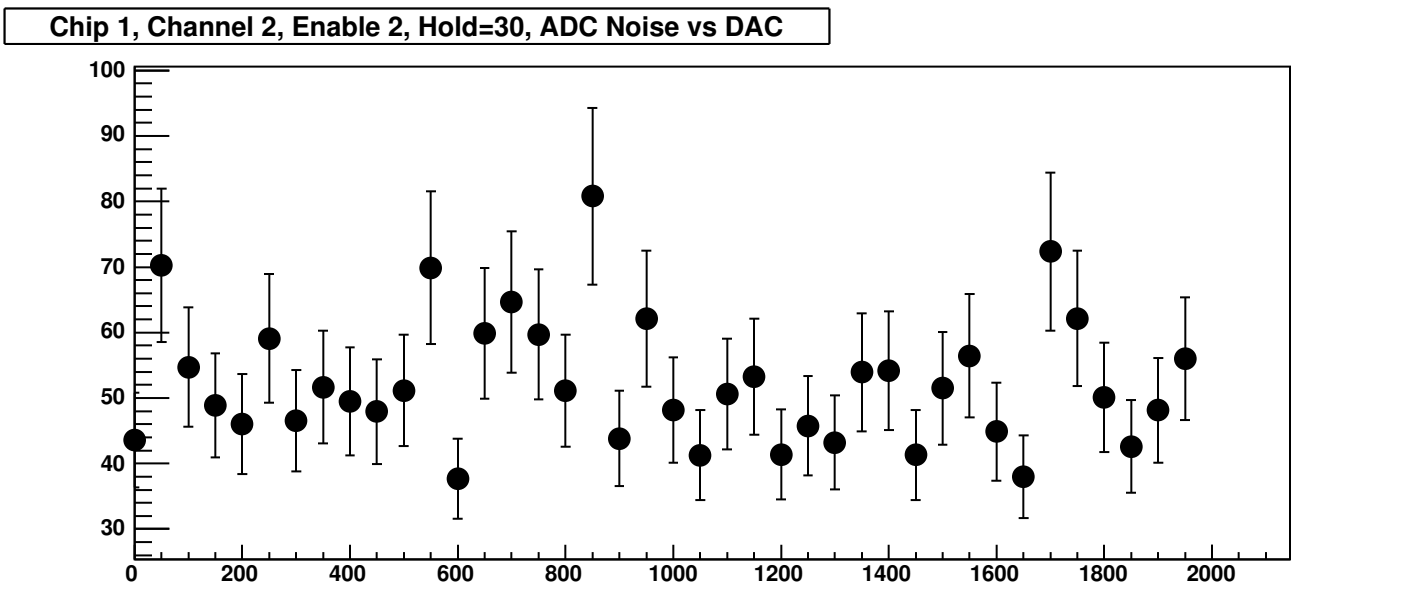

Chip 1, Channel 2, Enable 0, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

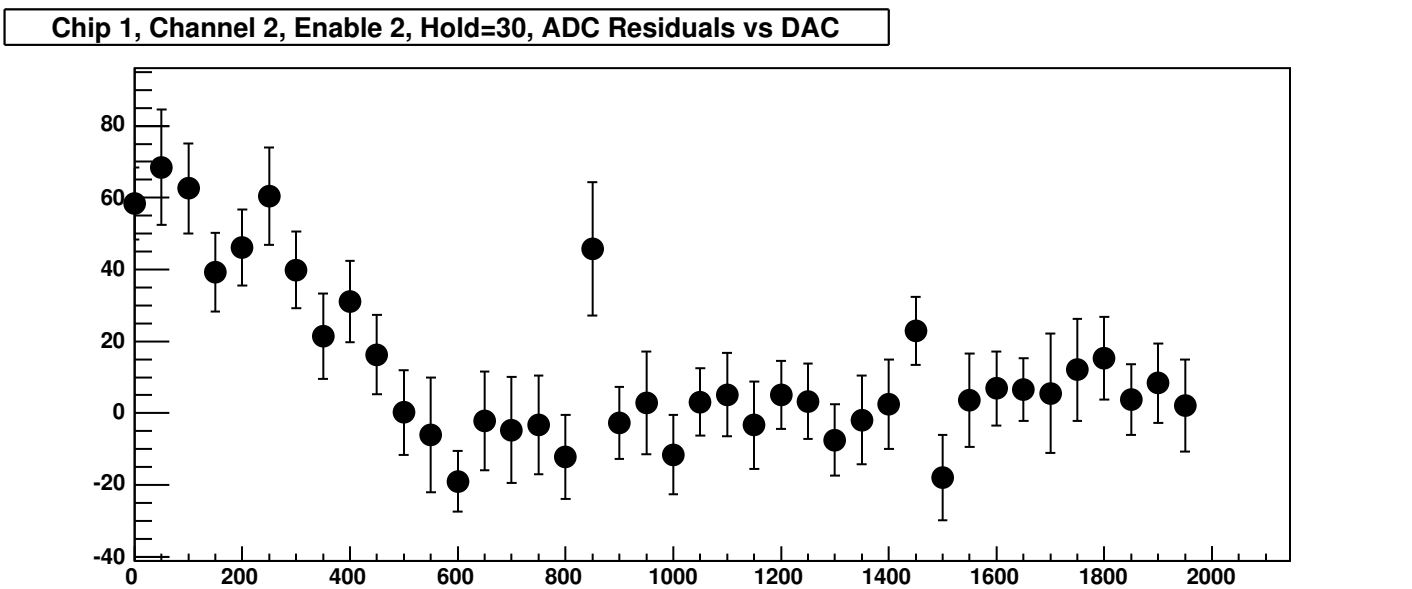

Chip 1, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 10.76 / 11
p0 -1875 ± 18.36
p1 0.07673 ± 0.0158
-1720
-1740
-1760
-1780
-1800
-1820
-1840
0
200
400
600
800
1000
1200
1400
1600
1800
2000

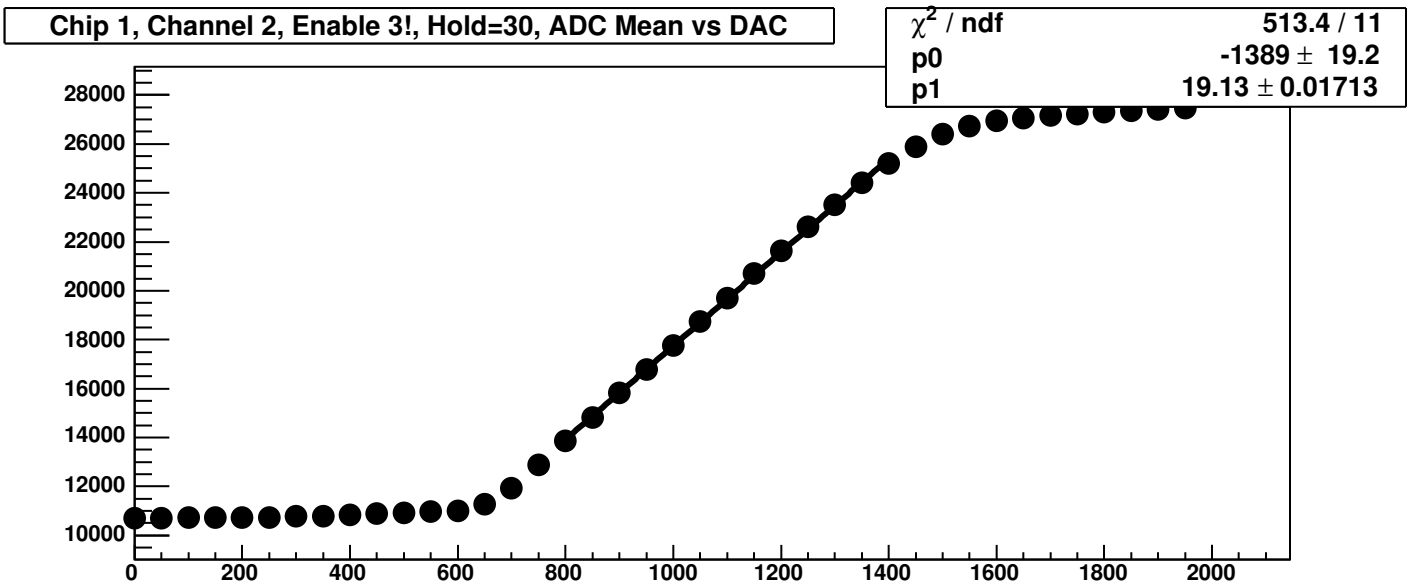

Chip 1, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

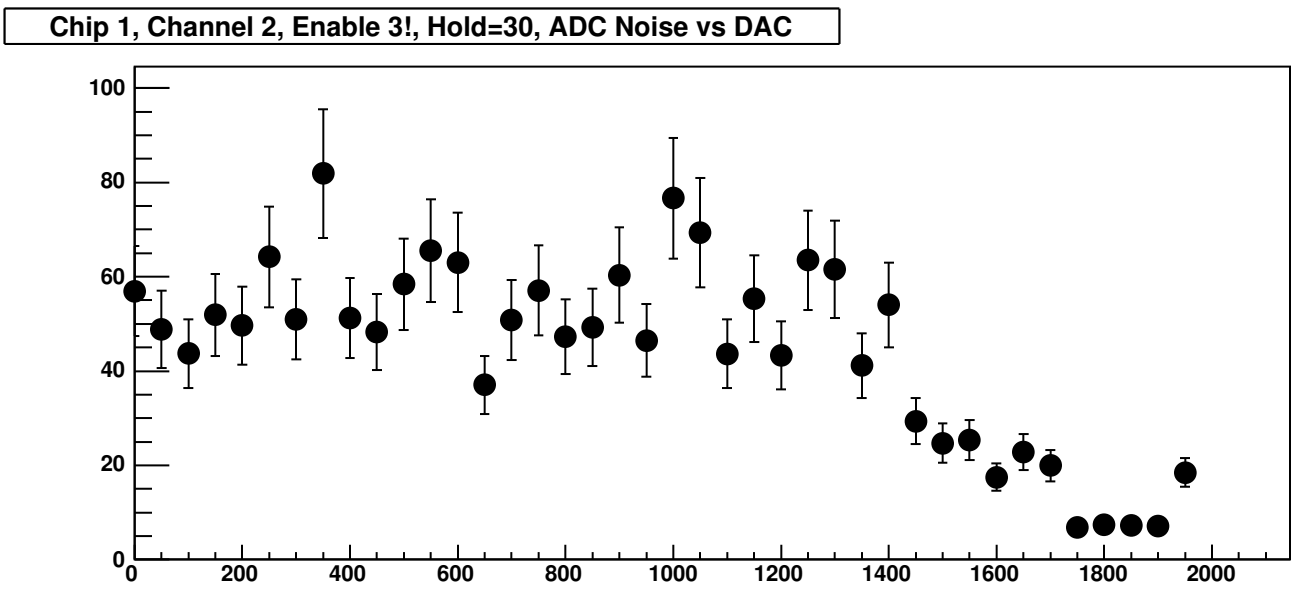

Chip 1, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

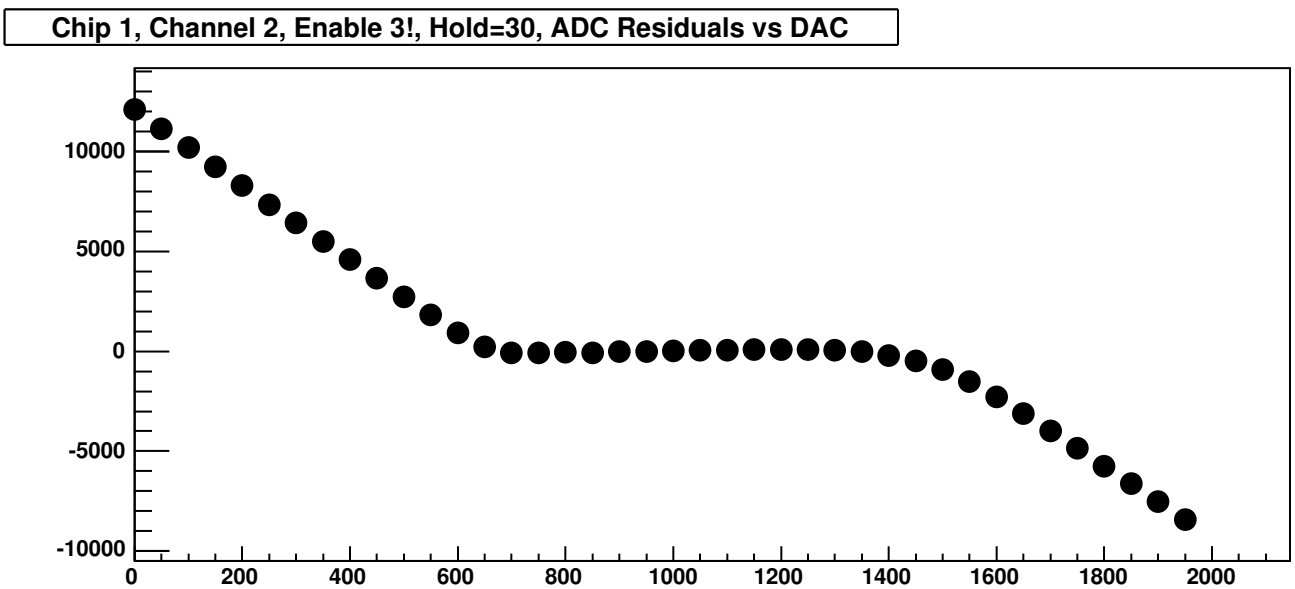

Chip 1, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 9.788 / 11
p0 -1843 ± 20.06
p1 0.1071 ± 0.01778
-1620
-1640
-1660
-1680
-1700
-1720
-1740
-1760
-1780
-1800
-1820
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

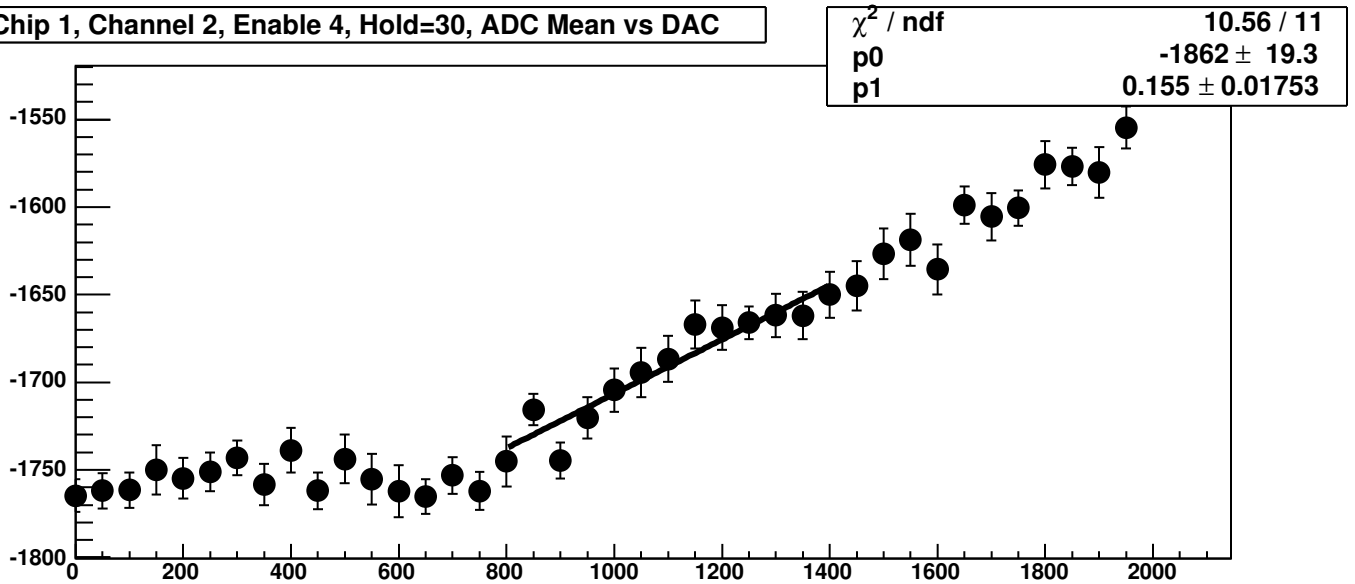

Chip 1, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 2, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 513.4 / 11
p0 -1389 ± 19.2
p1 19.13 ± 0.01713
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

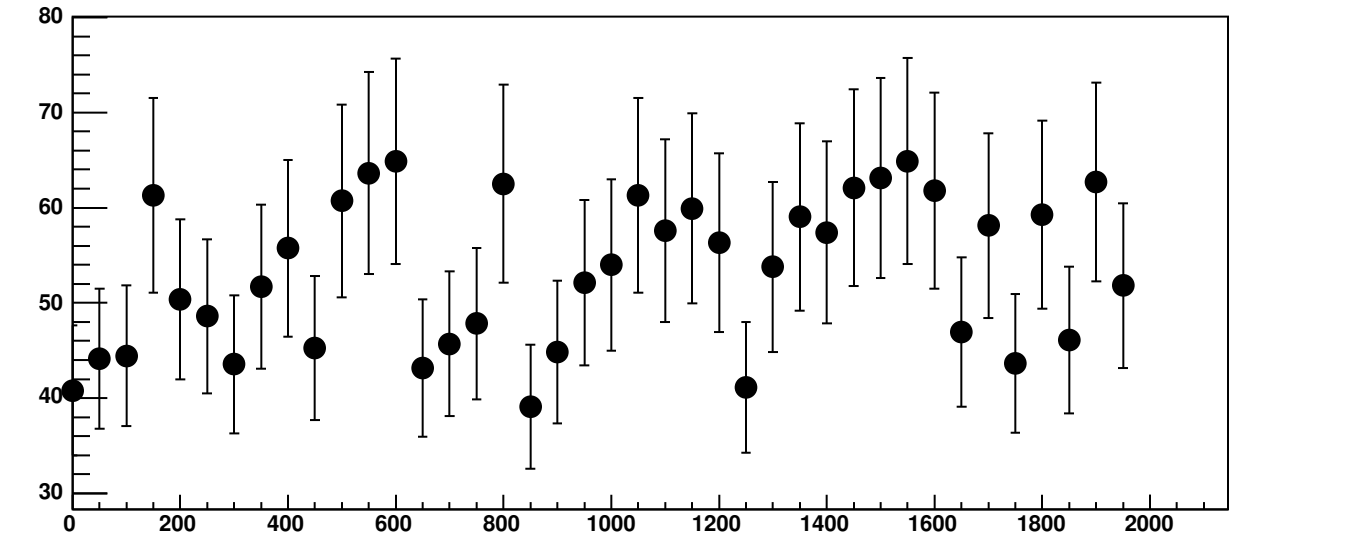

Chip 1, Channel 2, Enable 3!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 2, Enable 3!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

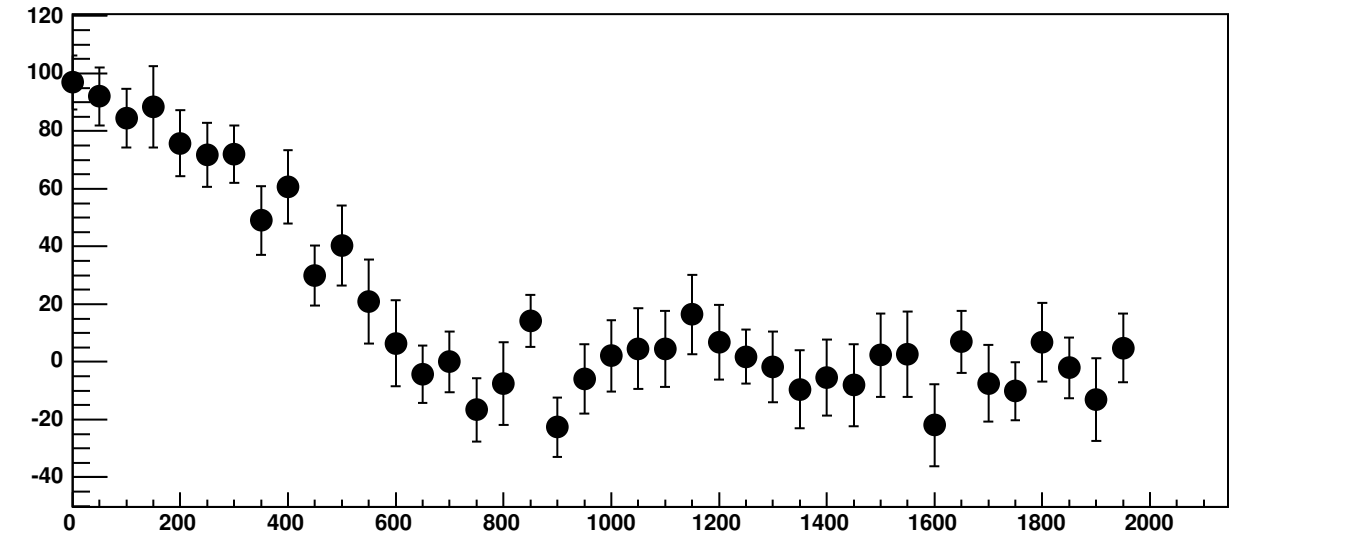
Chip 1, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC

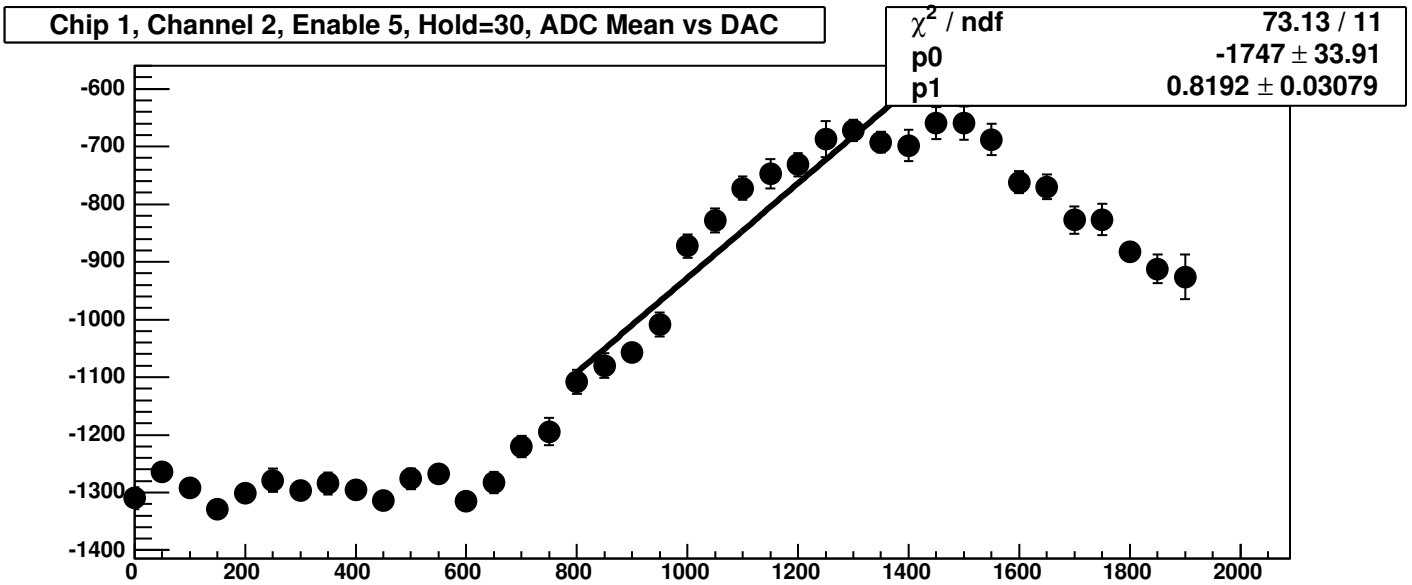


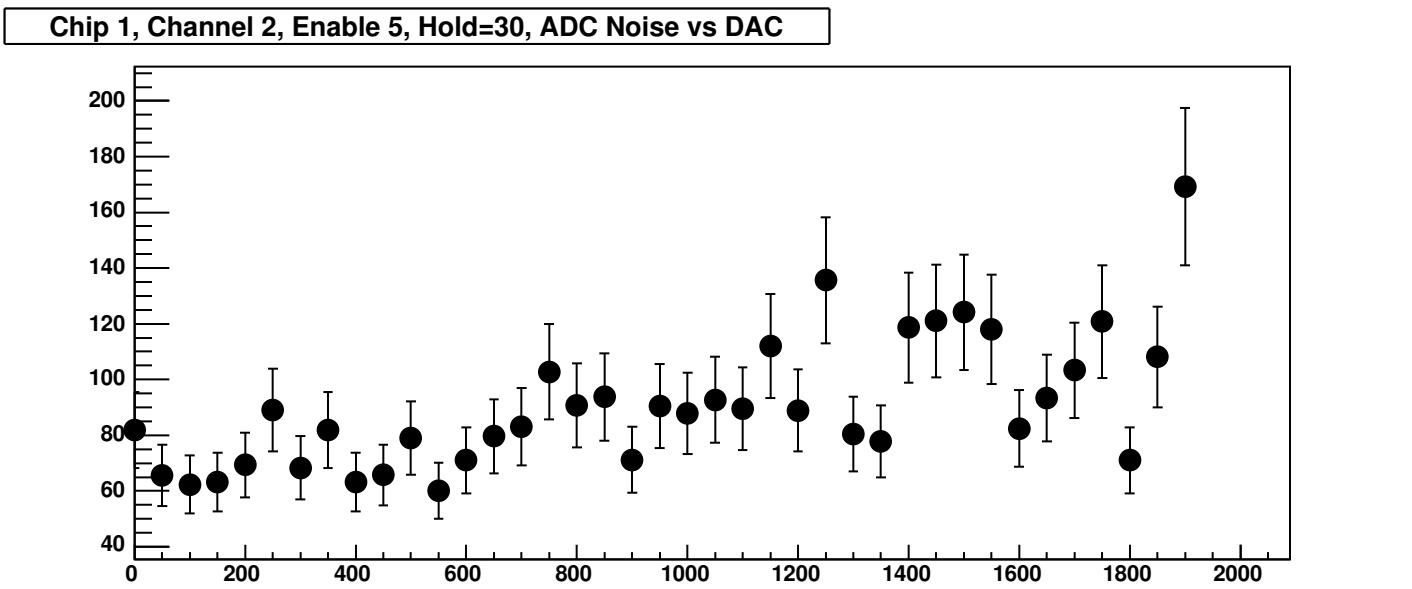
Chip 1, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC

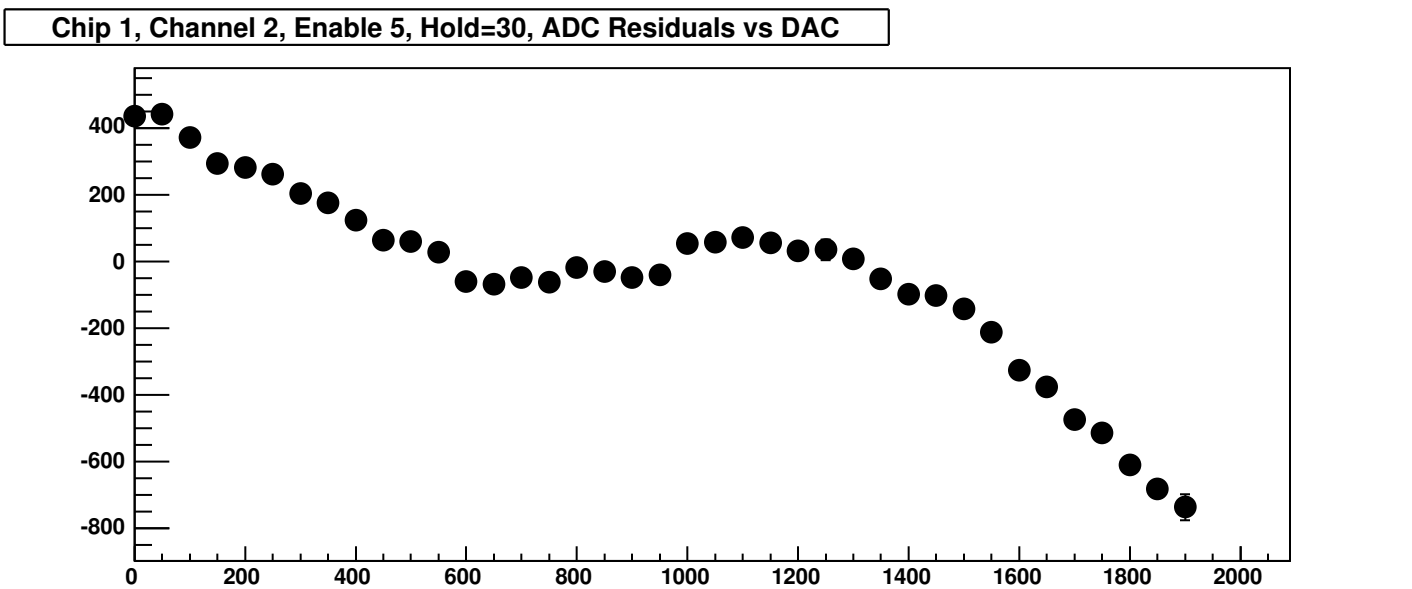


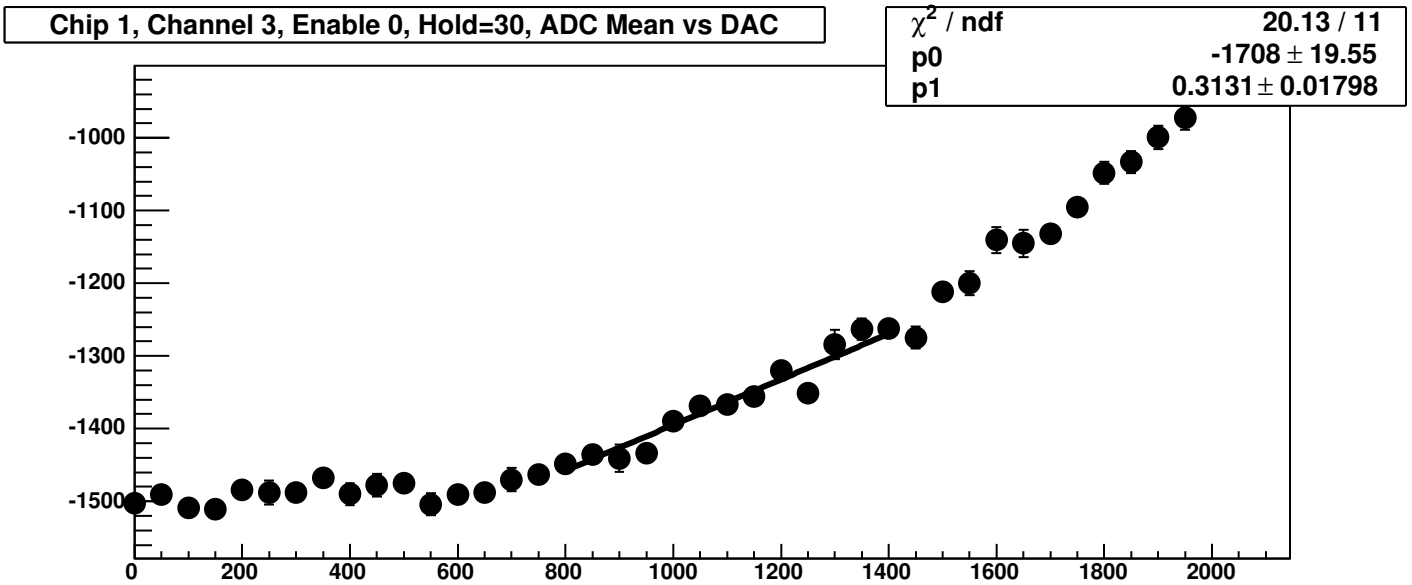
Chip 1, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC

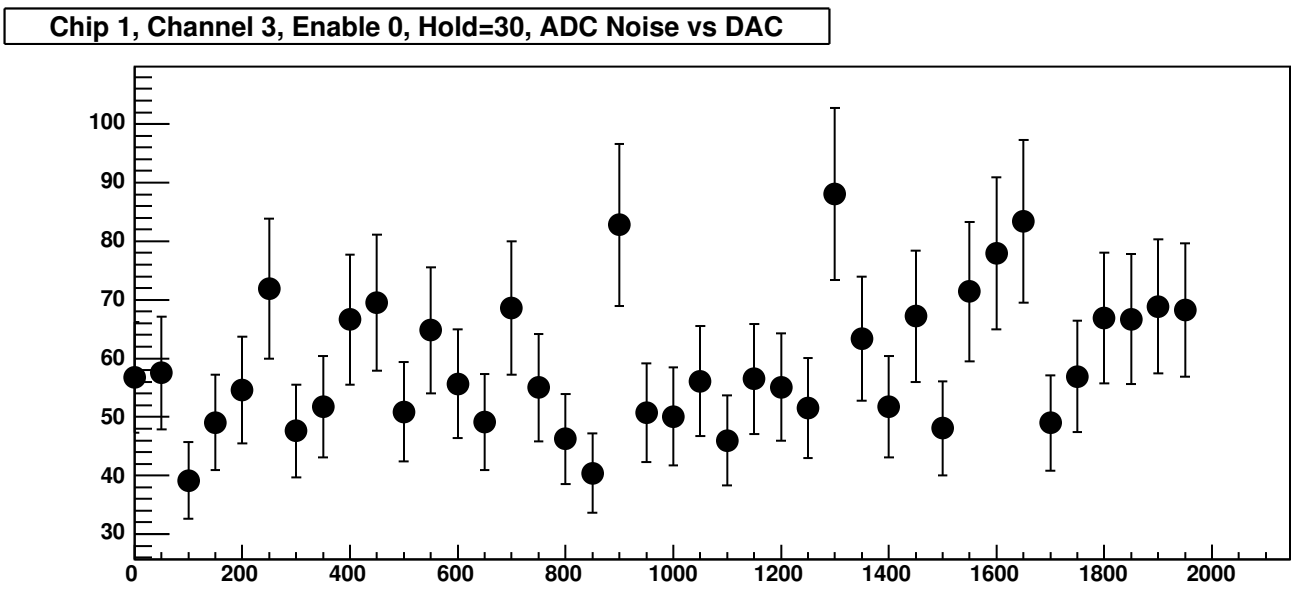


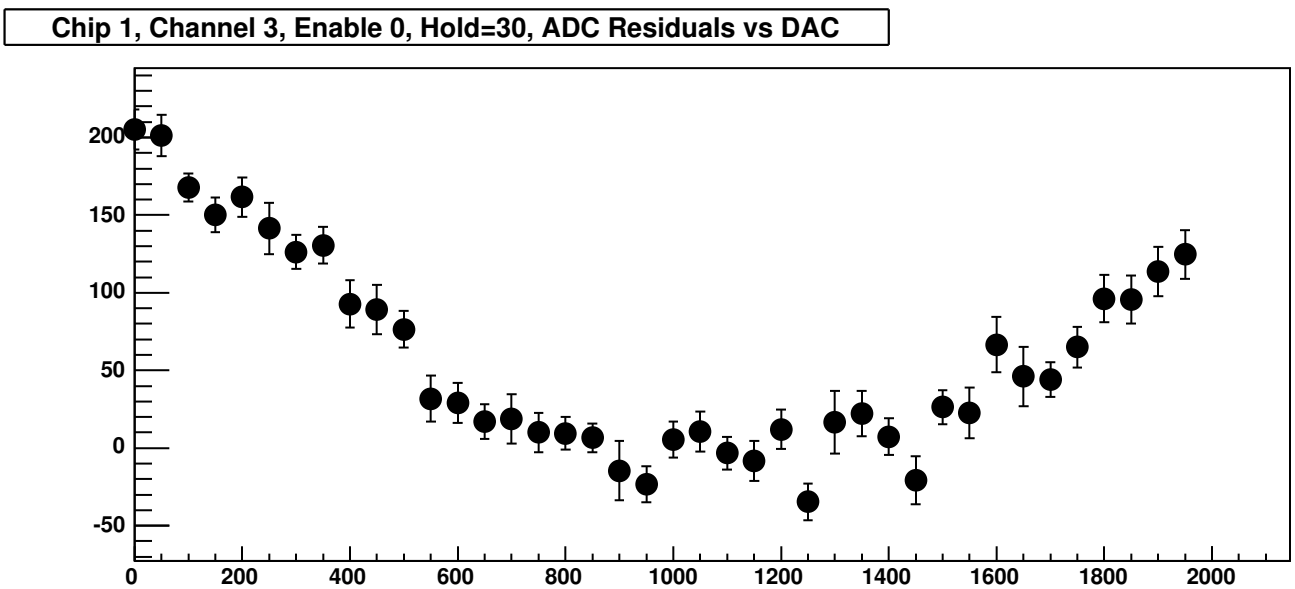

Chip 1, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 73.13 / 11
p0 -1747 ± 33.91
p1 0.8192 ± 0.03079
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

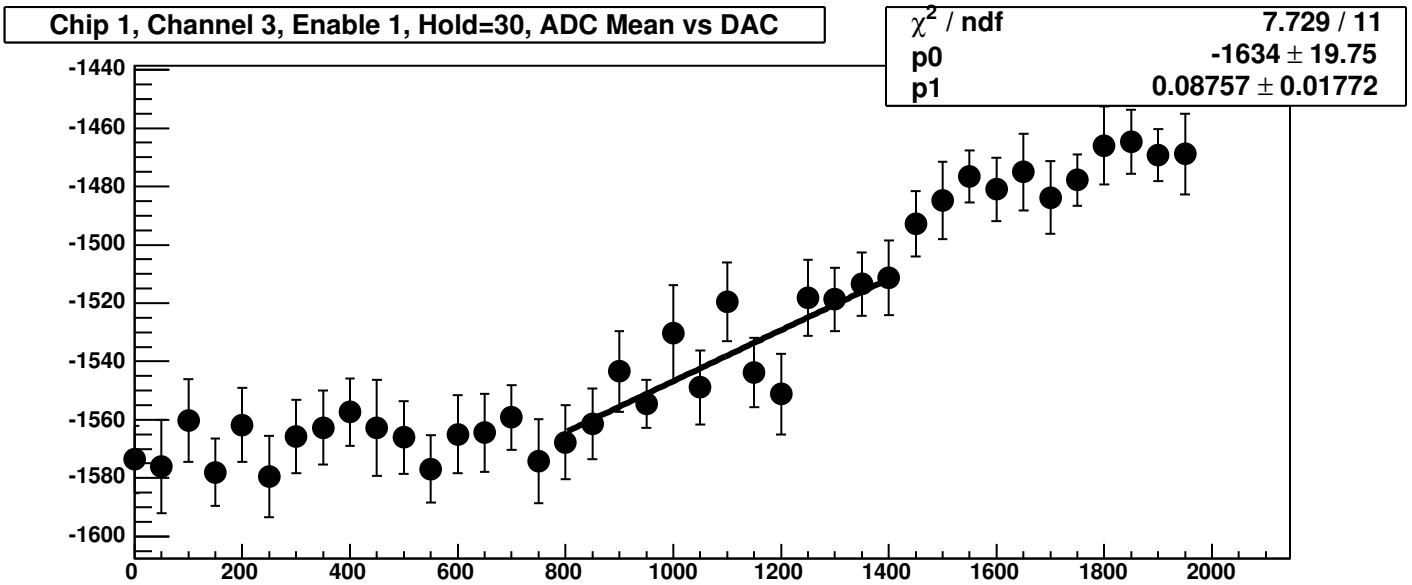

Chip 1, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

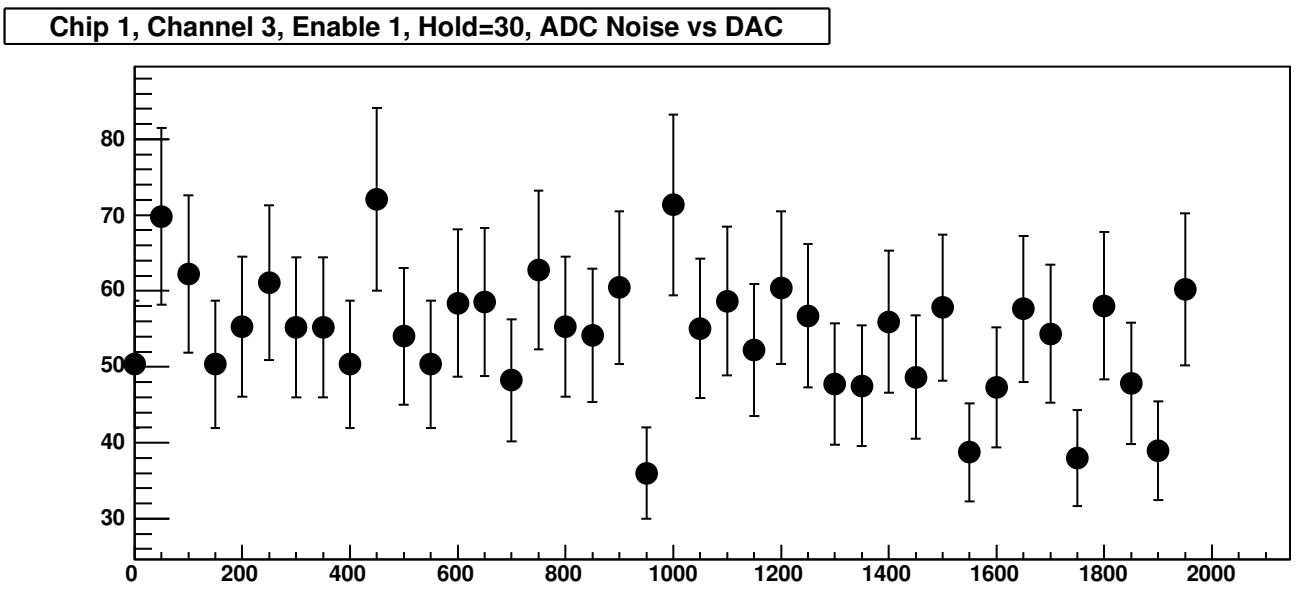

Chip 1, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

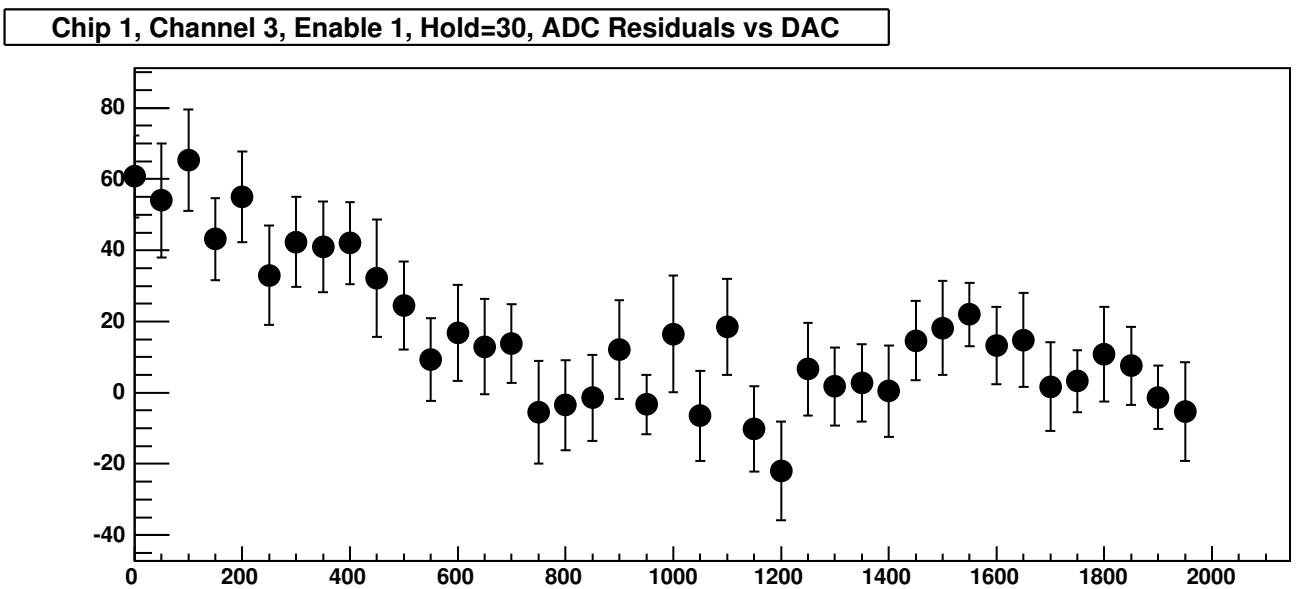

Chip 1, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 20.13 / 11
p0 -1708 ± 19.55
p1 0.3131 ± 0.01798
-1000
-1100
-1200
-1300
-1400
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

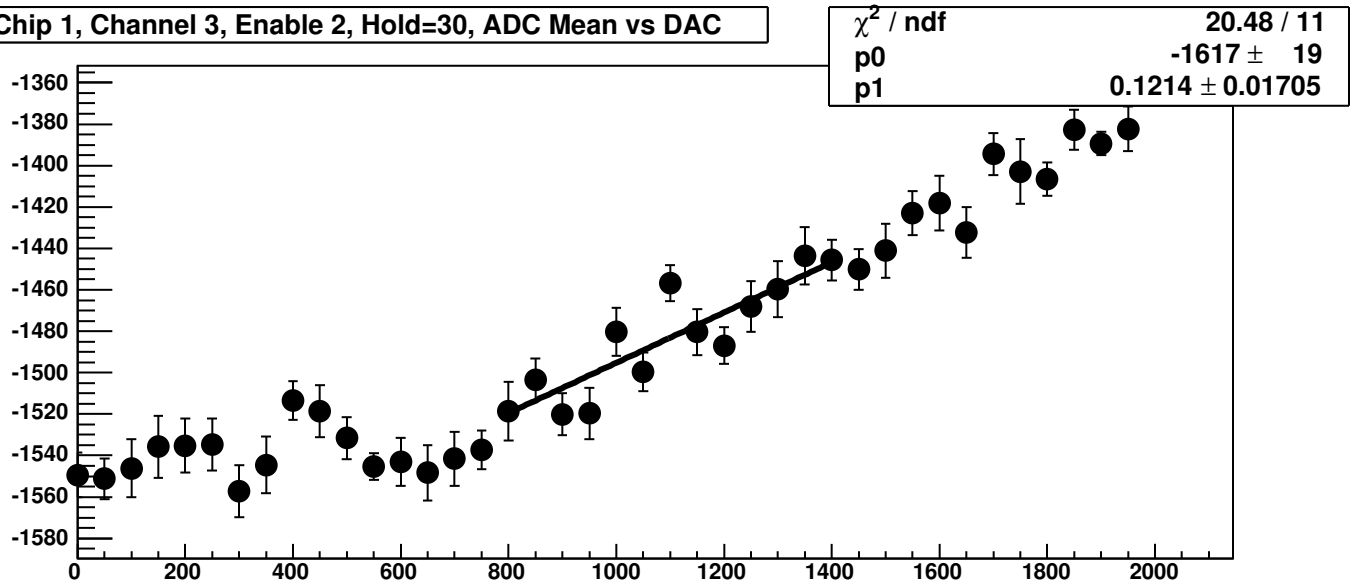

Chip 1, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 3, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7.729 / 11
p0 -1634 ± 19.75
p1 0.08757 ± 0.01772
-1440
-1460
-1480
-1500
-1520
-1540
-1560
-1580
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

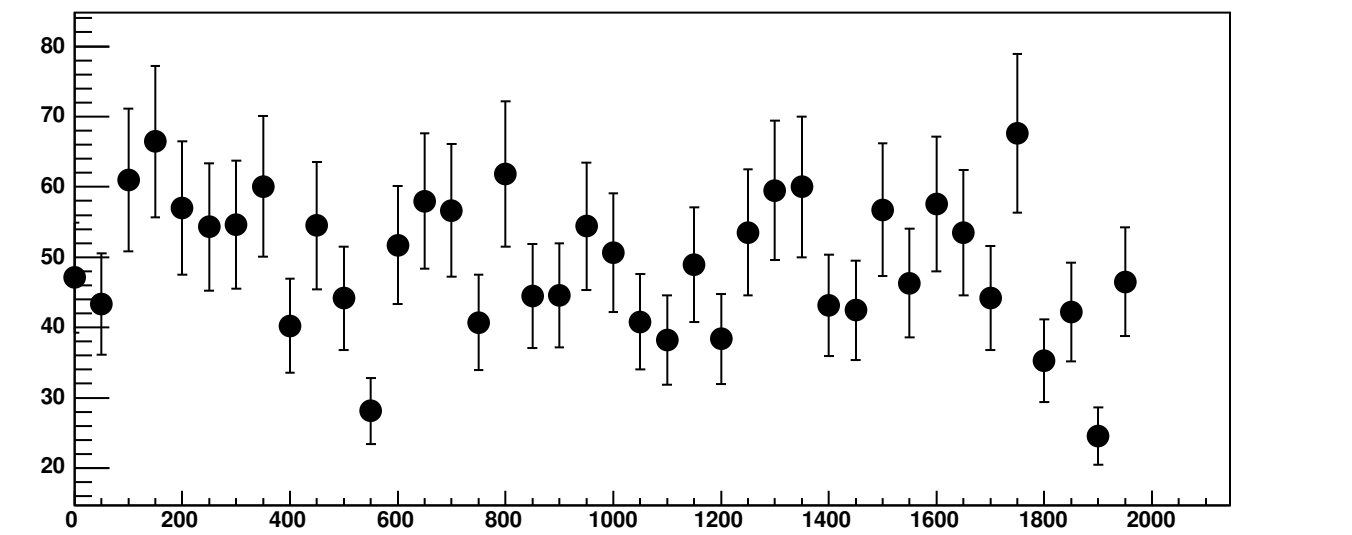

Chip 1, Channel 3, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 3, Enable 1, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

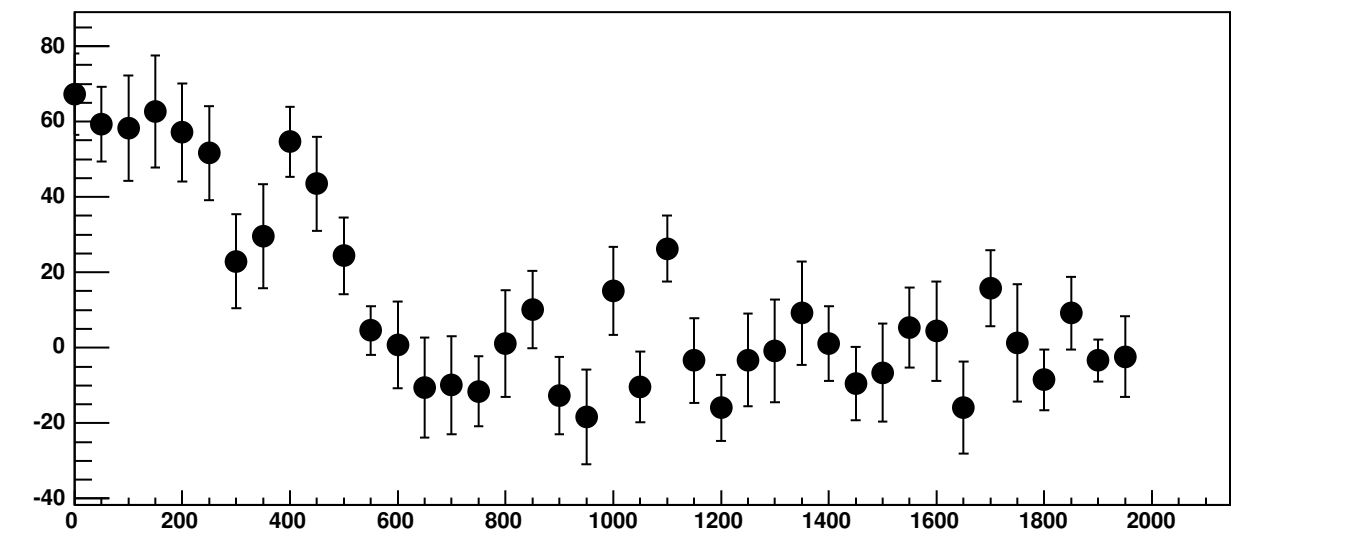
Chip 1, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC

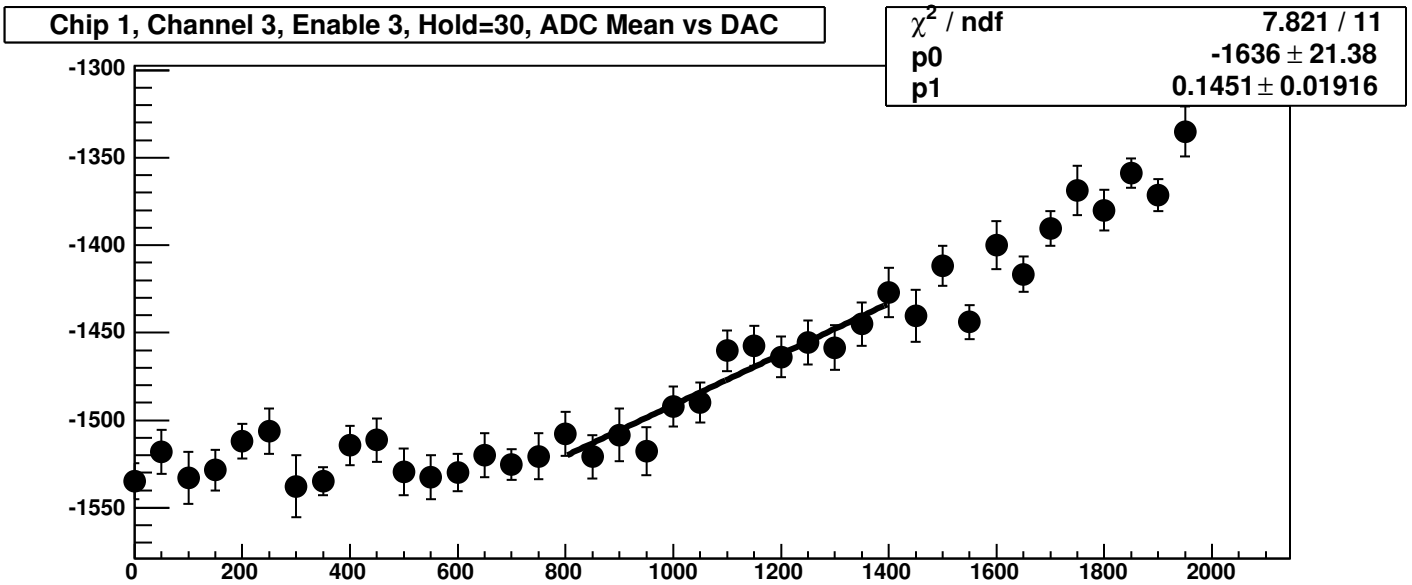


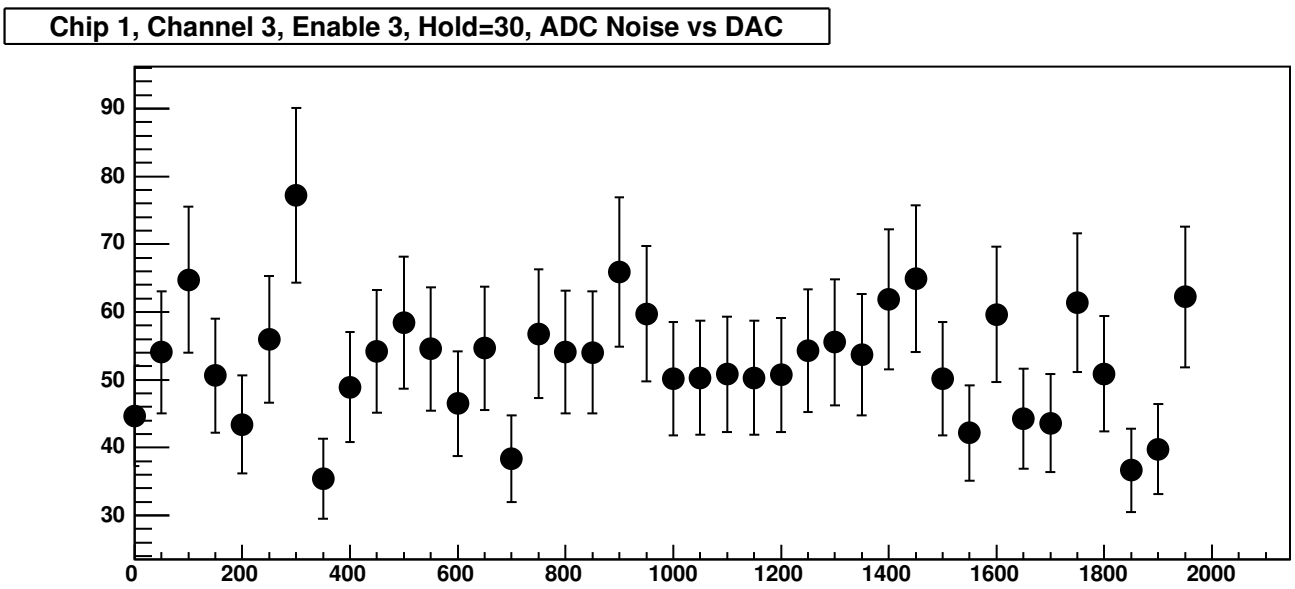
Chip 1, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC

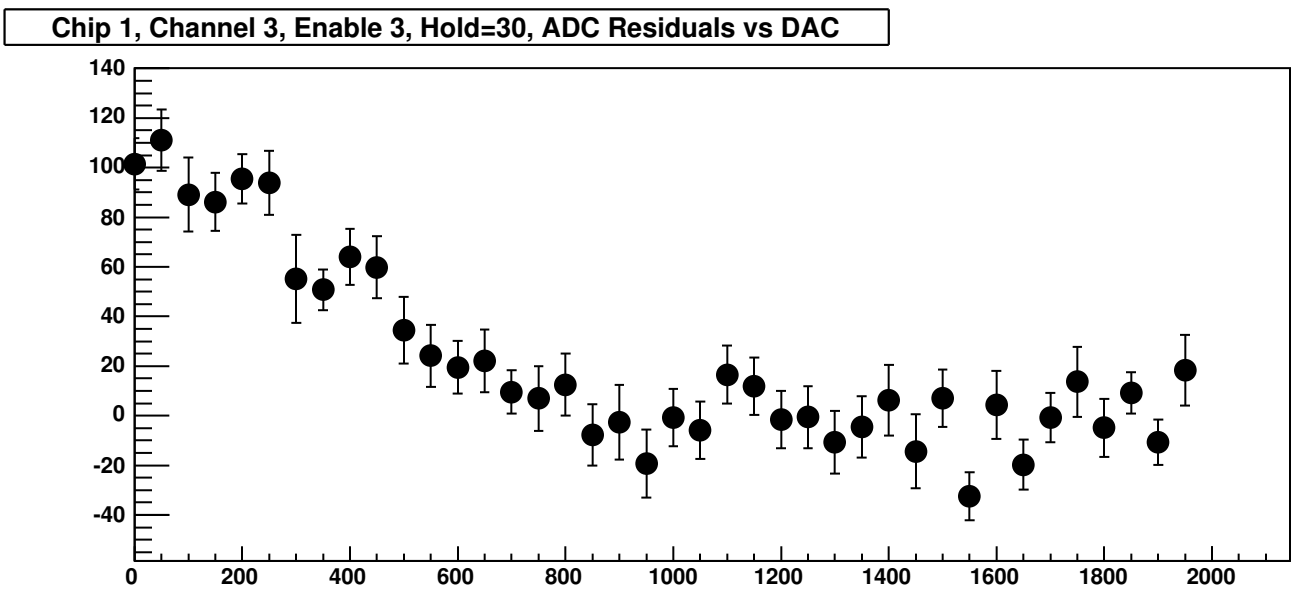


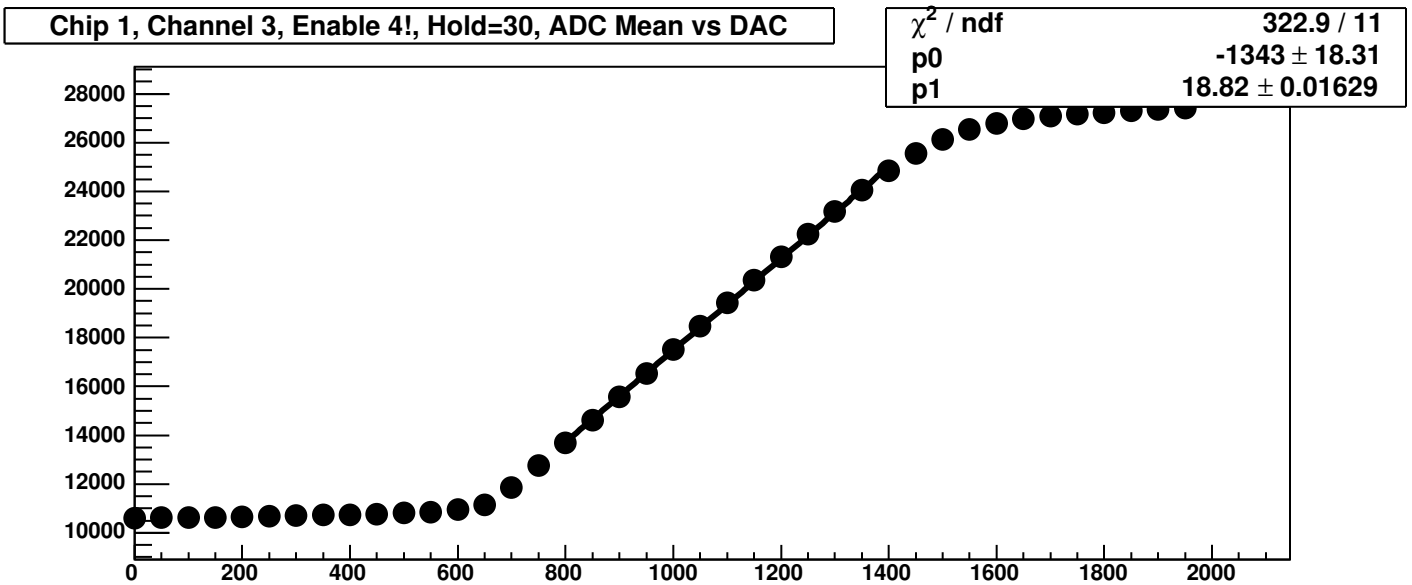
Chip 1, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC

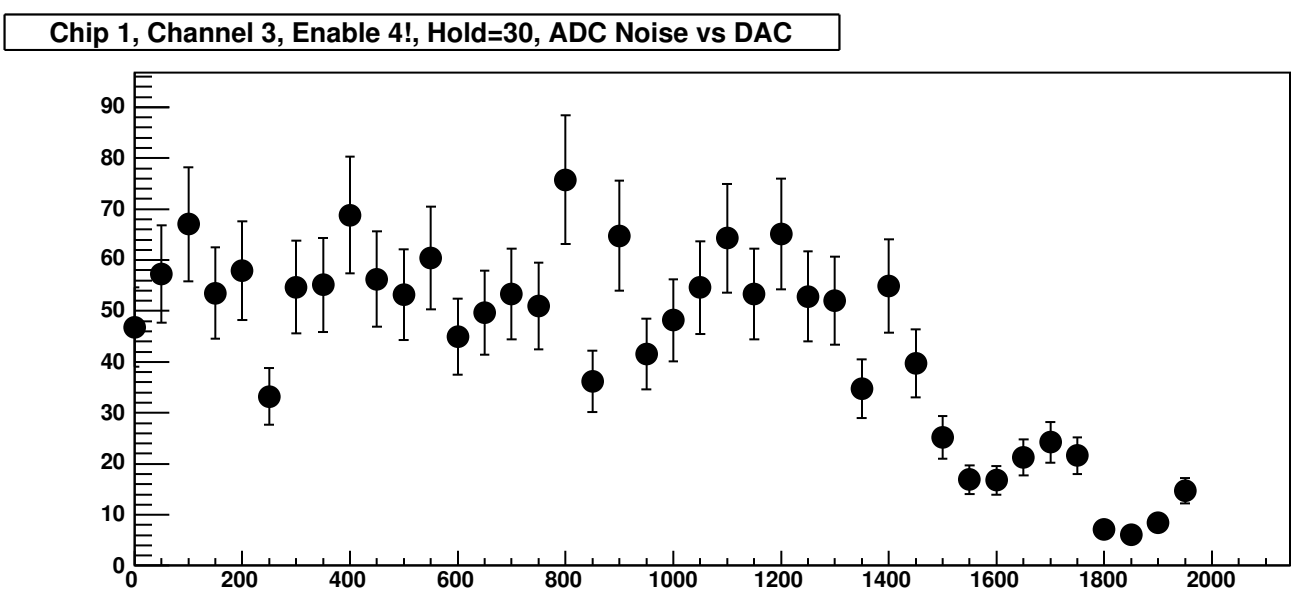


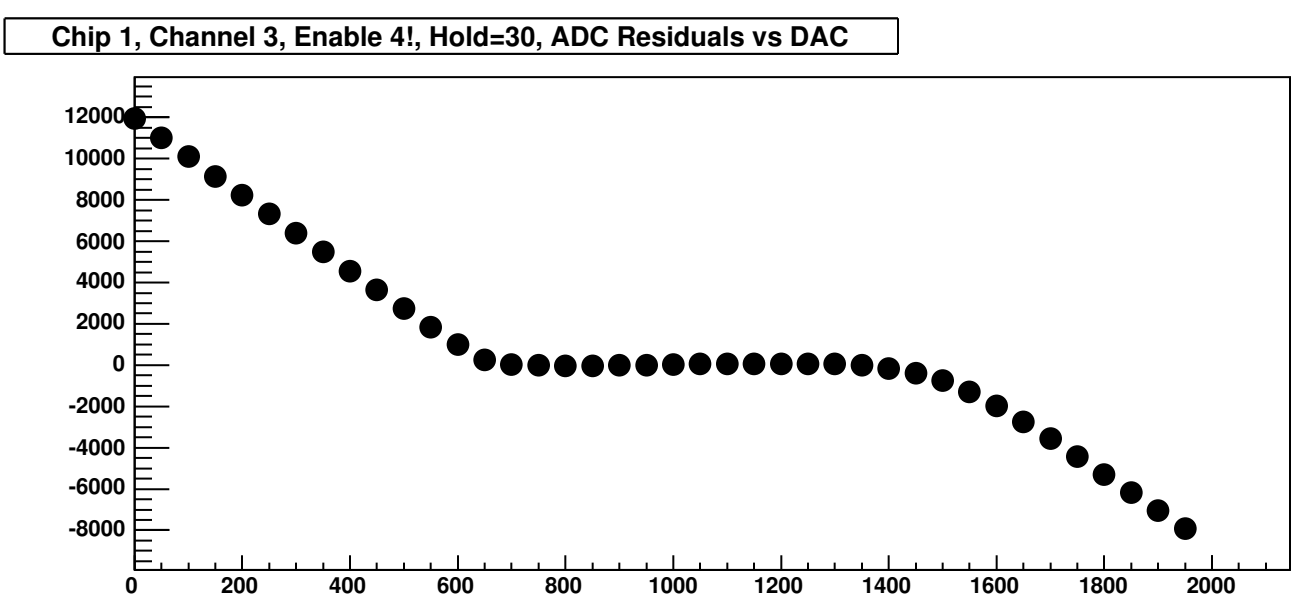

Chip 1, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7.821 / 11
p0 -1636 ± 21.38
p1 0.1451 ± 0.01916
-1300
-1350
-1400
-1450
-1500
-1550
0
200
400
600
800
1000
1200
1400
1600
1800
2000

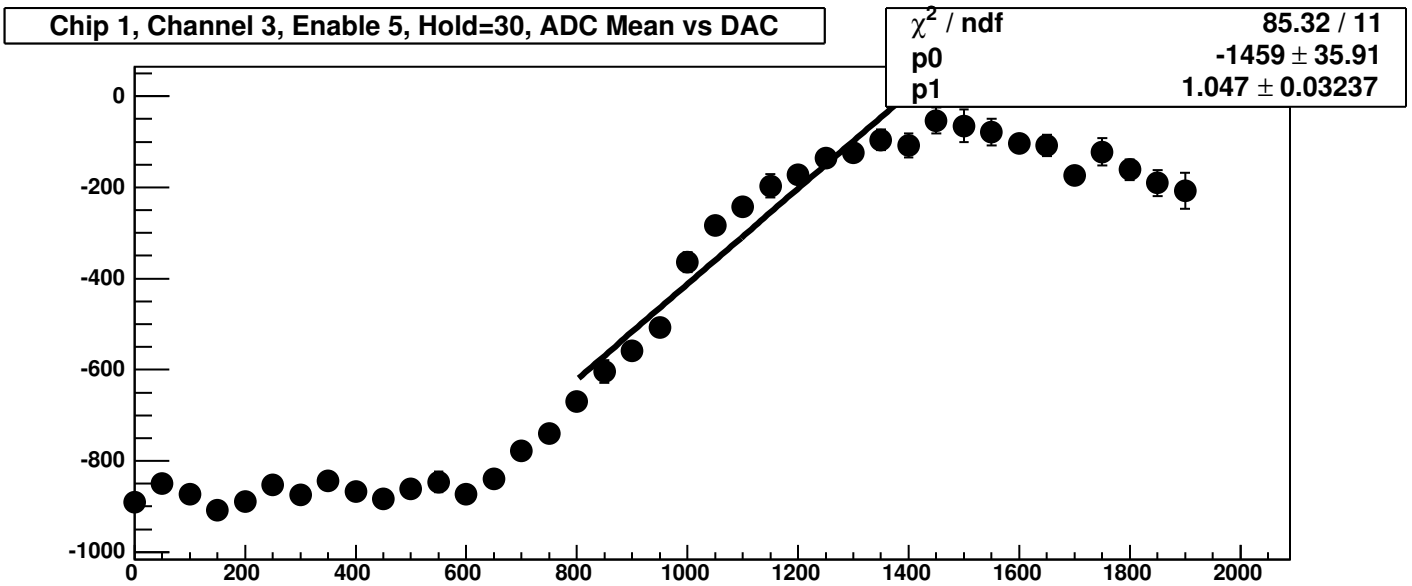

Chip 1, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

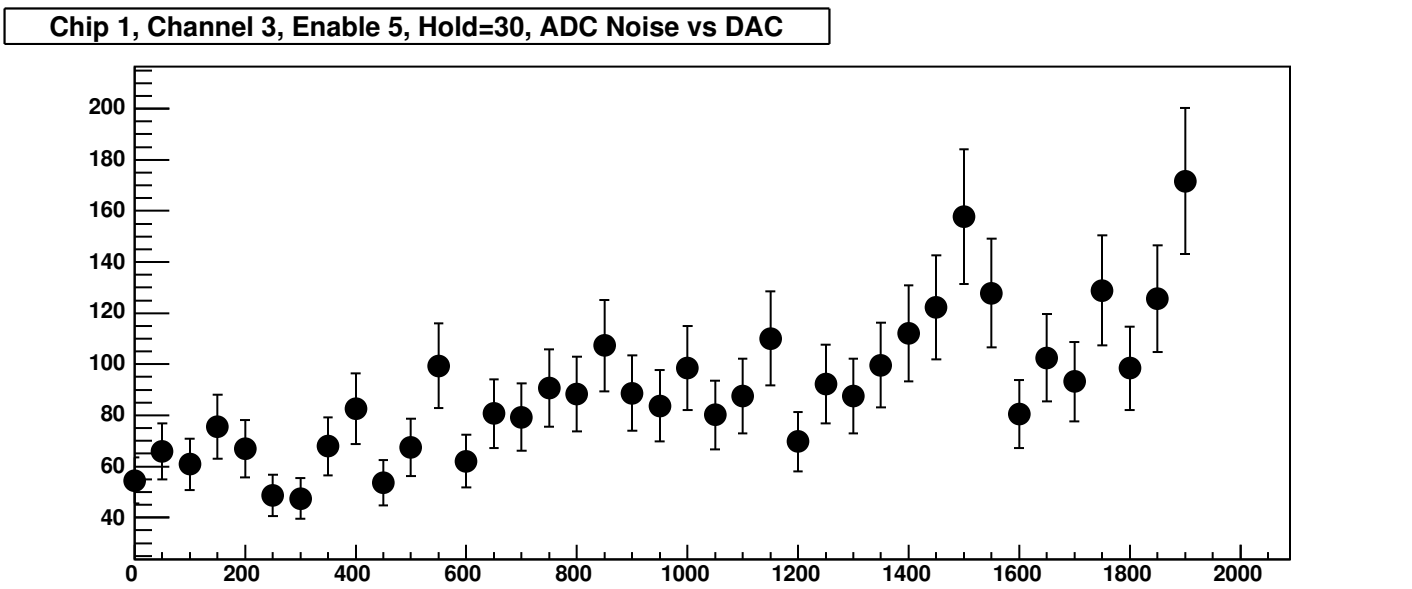

Chip 1, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

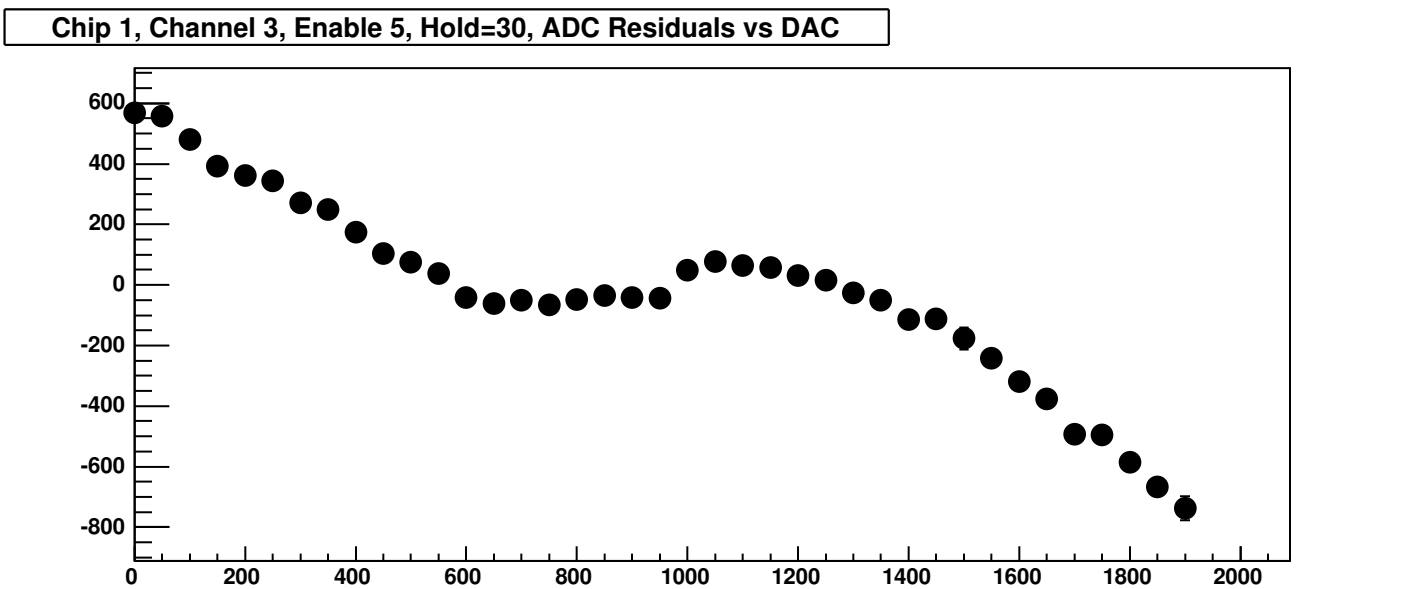

Chip 1, Channel 3, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 322.9 / 11
p0 -1343 ± 18.31
p1 18.82 ± 0.01629
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

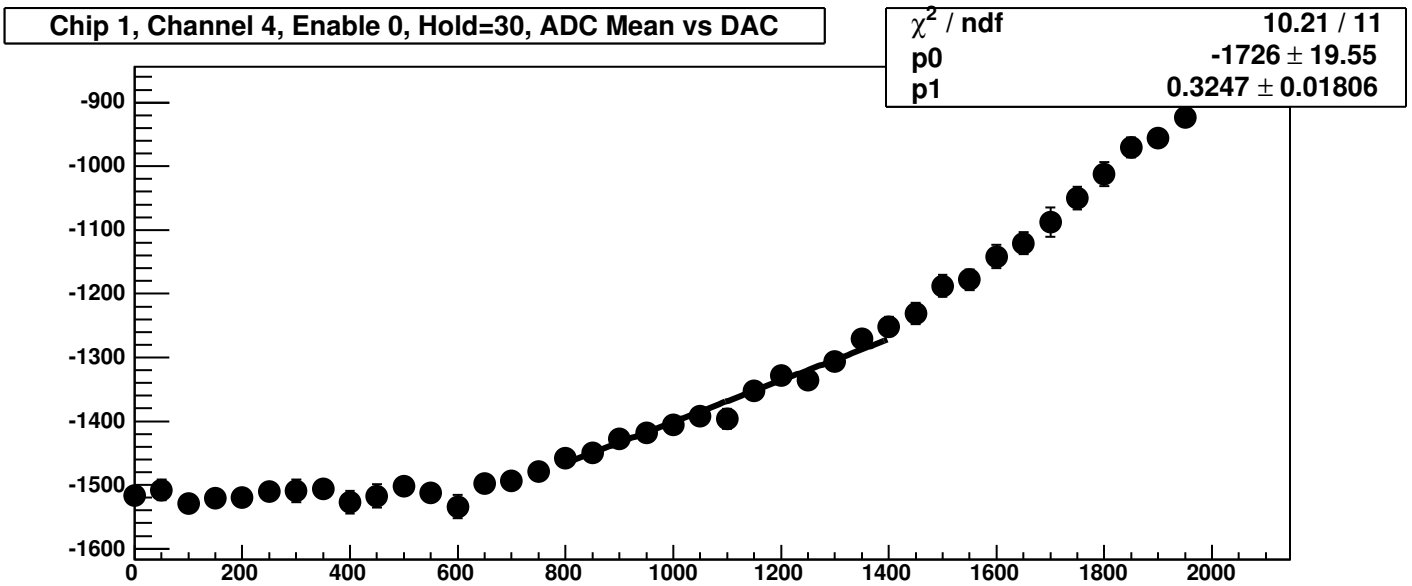

Chip 1, Channel 3, Enable 4!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

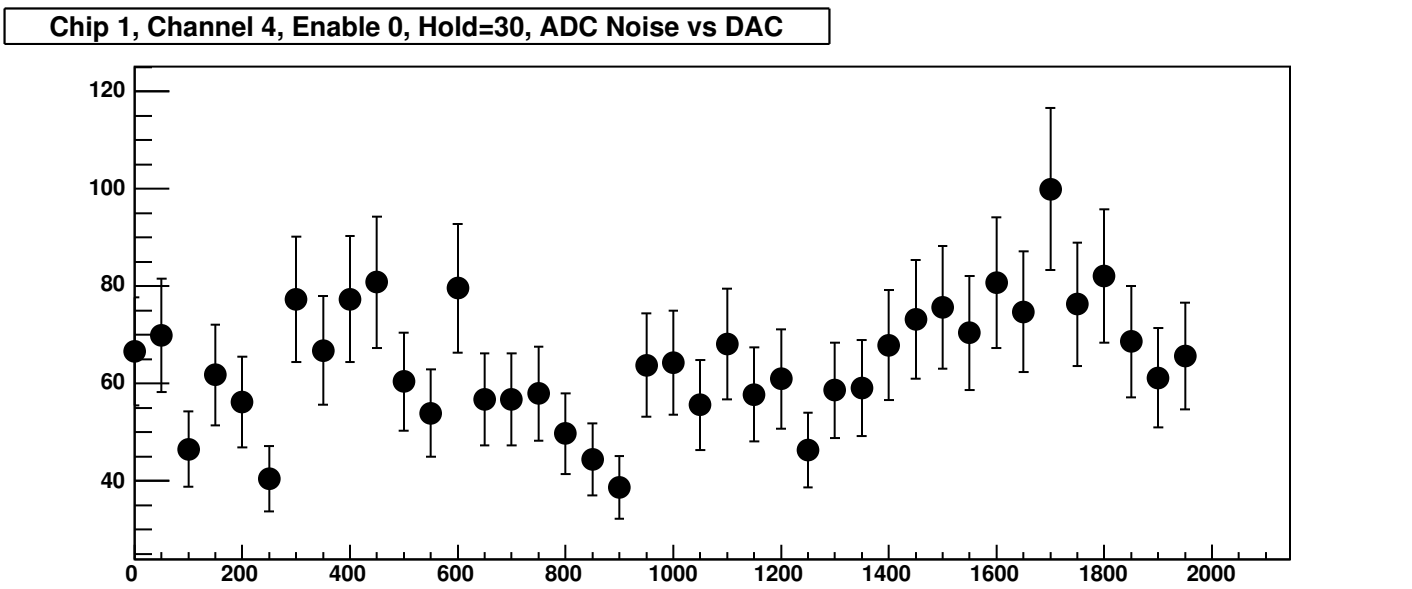

Chip 1, Channel 3, Enable 4!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

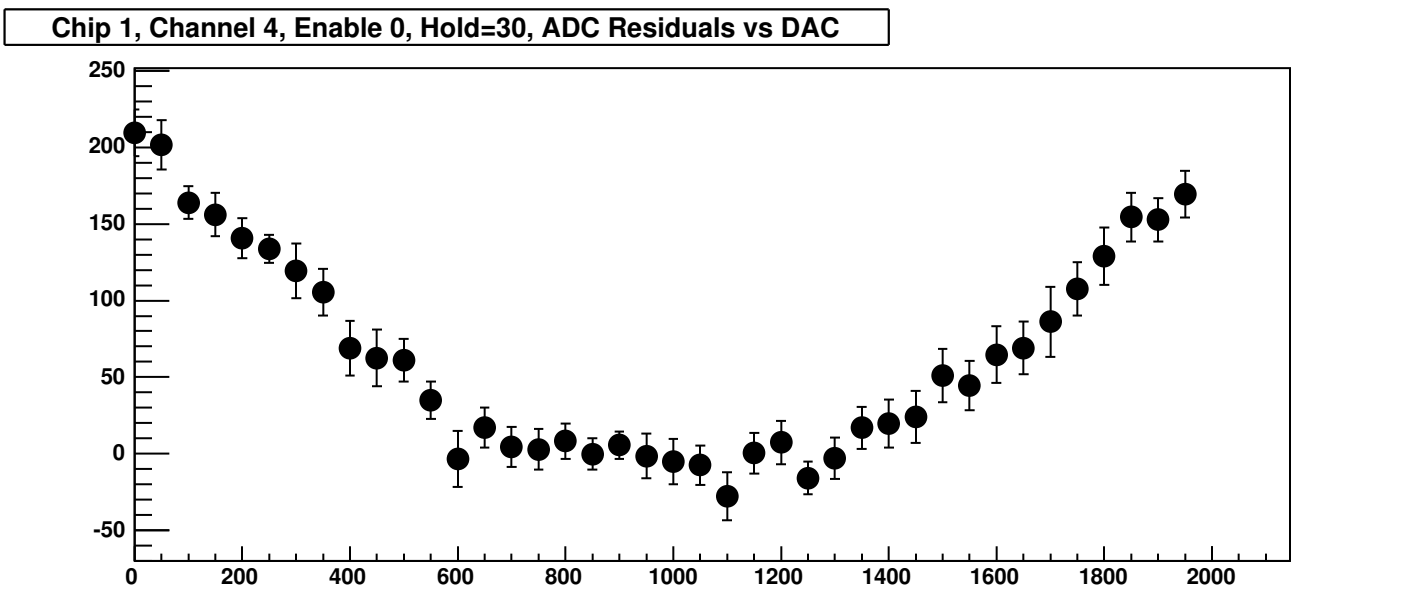

Chip 1, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 85.32 / 11
p0 -1459 ± 35.91
p1 1.047 ± 0.03237
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

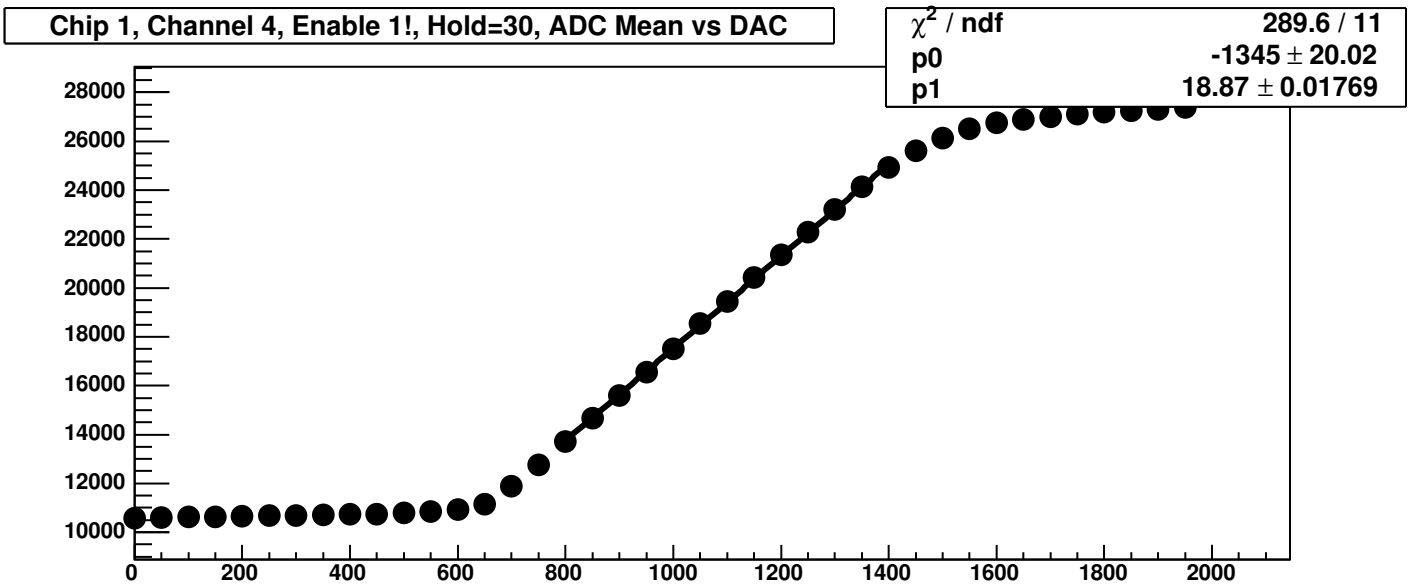

Chip 1, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

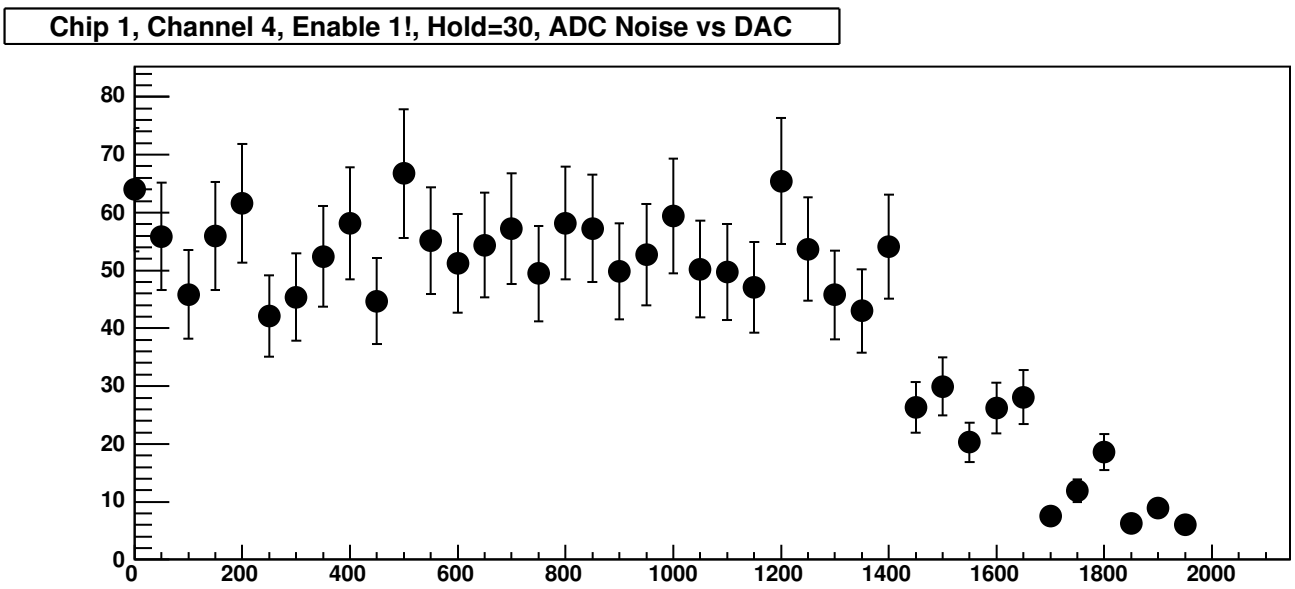

Chip 1, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
600
400
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

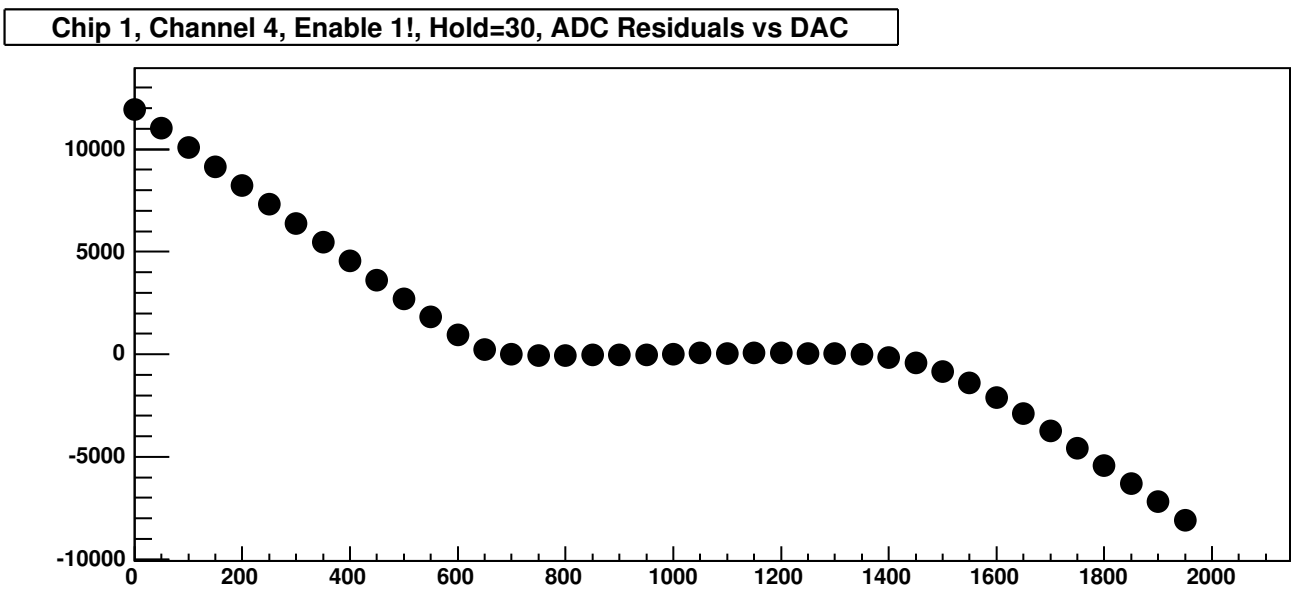

Chip 1, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 10.21 / 11
p0 -1726 ± 19.55
p1 0.3247 ± 0.01806
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

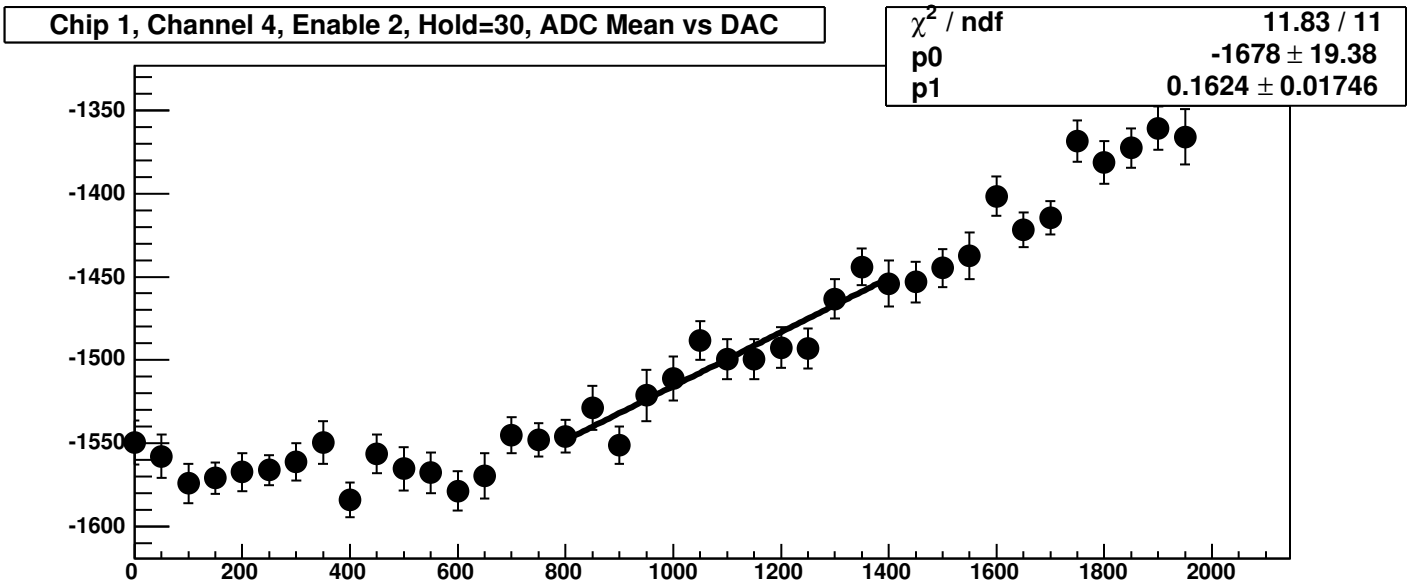

Chip 1, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

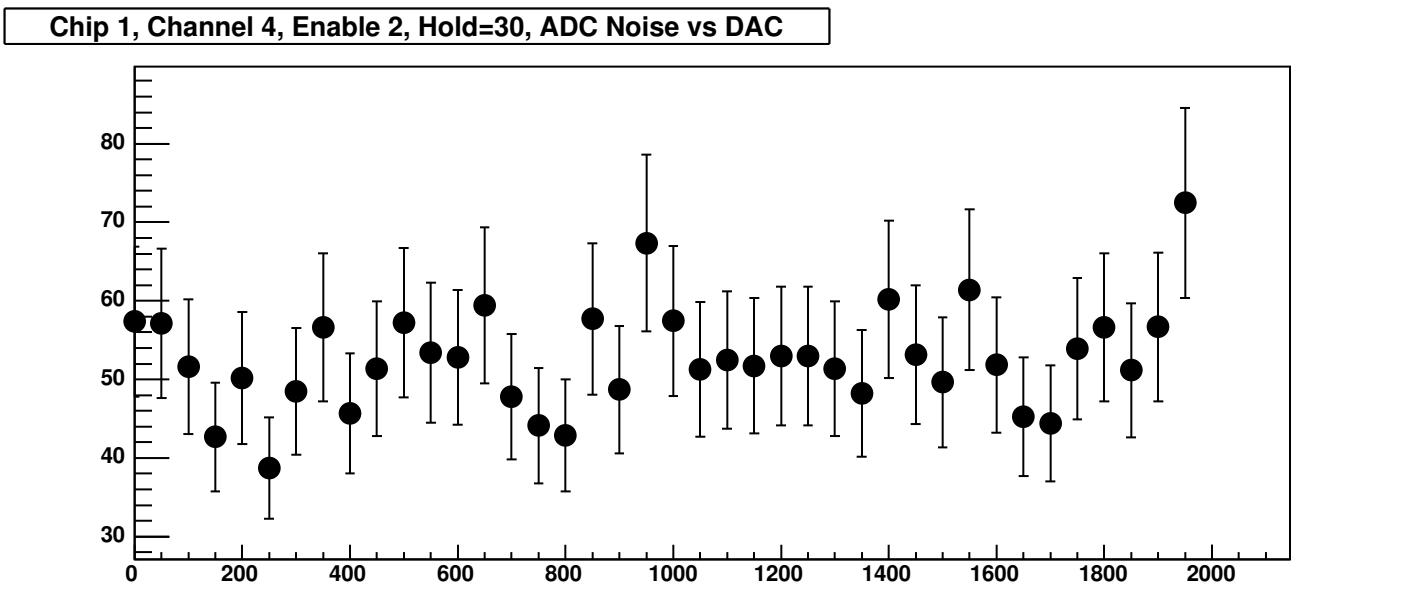

Chip 1, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

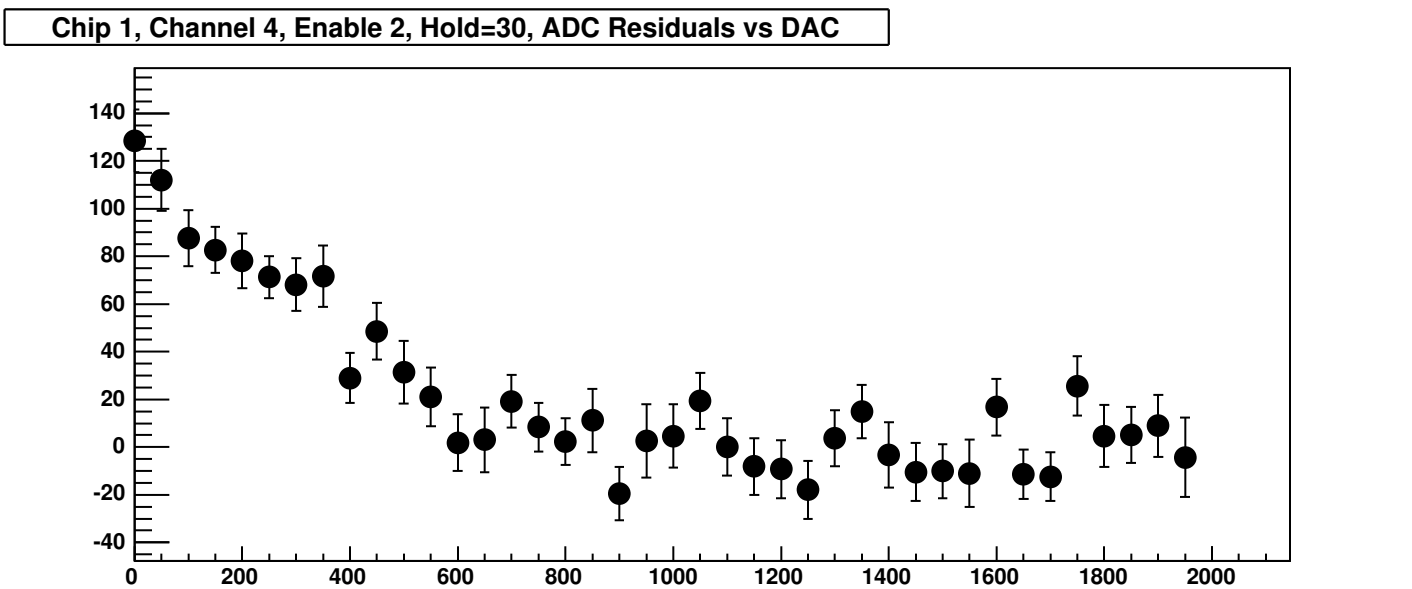

Chip 1, Channel 4, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 289.6 / 11
p0 -1345 ± 20.02
p1 18.87 ± 0.01769
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

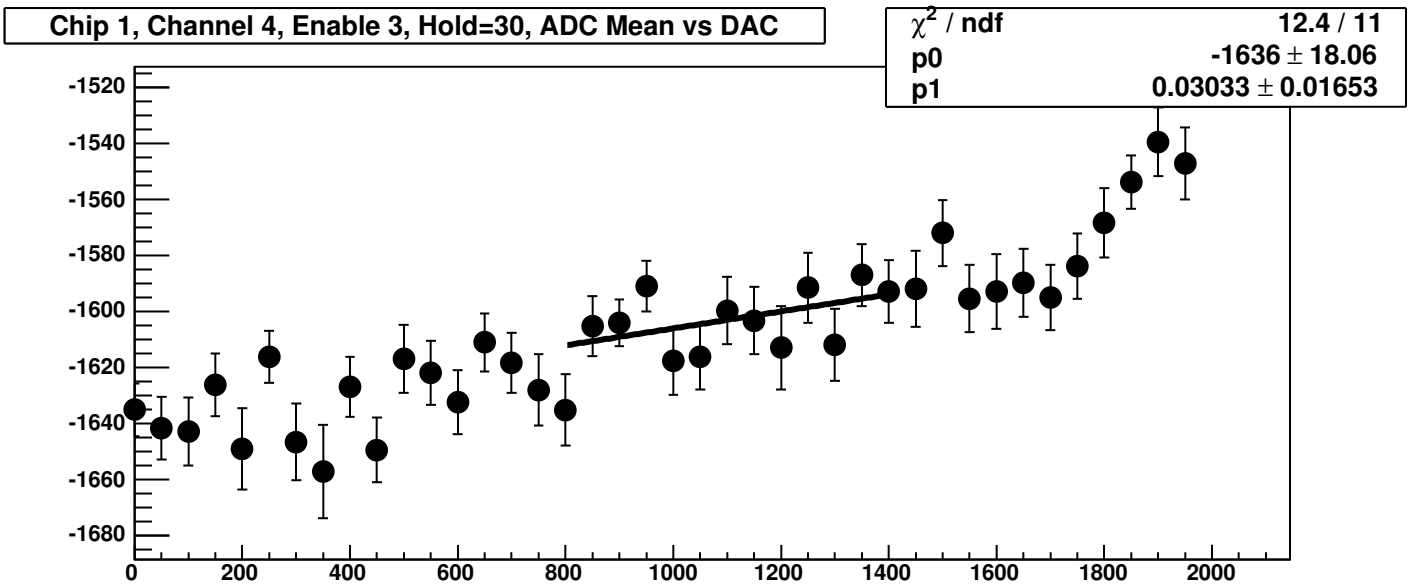

Chip 1, Channel 4, Enable 1!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

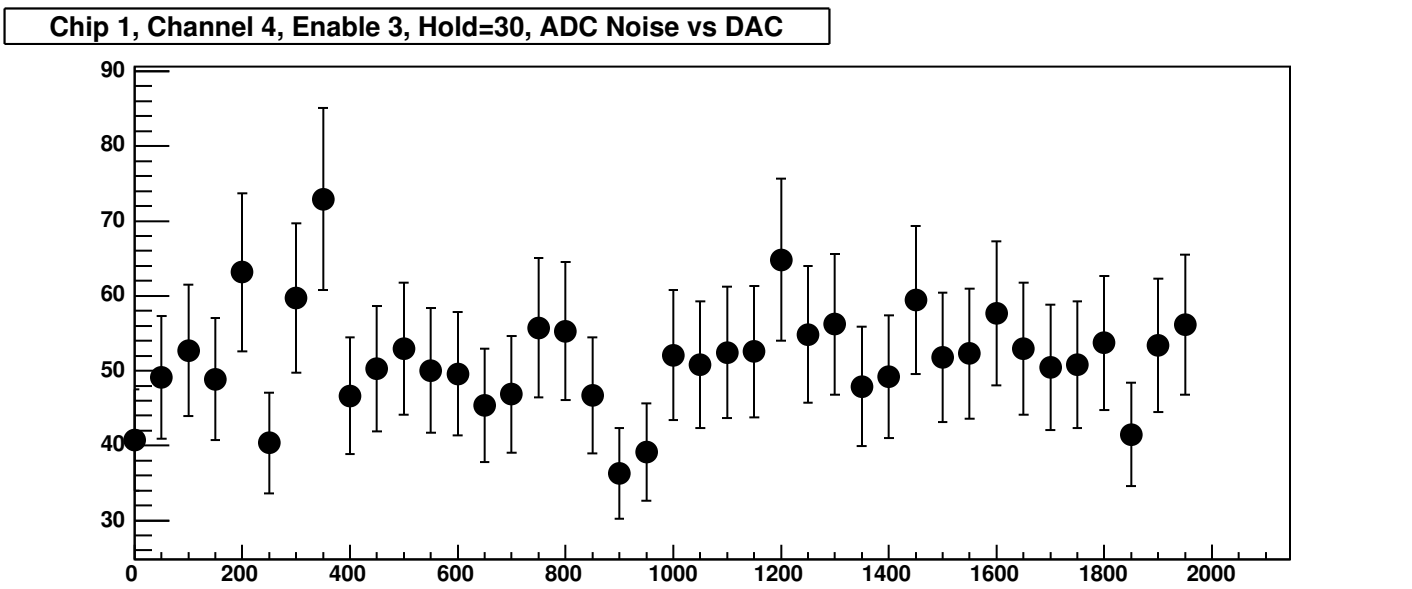

Chip 1, Channel 4, Enable 1!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

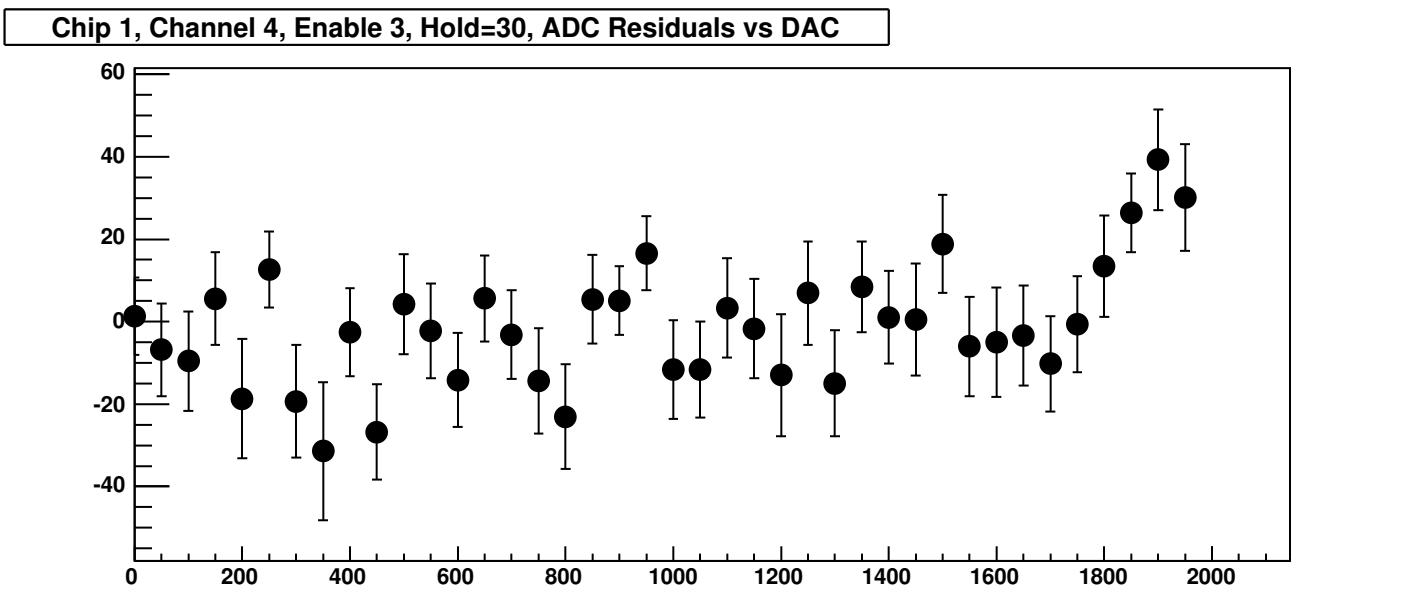

Chip 1, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 11.83 / 11
p0 -1678 ± 19.38
p1 0.1624 ± 0.01746
-1350
-1400
-1450
-1500
-1550
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

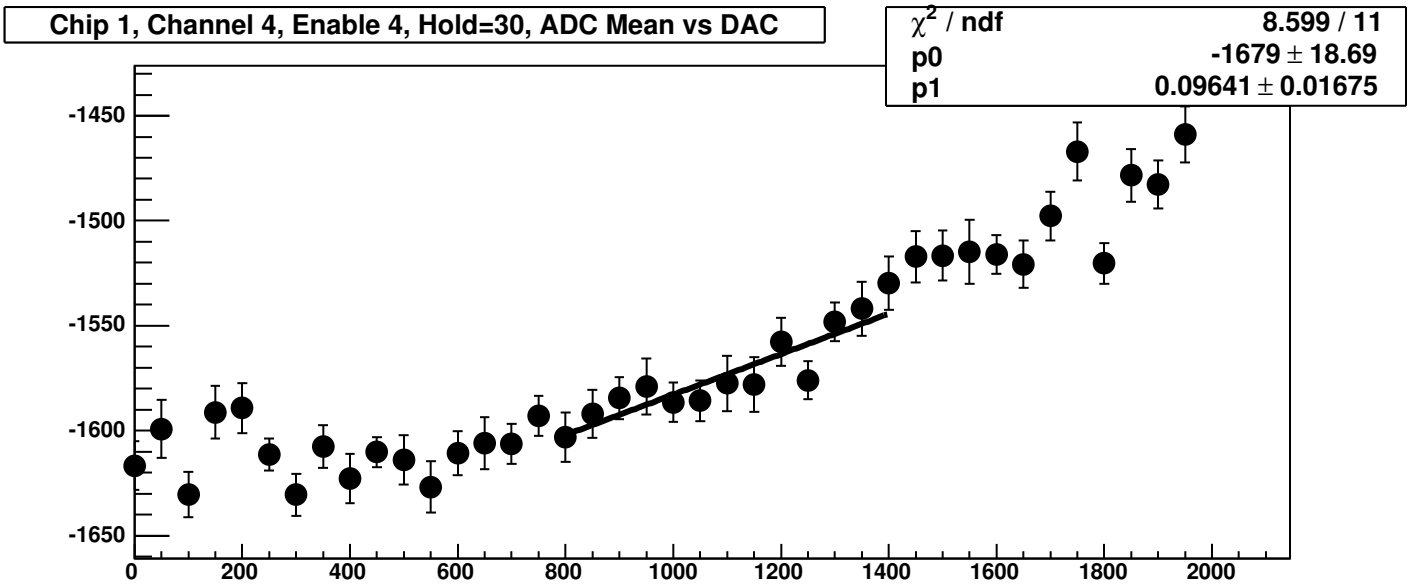

Chip 1, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

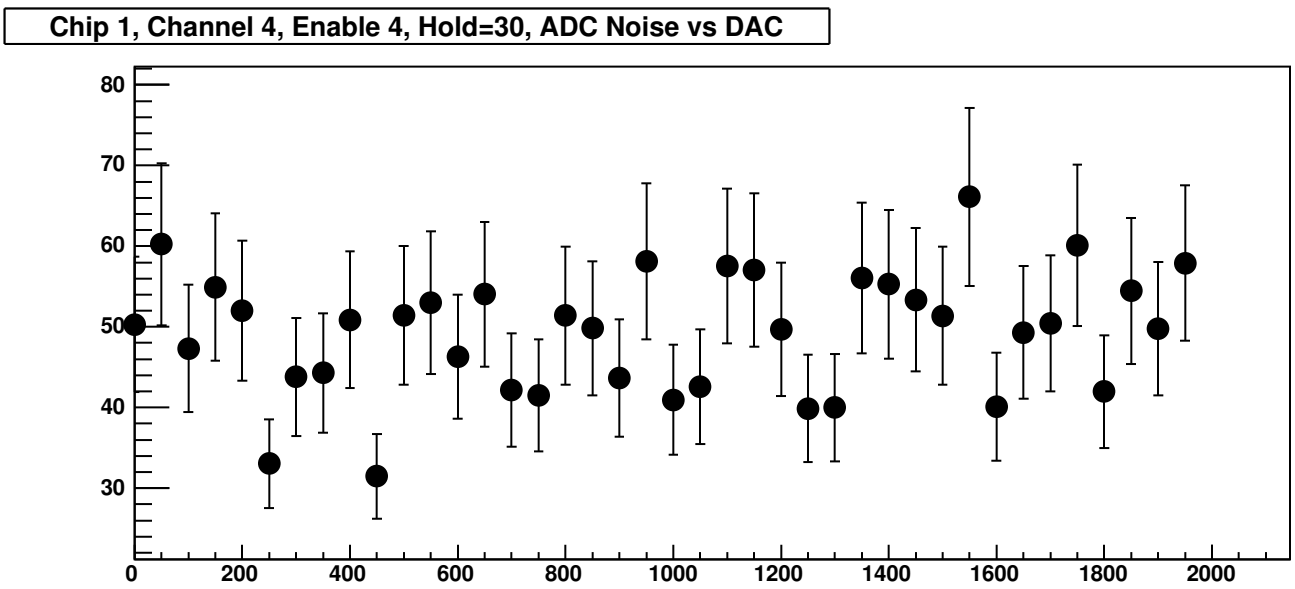

Chip 1, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

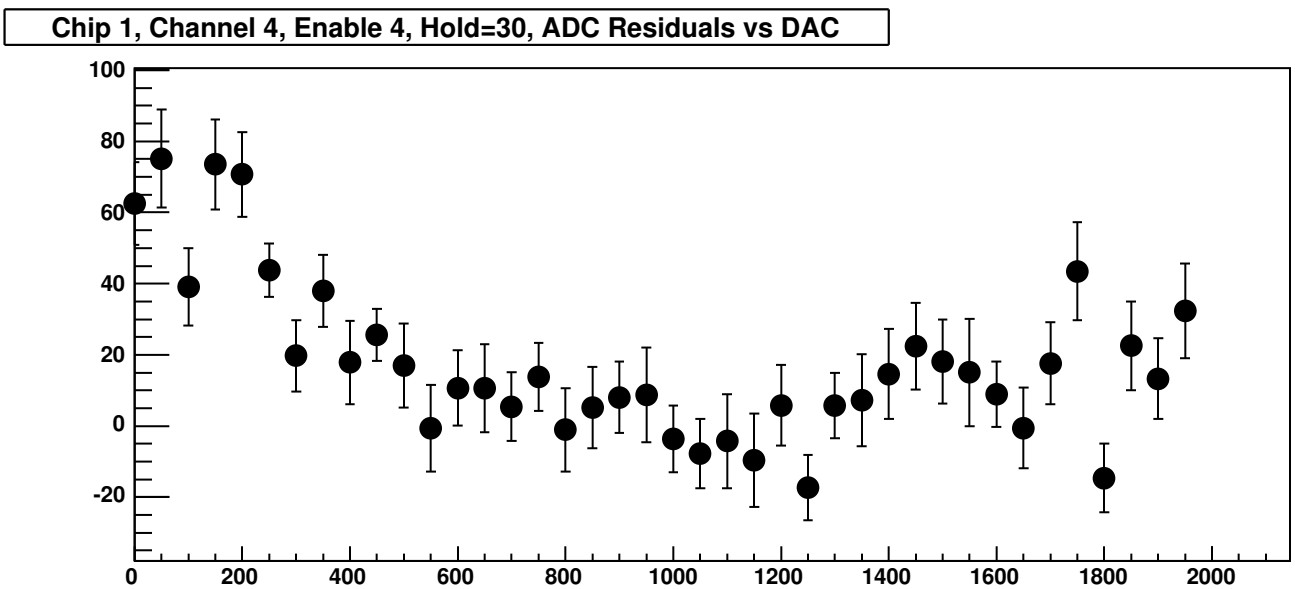

Chip 1, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 12.4 / 11
p0 -1636 ± 18.06
p1 0.03033 ± 0.01653
-1520
-1540
-1560
-1580
-1600
-1620
-1640
-1660
-1680
0
200
400
600
800
1000
1200
1400
1600
1800
2000

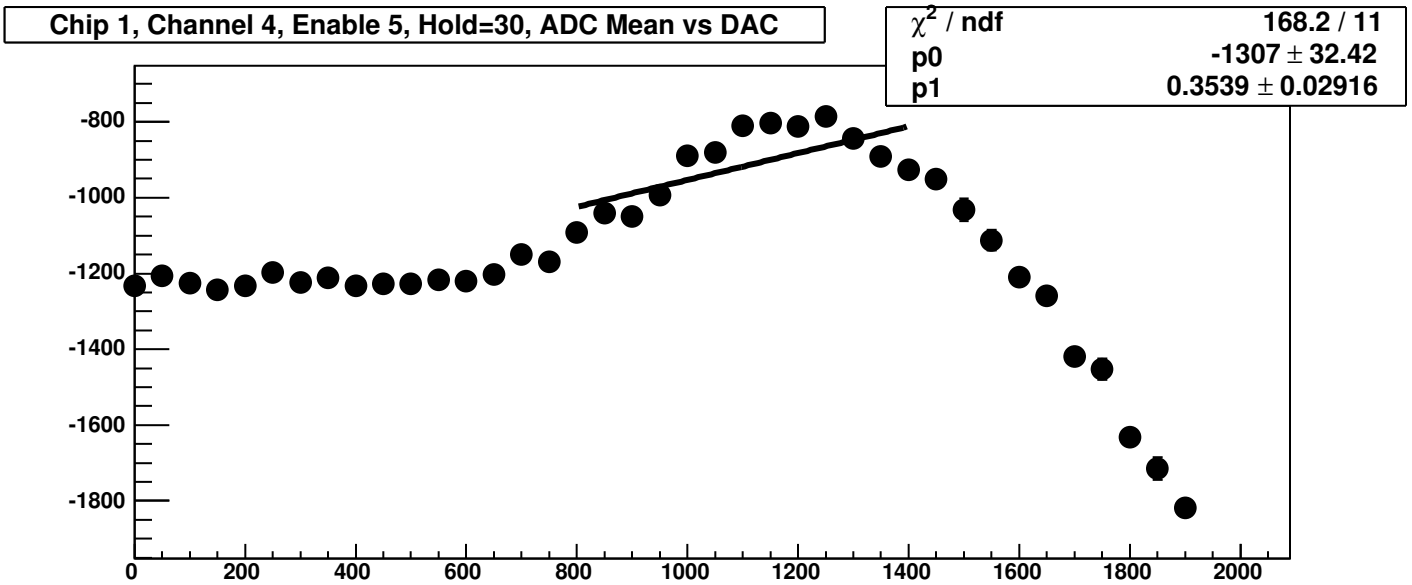

Chip 1, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

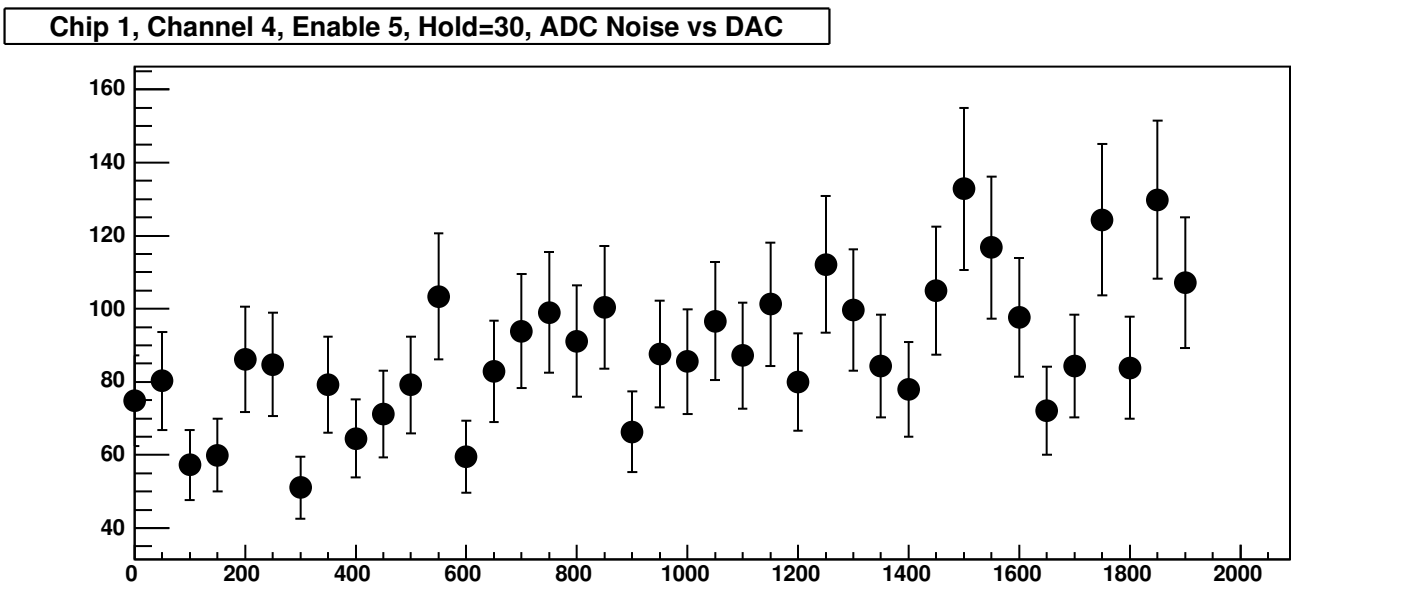

Chip 1, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

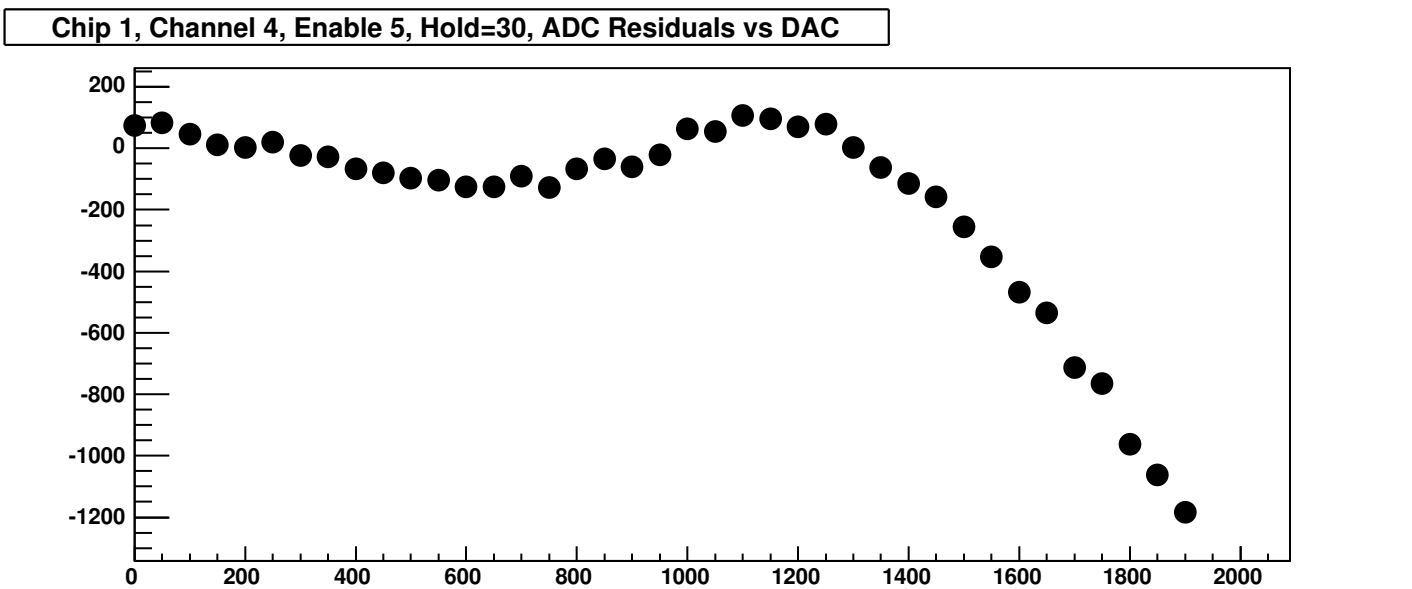

Chip 1, Channel 4, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 8.599 / 11
p0 -1679 ± 18.69
p1 0.09641 ± 0.01675
-1450
-1500
-1550
-1600
-1650
0
200
400
600
800
1000
1200
1400
1600
1800
2000

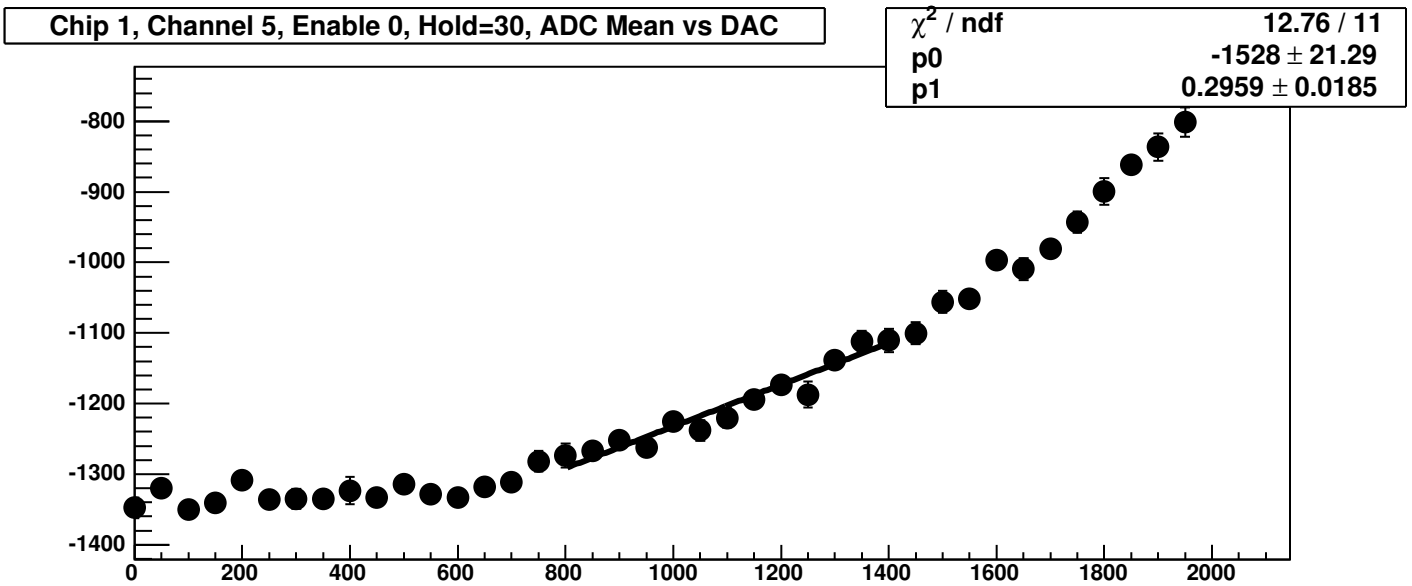

Chip 1, Channel 4, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

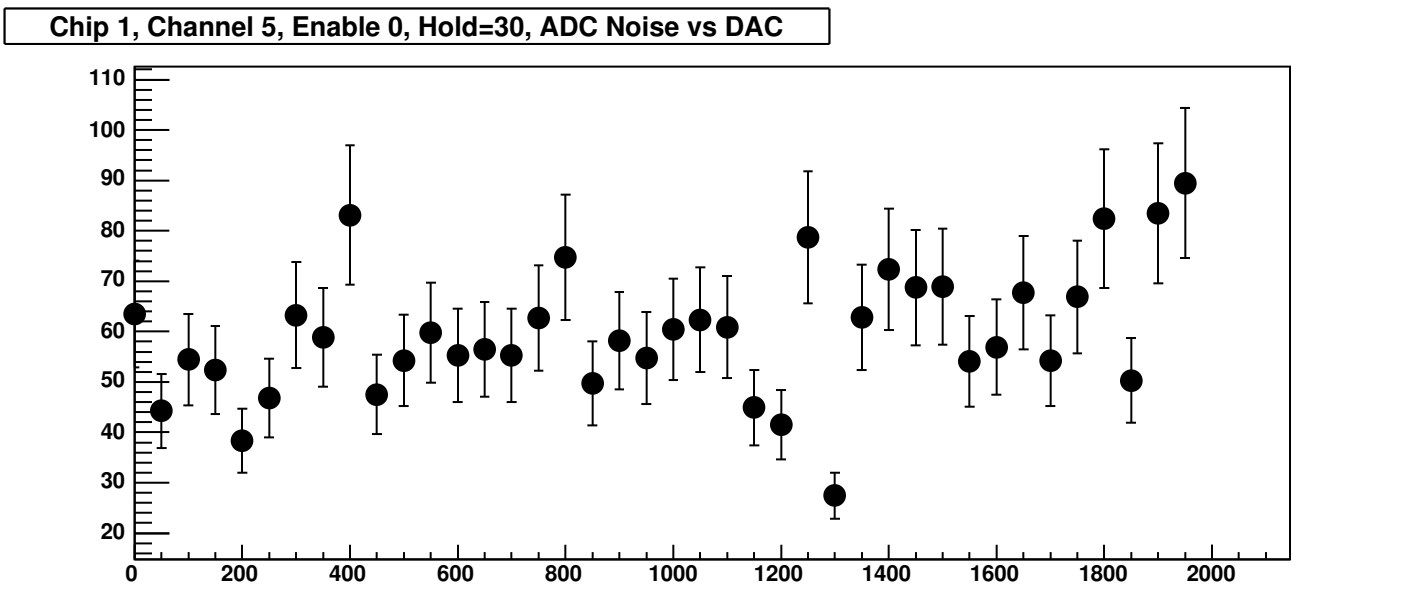

Chip 1, Channel 4, Enable 4, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

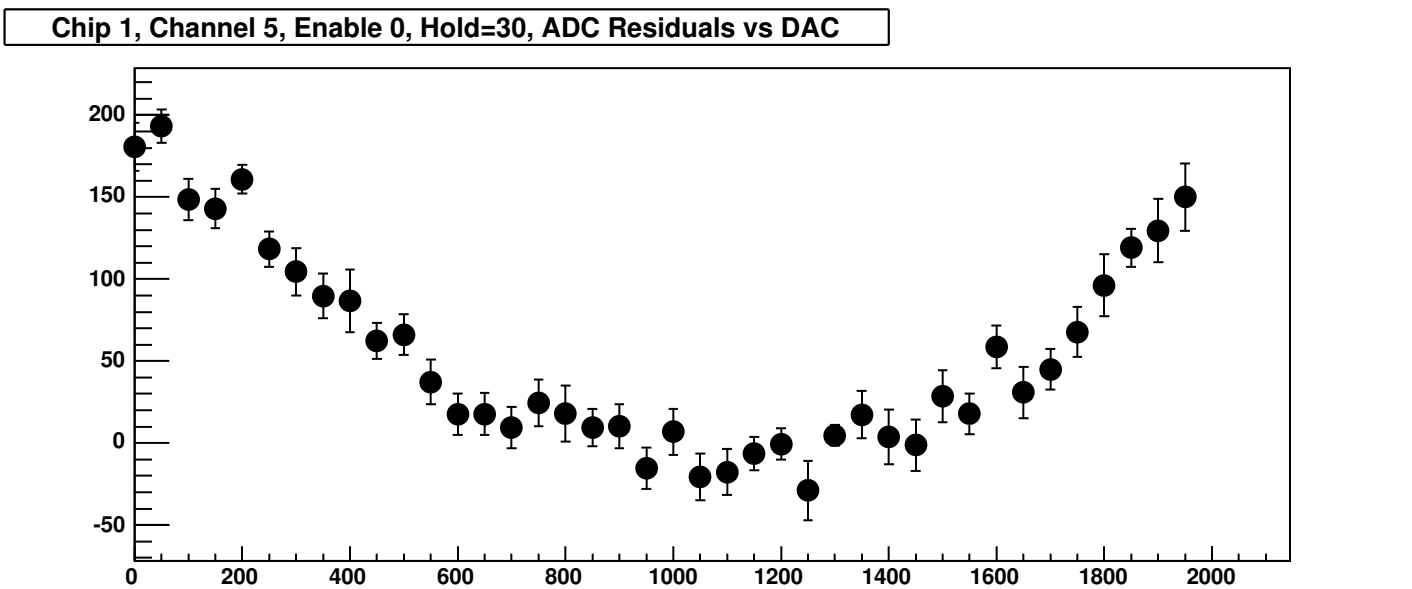

Chip 1, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 168.2 / 11
p0 -1307 ± 32.42
p1 0.3539 ± 0.02916
-800
-1000
-1200
-1400
-1600
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

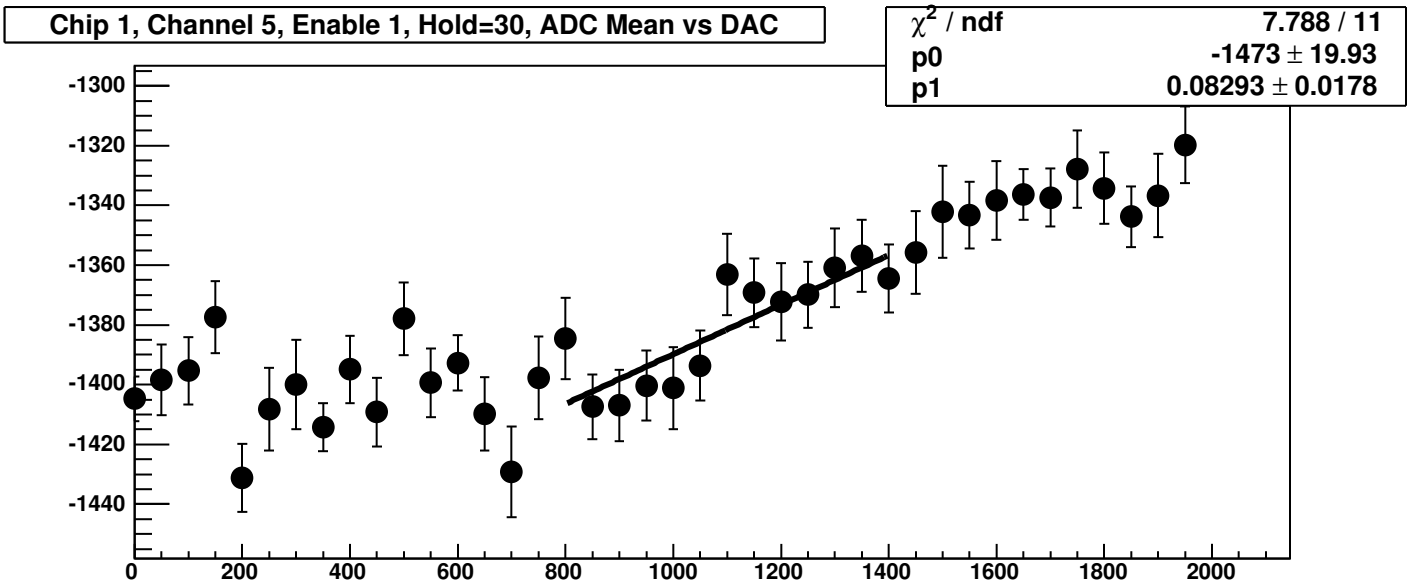

Chip 1, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

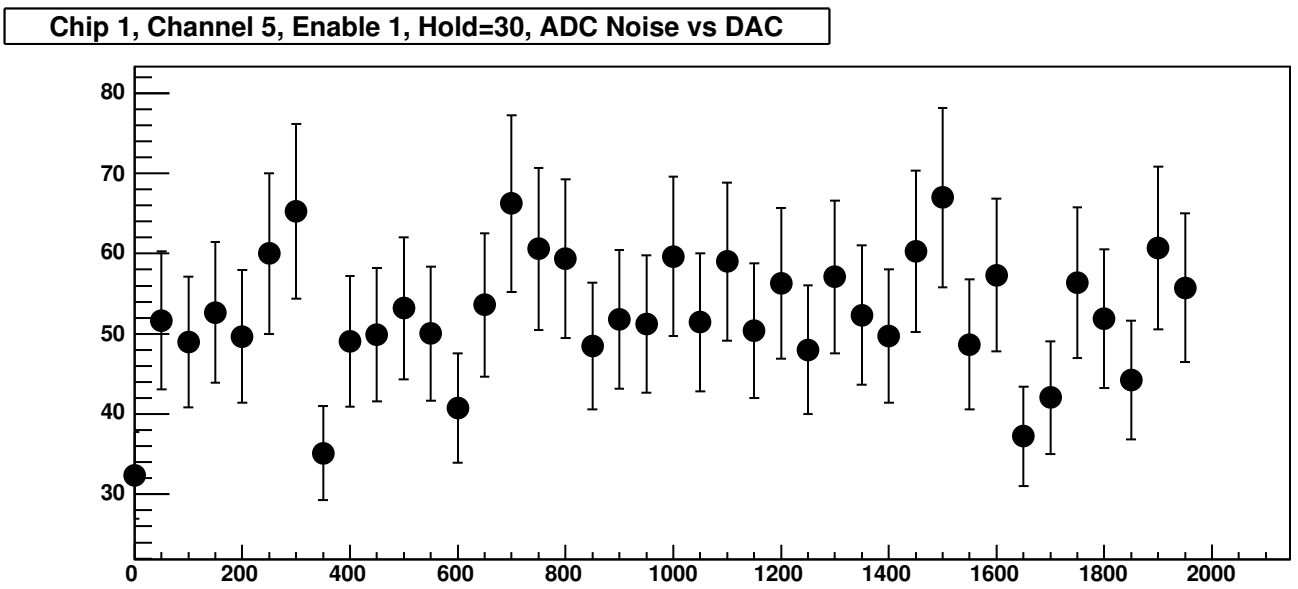

Chip 1, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

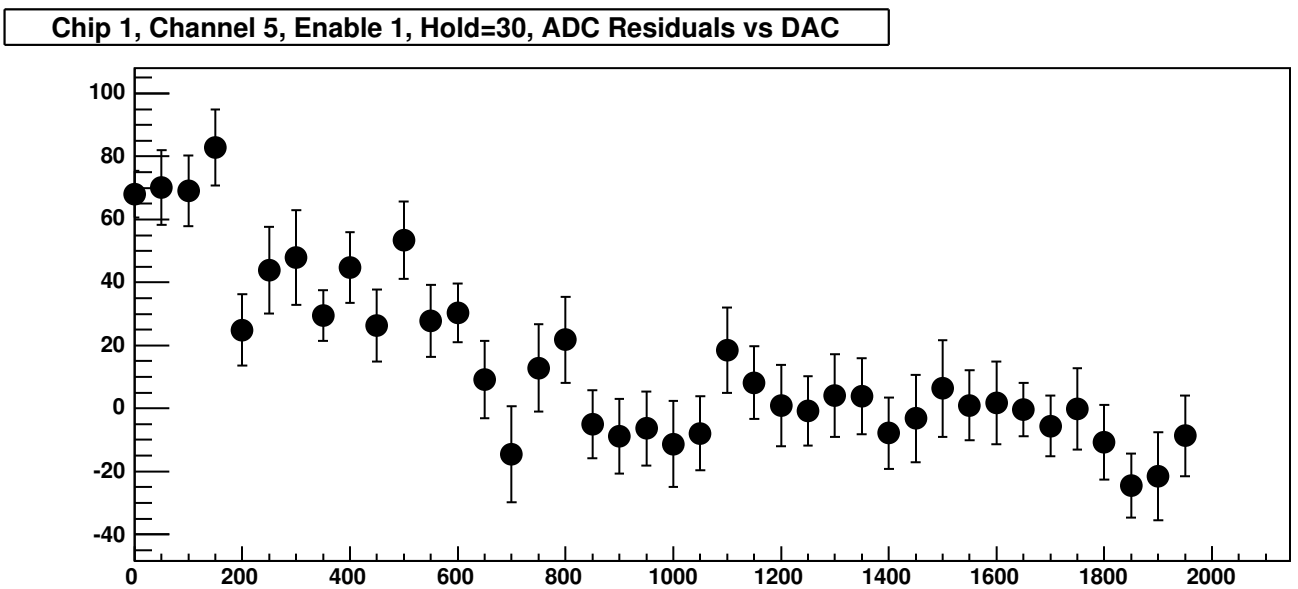

Chip 1, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 12.76 / 11
p0 -1528 ± 21.29
p1 0.2959 ± 0.0185
-800
-900
-1000
-1100
-1200
-1300
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

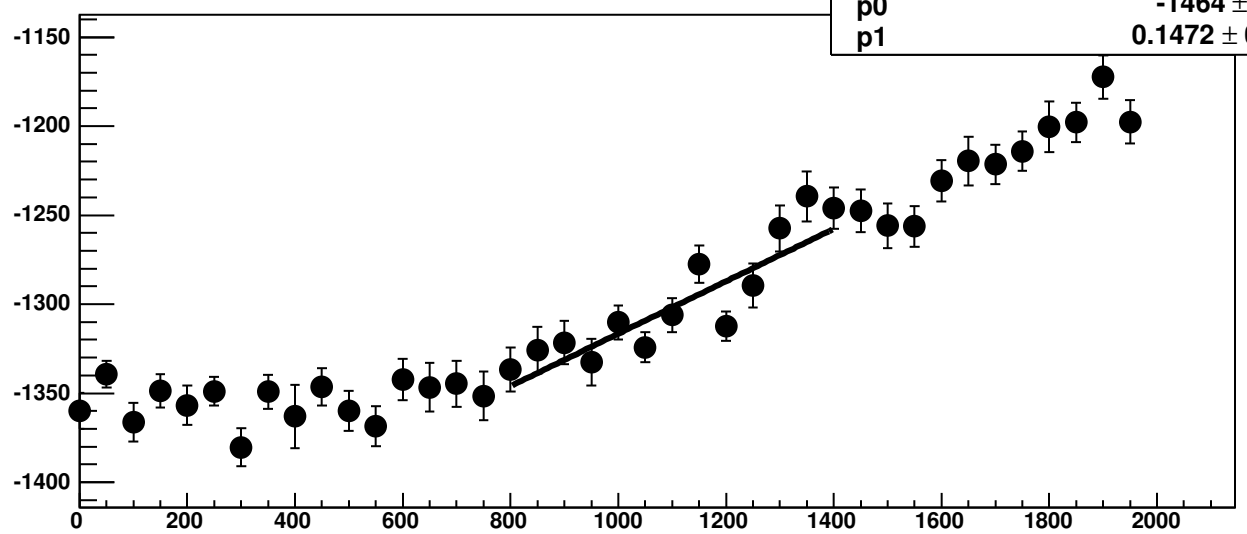

Chip 1, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 5, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7.788 / 11
p0 -1473 ± 19.93
p1 0.08293 ± 0.0178
-1300
-1320
-1340
-1360
-1380
-1400
-1420
-1440
0
200
400
600
800
1000
1200
1400
1600
1800
2000

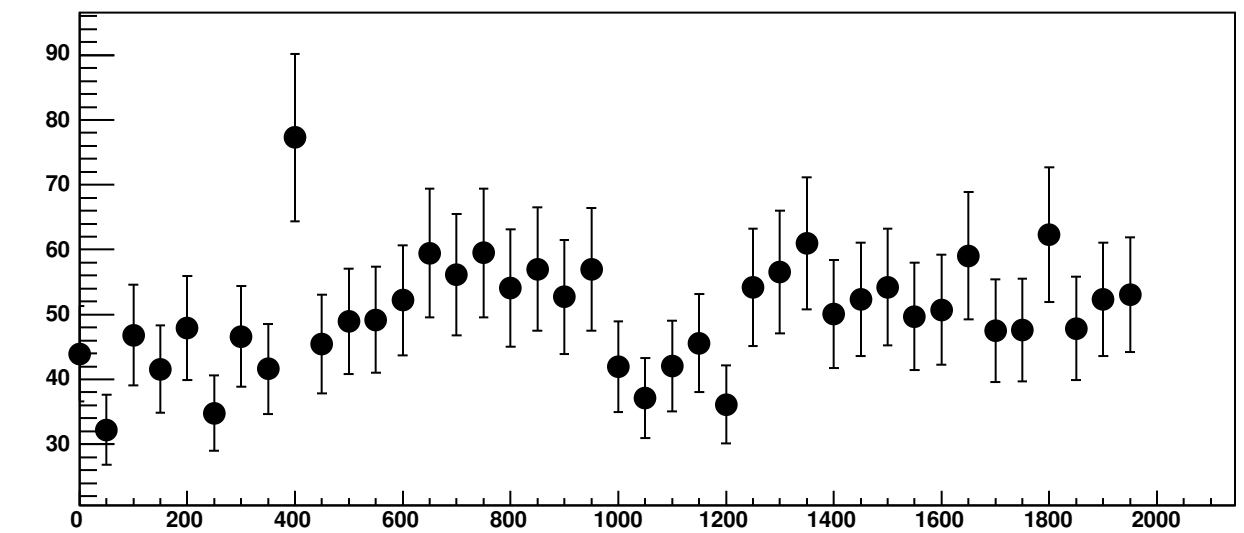

Chip 1, Channel 5, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 5, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

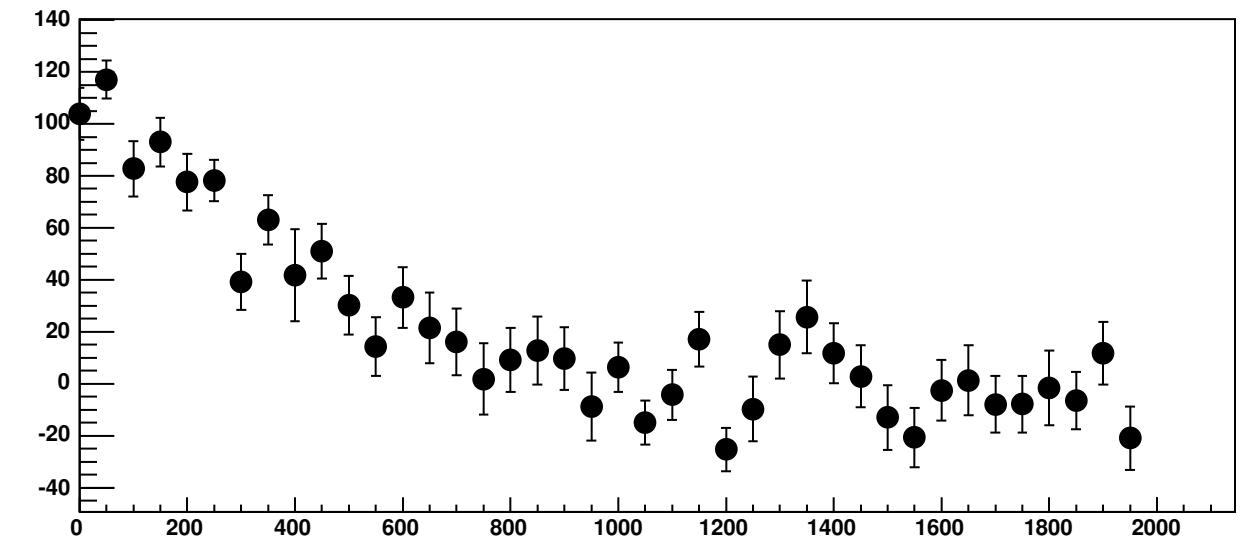
Chip 1, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC



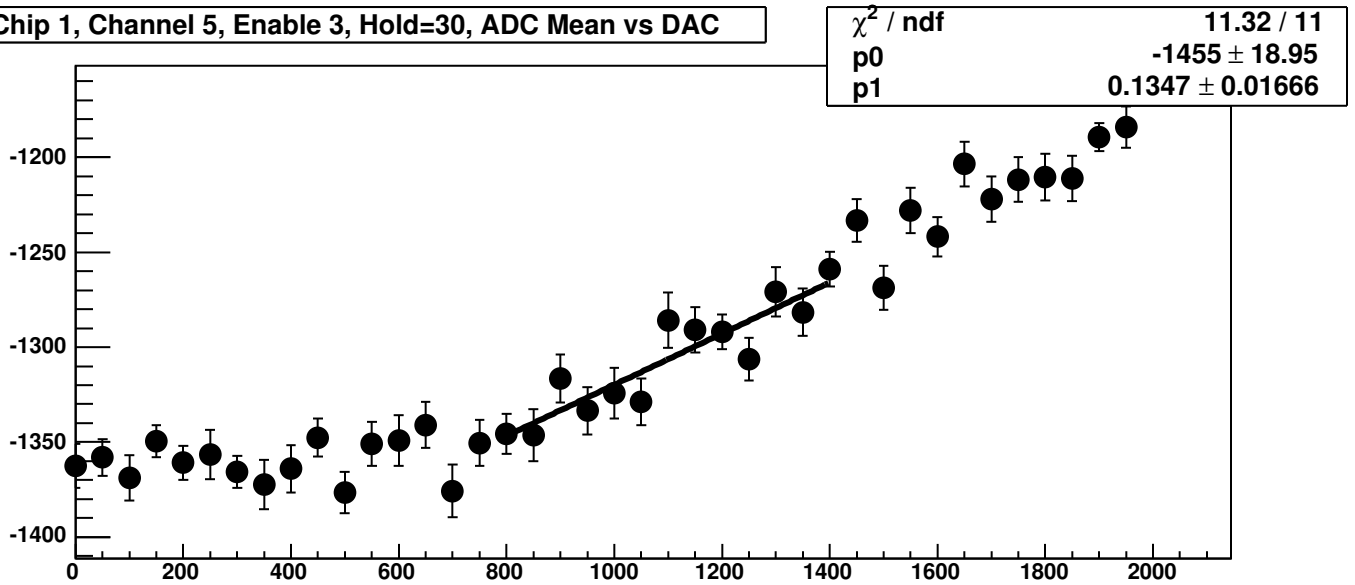
Chip 1, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC



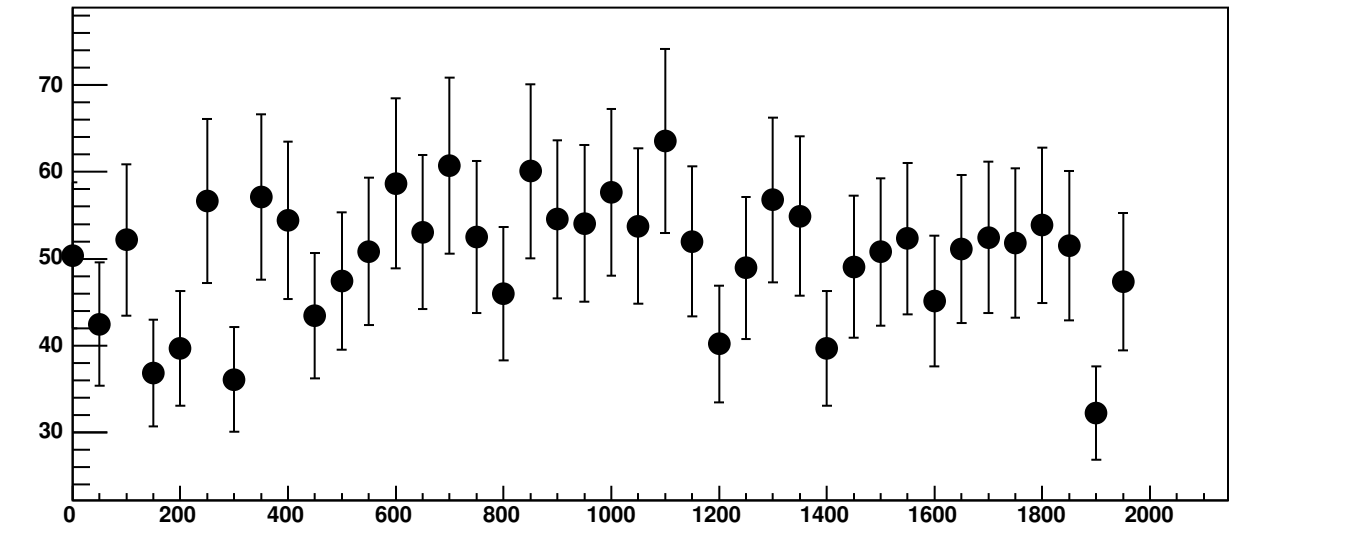
Chip 1, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC



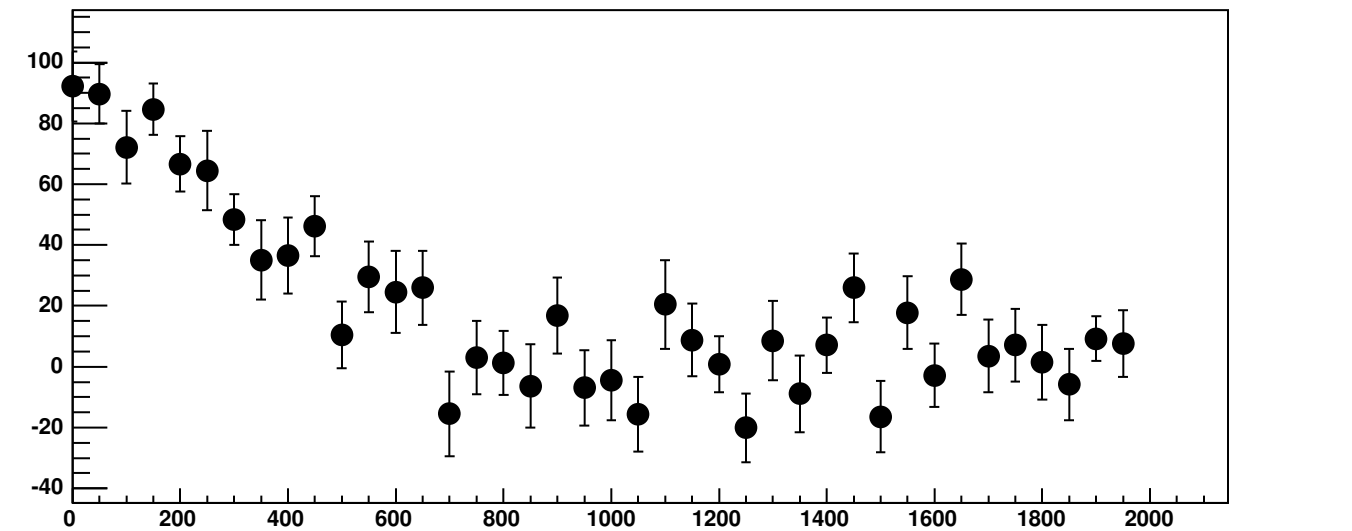
Chip 1, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC

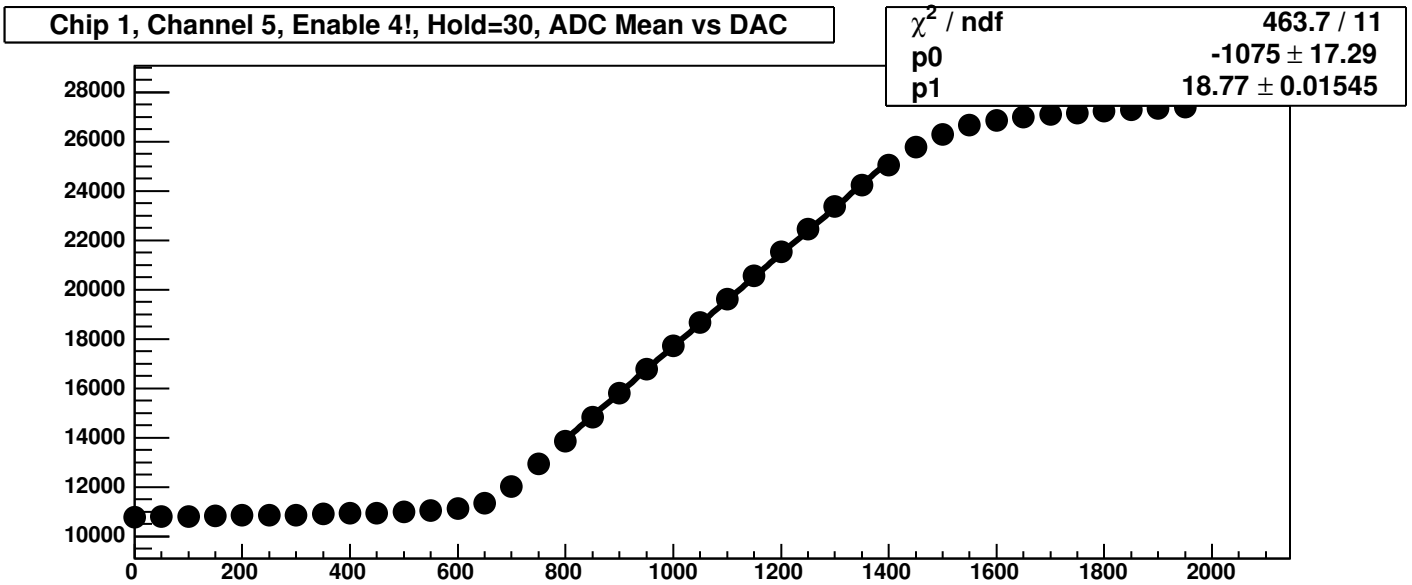


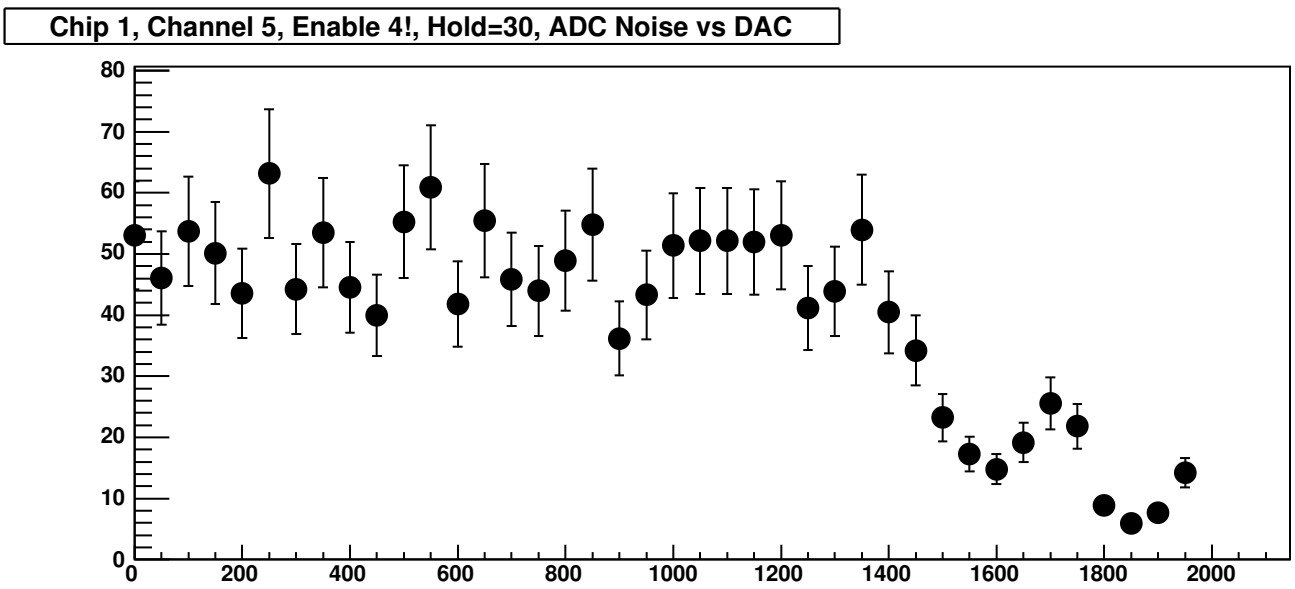
Chip 1, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC

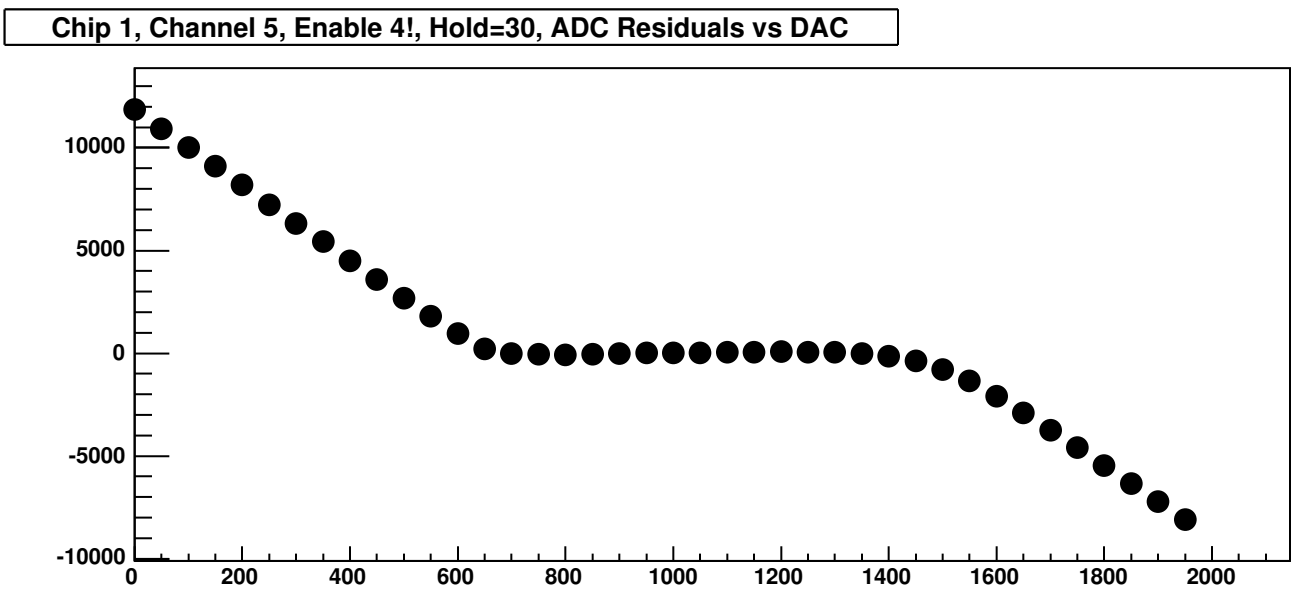


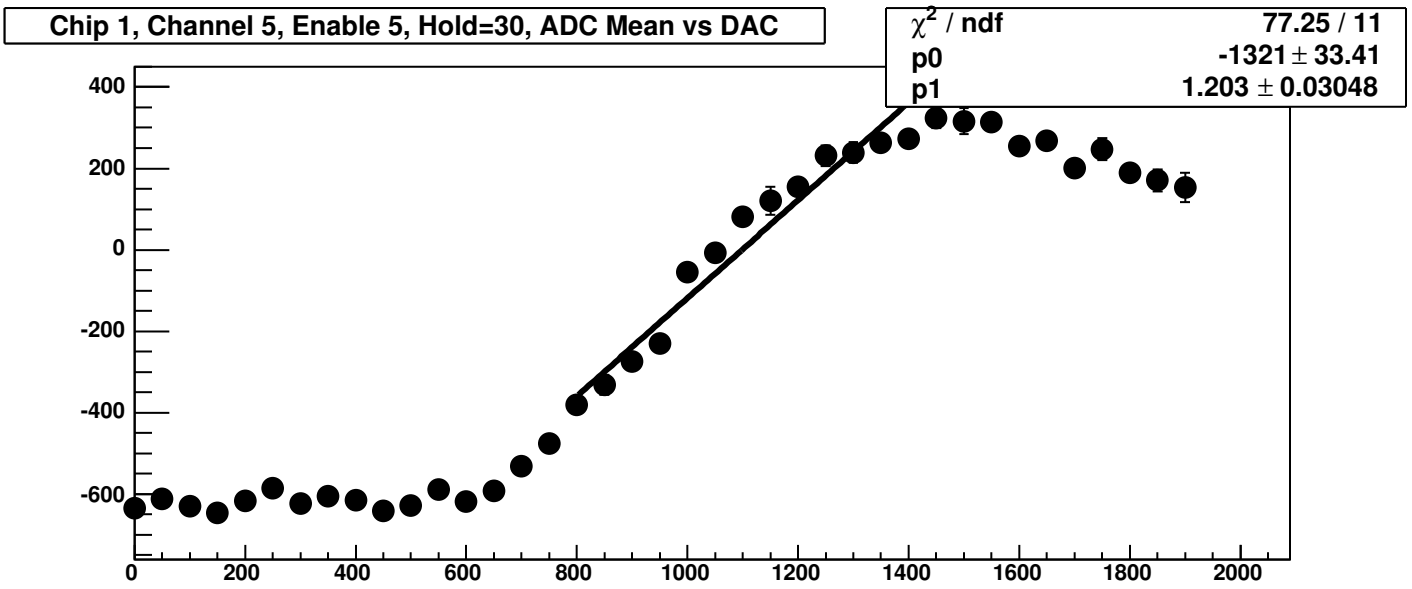
Chip 1, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC

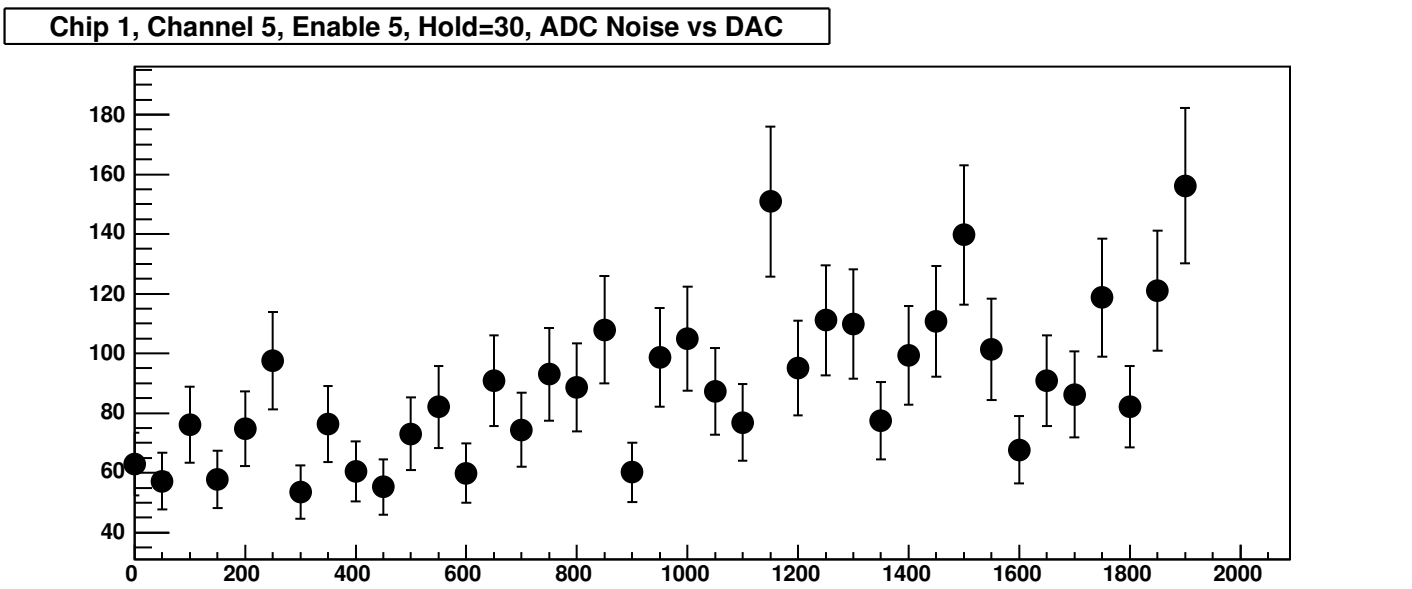


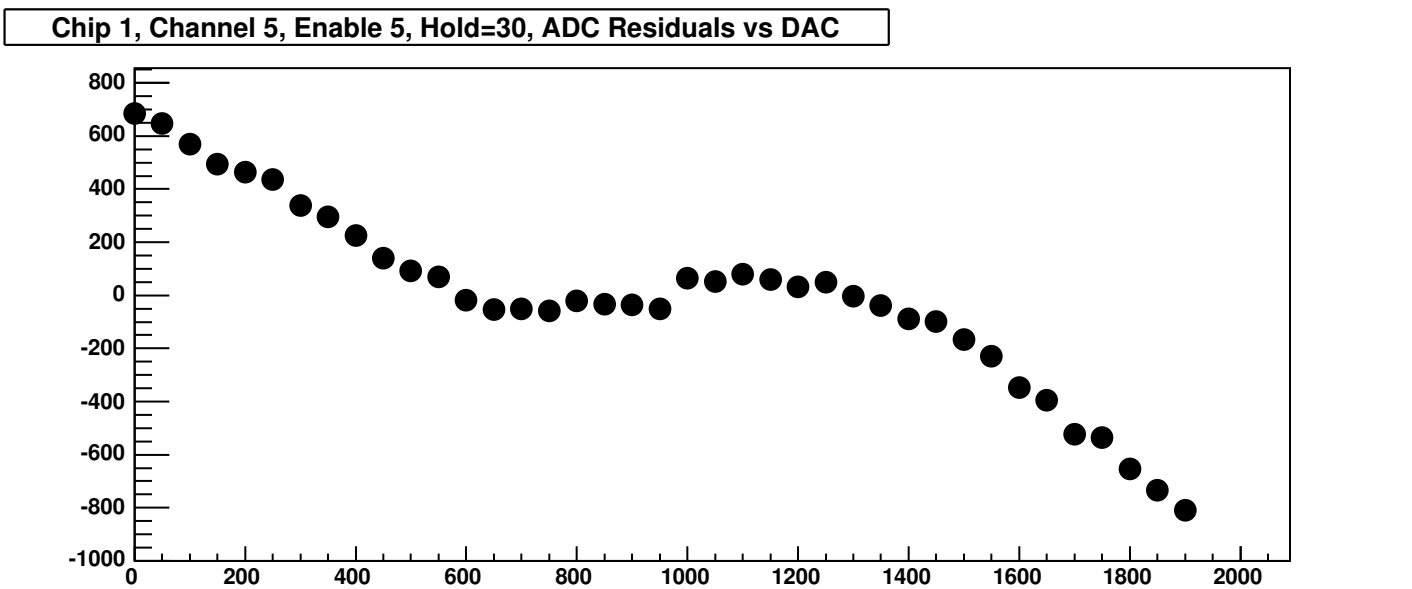

Chip 1, Channel 5, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 463.7 / 11
p0 -1075 ± 17.29
p1 18.77 ± 0.01545
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

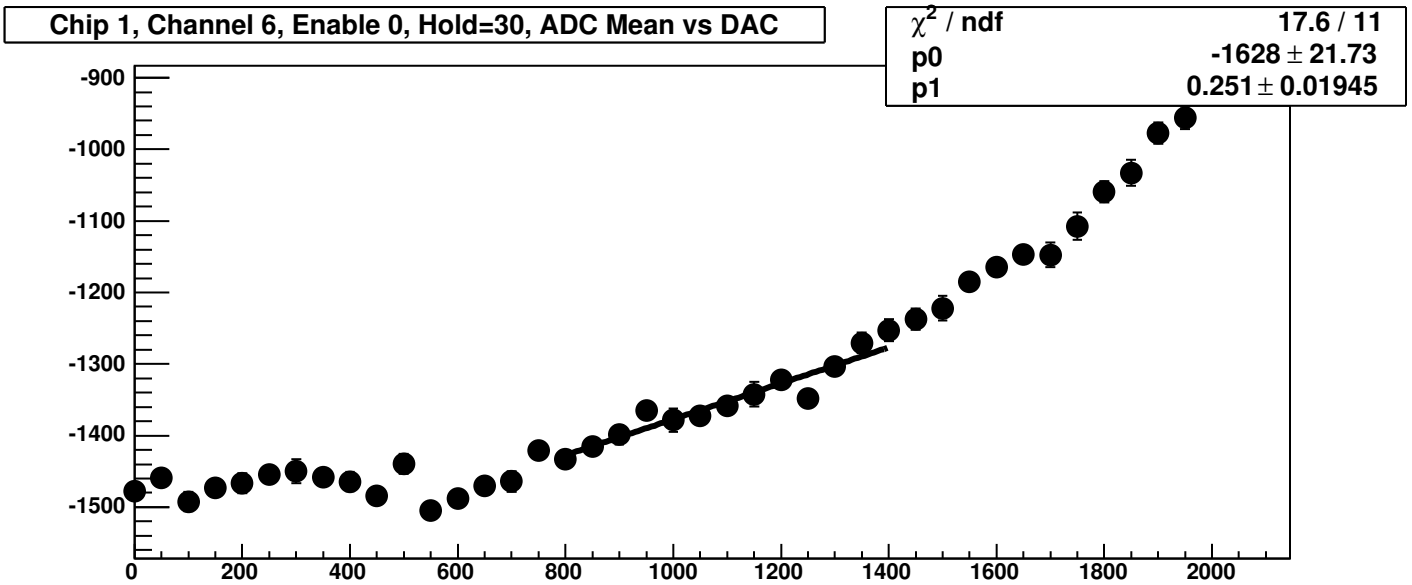

Chip 1, Channel 5, Enable 4!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

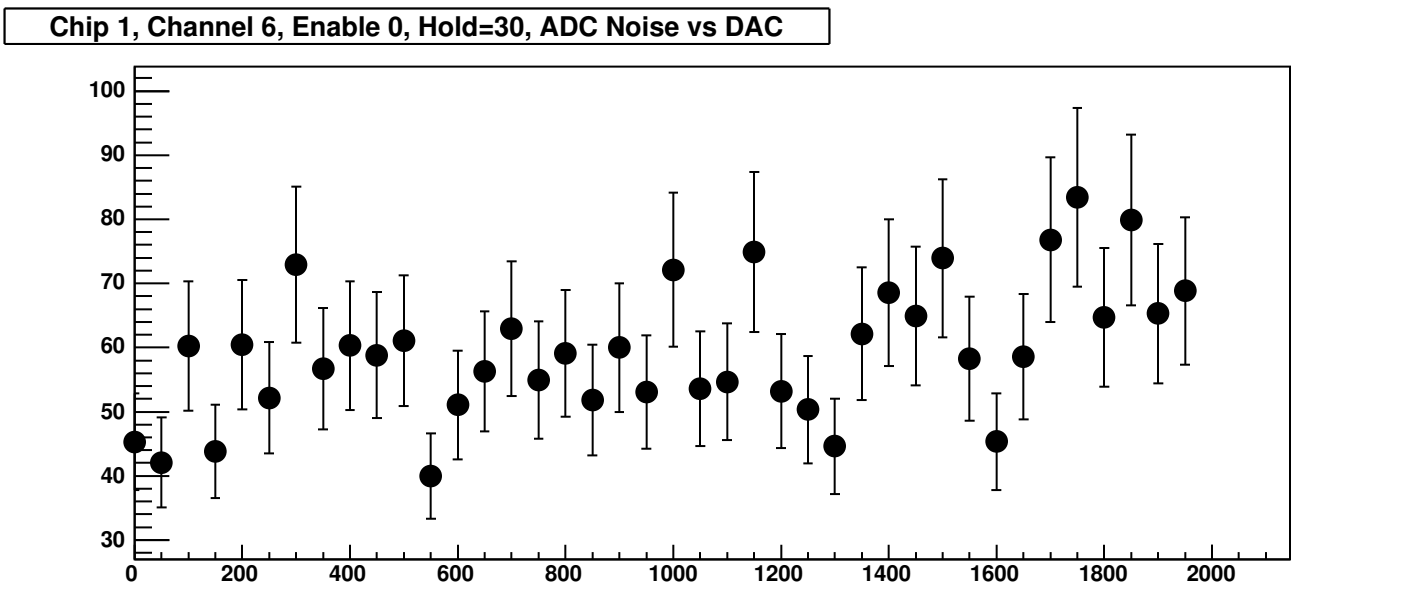

Chip 1, Channel 5, Enable 4!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

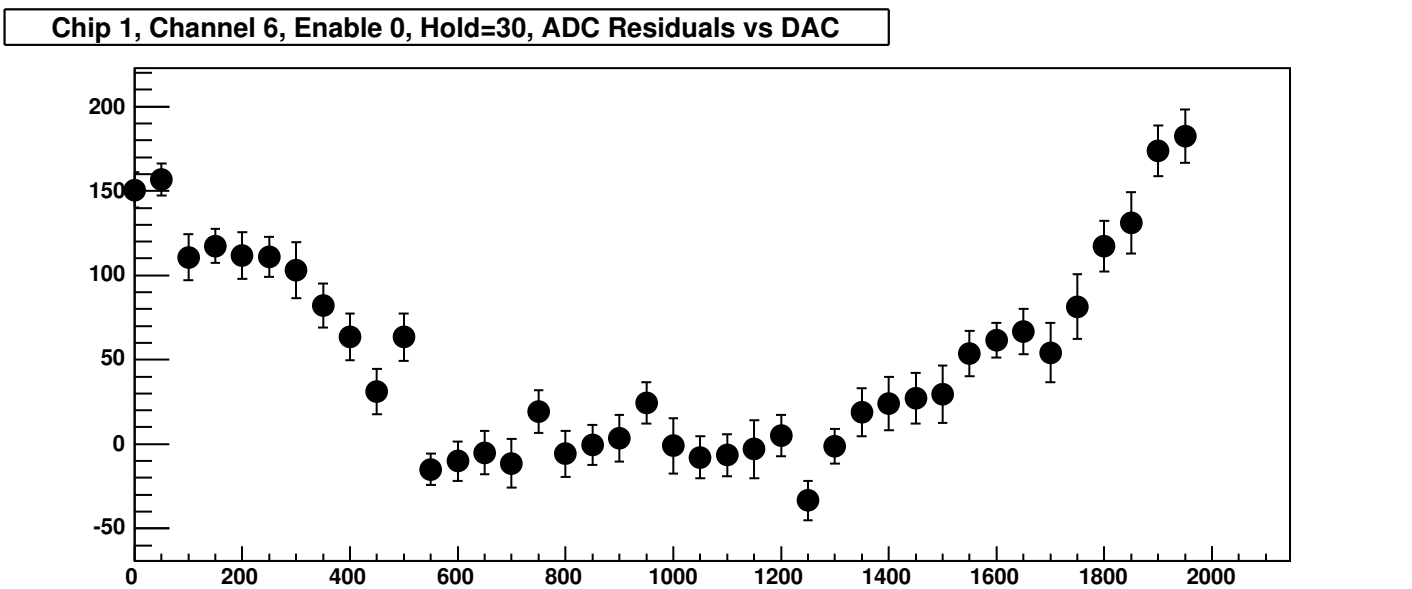

Chip 1, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 77.25 / 11
p0 -1321 ± 33.41
p1 1.203 ± 0.03048
400
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

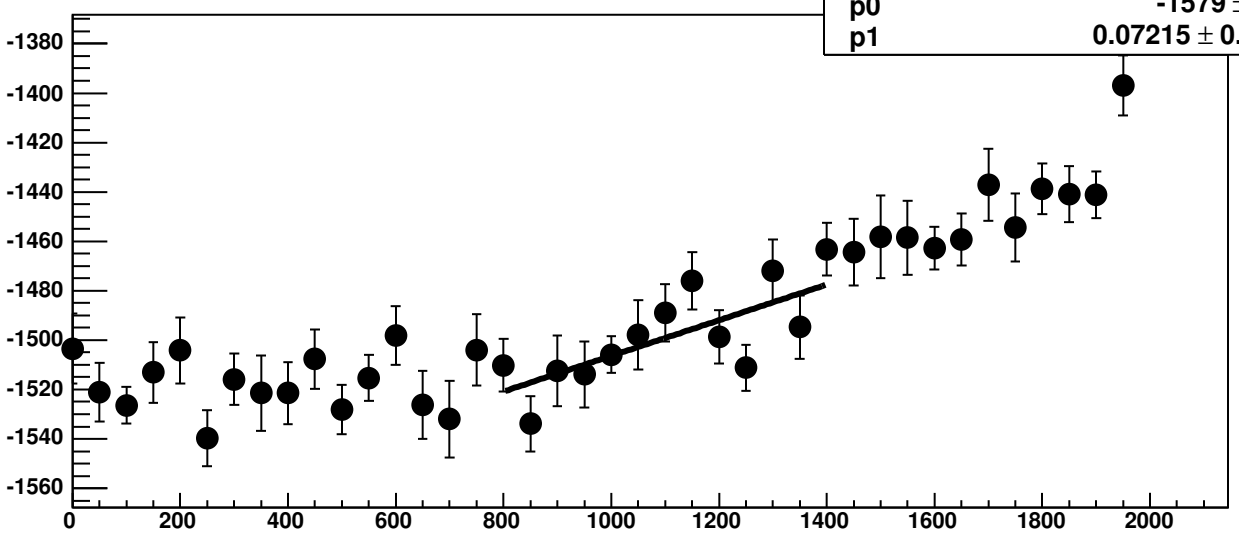

Chip 1, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 17.6 / 11
p0 -1628 ± 21.73
p1 0.251 ± 0.01945
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

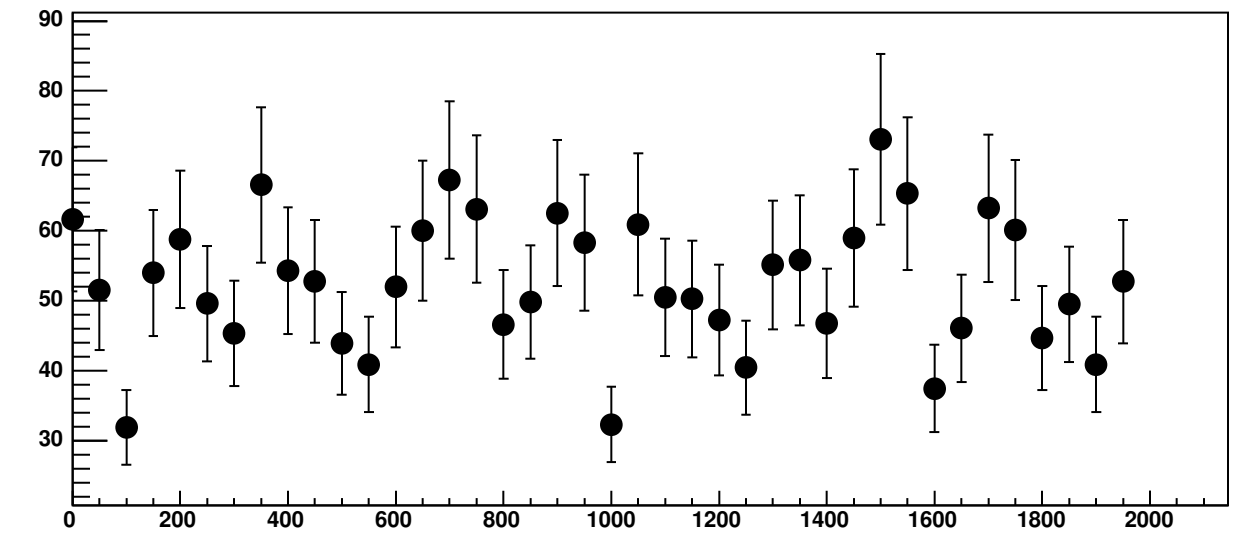

Chip 1, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

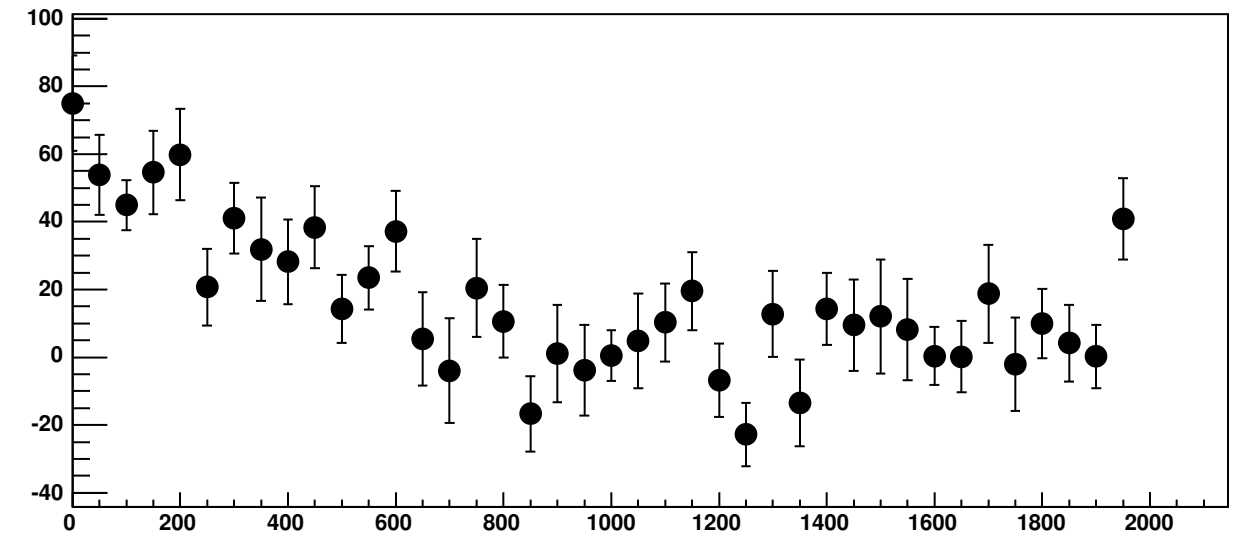
Chip 1, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC

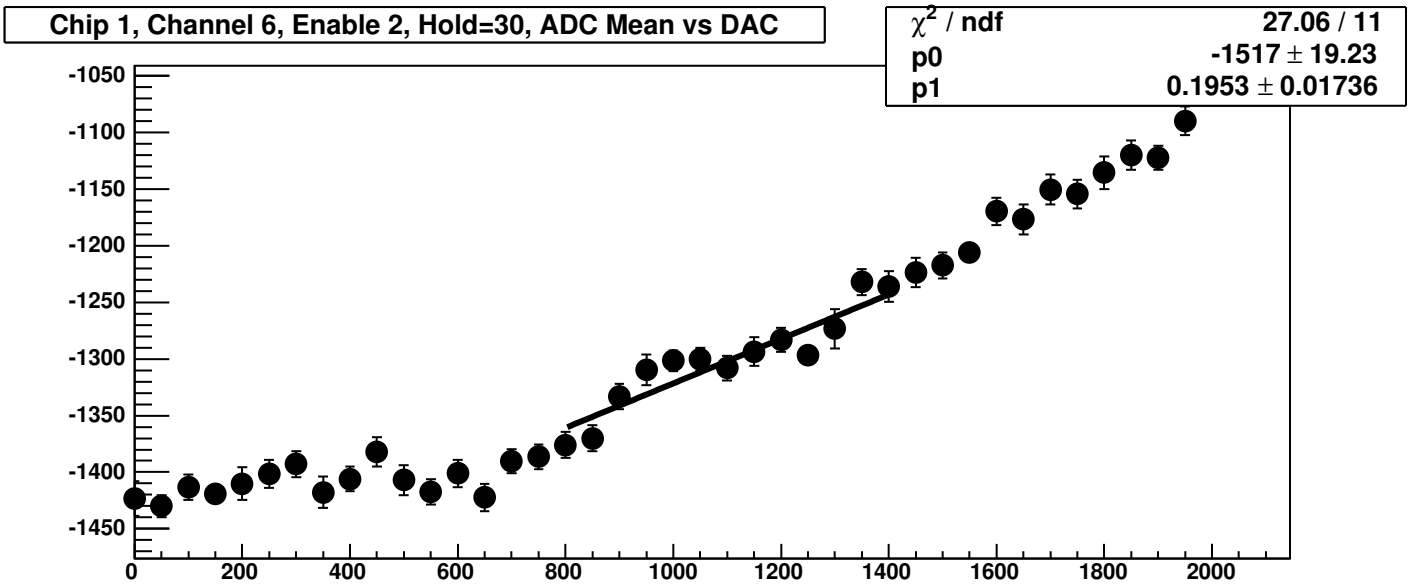


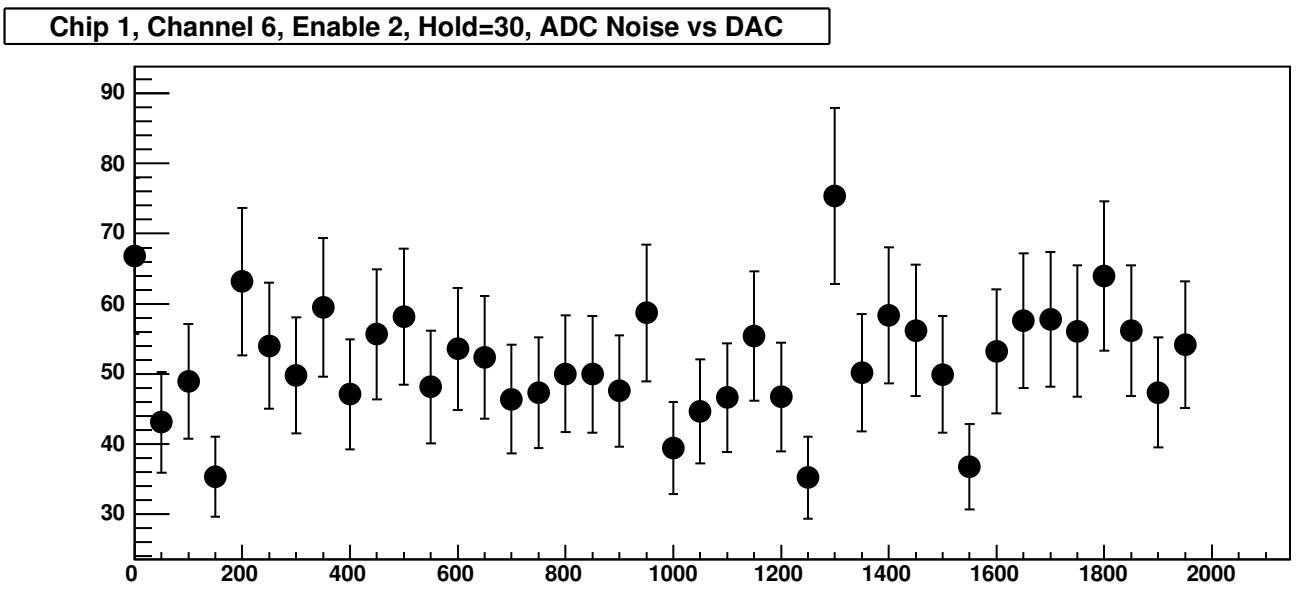
Chip 1, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC

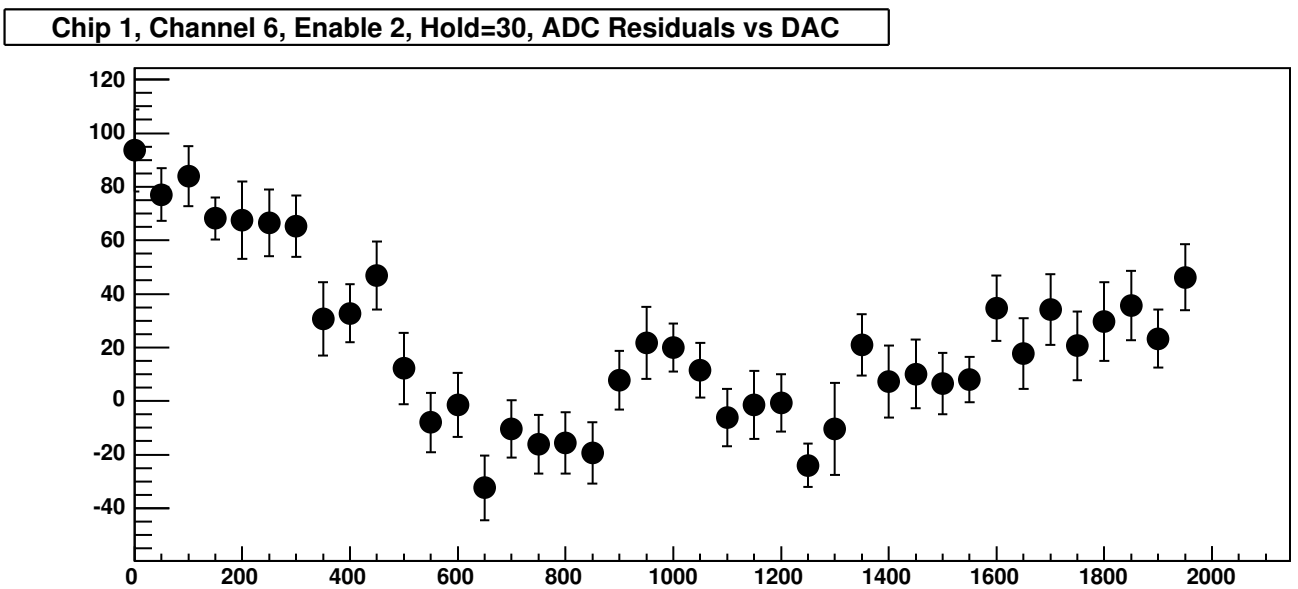


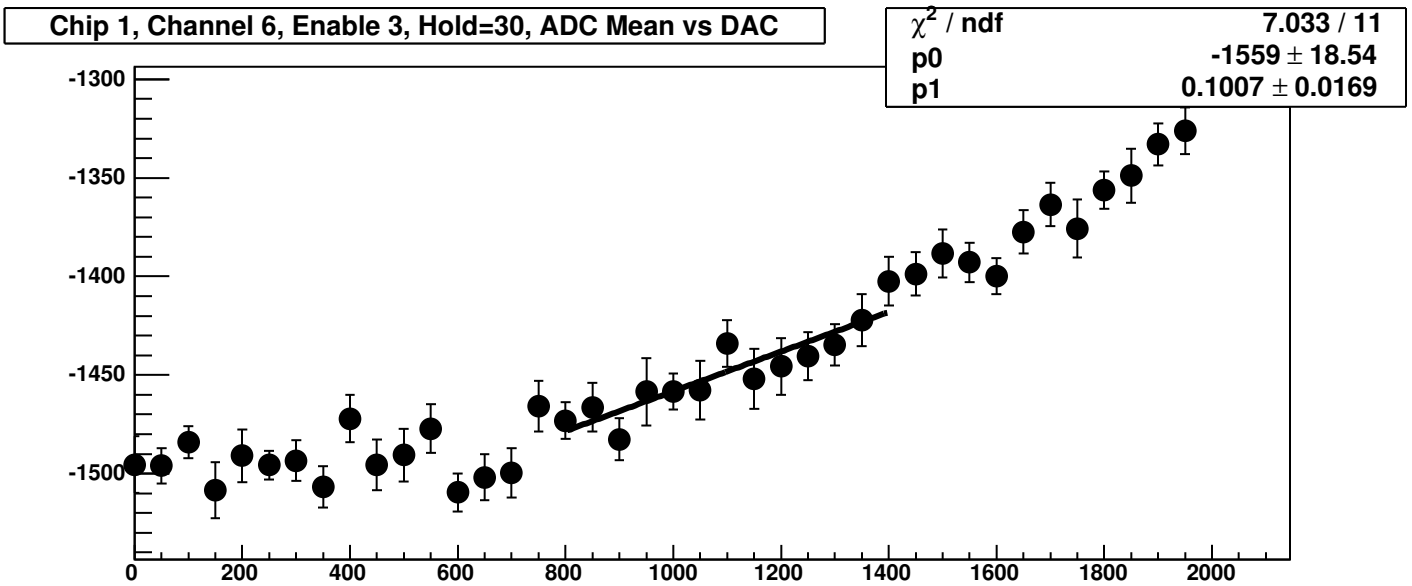
Chip 1, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC

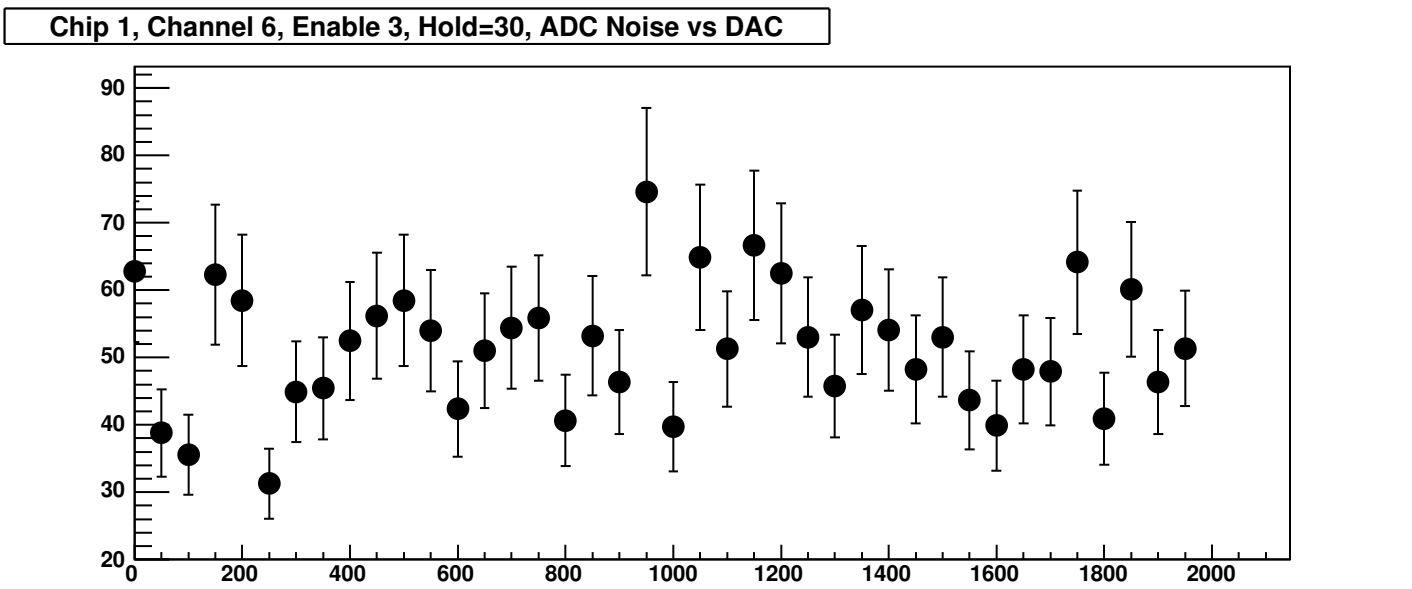


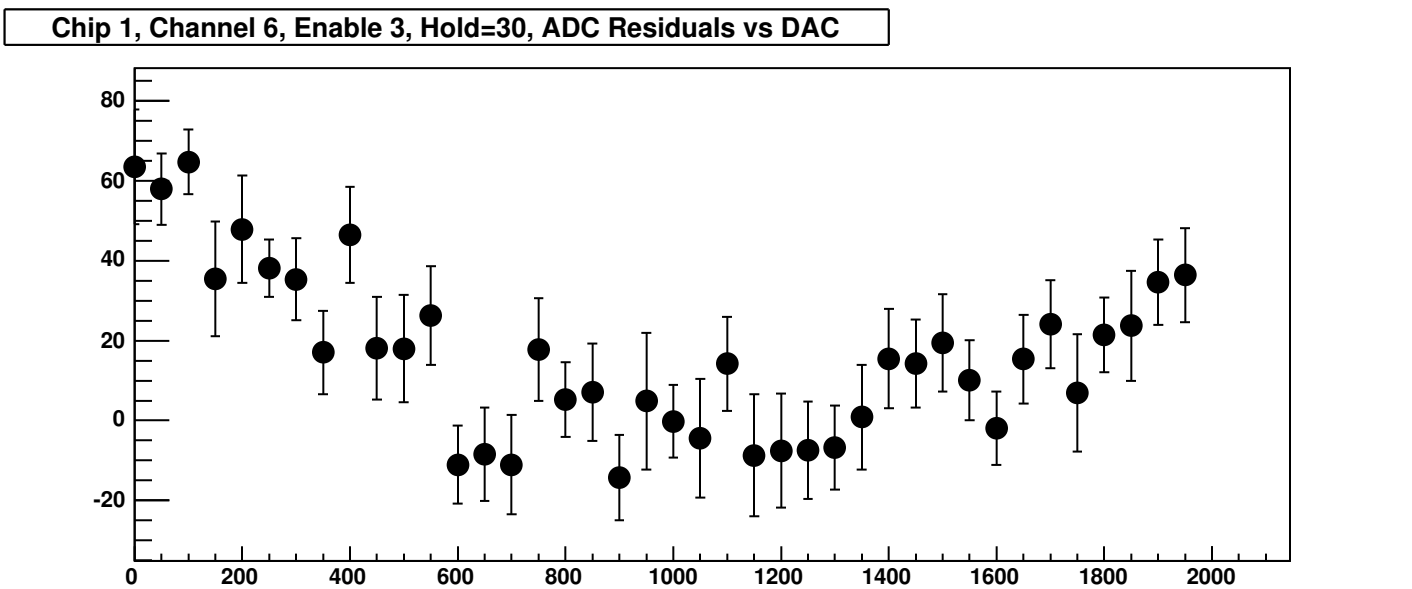

Chip 1, Channel 6, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 27.06 / 11
p0 -1517 ± 19.23
p1 0.1953 ± 0.01736
-1050
-1100
-1150
-1200
-1250
-1300
-1350
-1400
-1450
0
200
400
600
800
1000
1200
1400
1600
1800
2000

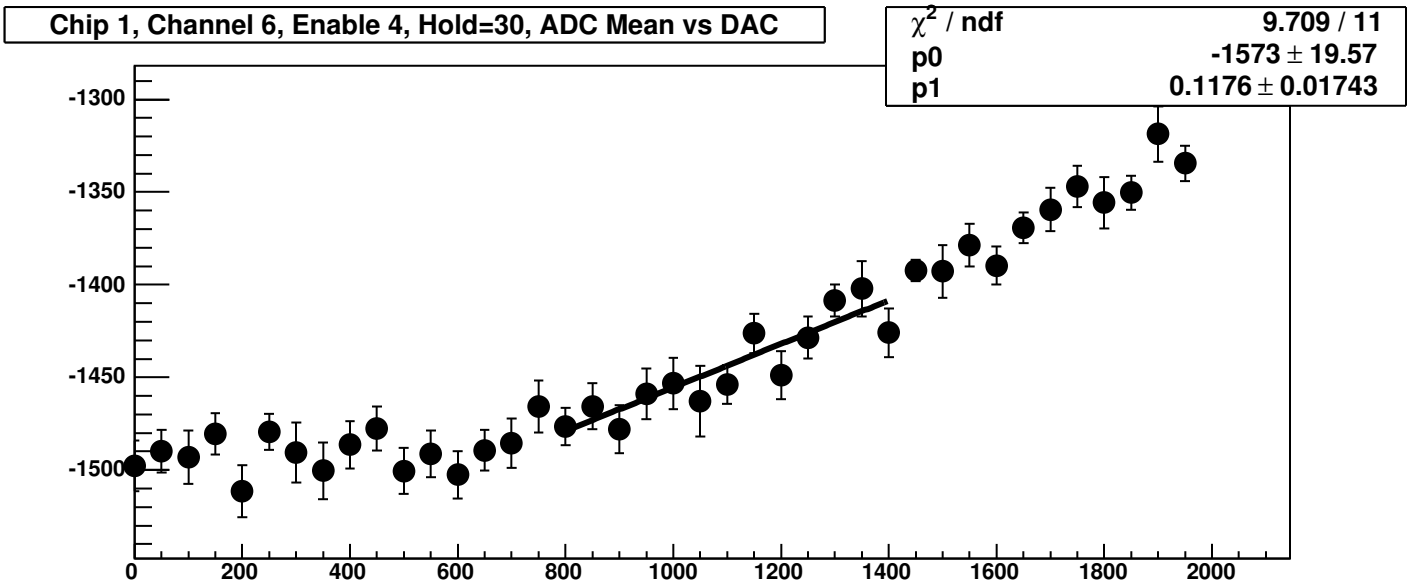

Chip 1, Channel 6, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

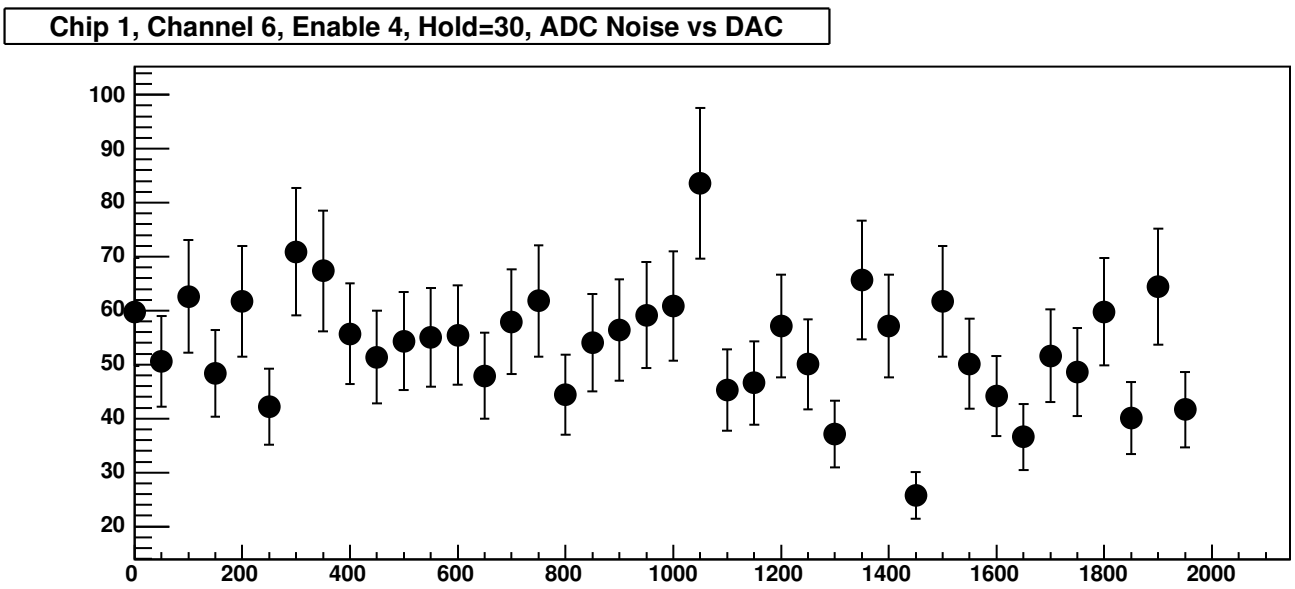

Chip 1, Channel 6, Enable 2, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

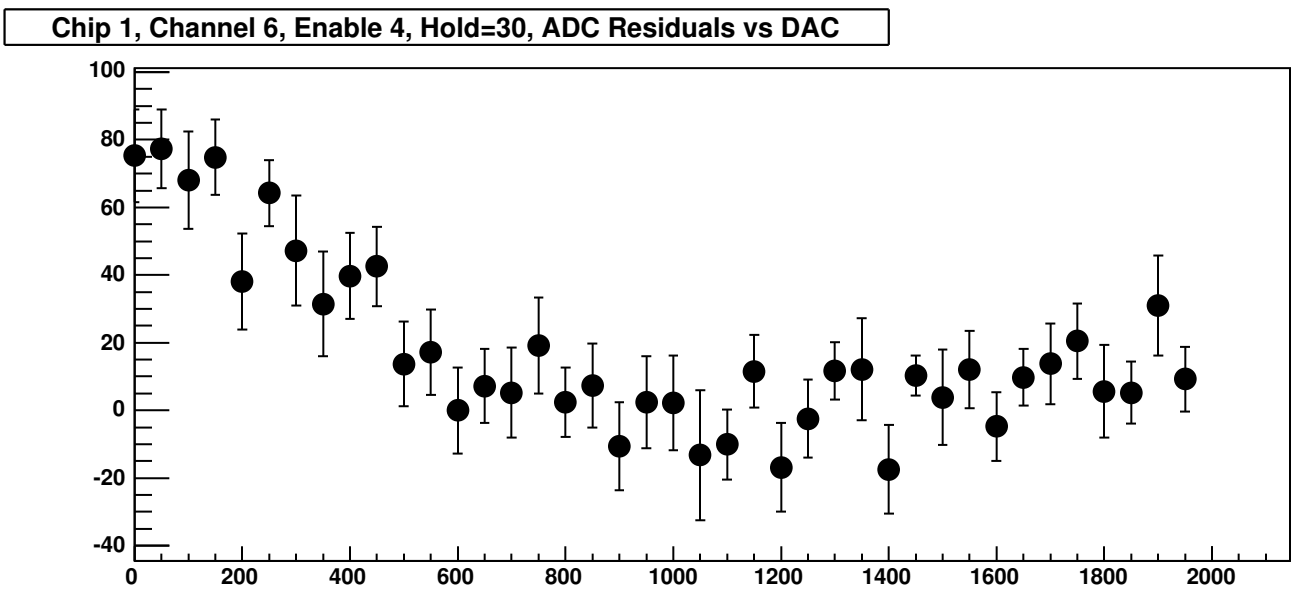

Chip 1, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7.033 / 11
p0 -1559 ± 18.54
p1 0.1007 ± 0.0169
-1300
-1350
-1400
-1450
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

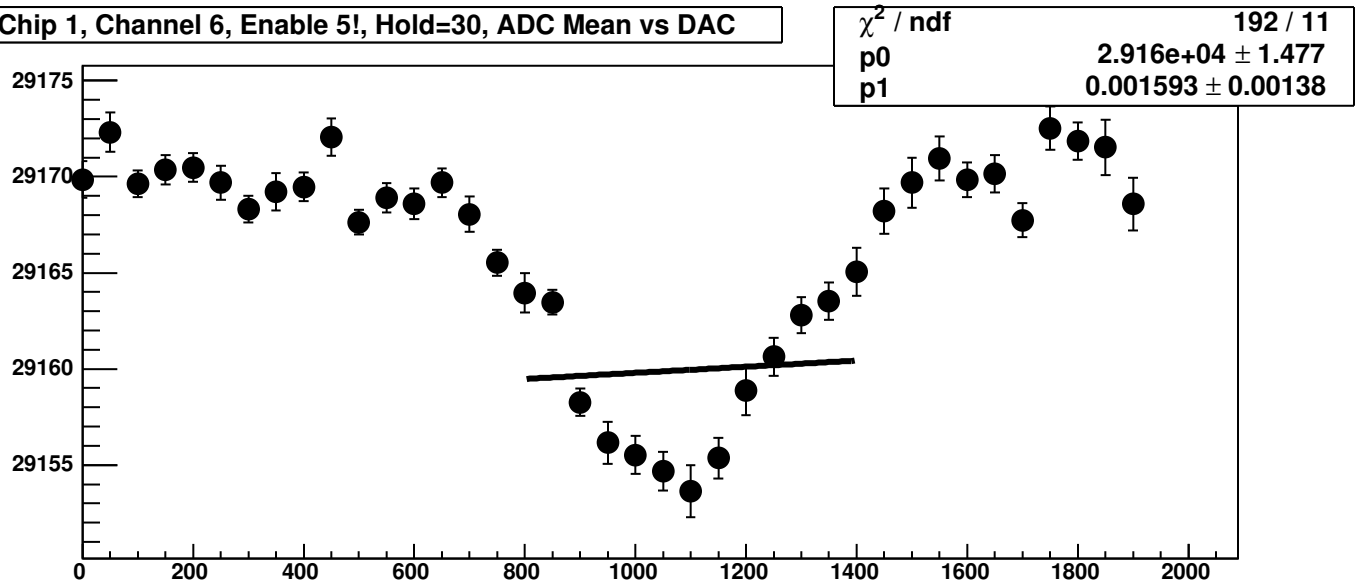

Chip 1, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 9.709 / 11
p0 -1573 ± 19.57
p1 0.1176 ± 0.01743
-1300
-1350
-1400
-1450
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

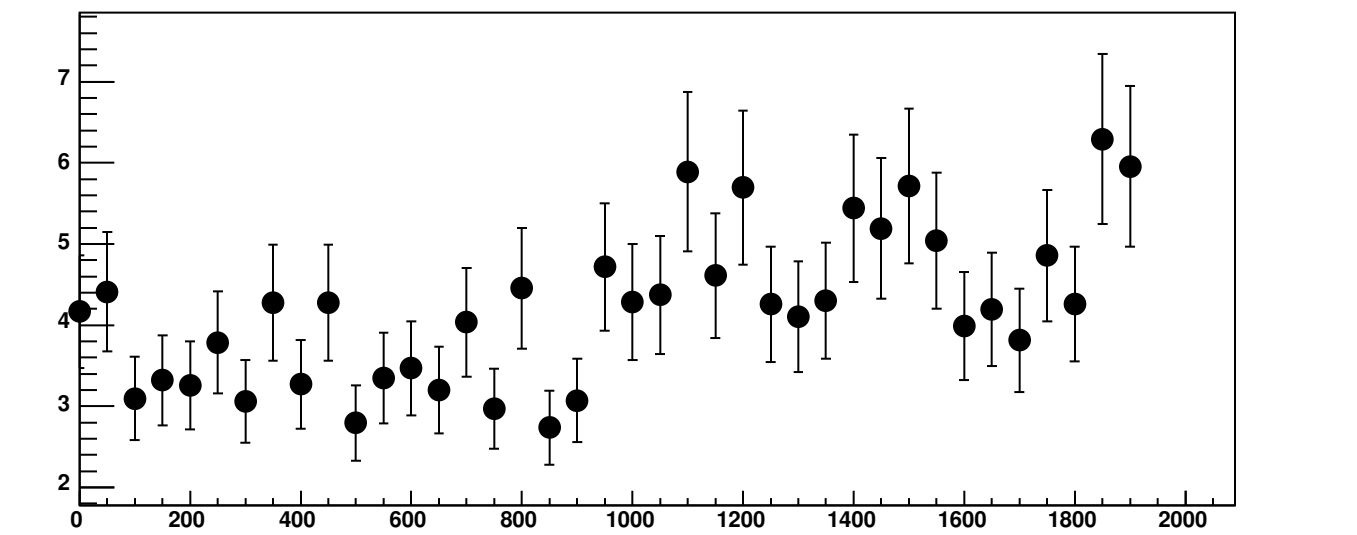

Chip 1, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

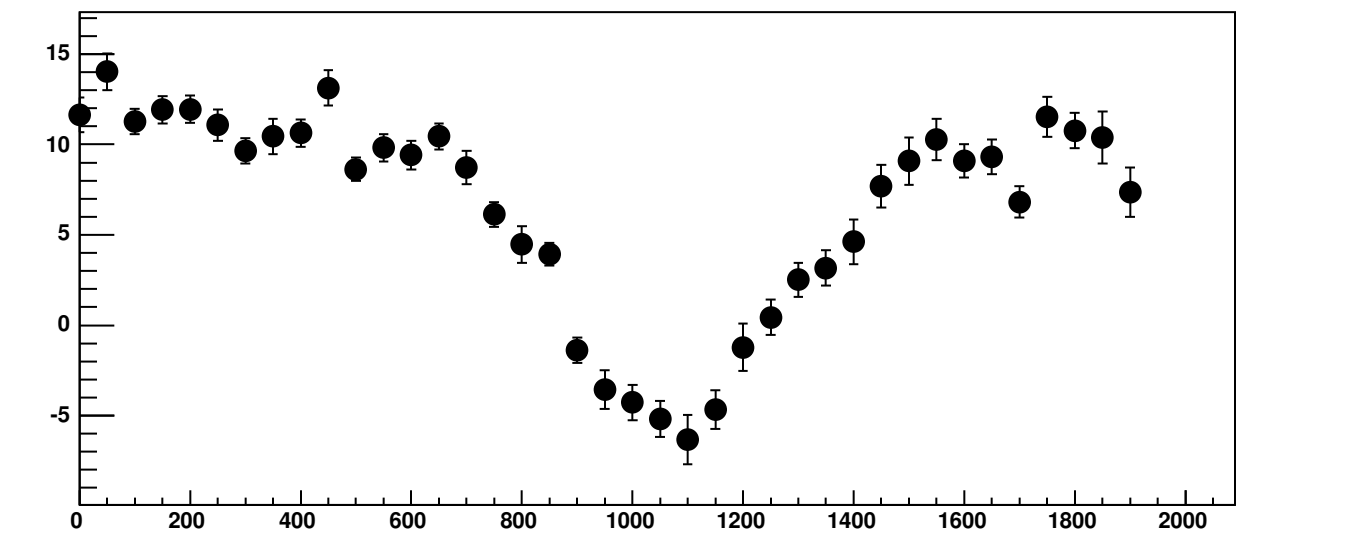
Chip 1, Channel 6, Enable 5!, Hold=30, ADC Mean vs DAC

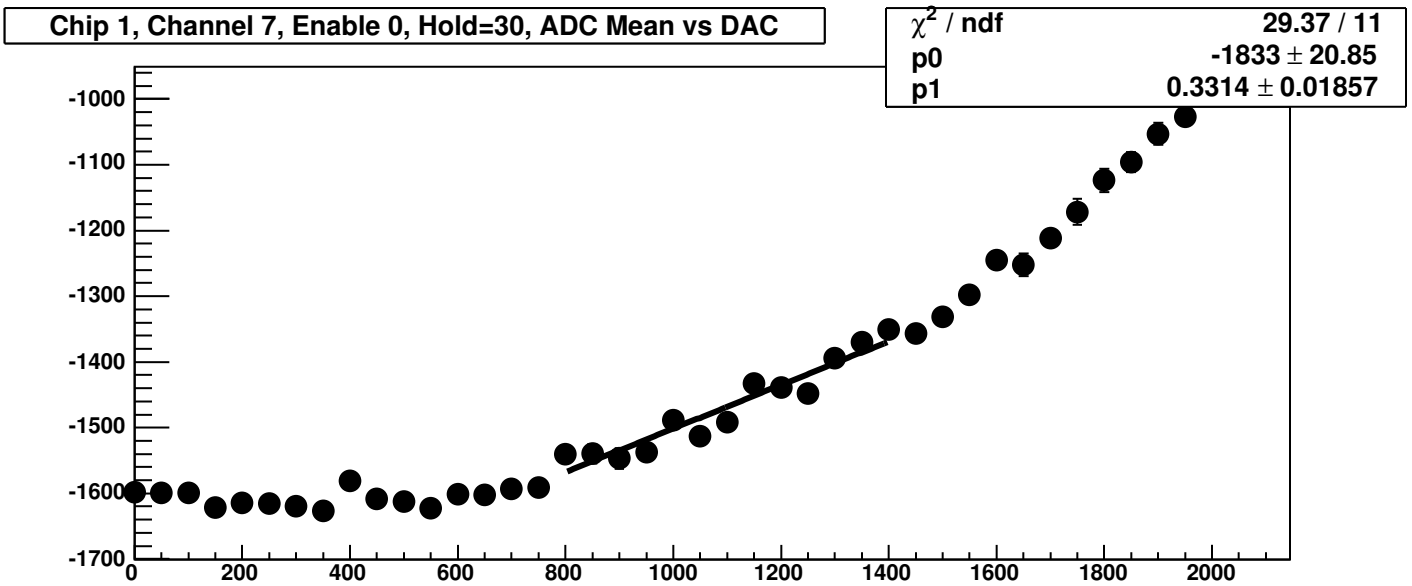


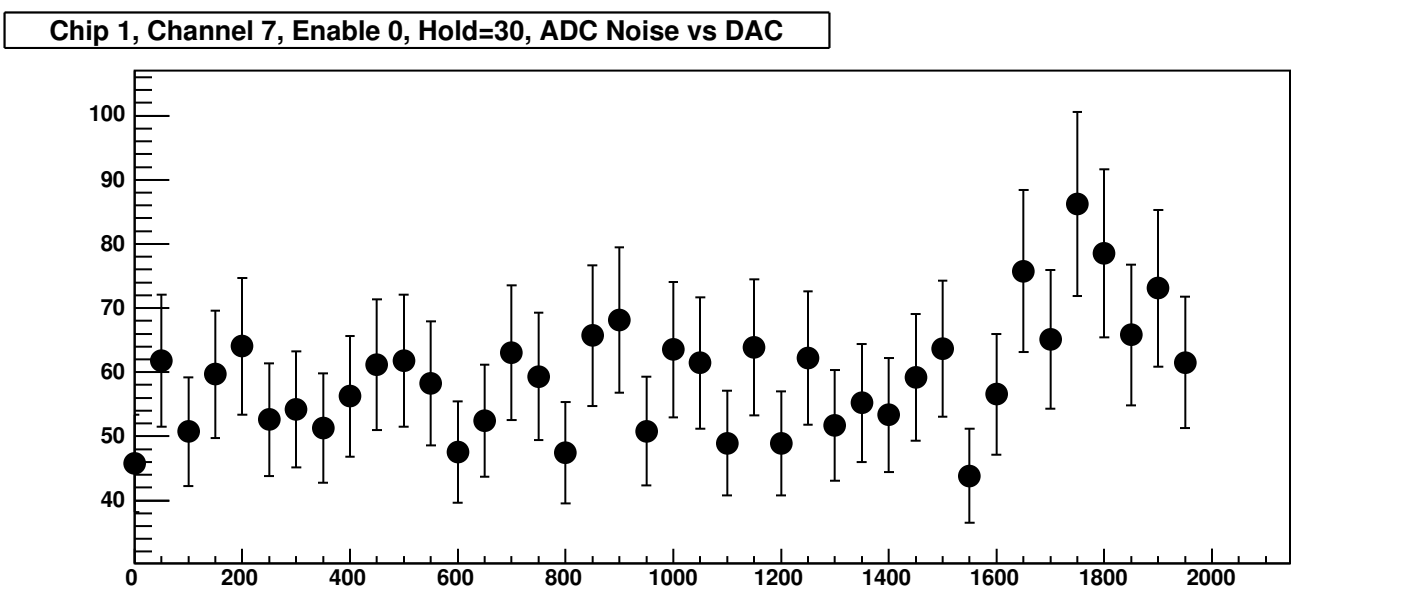
Chip 1, Channel 6, Enable 5!, Hold=30, ADC Noise vs DAC

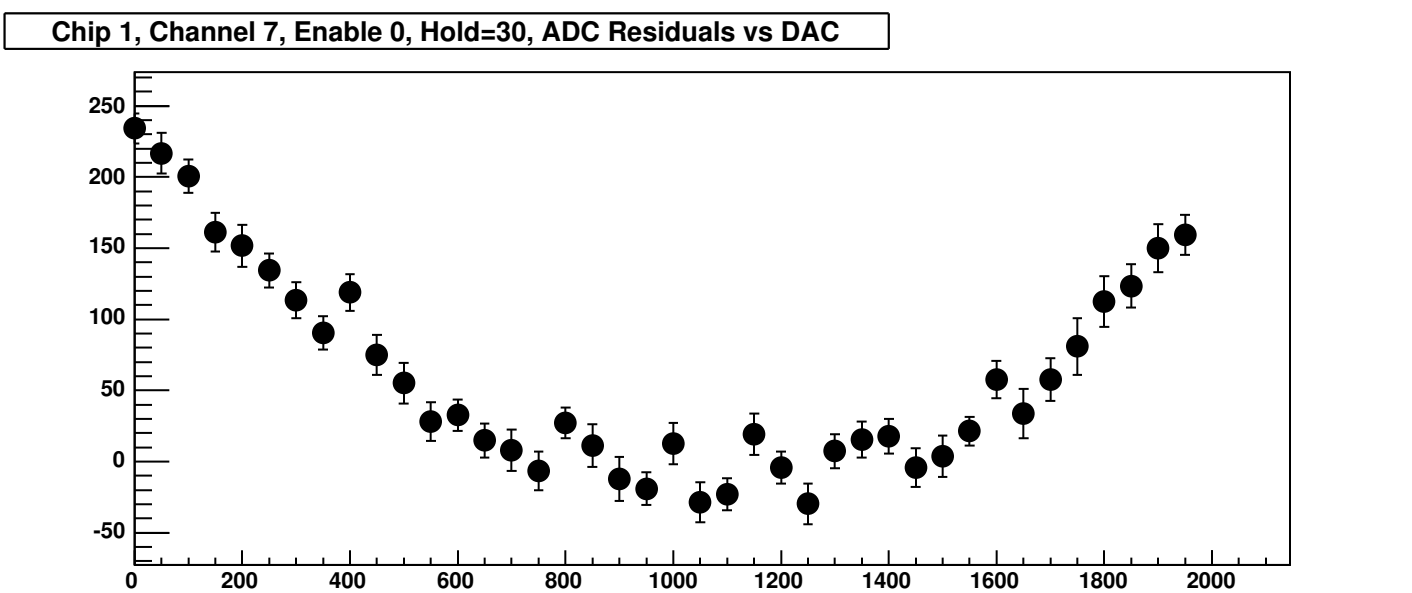


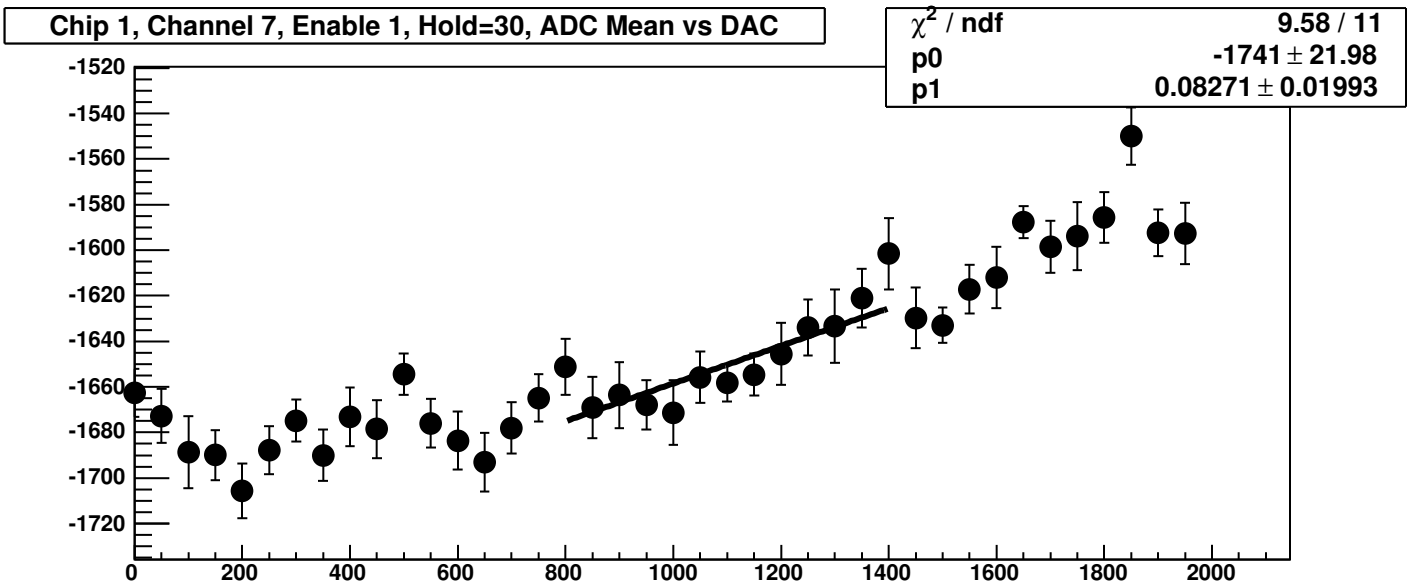
Chip 1, Channel 6, Enable 5!, Hold=30, ADC Residuals vs DAC

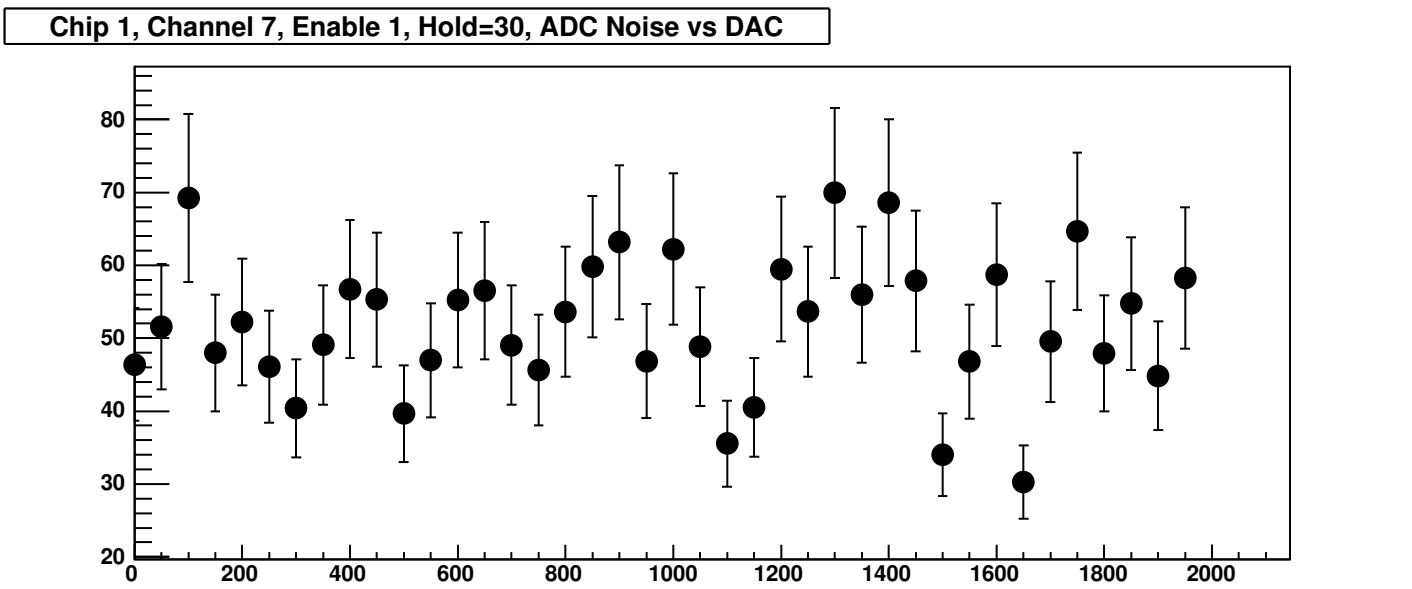


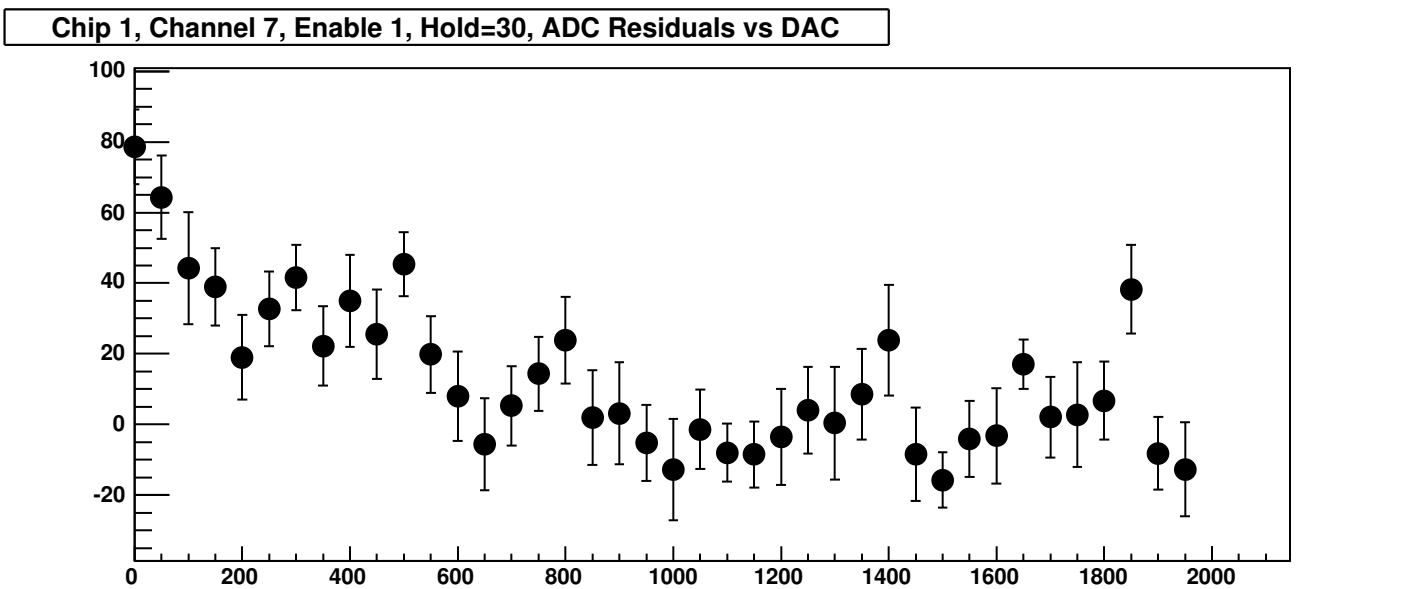

Chip 1, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC
χ^2 / ndf 29.37 / 11
p0 -1833 ± 20.85
p1 0.3314 ± 0.01857
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

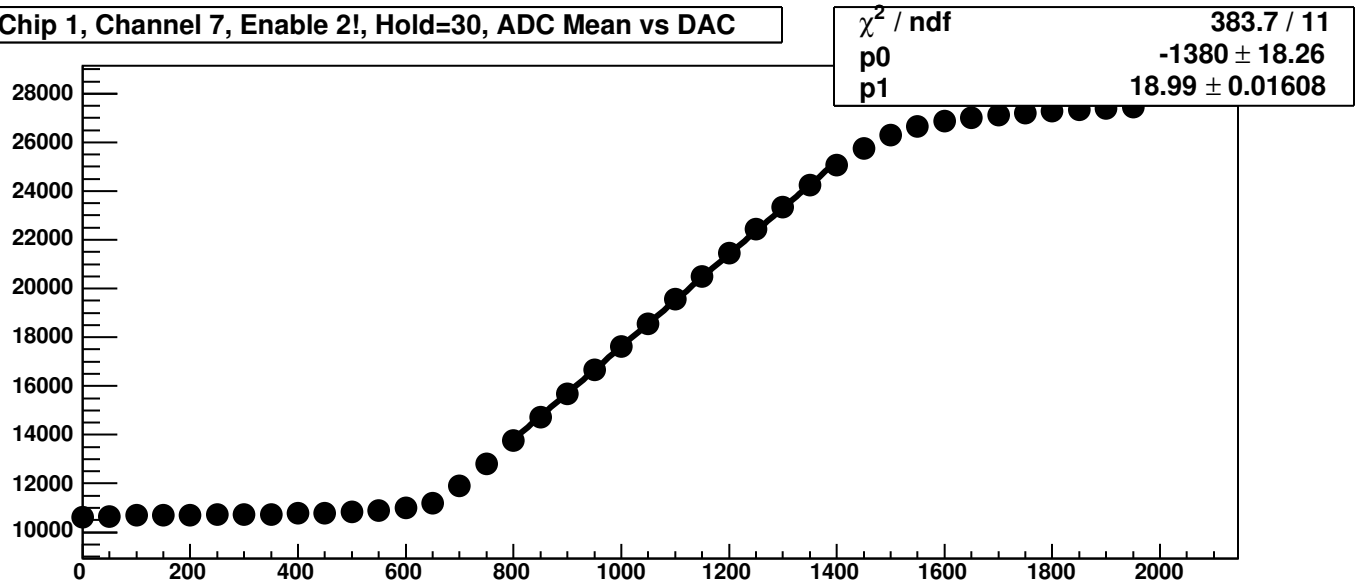

Chip 1, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 9.58 / 11
p0 -1741 ± 21.98
p1 0.08271 ± 0.01993
-1520
-1540
-1560
-1580
-1600
-1620
-1640
-1660
-1680
-1700
-1720
0
200
400
600
800
1000
1200
1400
1600
1800
2000

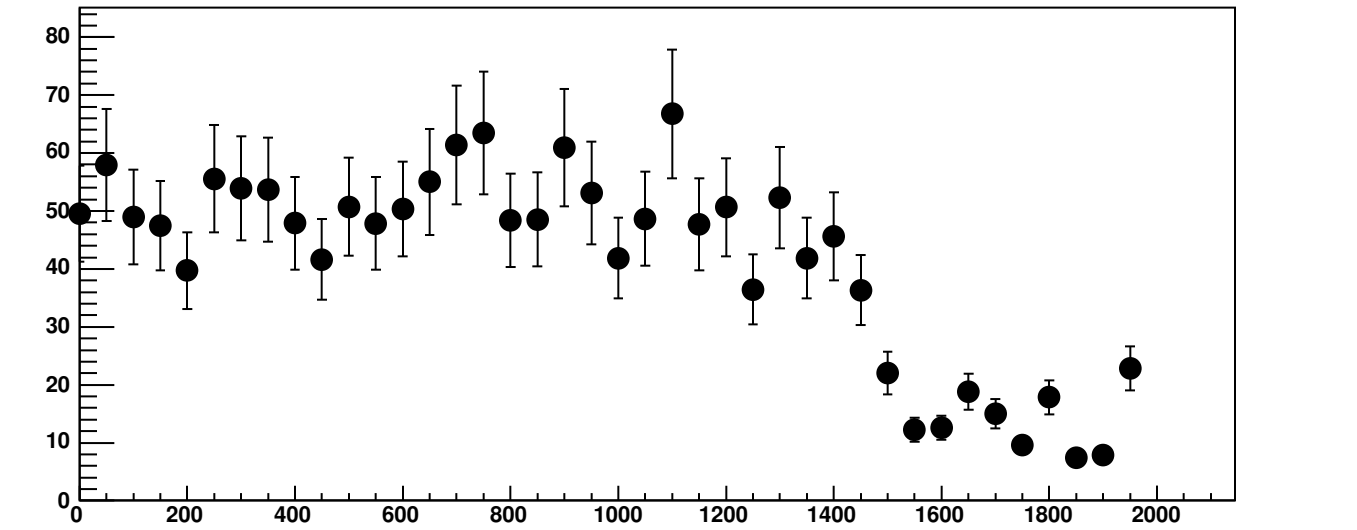

Chip 1, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

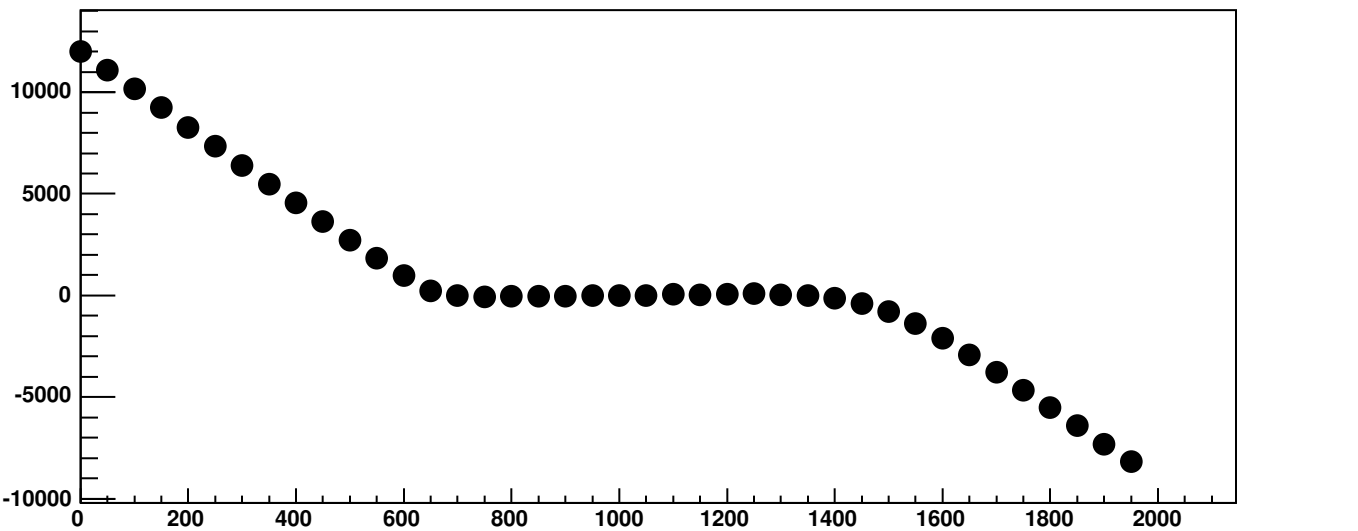
Chip 1, Channel 7, Enable 2!, Hold=30, ADC Mean vs DAC

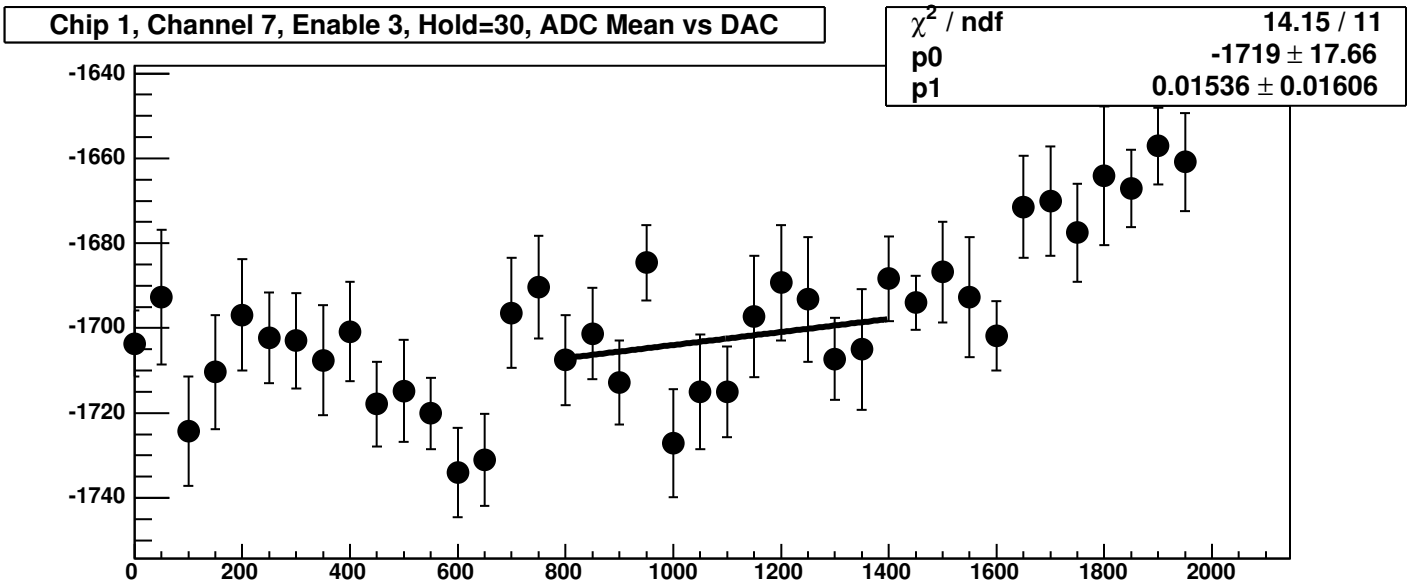


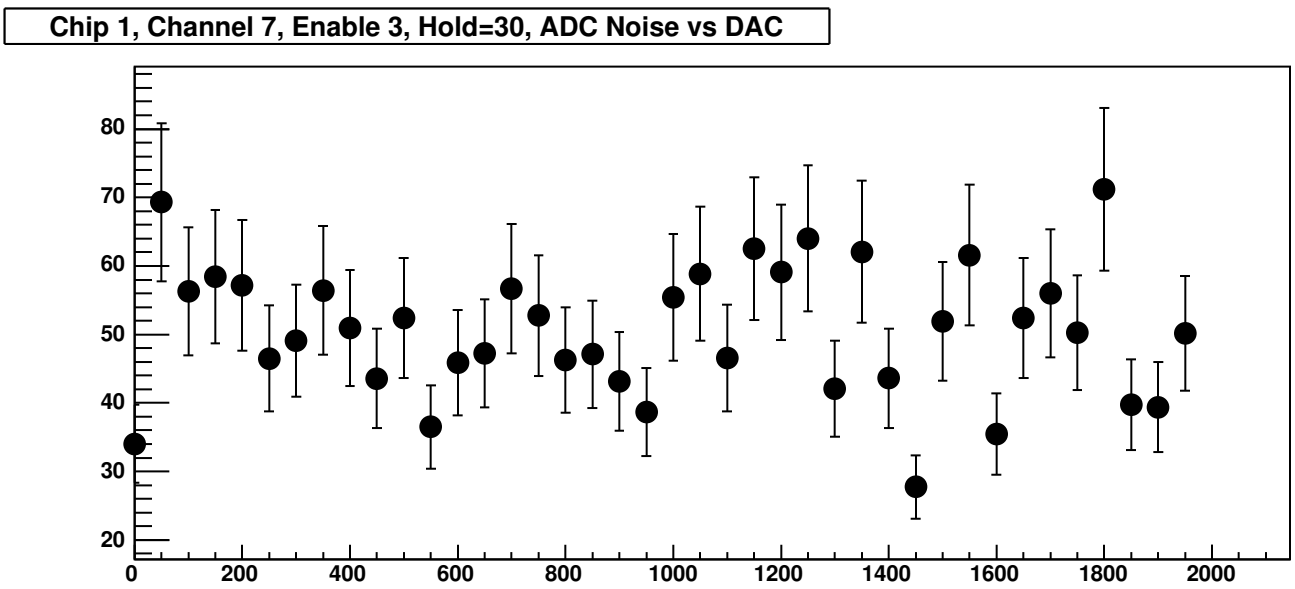
Chip 1, Channel 7, Enable 2!, Hold=30, ADC Noise vs DAC

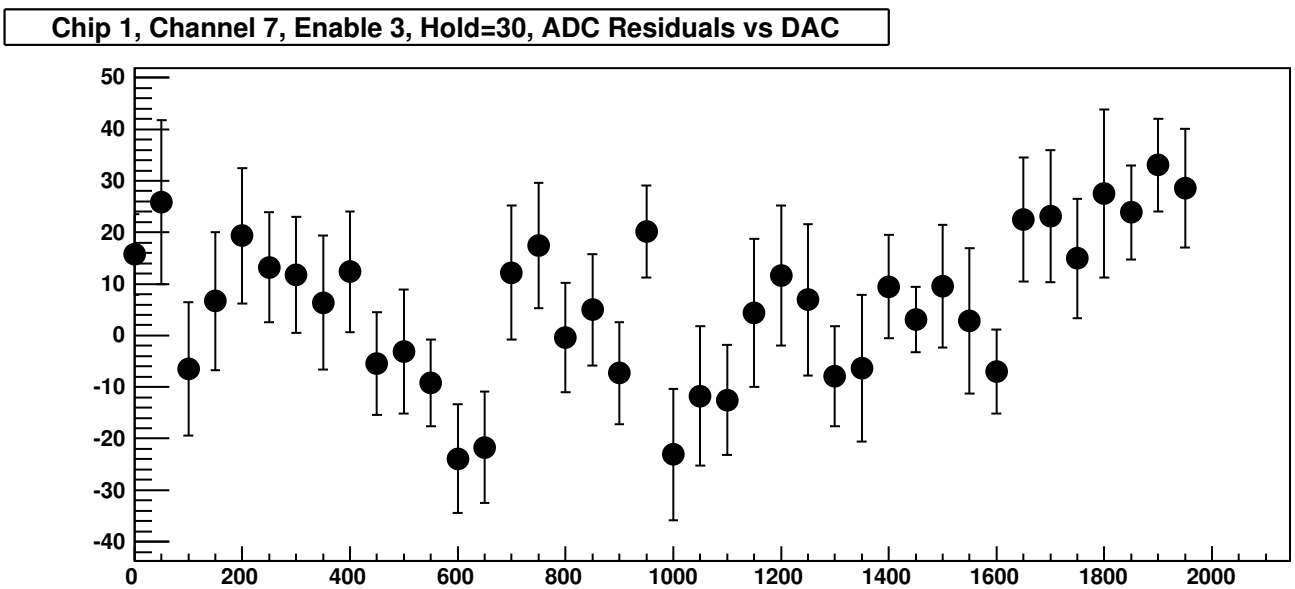


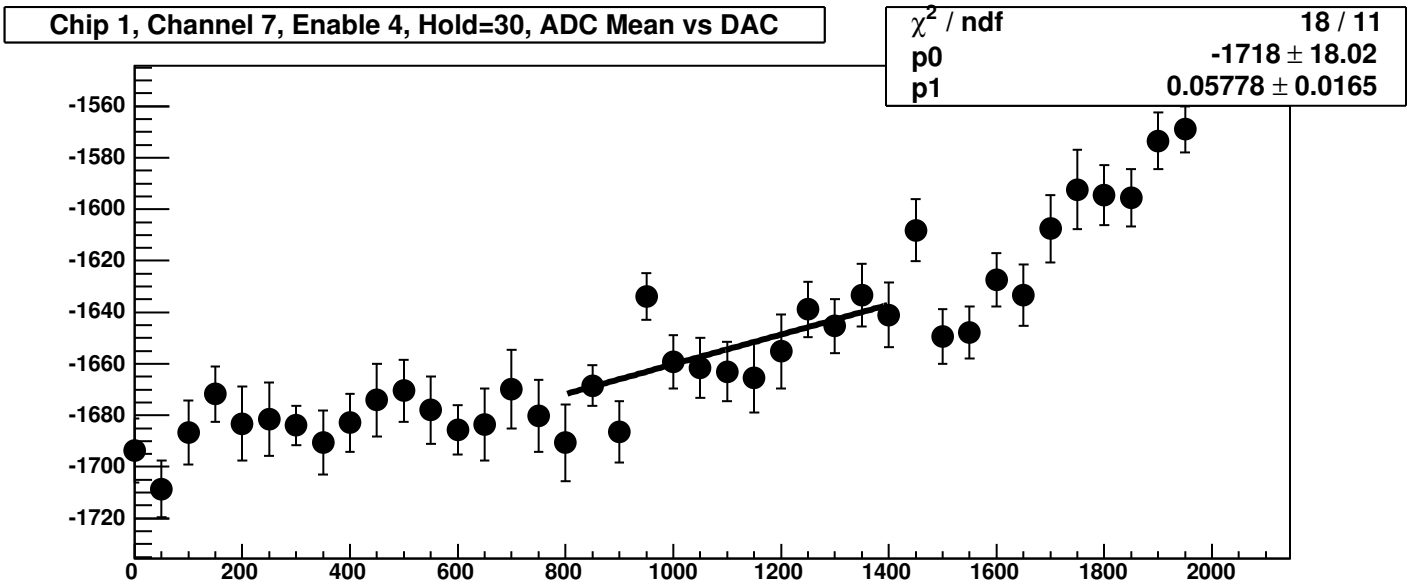
Chip 1, Channel 7, Enable 2!, Hold=30, ADC Residuals vs DAC

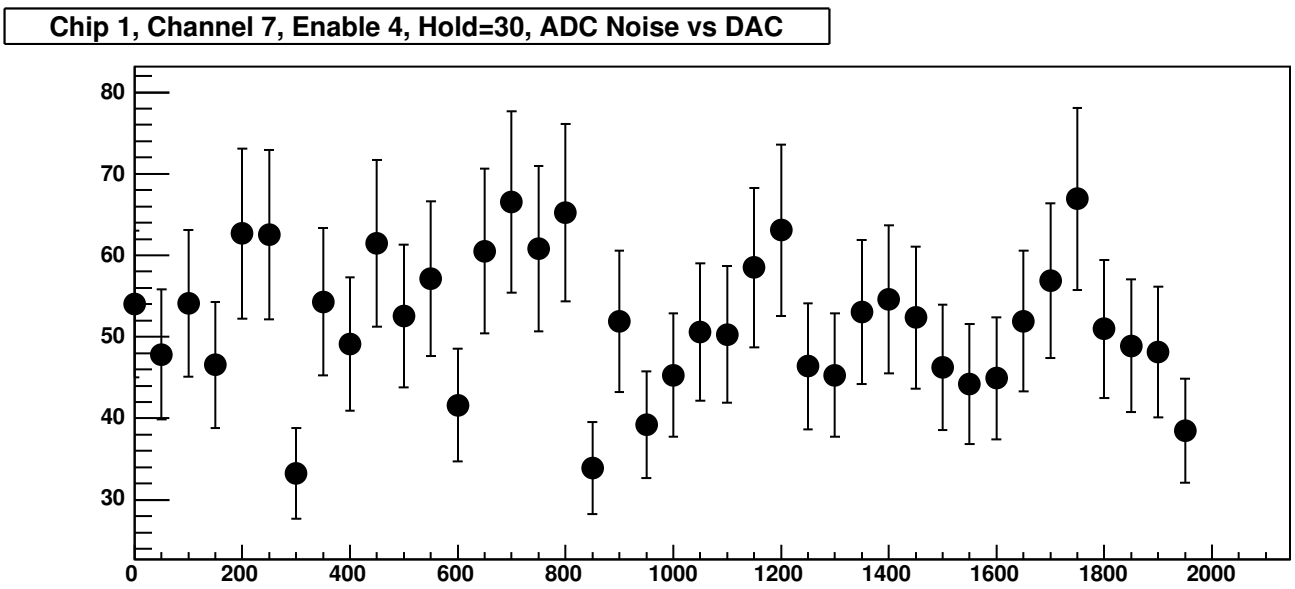


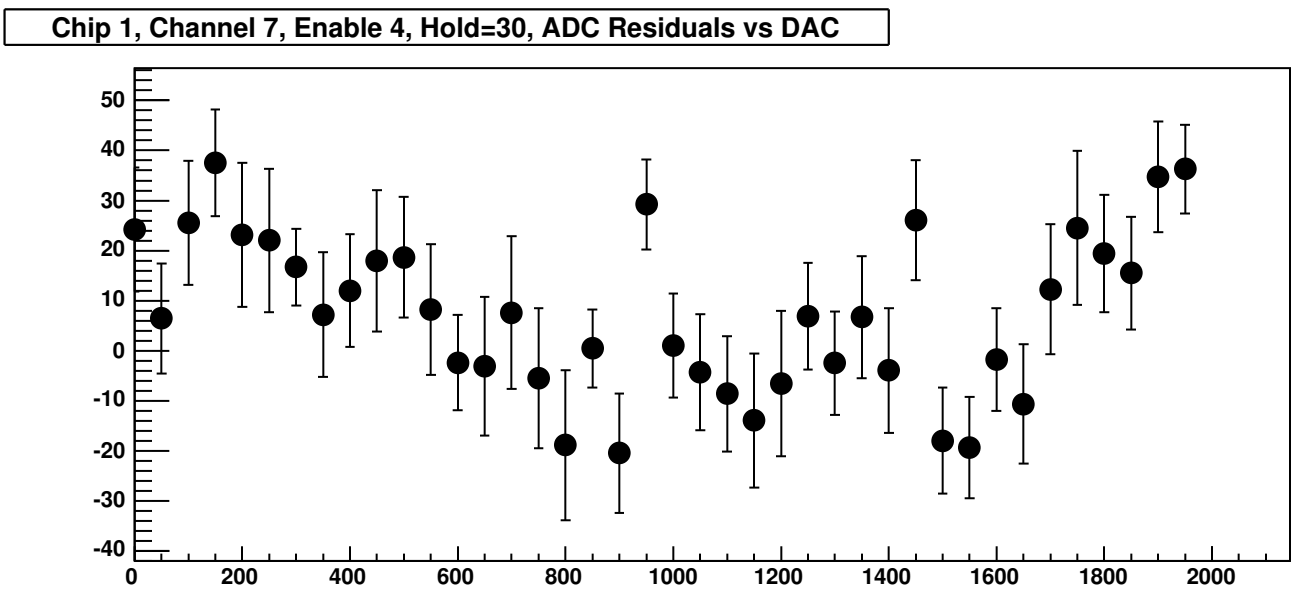

Chip 1, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 14.15 / 11
p0 -1719 ± 17.66
p1 0.01536 ± 0.01606
-1640
-1660
-1680
-1700
-1720
-1740
0
200
400
600
800
1000
1200
1400
1600
1800
2000

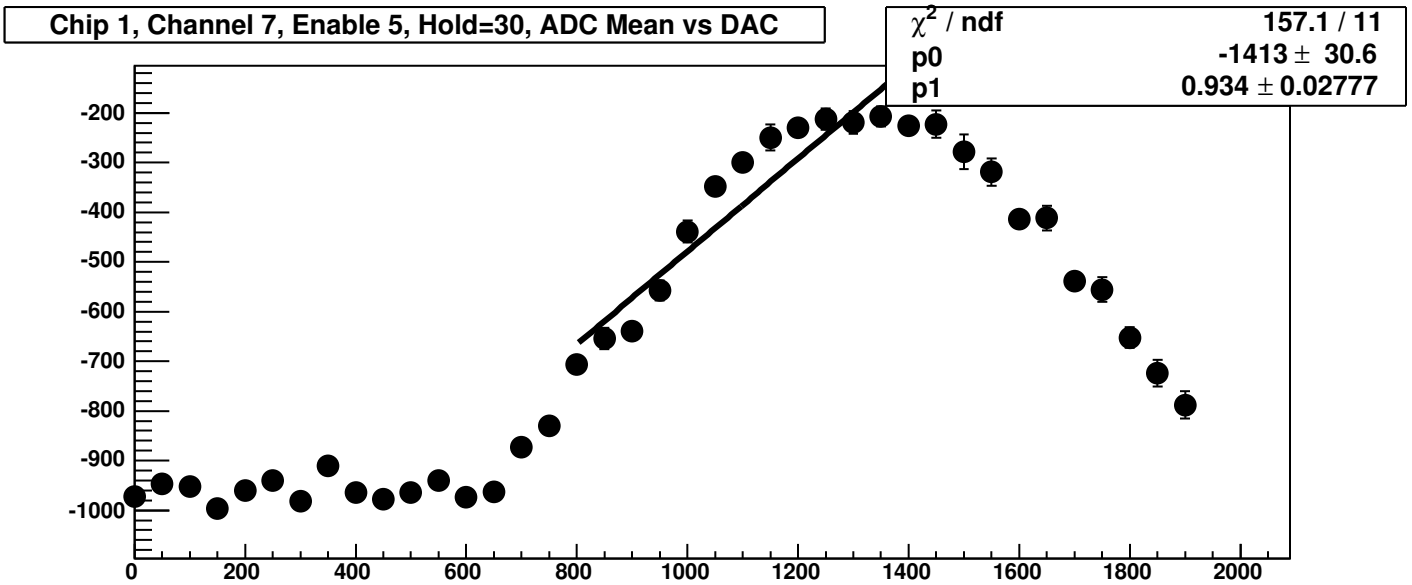

Chip 1, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

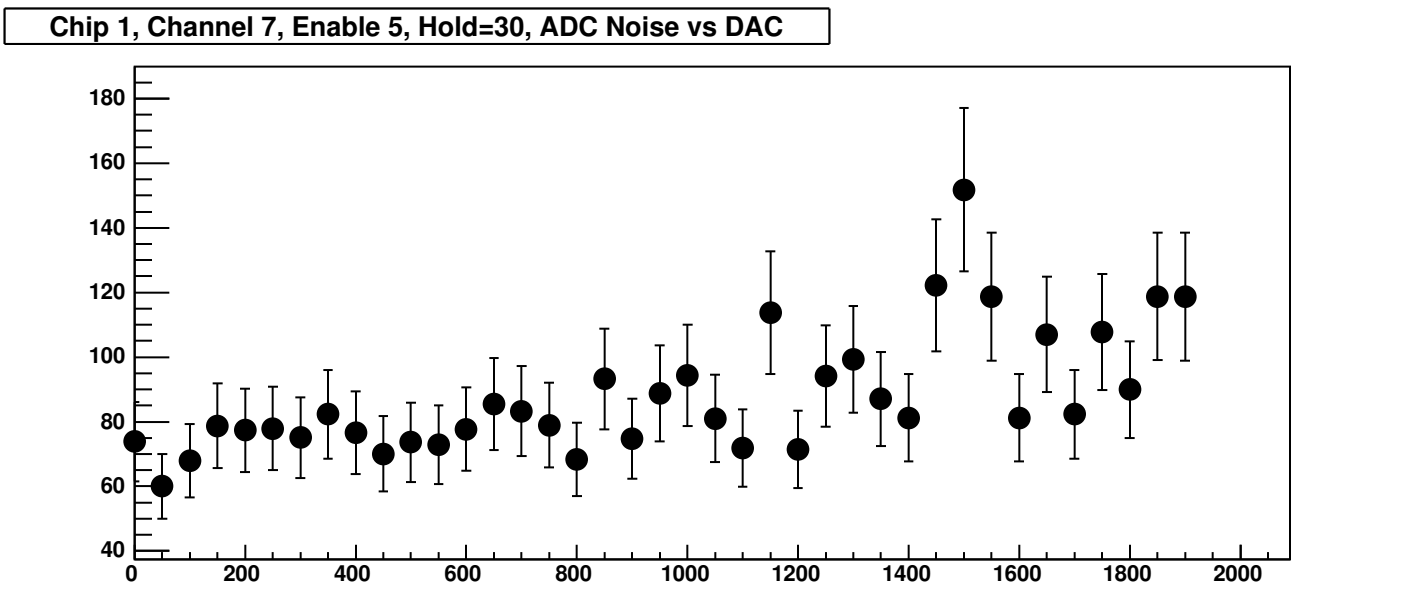

Chip 1, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC
50
40
30
20
10
0
-10
-20
-30
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

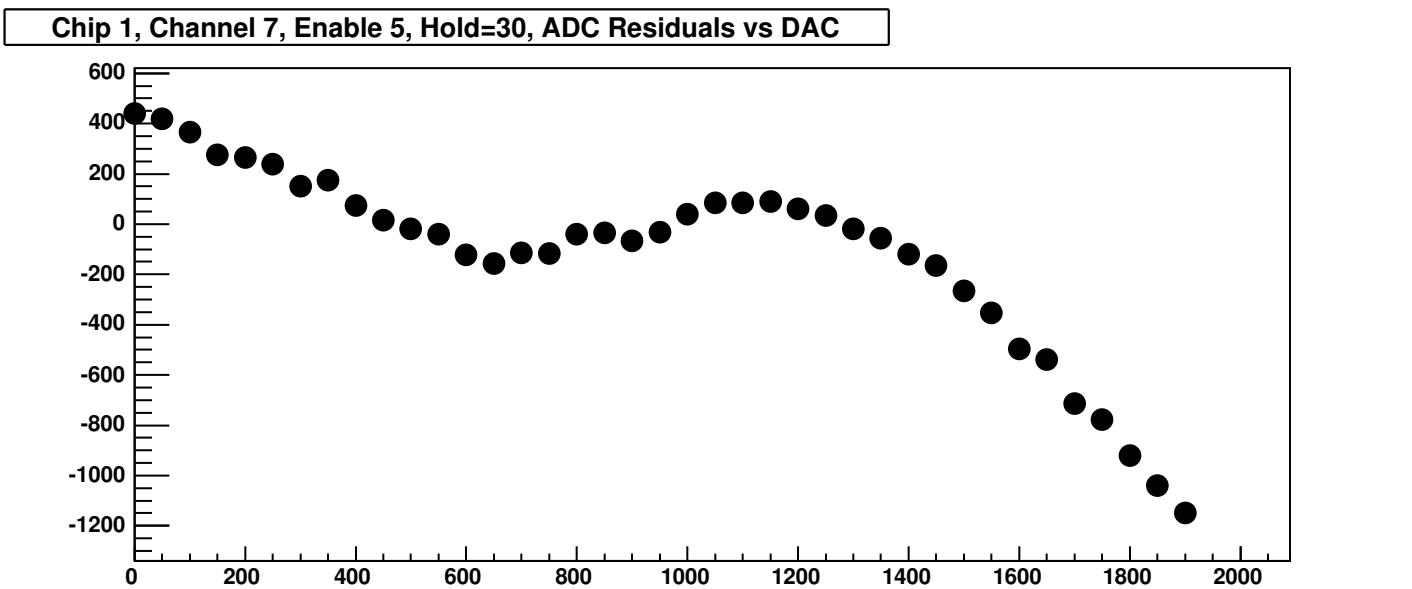

Chip 1, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 18 / 11
p0 -1718 ± 18.02
p1 0.05778 ± 0.0165
-1560
-1580
-1600
-1620
-1640
-1660
-1680
-1700
-1720
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

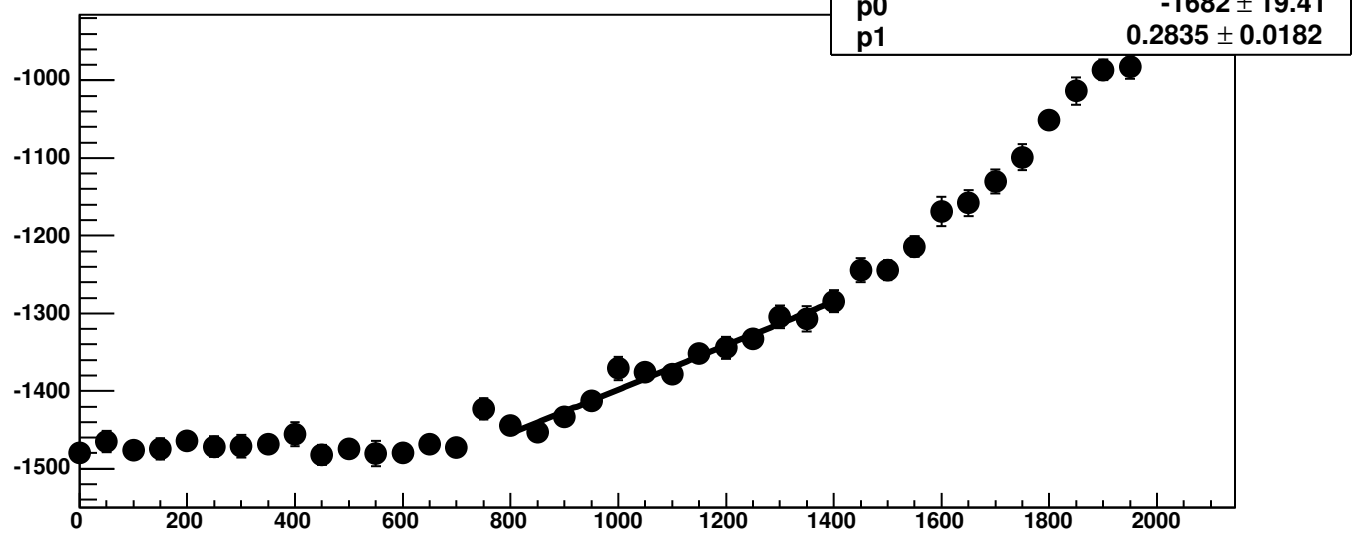

Chip 1, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC
50
40
30
20
10
0
-10
-20
-30
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 7, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 157.1 / 11
p0 -1413 ± 30.6
p1 0.934 ± 0.02777
-200
-300
-400
-500
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

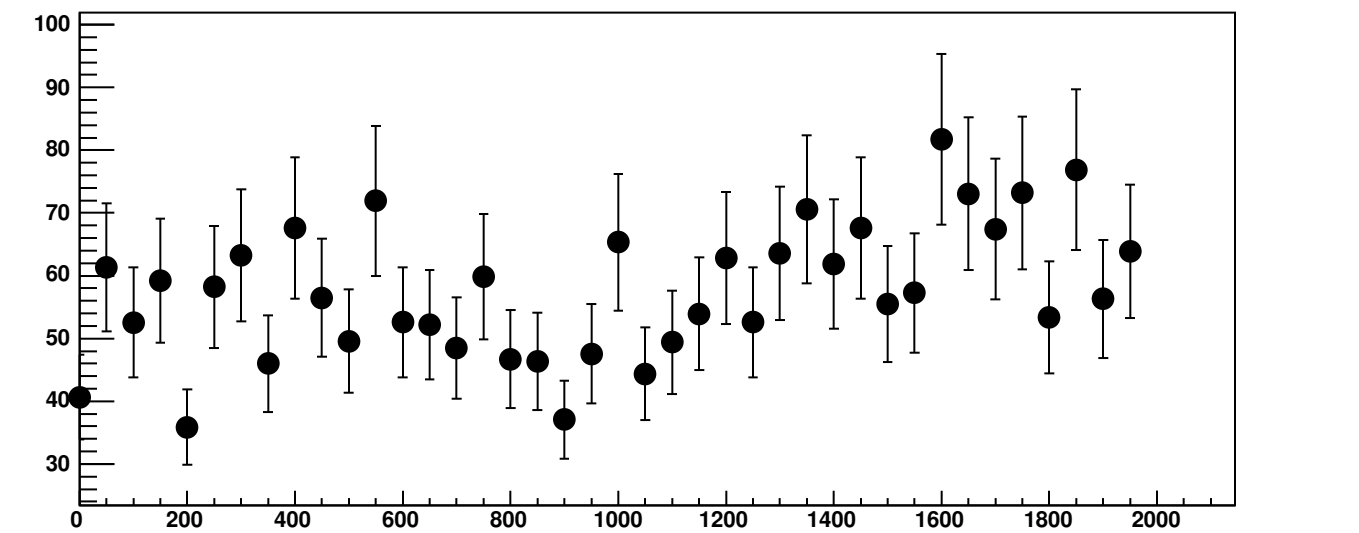

Chip 1, Channel 7, Enable 5, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 7, Enable 5, Hold=30, ADC Residuals vs DAC
600
400
200
0
-200
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

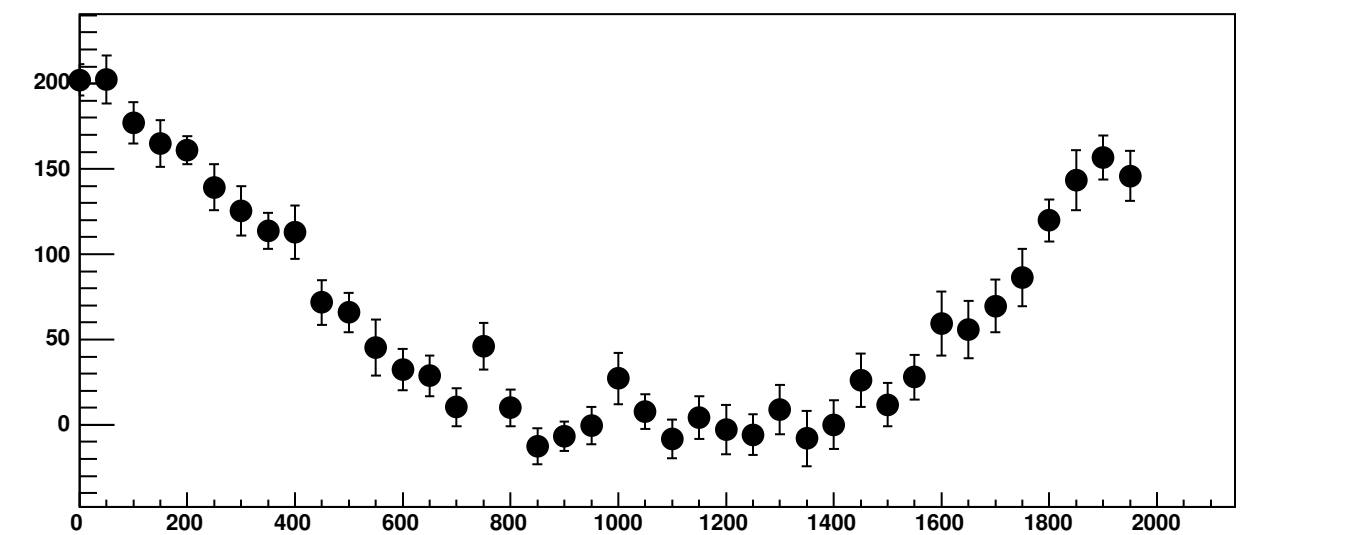
Chip 1, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC

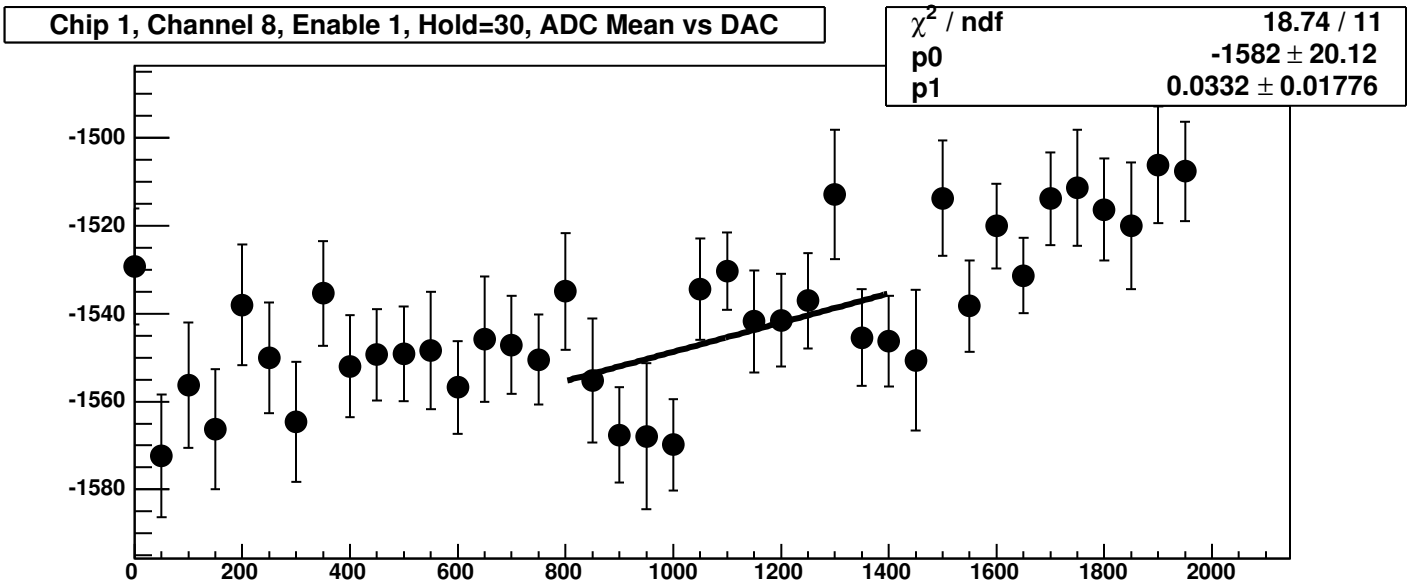


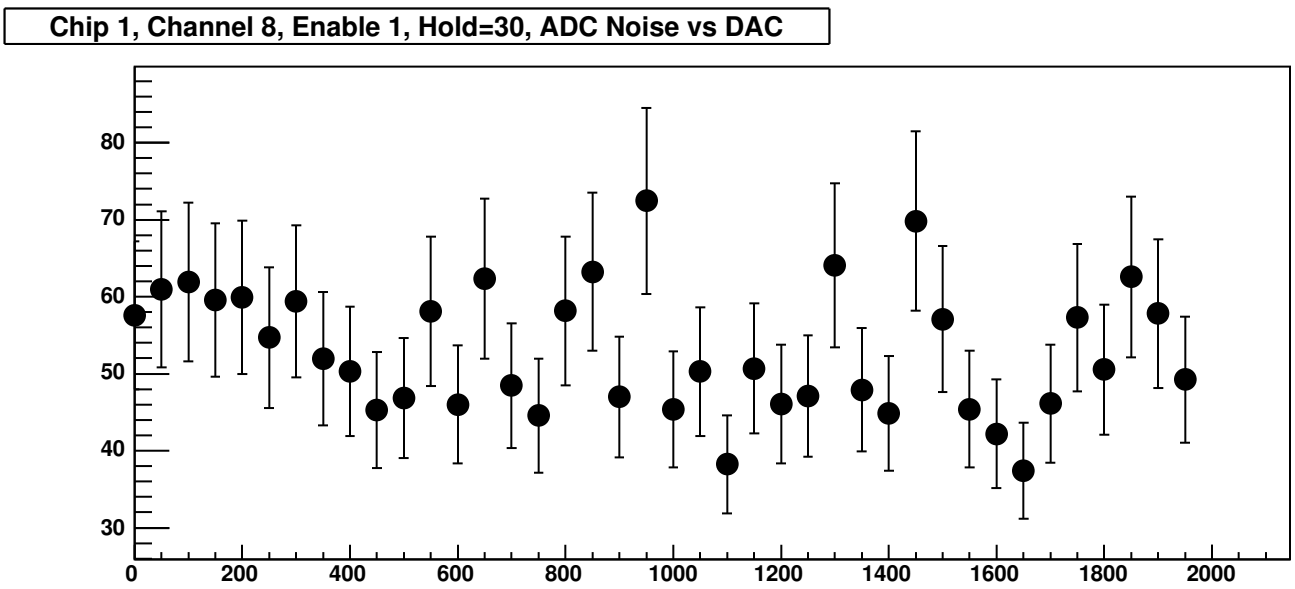
Chip 1, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC

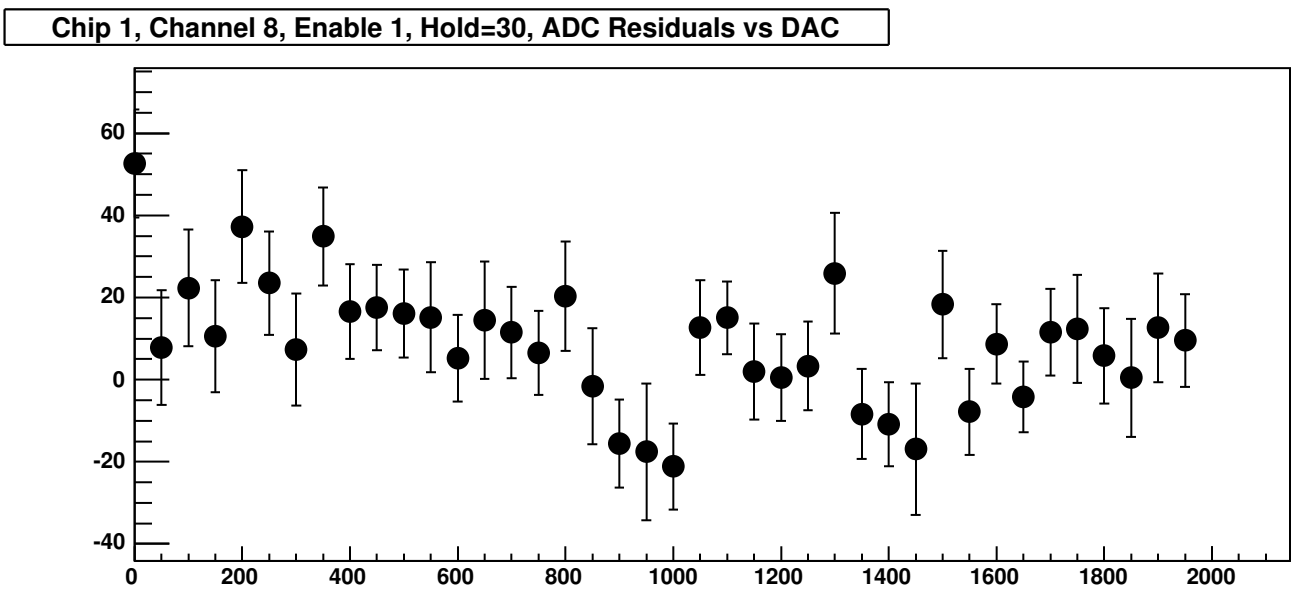


Chip 1, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC

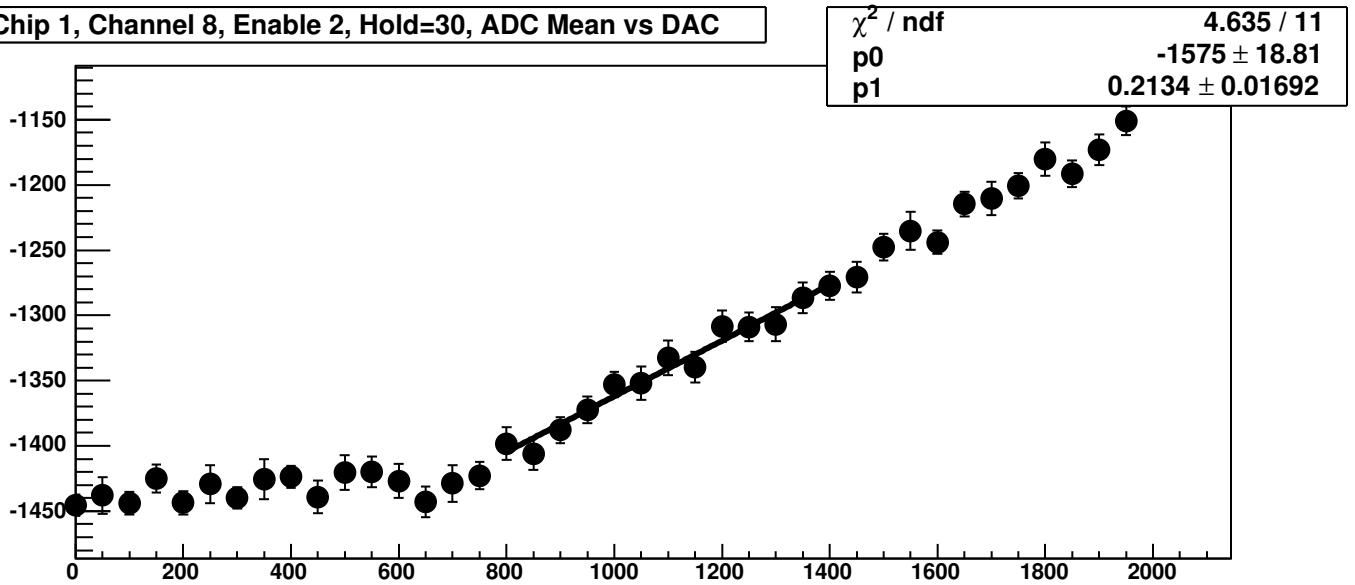



Chip 1, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 18.74 / 11
p0 -1582 ± 20.12
p1 0.0332 ± 0.01776
-1500
-1520
-1540
-1560
-1580
0
200
400
600
800
1000
1200
1400
1600
1800
2000

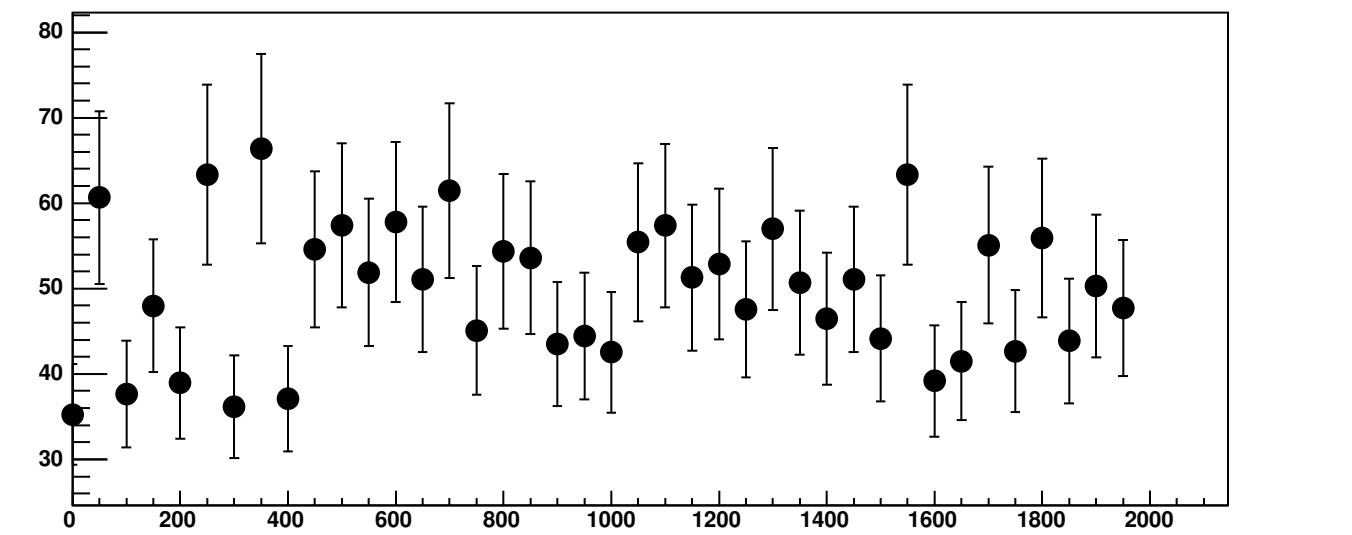

Chip 1, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

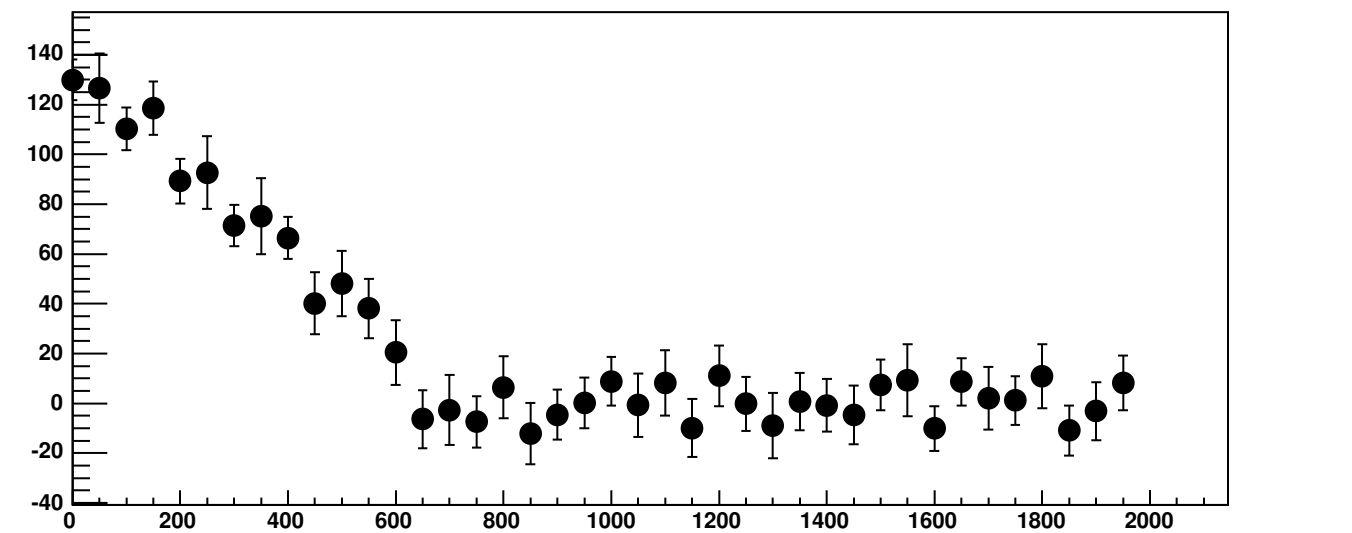
Chip 1, Channel 8, Enable 2, Hold=30, ADC Mean vs DAC



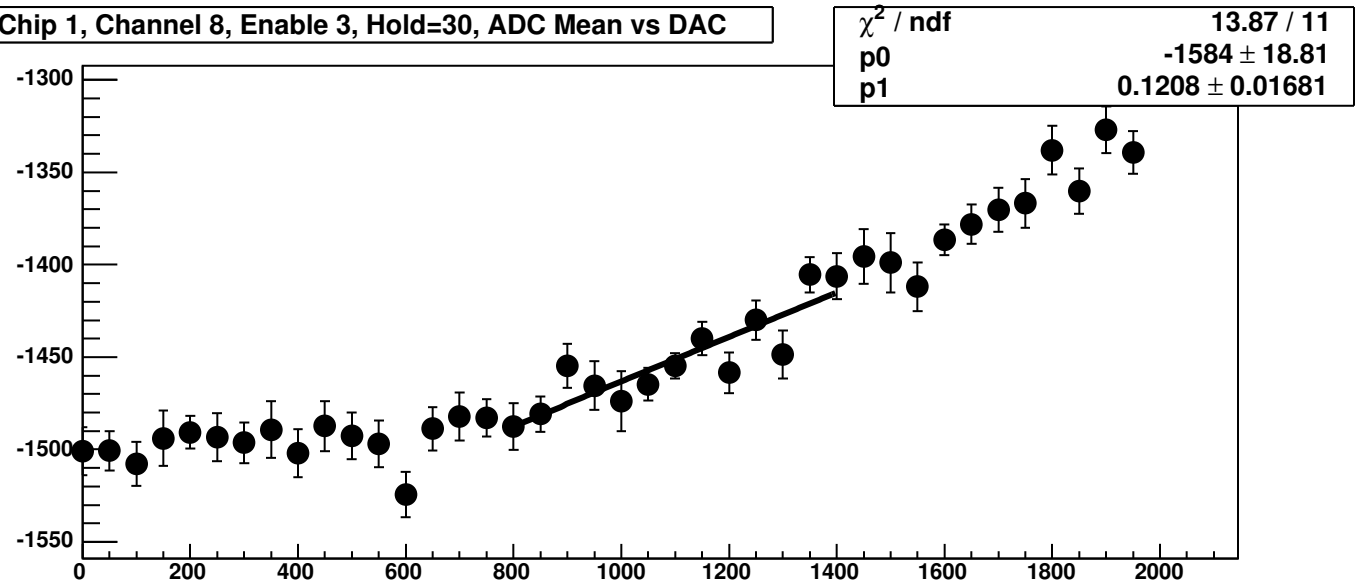
Chip 1, Channel 8, Enable 2, Hold=30, ADC Noise vs DAC



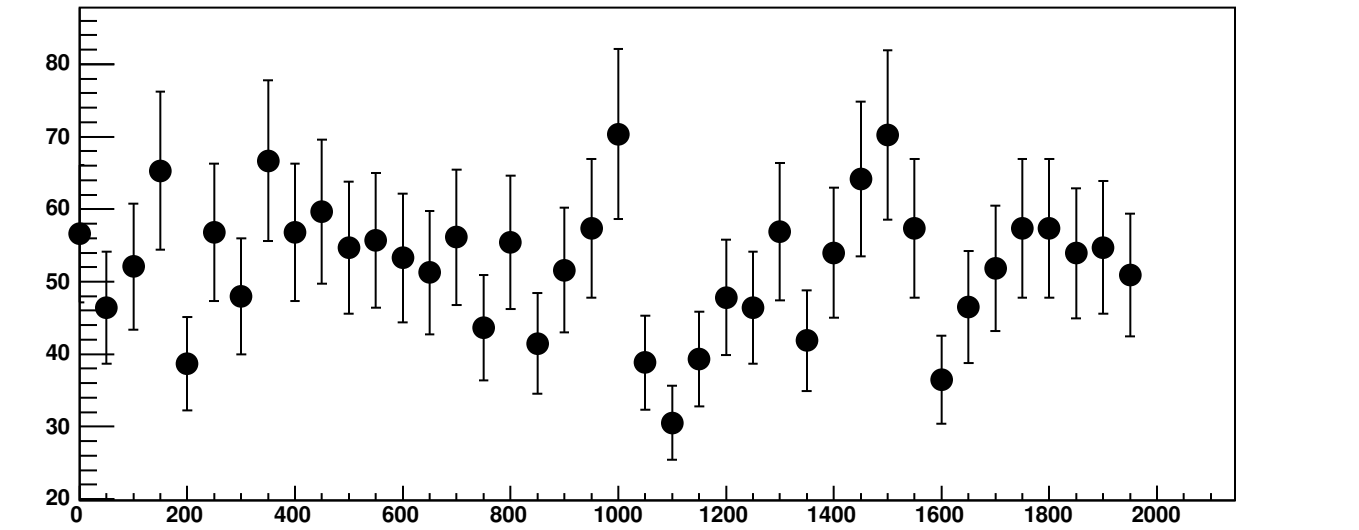
Chip 1, Channel 8, Enable 2, Hold=30, ADC Residuals vs DAC



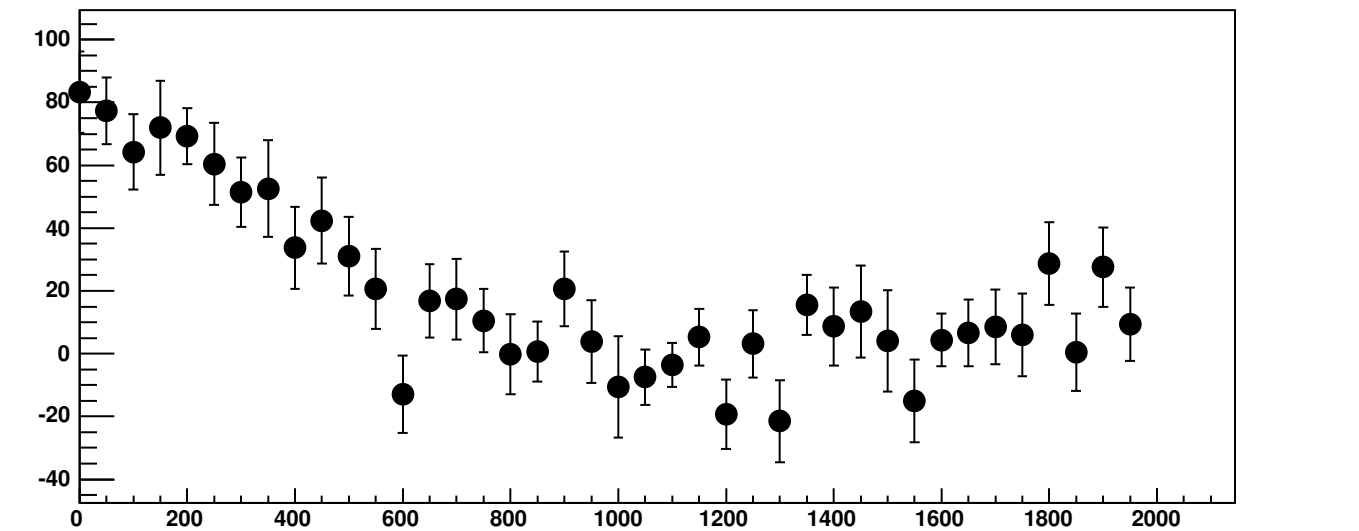
Chip 1, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC

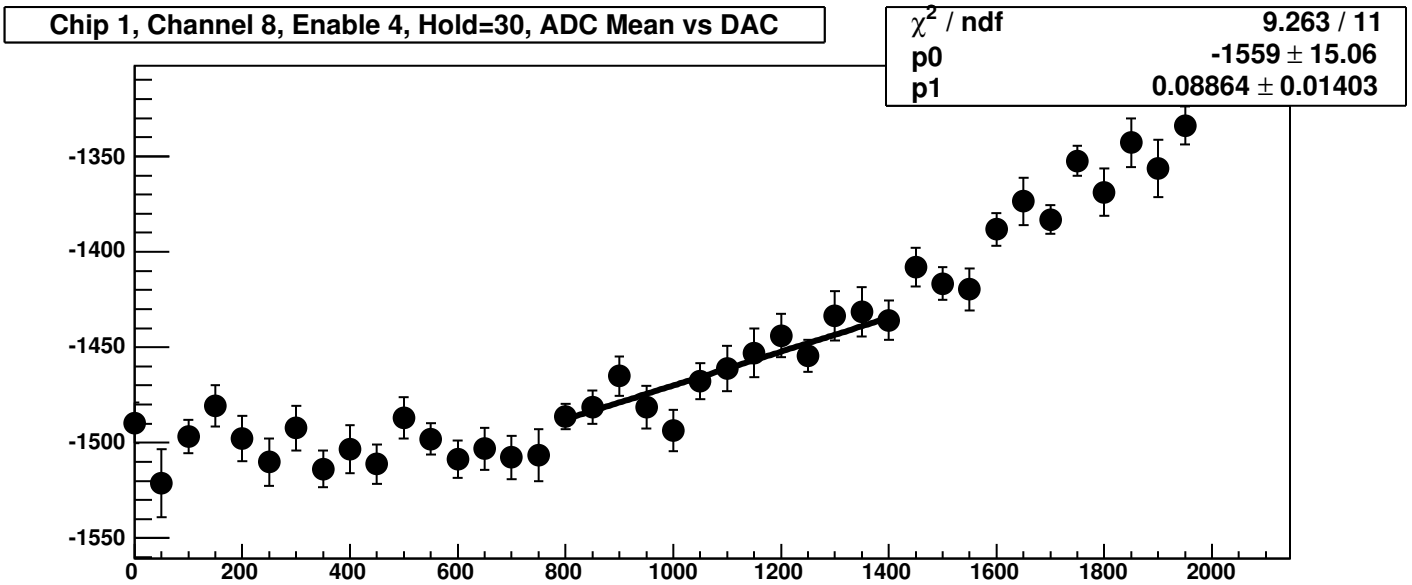


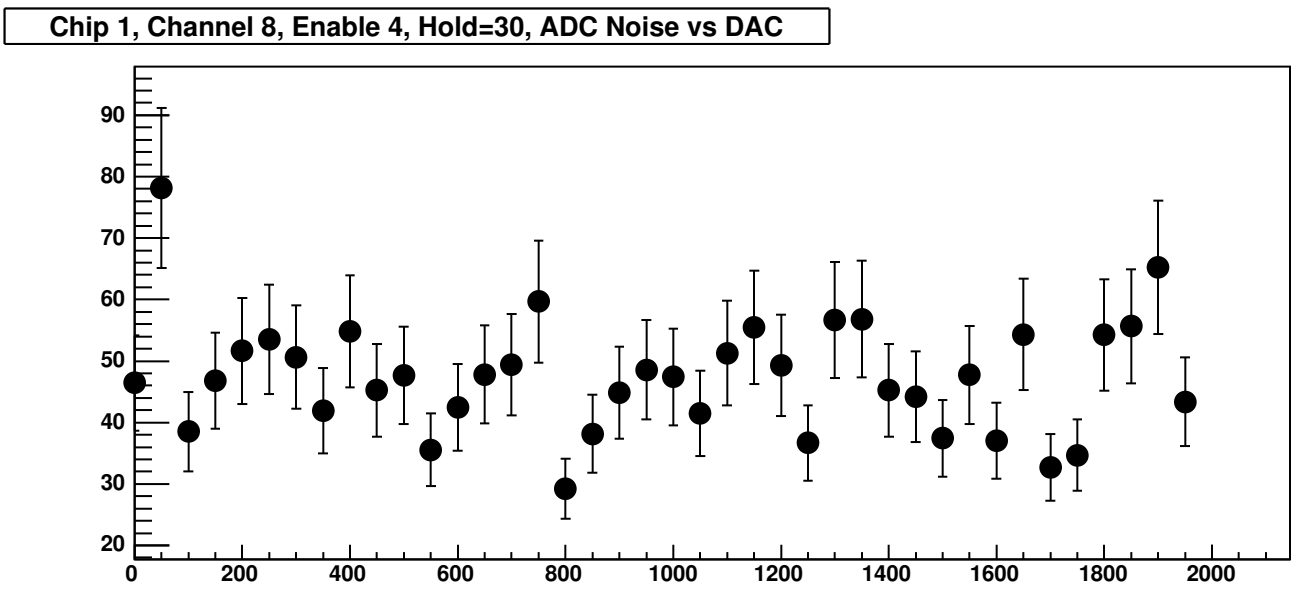
Chip 1, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC

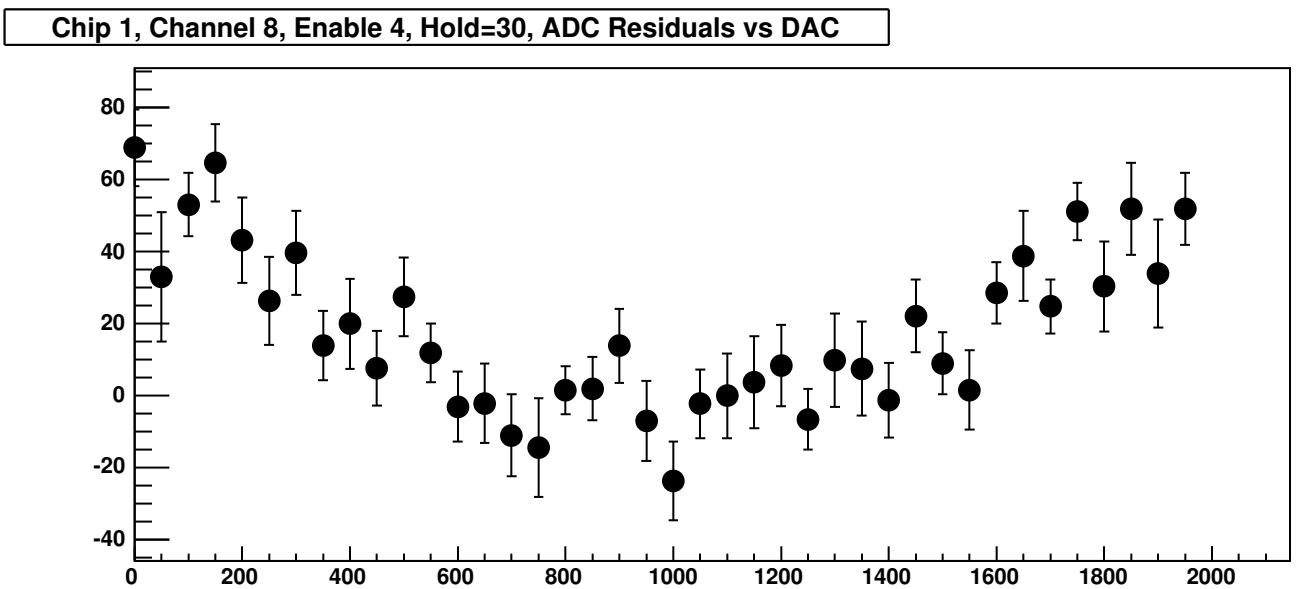


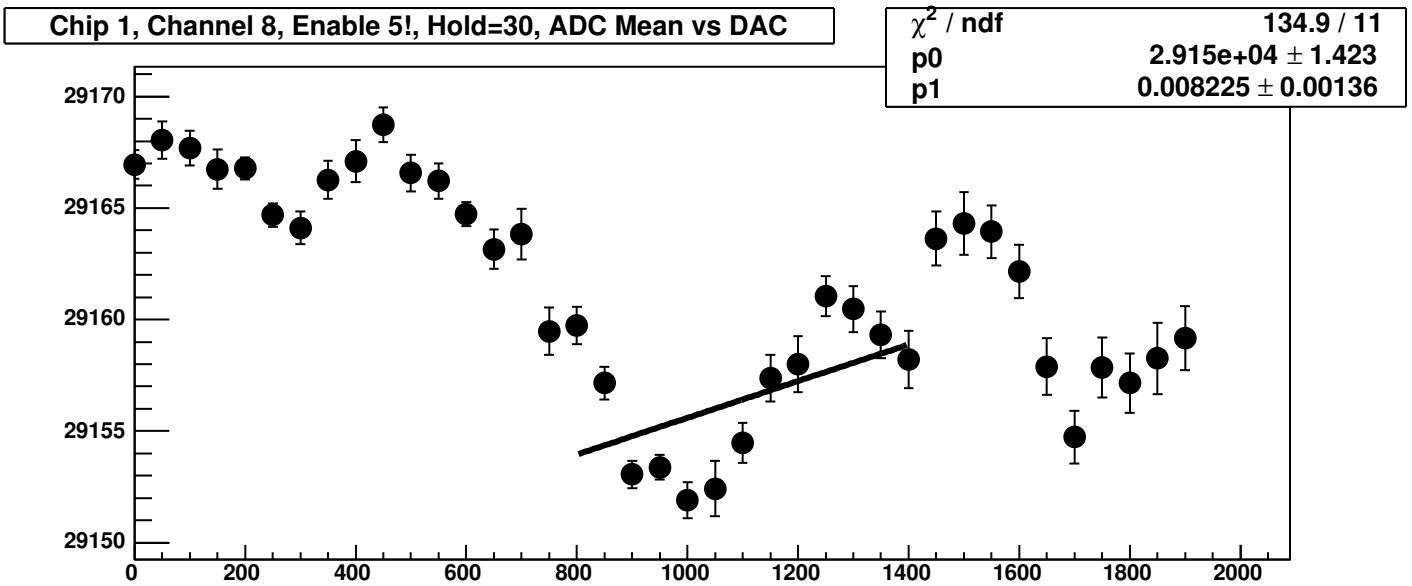
Chip 1, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC

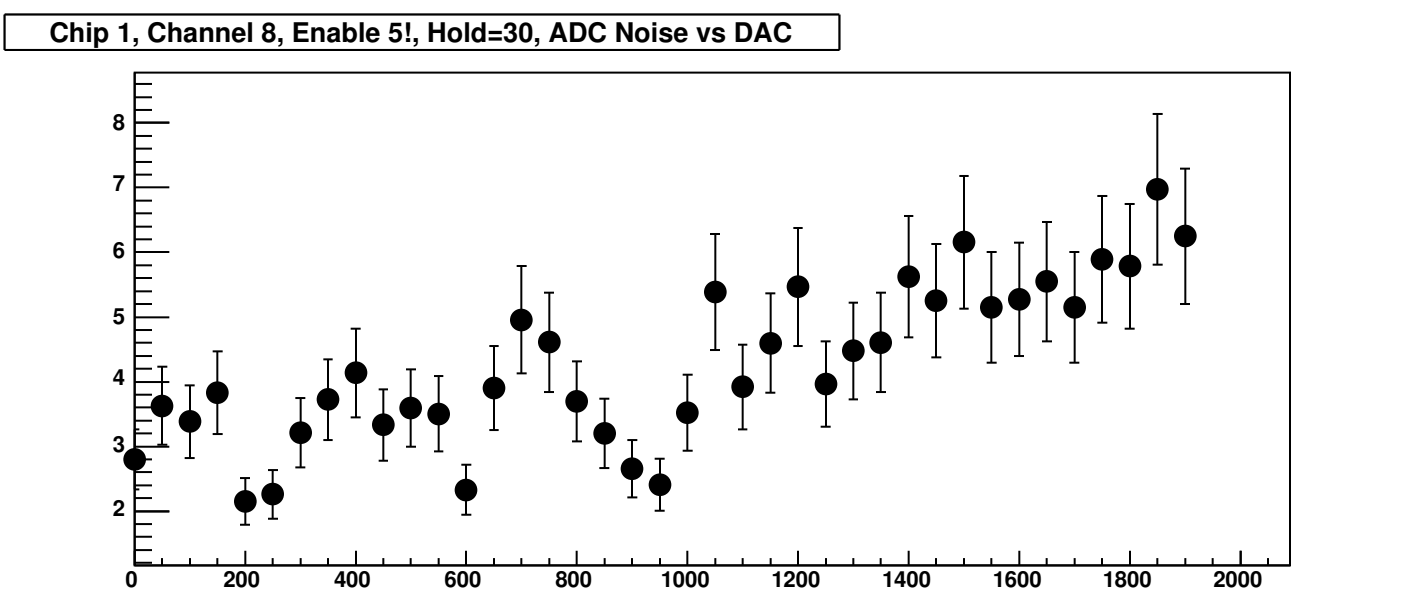


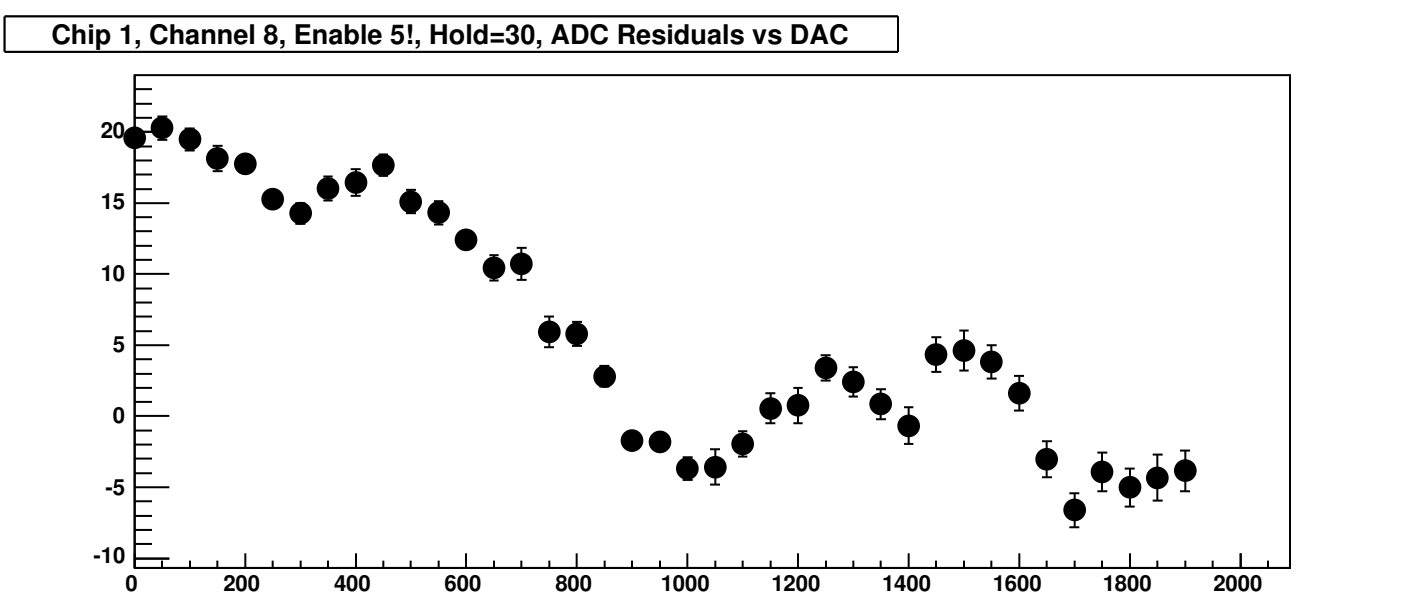

Chip 1, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 9.263 / 11
p0 -1559 ± 15.06
p1 0.08864 ± 0.01403
-1350
-1400
-1450
-1500
-1550
0
200
400
600
800
1000
1200
1400
1600
1800
2000

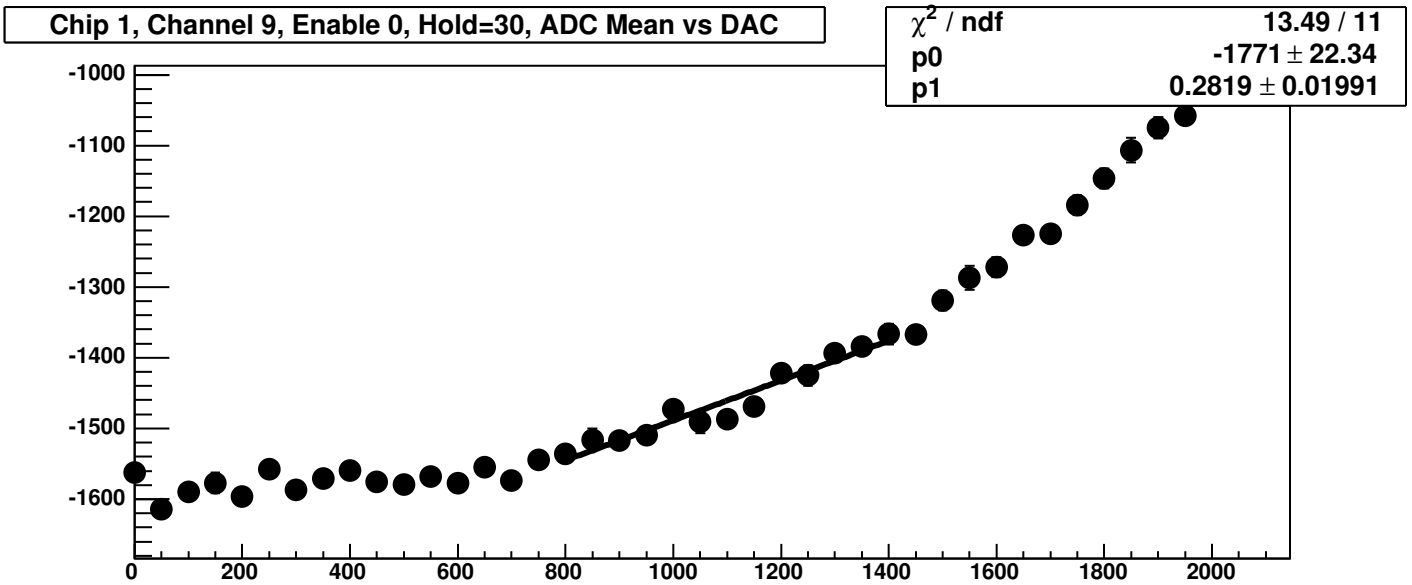

Chip 1, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

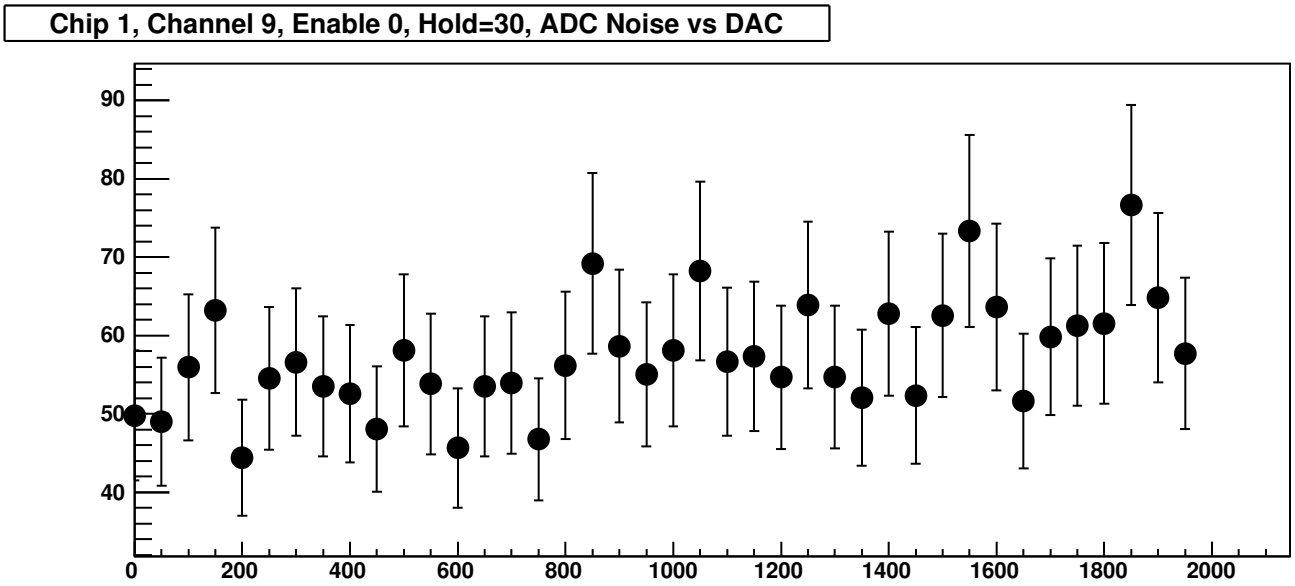

Chip 1, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

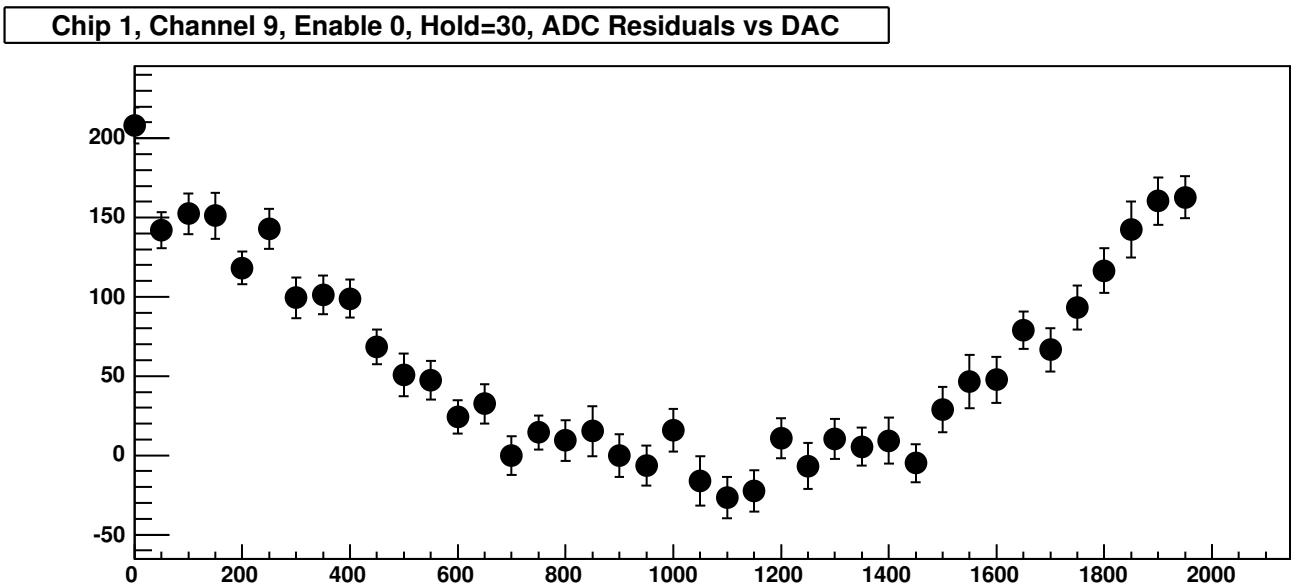

Chip 1, Channel 8, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 134.9 / 11
p0 2.915e+04 ± 1.423
p1 0.008225 ± 0.00136
29170
29165
29160
29155
29150
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 8, Enable 5!, Hold=30, ADC Noise vs DAC
8
7
6
5
4
3
2
0
200
400
600
800
1000
1200
1400
1600
1800
2000

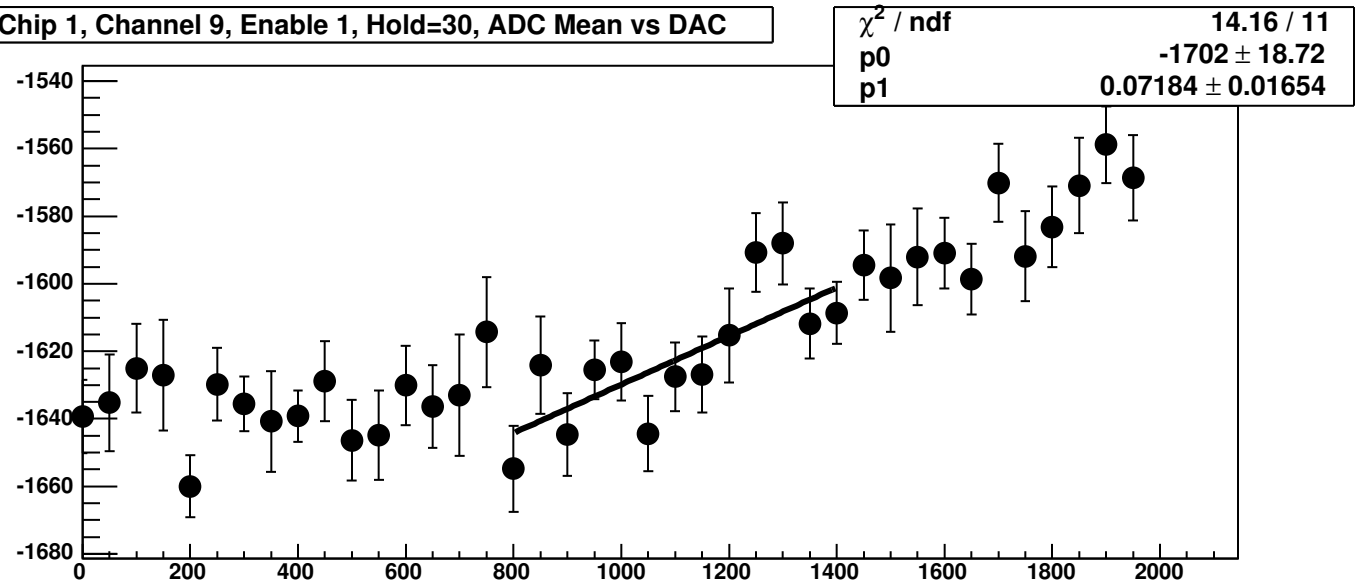

Chip 1, Channel 8, Enable 5!, Hold=30, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 9, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 13.49 / 11
p0 -1771 ± 22.34
p1 0.2819 ± 0.01991
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

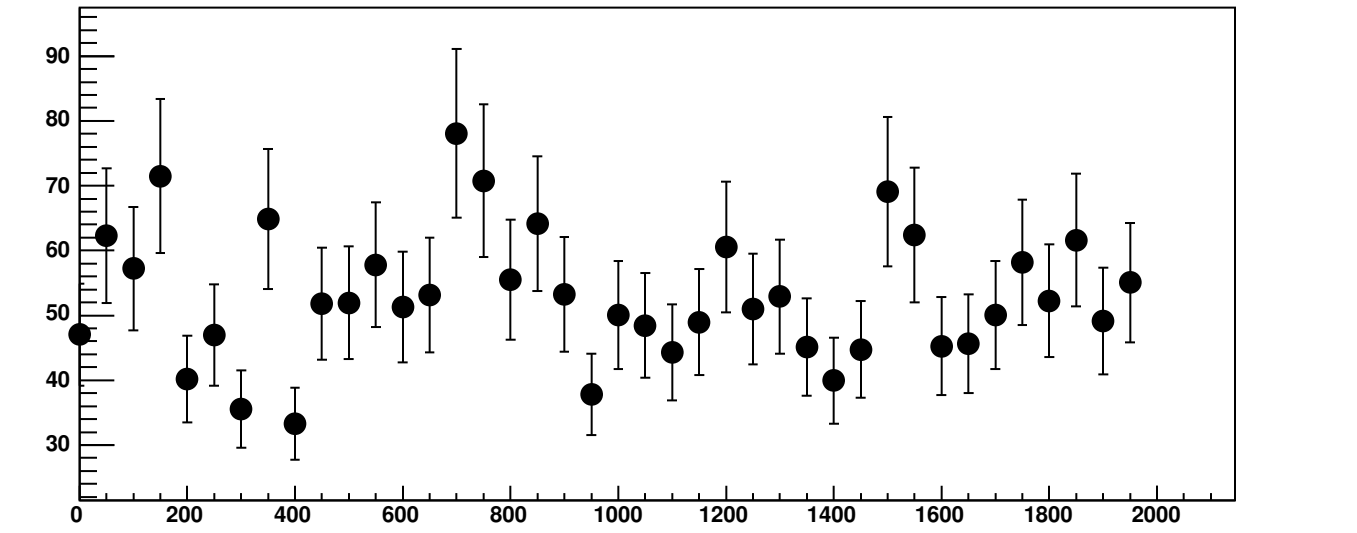

Chip 1, Channel 9, Enable 0, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 9, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

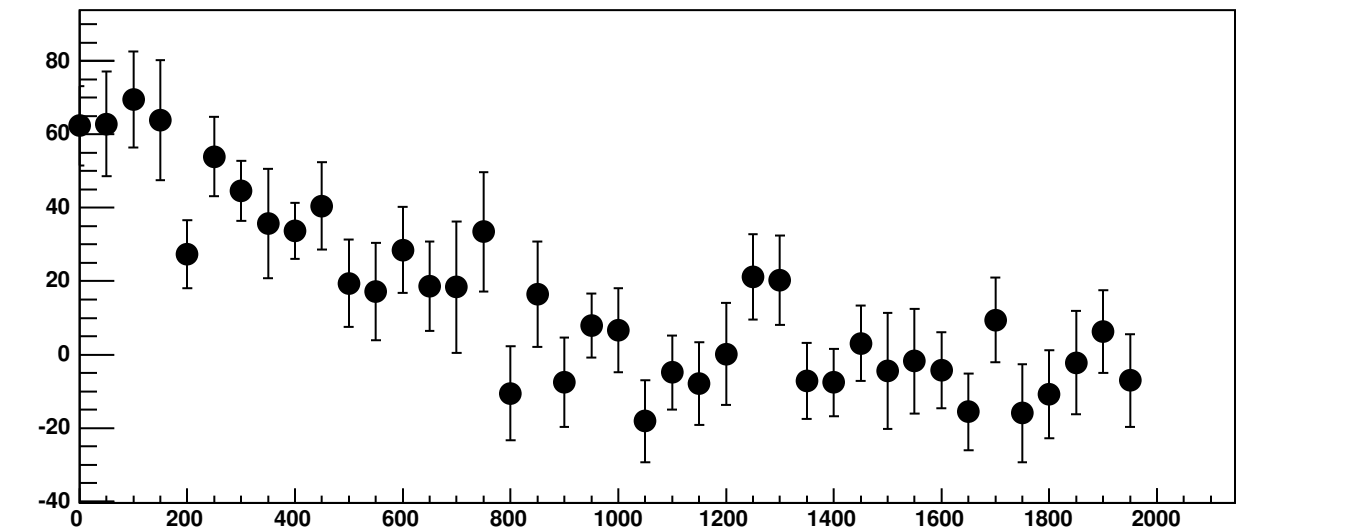
Chip 1, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC

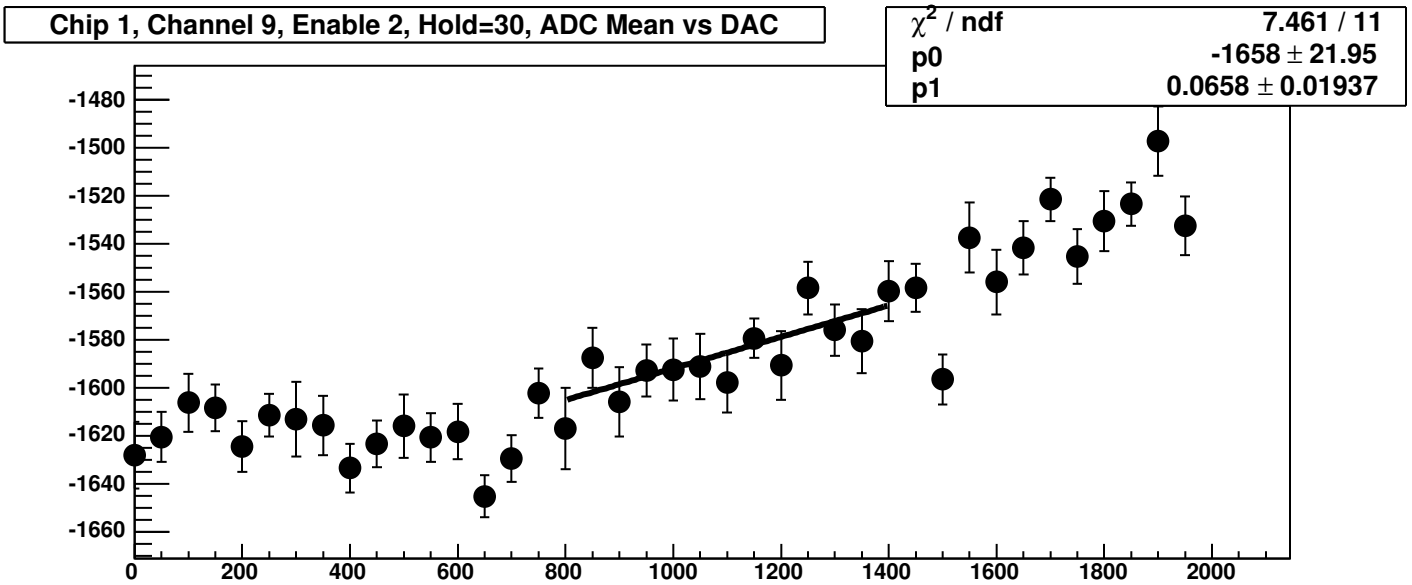


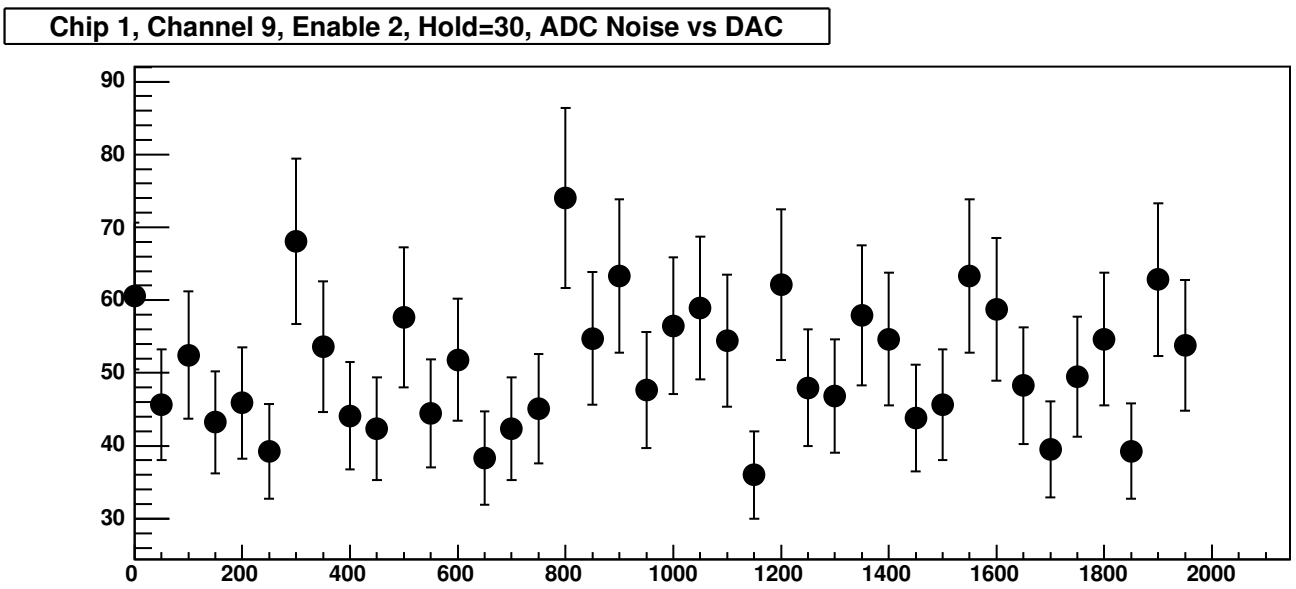
Chip 1, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC

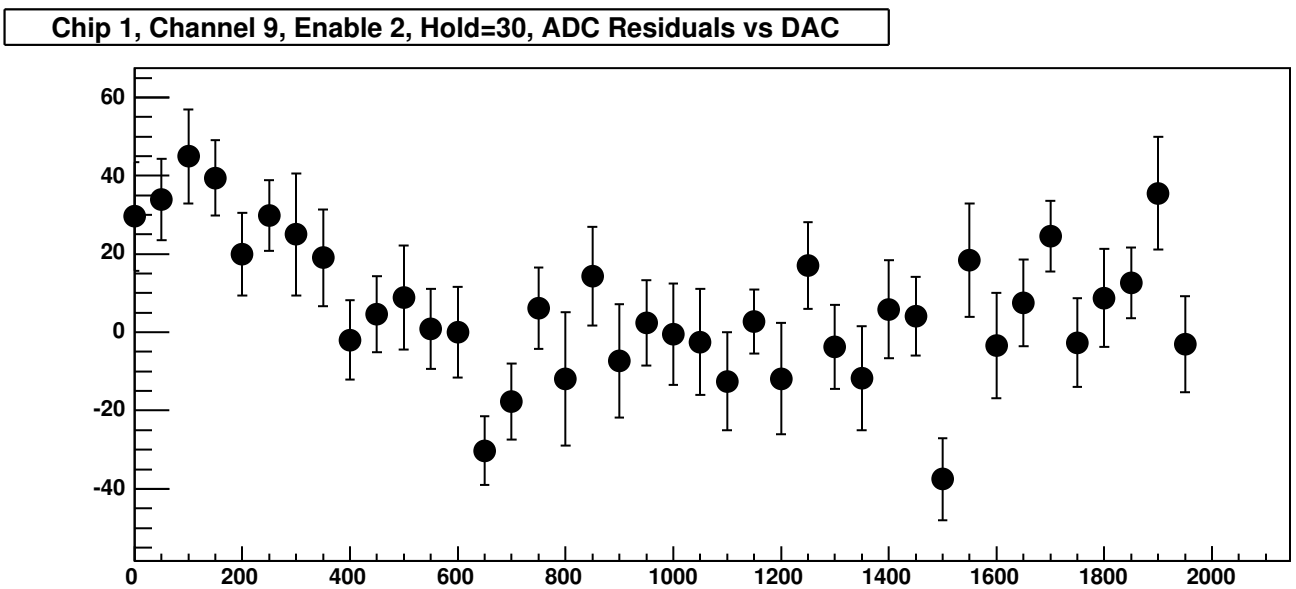


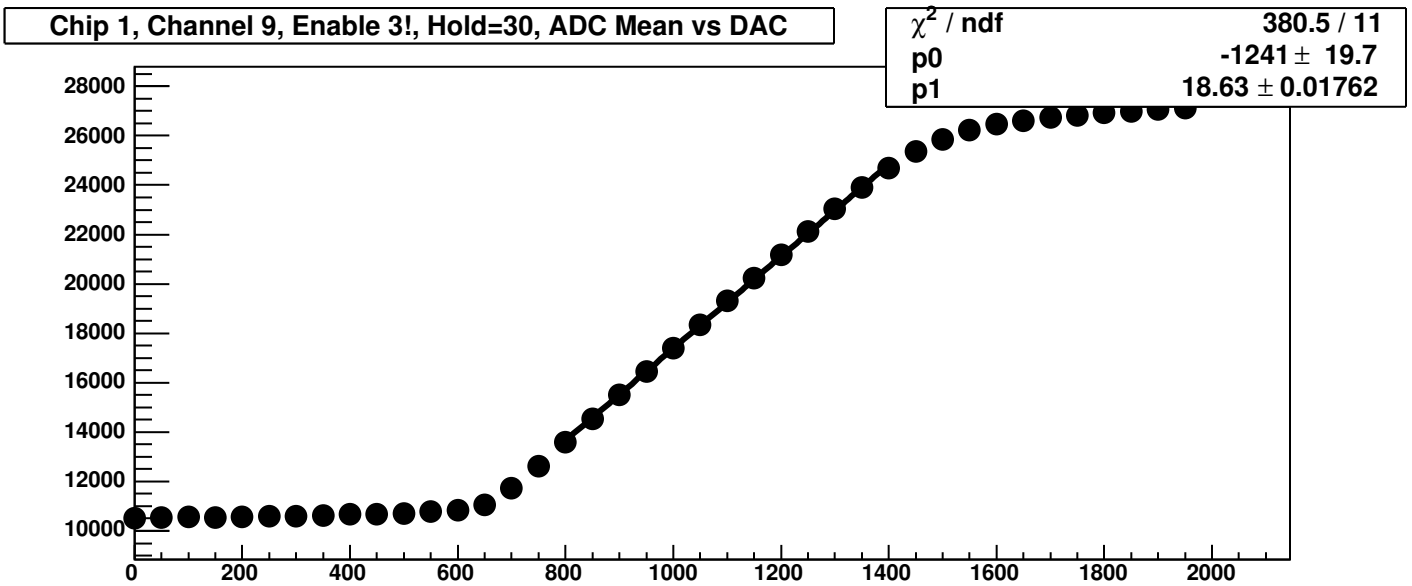
Chip 1, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC

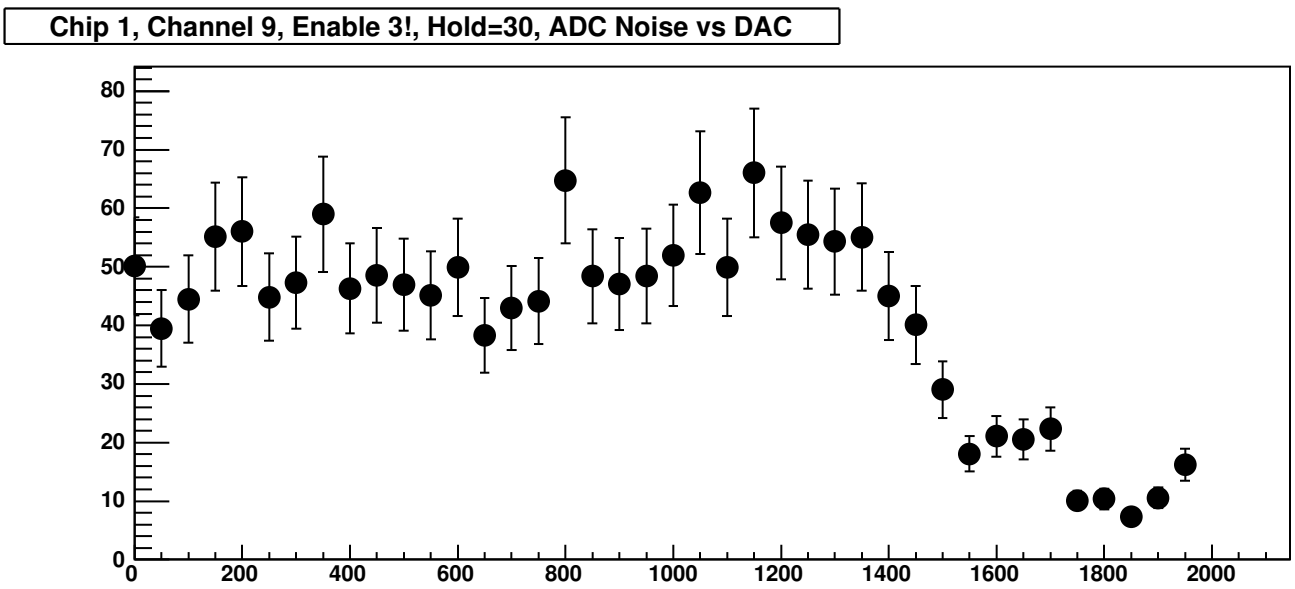


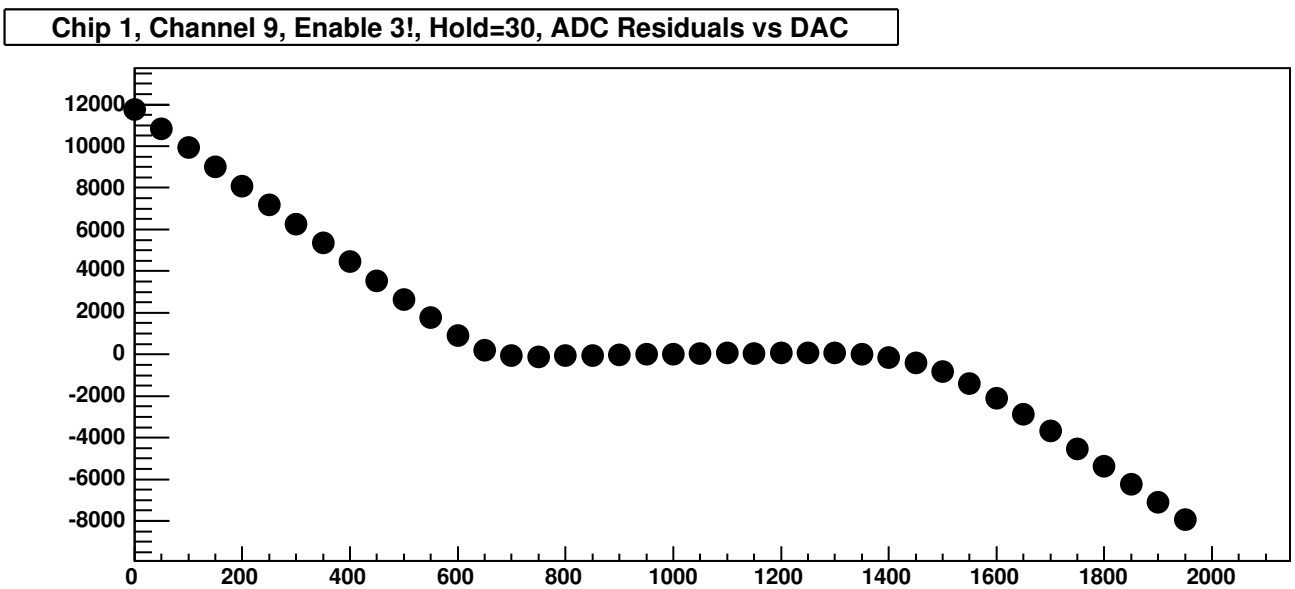

Chip 1, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 7.461 / 11
p0 -1658 ± 21.95
p1 0.0658 ± 0.01937
-1480
-1500
-1520
-1540
-1560
-1580
-1600
-1620
-1640
-1660
0
200
400
600
800
1000
1200
1400
1600
1800
2000

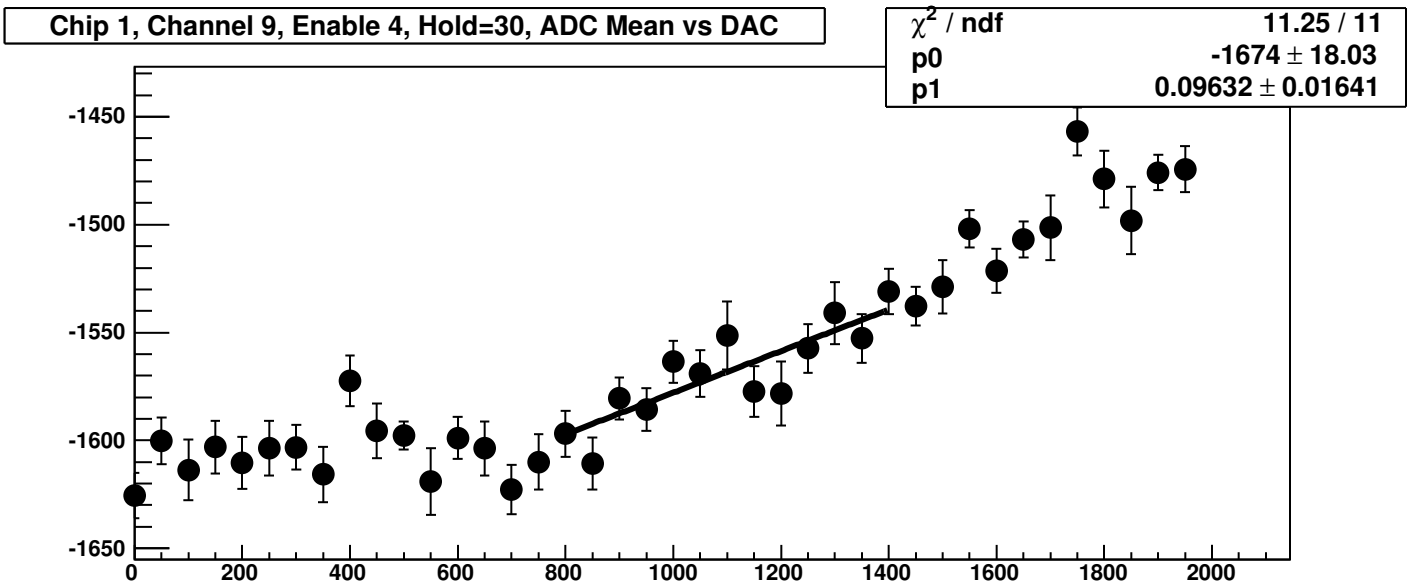

Chip 1, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

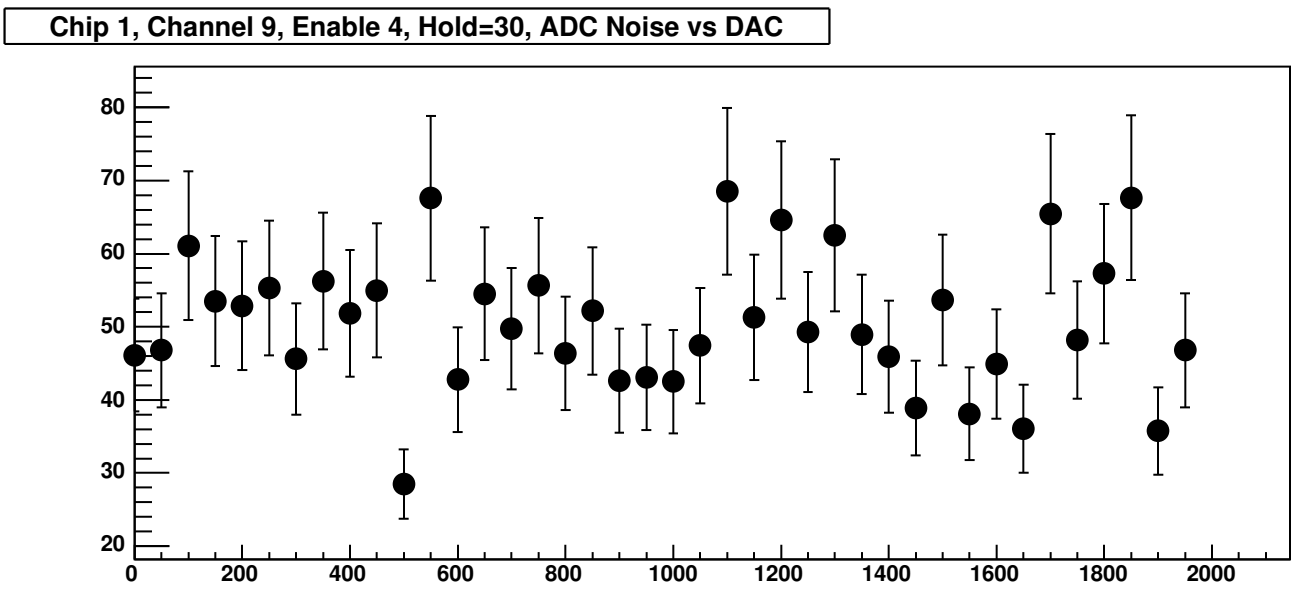

Chip 1, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

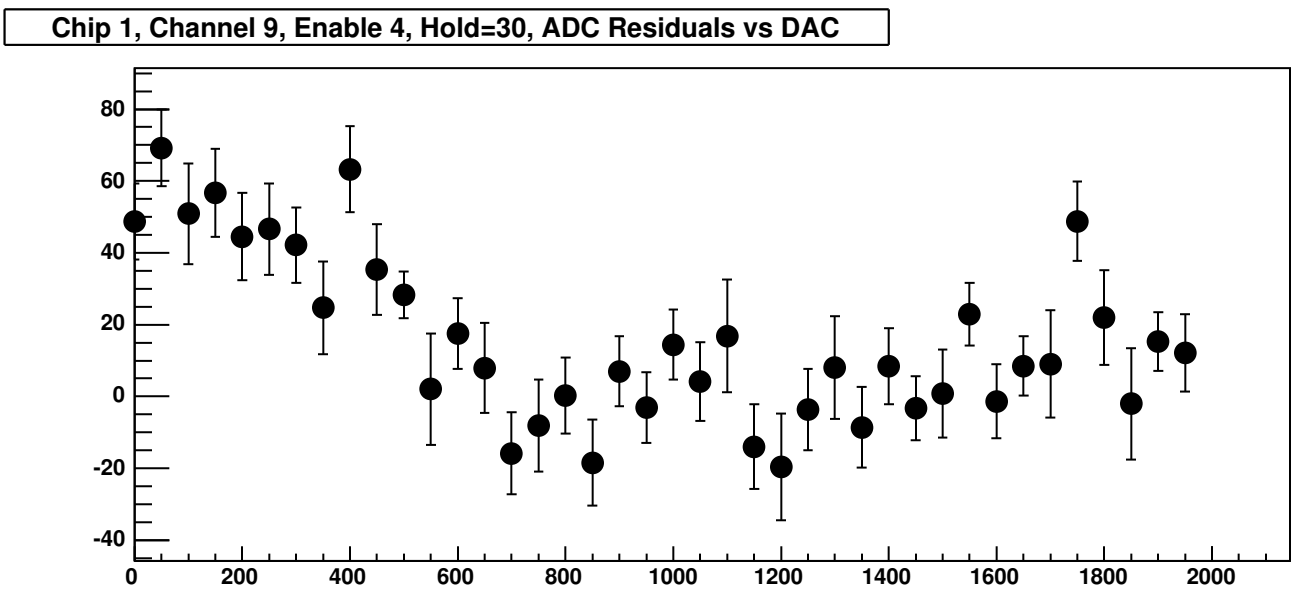

Chip 1, Channel 9, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 380.5 / 11
p0 -1241 ± 19.7
p1 18.63 ± 0.01762
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

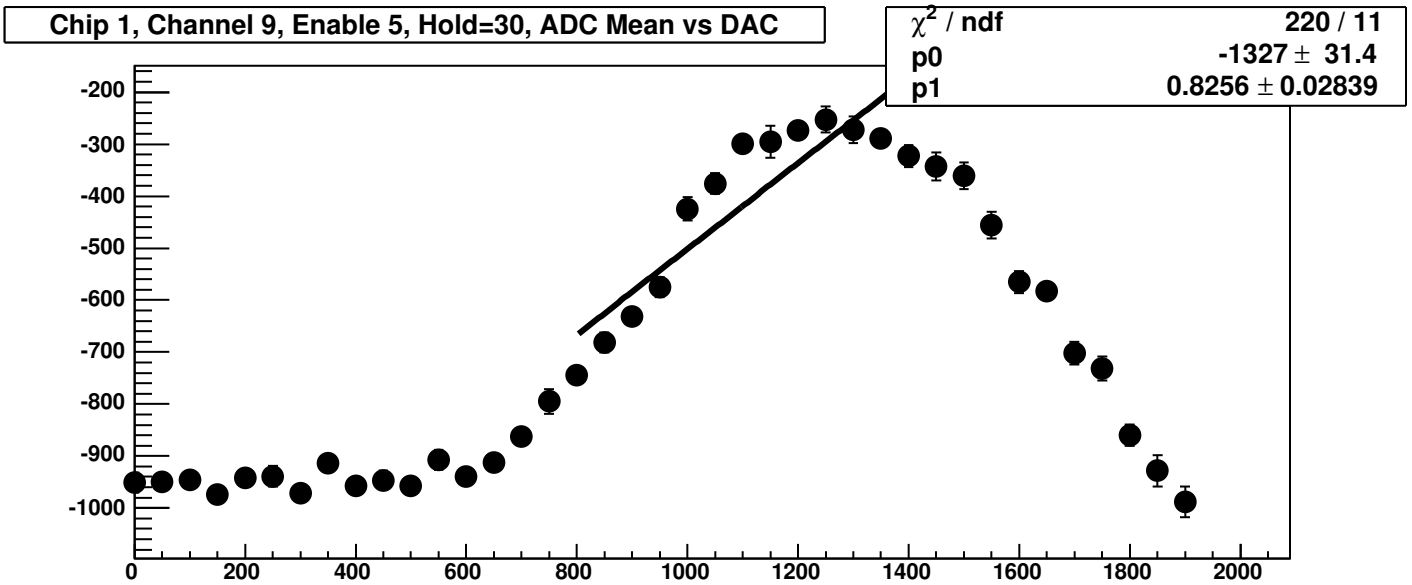

Chip 1, Channel 9, Enable 3!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

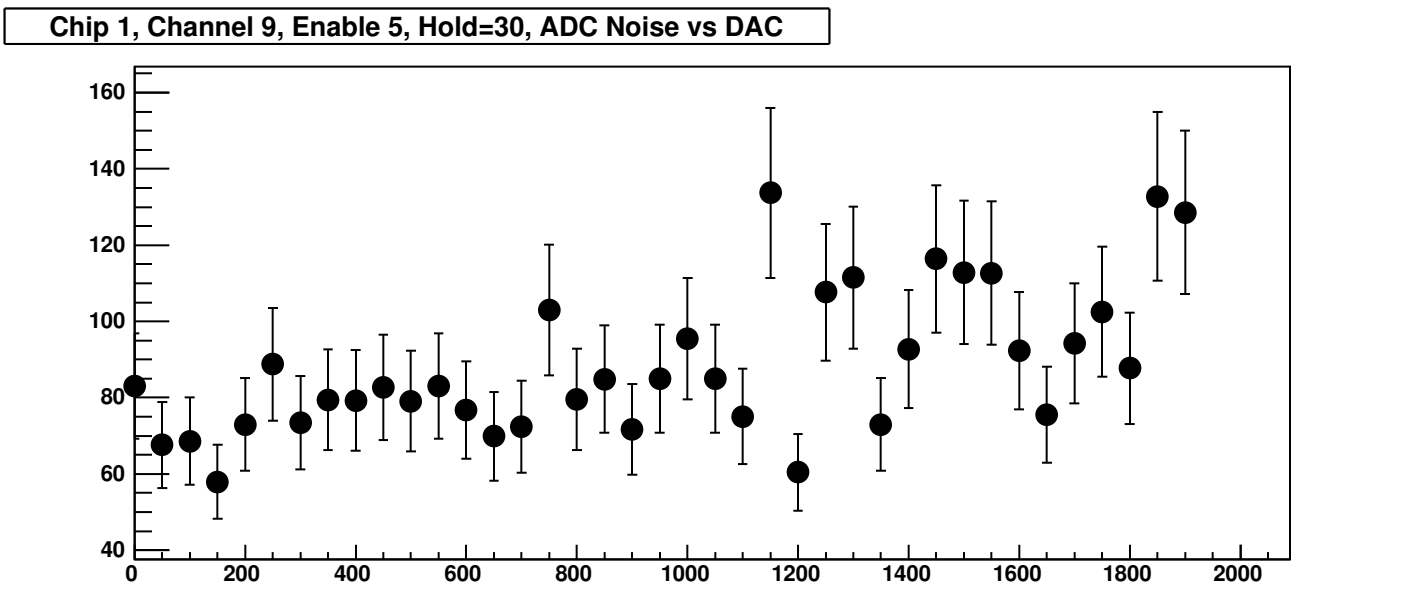

Chip 1, Channel 9, Enable 3!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

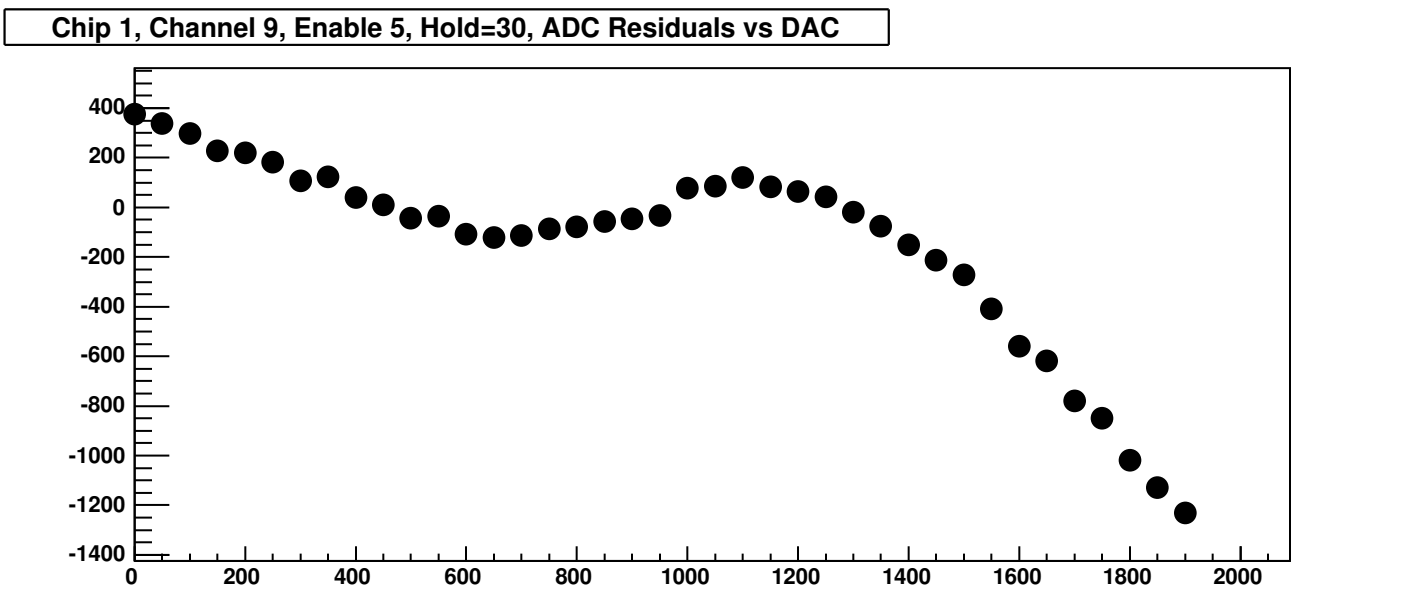

Chip 1, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 11.25 / 11
p0 -1674 ± 18.03
p1 0.09632 ± 0.01641
-1450
-1500
-1550
-1600
-1650
0
200
400
600
800
1000
1200
1400
1600
1800
2000

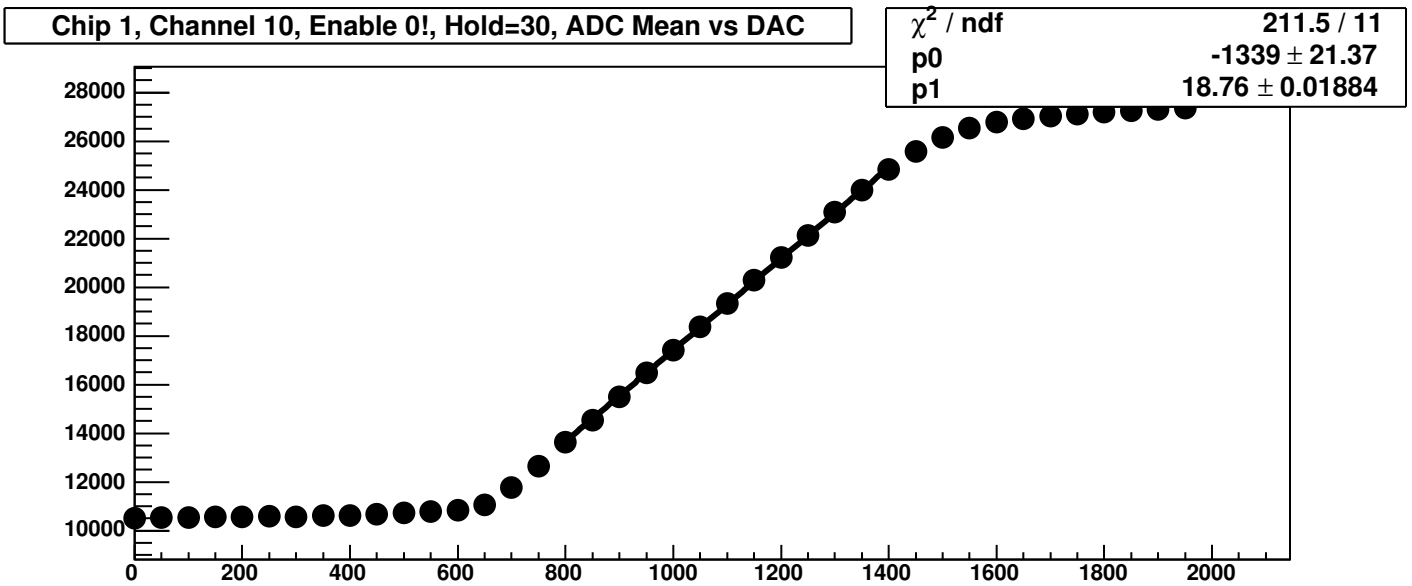

Chip 1, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

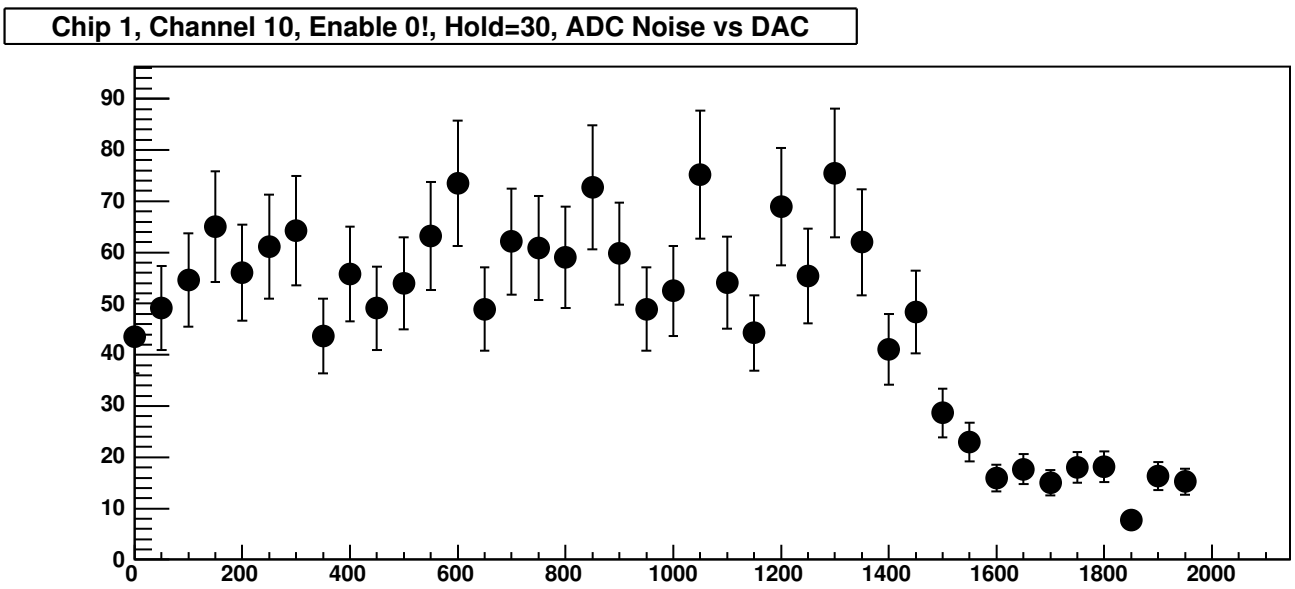

Chip 1, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

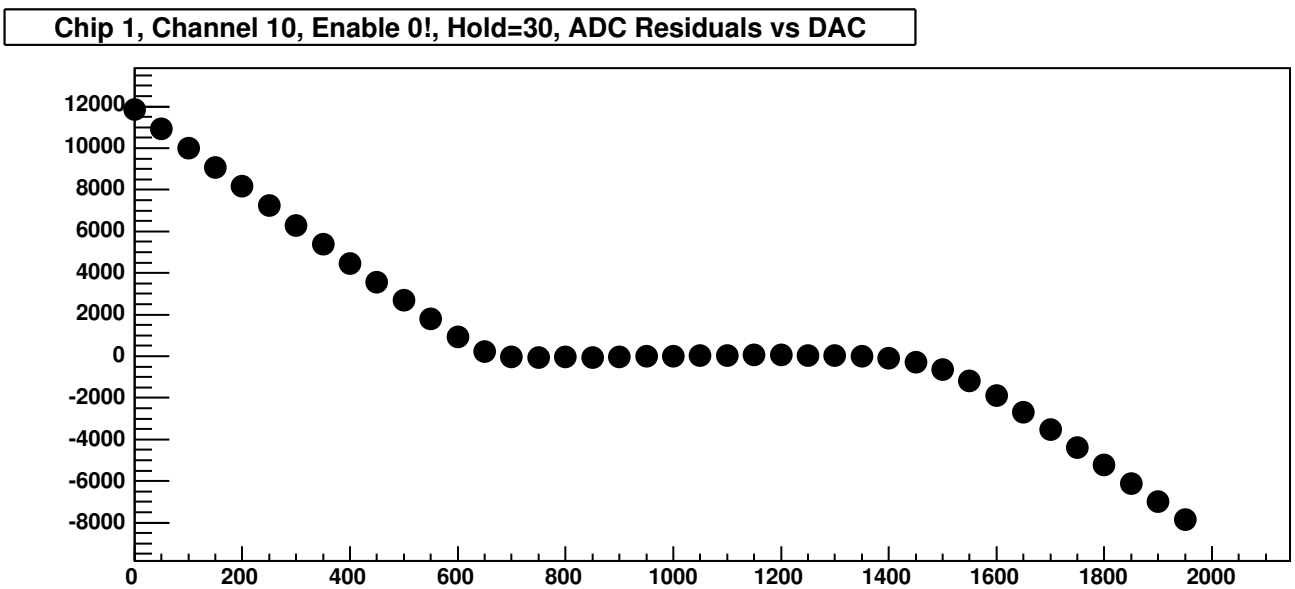

Chip 1, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 220 / 11
p0 -1327 ± 31.4
p1 0.8256 ± 0.02839
-200
-300
-400
-500
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

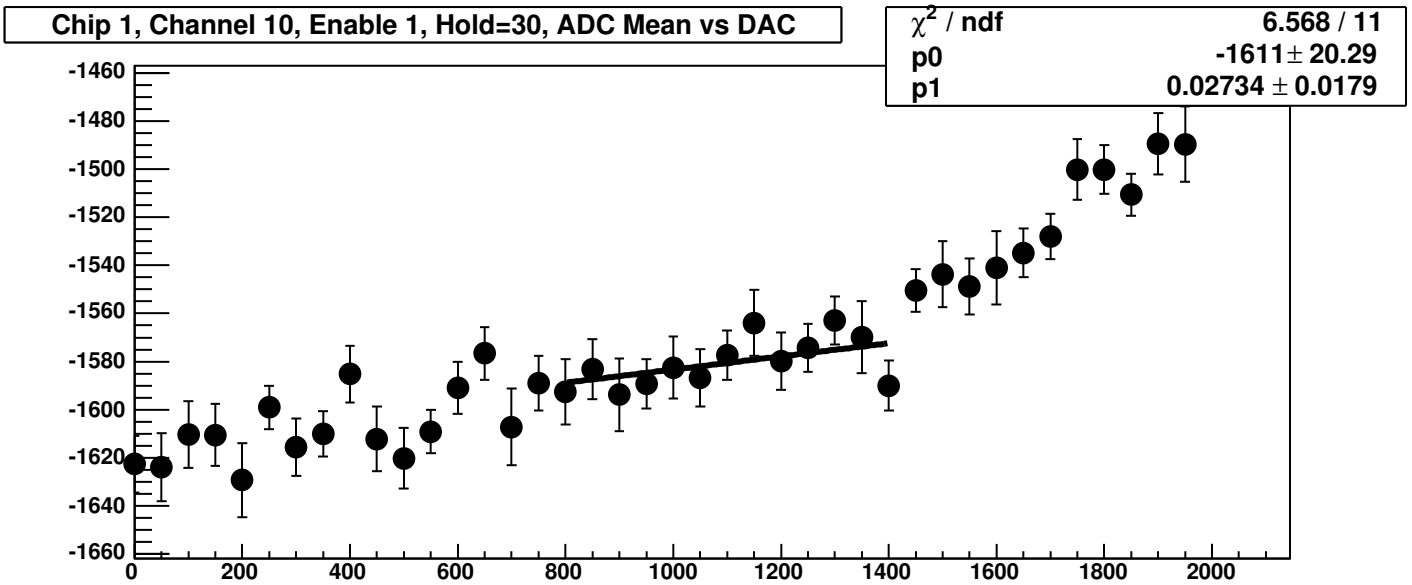

Chip 1, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

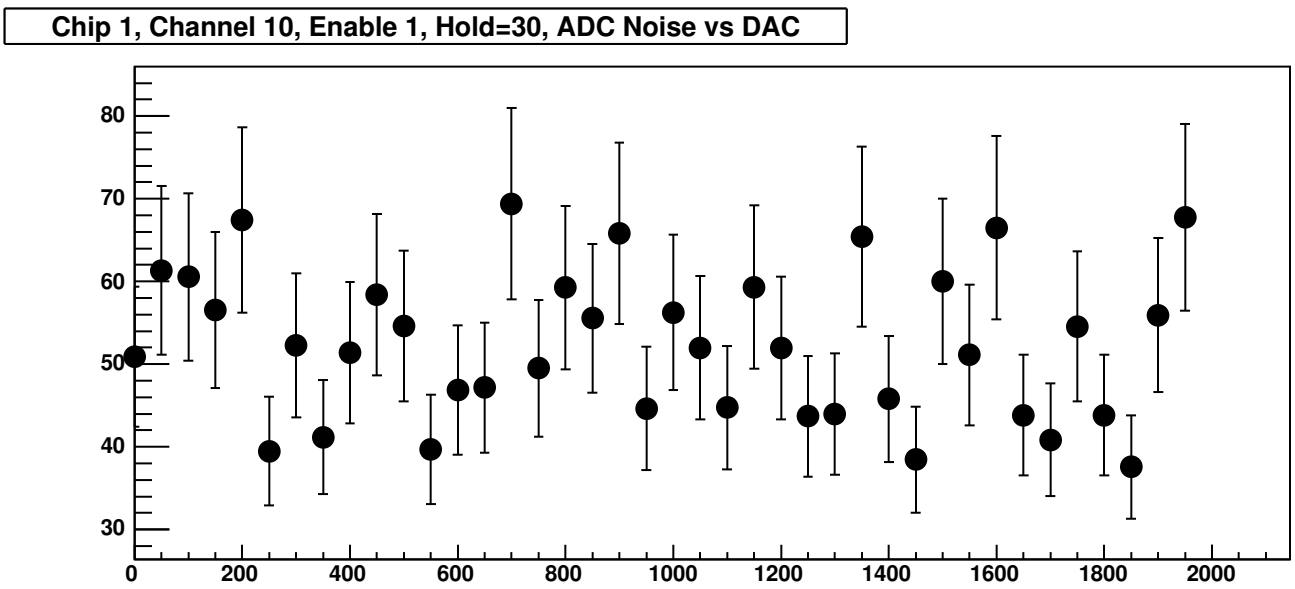

Chip 1, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

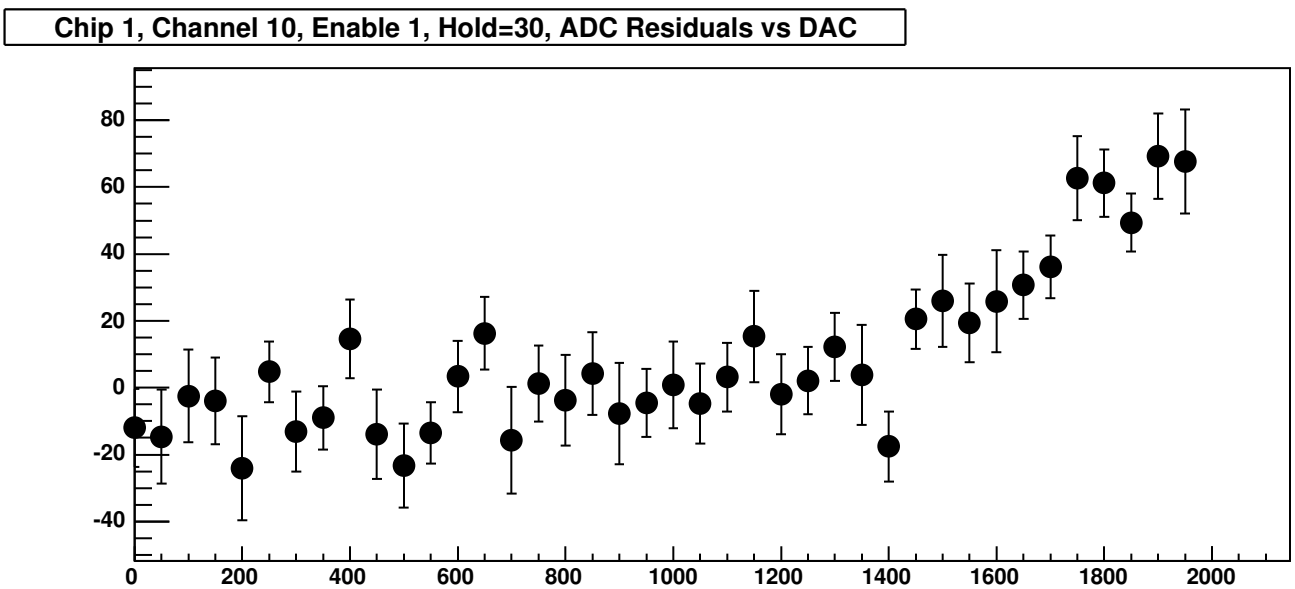

Chip 1, Channel 10, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 211.5 / 11
p0 -1339 ± 21.37
p1 18.76 ± 0.01884
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

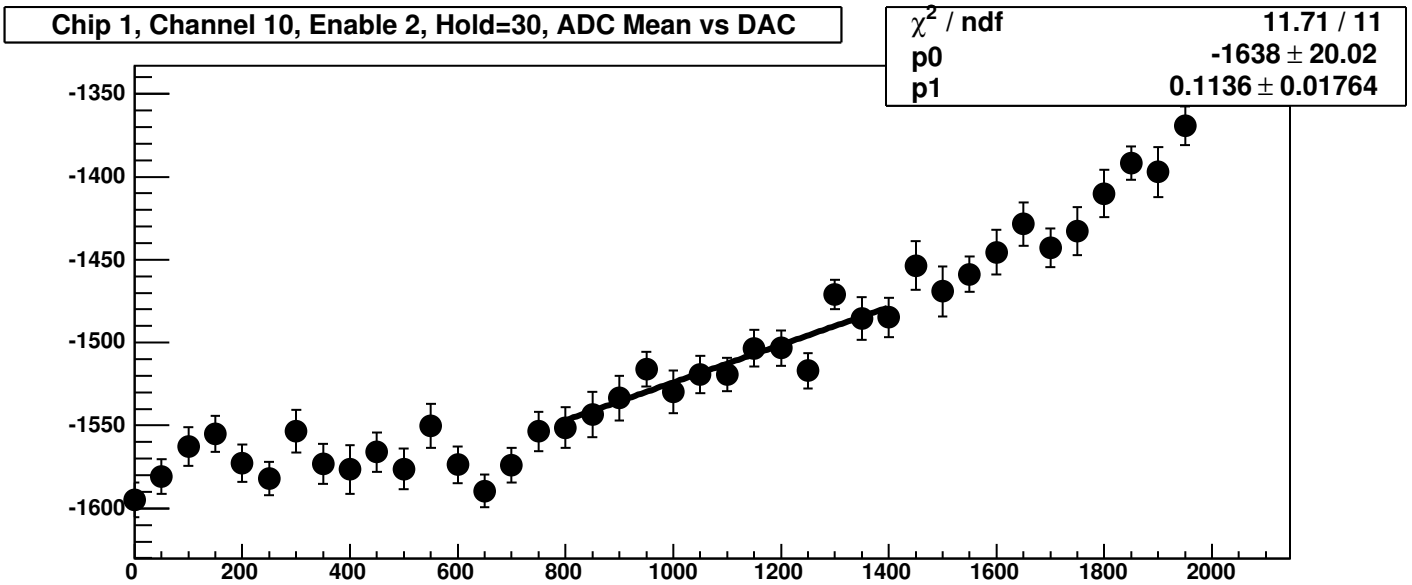

Chip 1, Channel 10, Enable 0!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

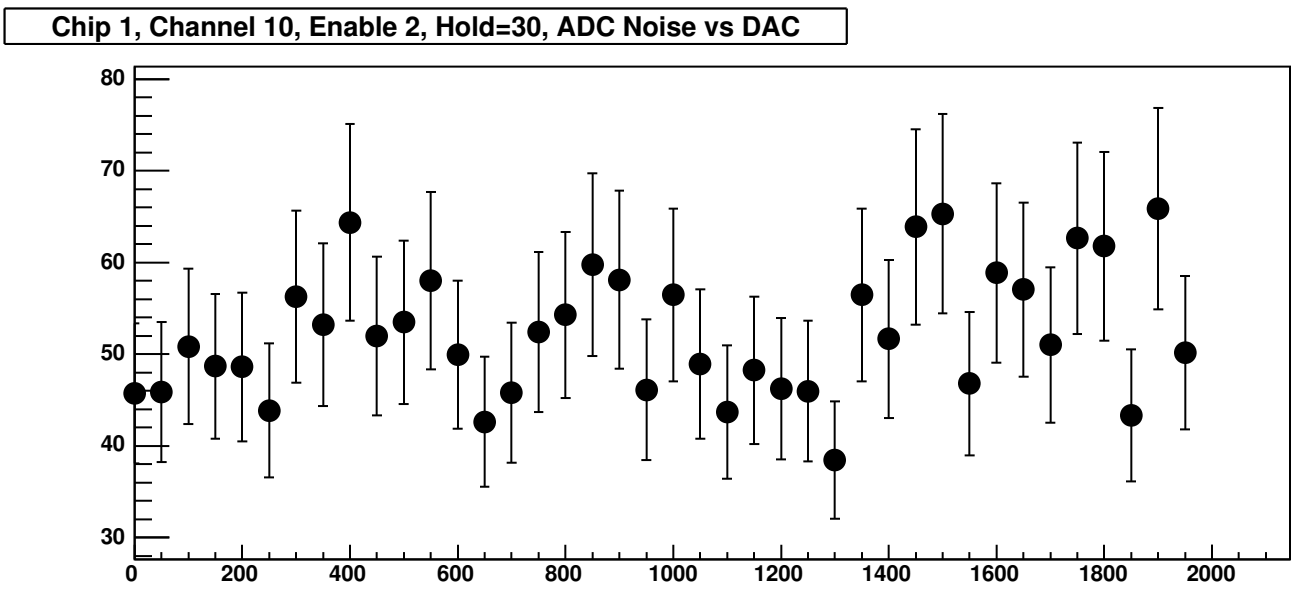

Chip 1, Channel 10, Enable 0!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

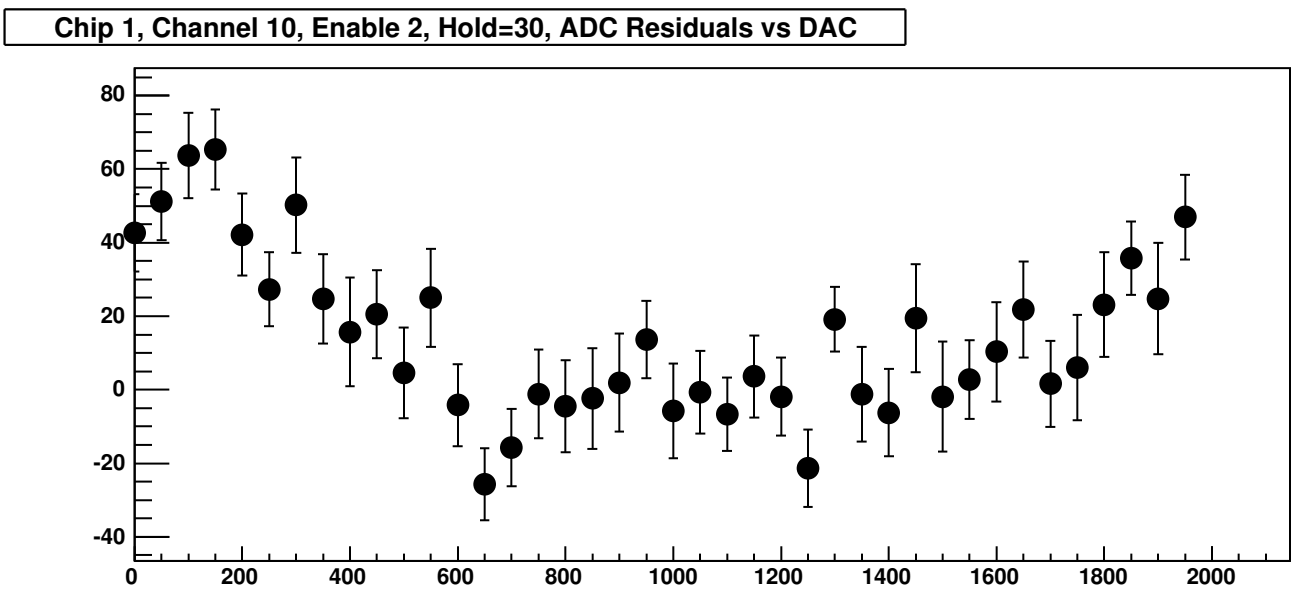

Chip 1, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 6.568 / 11
p0 -1611 ± 20.29
p1 0.02734 ± 0.0179
-1460
-1480
-1500
-1520
-1540
-1560
-1580
-1600
-1620
-1640
-1660
0
200
400
600
800
1000
1200
1400
1600
1800
2000

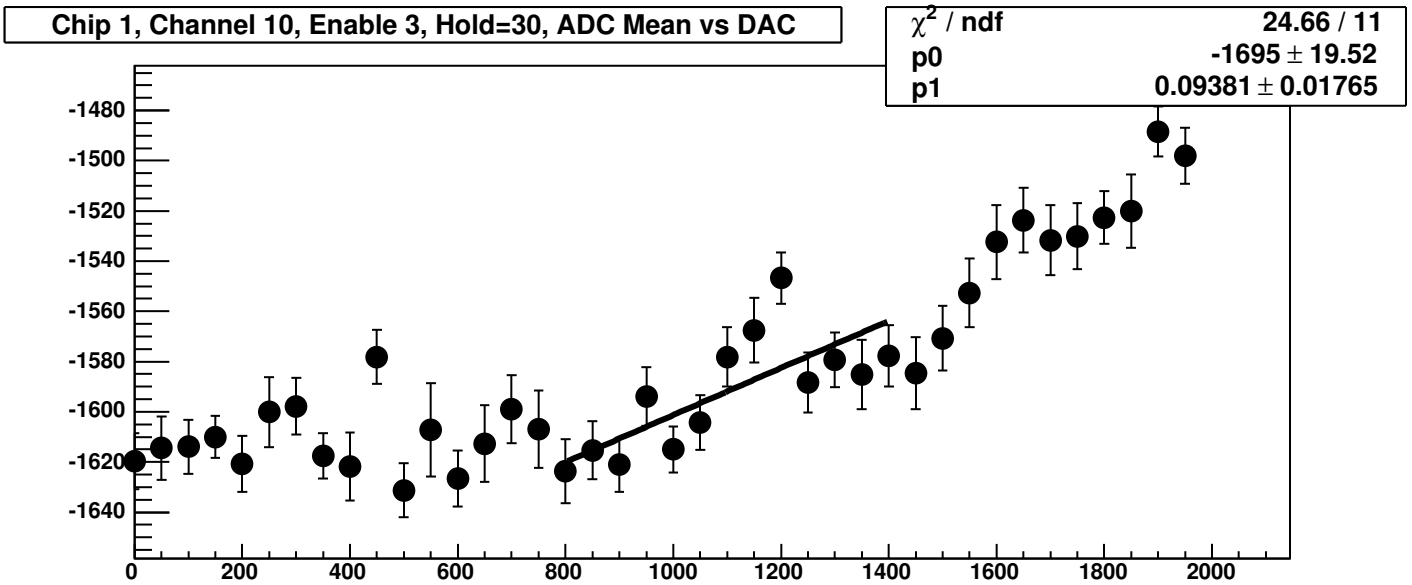

Chip 1, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

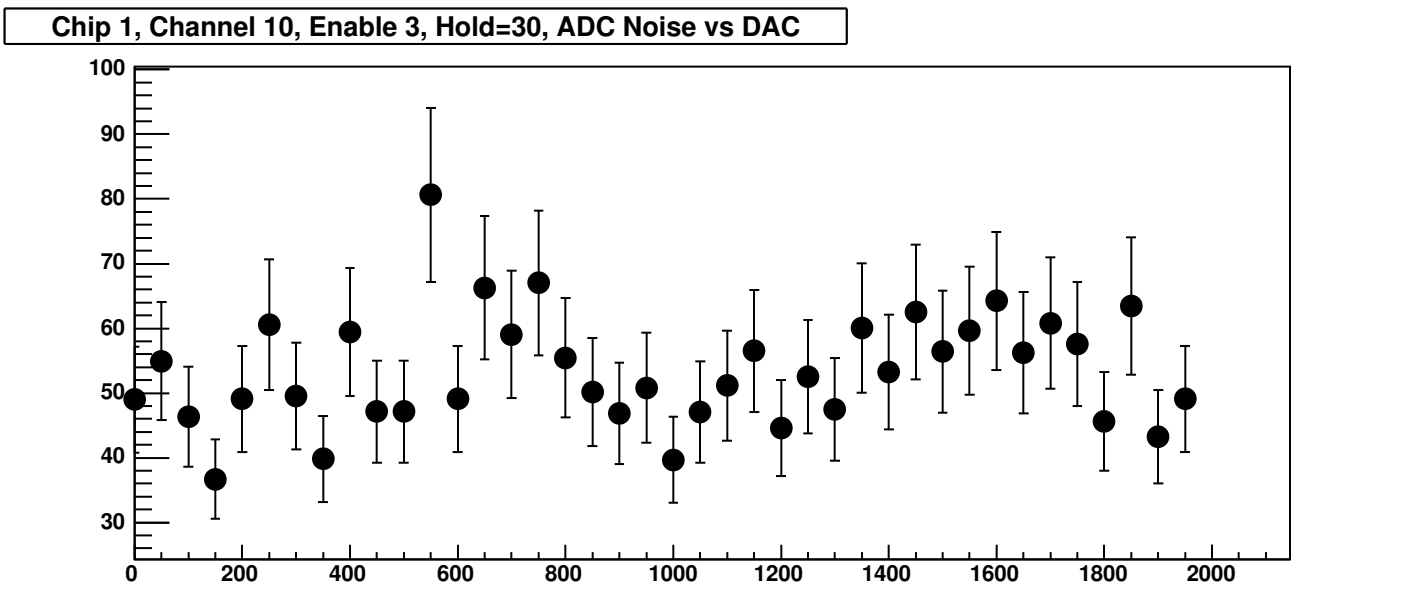

Chip 1, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

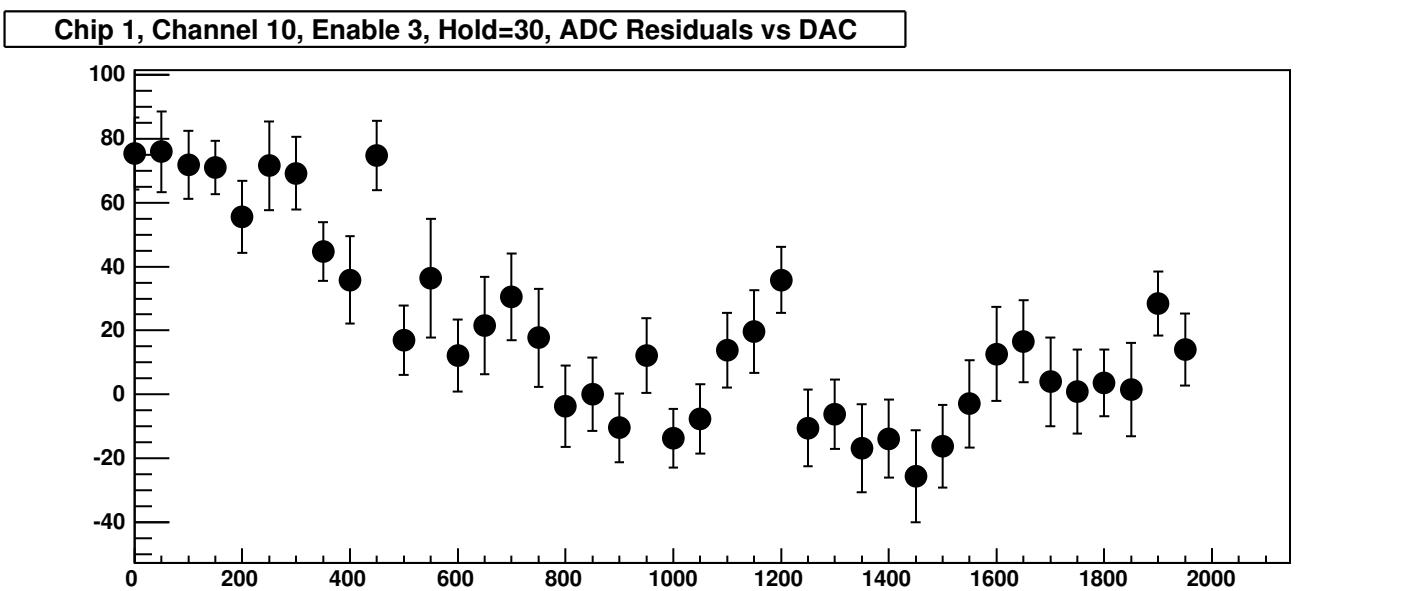

Chip 1, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 11.71 / 11
p0 -1638 ± 20.02
p1 0.1136 ± 0.01764
-1350
-1400
-1450
-1500
-1550
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

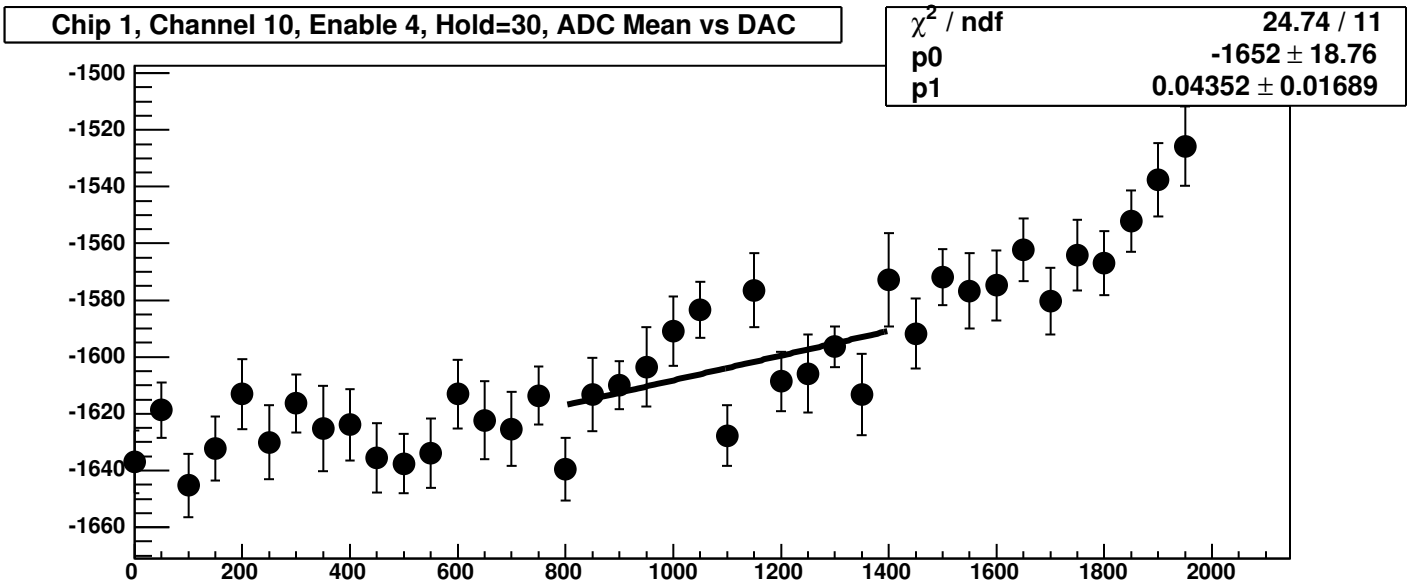

Chip 1, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

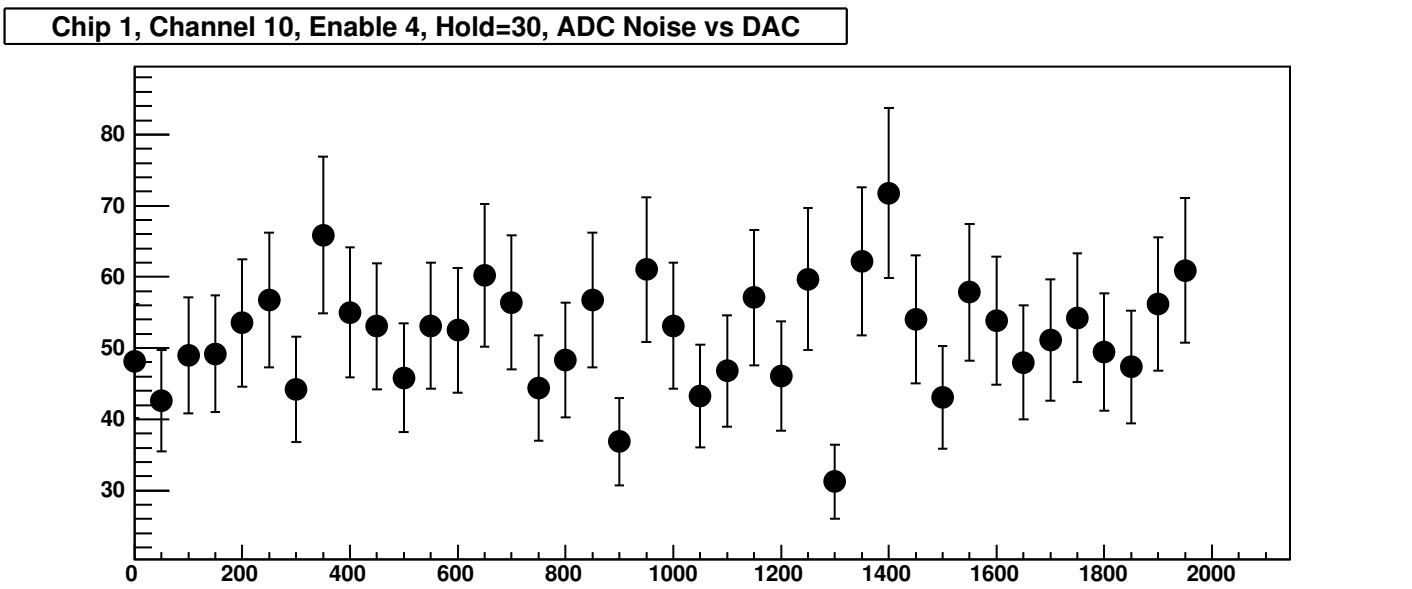

Chip 1, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

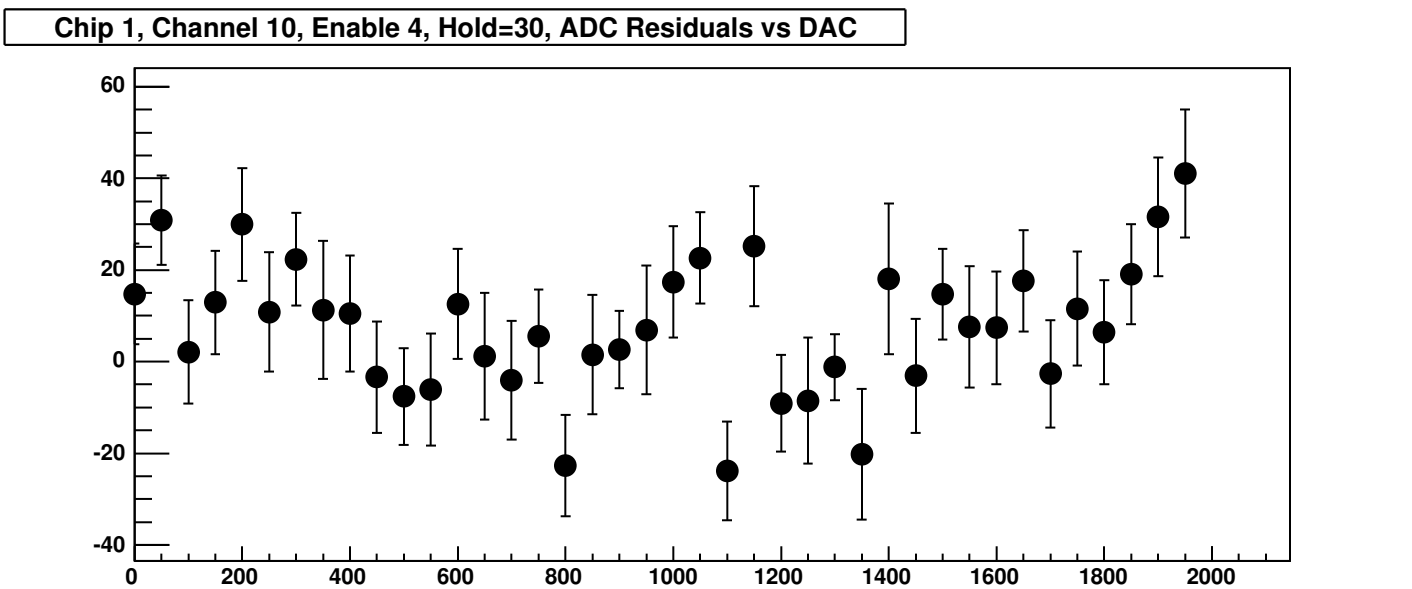

Chip 1, Channel 10, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 24.66 / 11
p0 -1695 ± 19.52
p1 0.09381 ± 0.01765
-1480
-1500
-1520
-1540
-1560
-1580
-1600
-1620
-1640
0
200
400
600
800
1000
1200
1400
1600
1800
2000

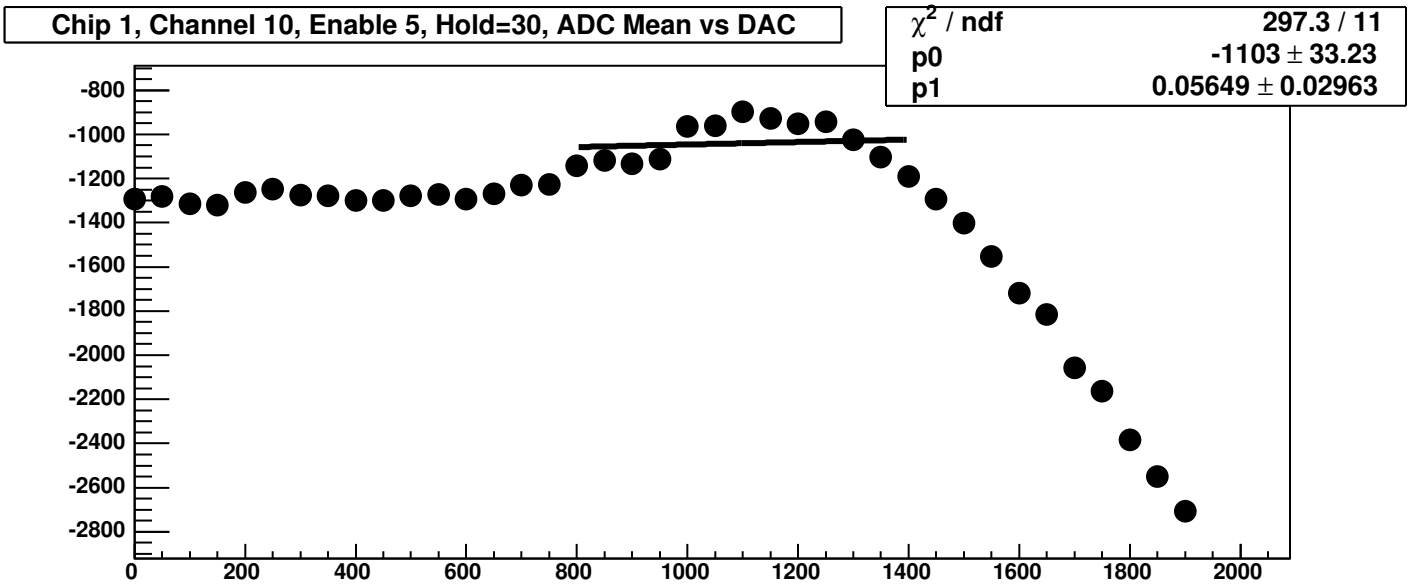

Chip 1, Channel 10, Enable 3, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

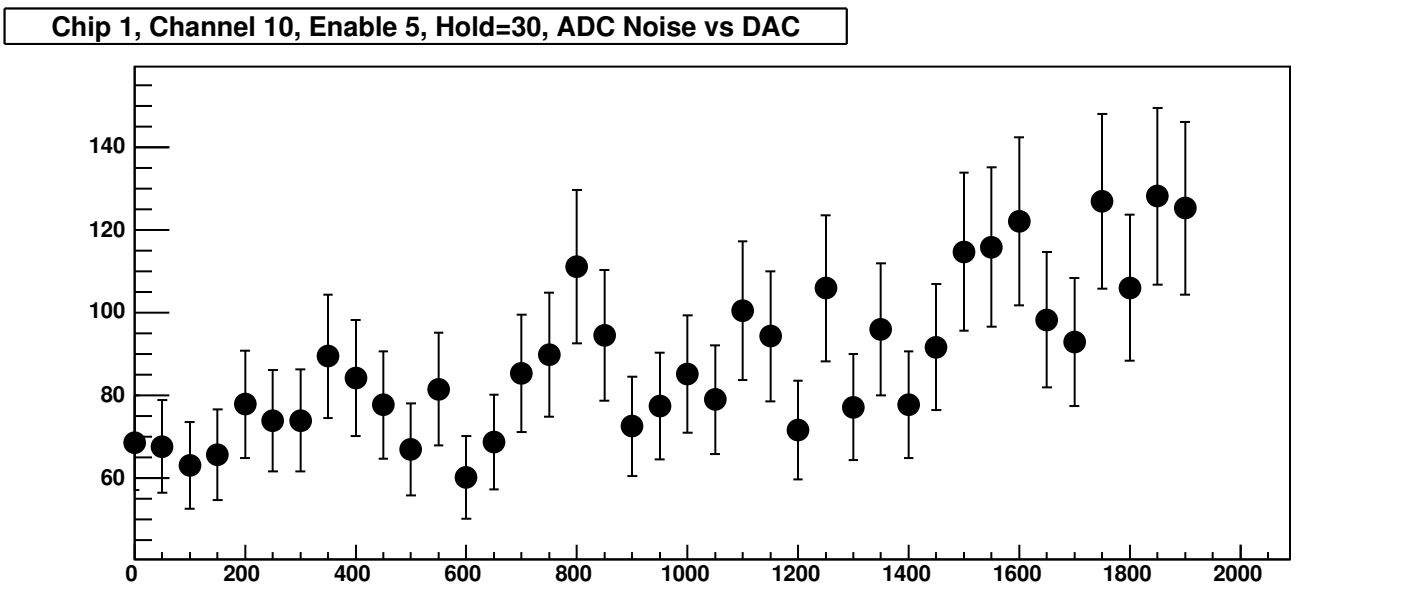

Chip 1, Channel 10, Enable 3, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

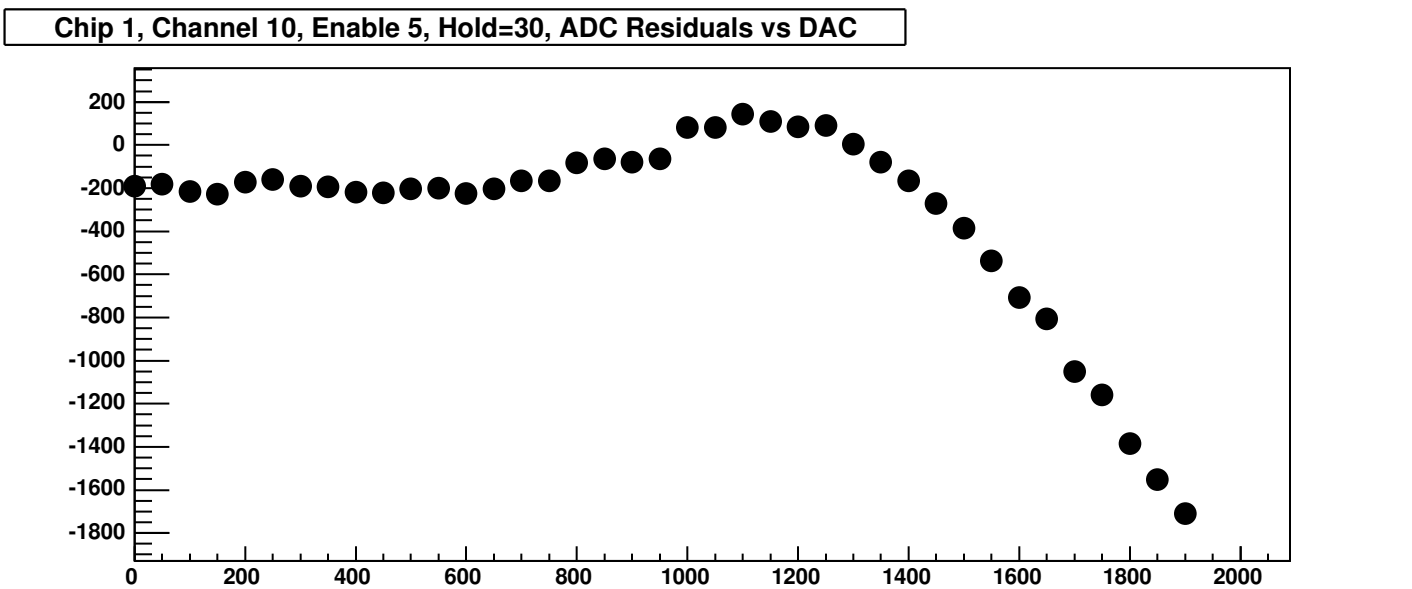

Chip 1, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 24.74 / 11
p0 -1652 ± 18.76
p1 0.04352 ± 0.01689
-1500
-1520
-1540
-1560
-1580
-1600
-1620
-1640
-1660
0
200
400
600
800
1000
1200
1400
1600
1800
2000

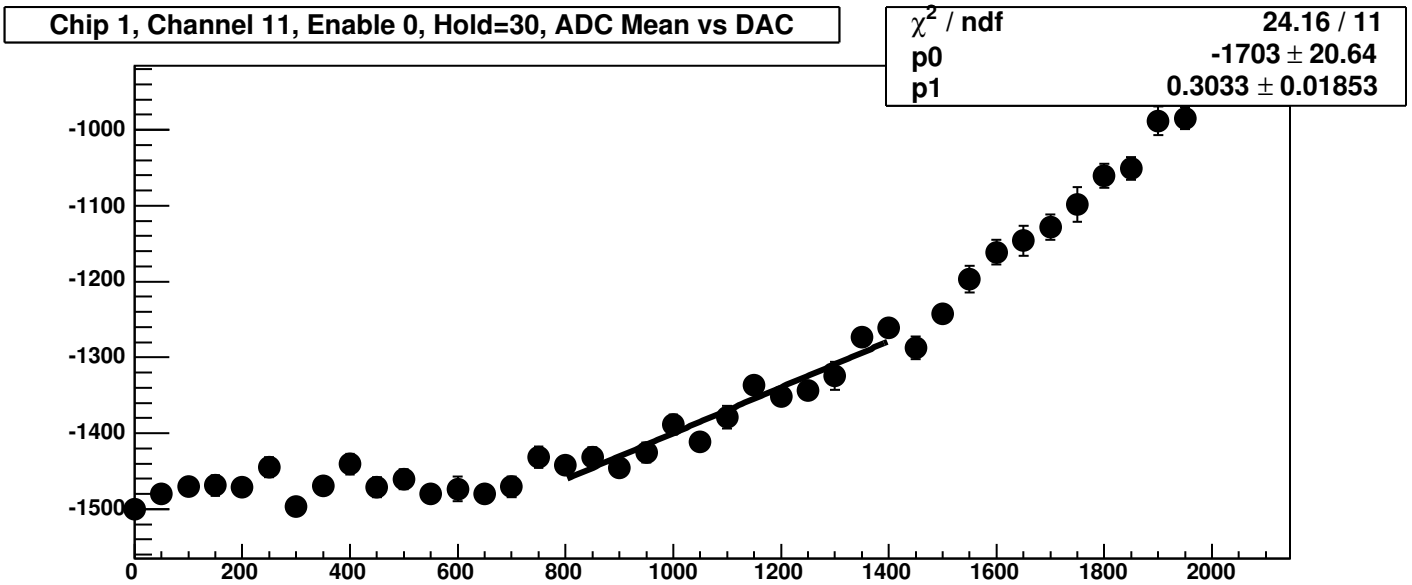

Chip 1, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

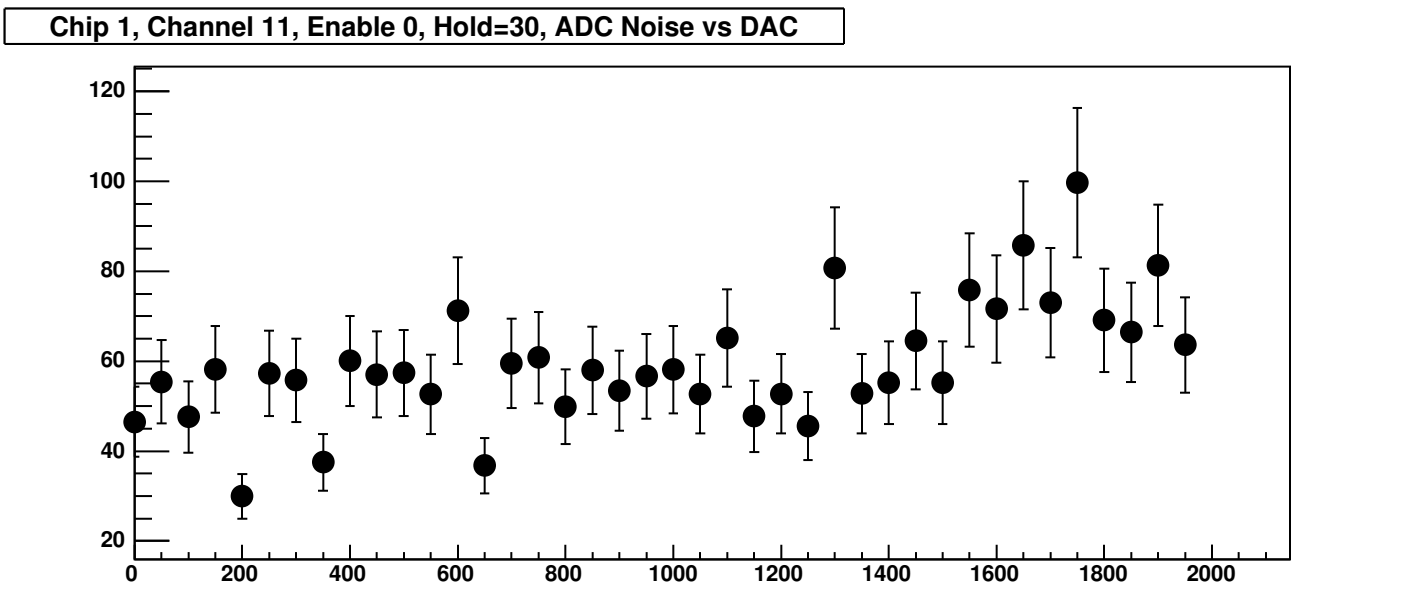

Chip 1, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

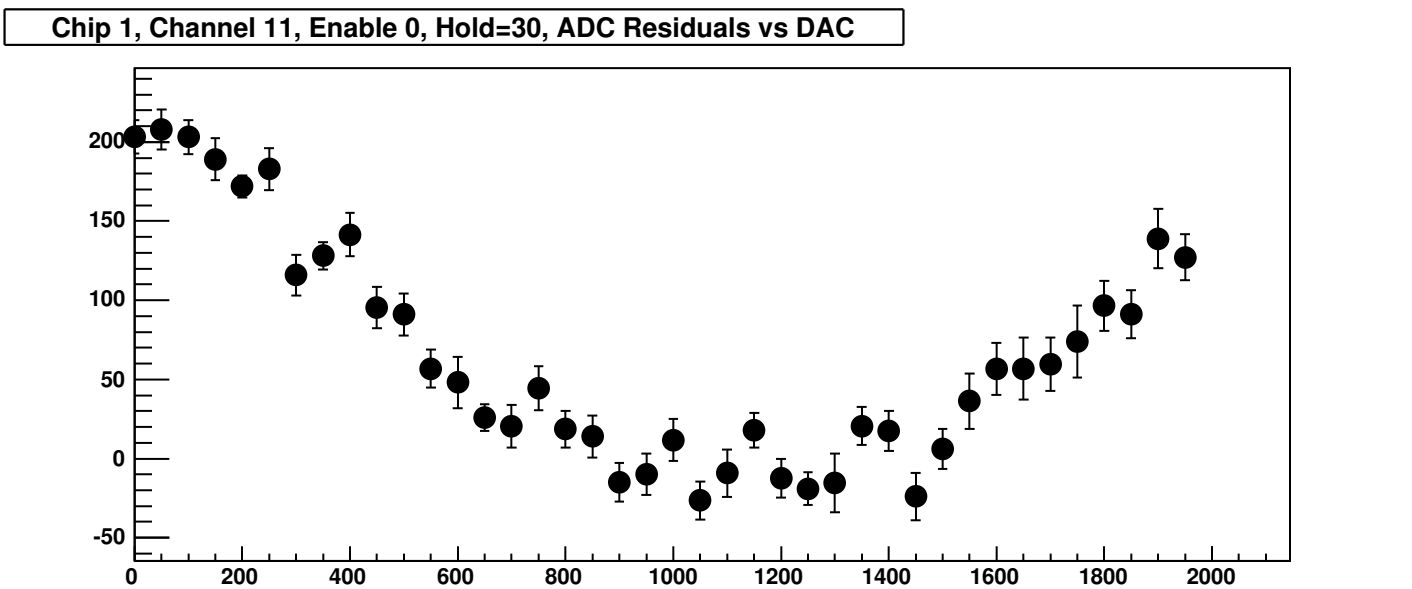

Chip 1, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 297.3 / 11
p0 -1103 ± 33.23
p1 0.05649 ± 0.02963
-800
-1000
-1200
-1400
-1600
-1800
-2000
-2200
-2400
-2600
-2800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

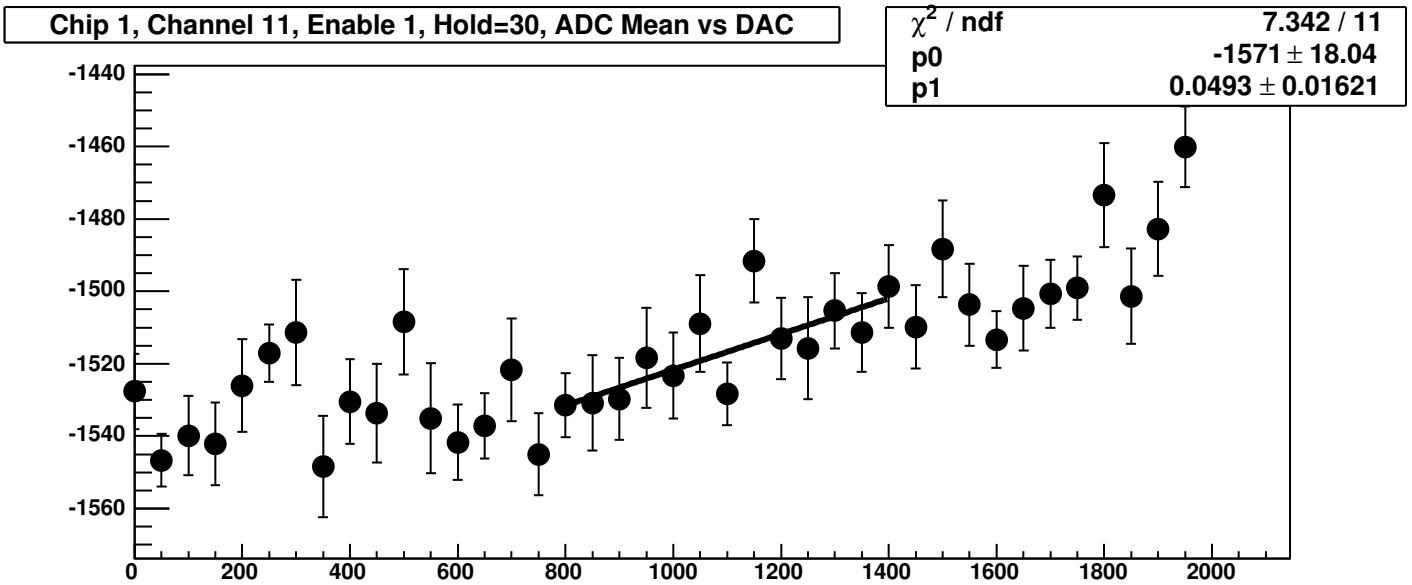

Chip 1, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC
140
120
100
80
60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

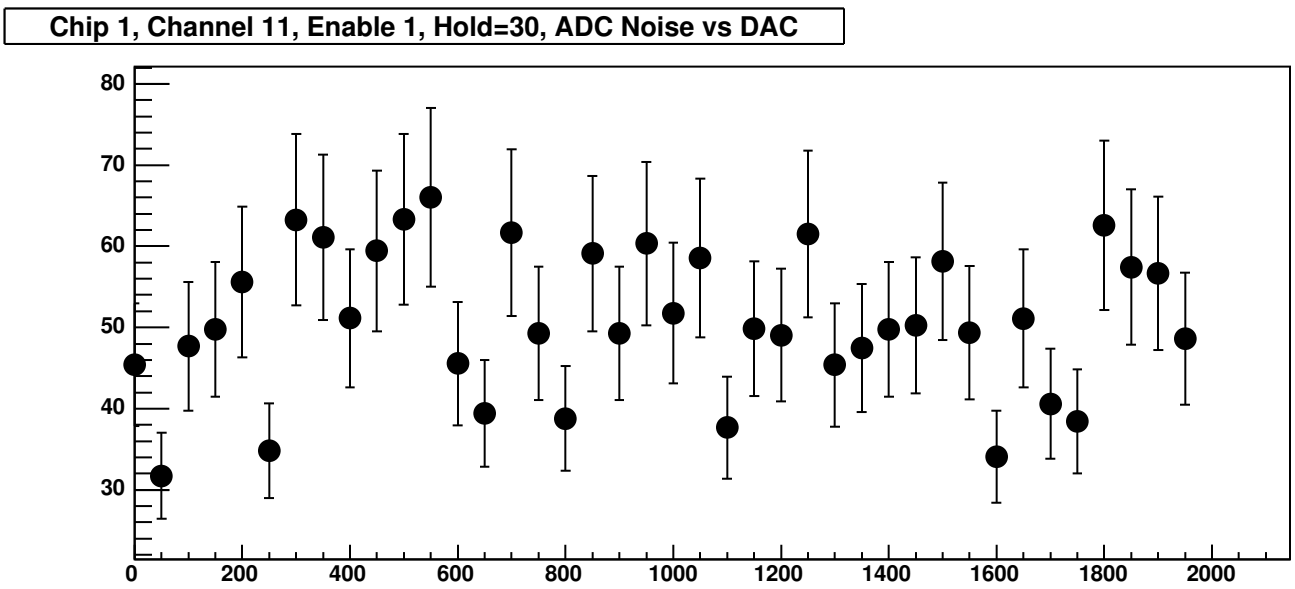

Chip 1, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

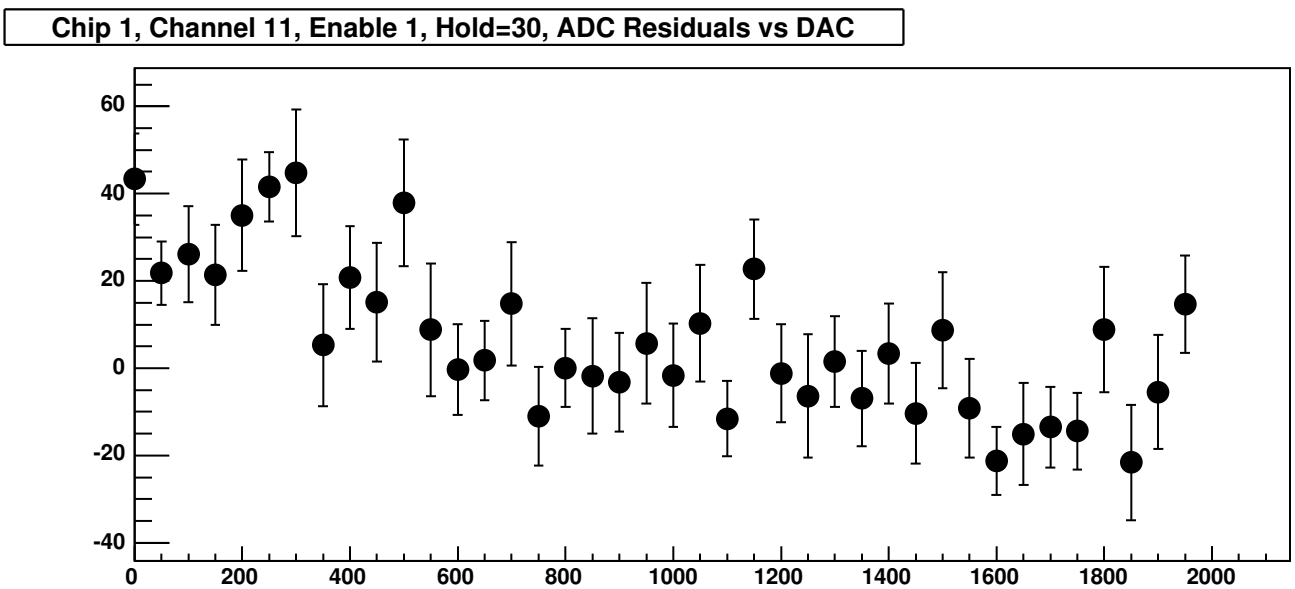

Chip 1, Channel 11, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 24.16 / 11
p0 -1703 ± 20.64
p1 0.3033 ± 0.01853
-1000
-1100
-1200
-1300
-1400
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

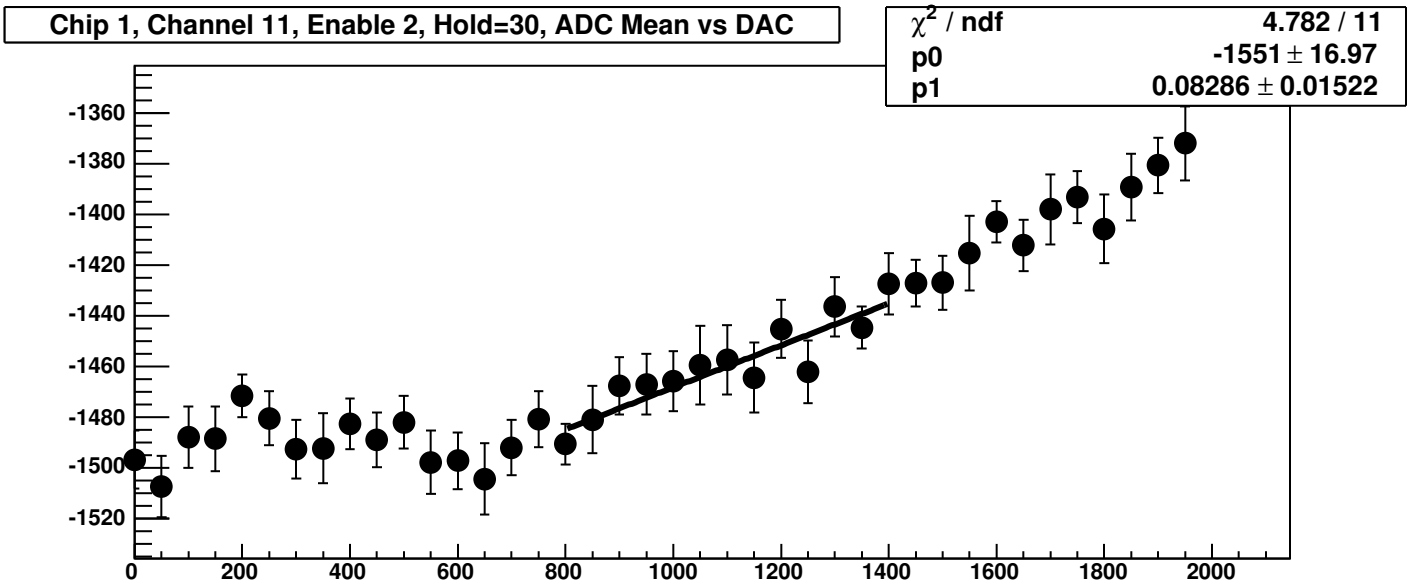

Chip 1, Channel 11, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

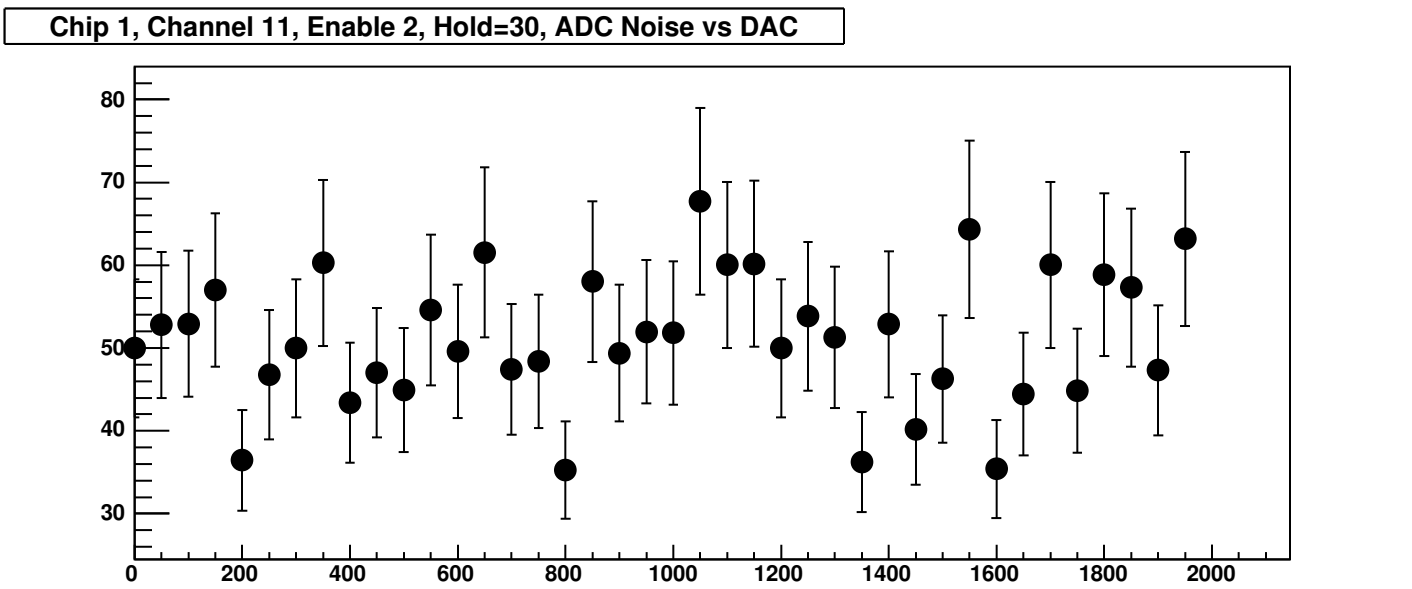

Chip 1, Channel 11, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

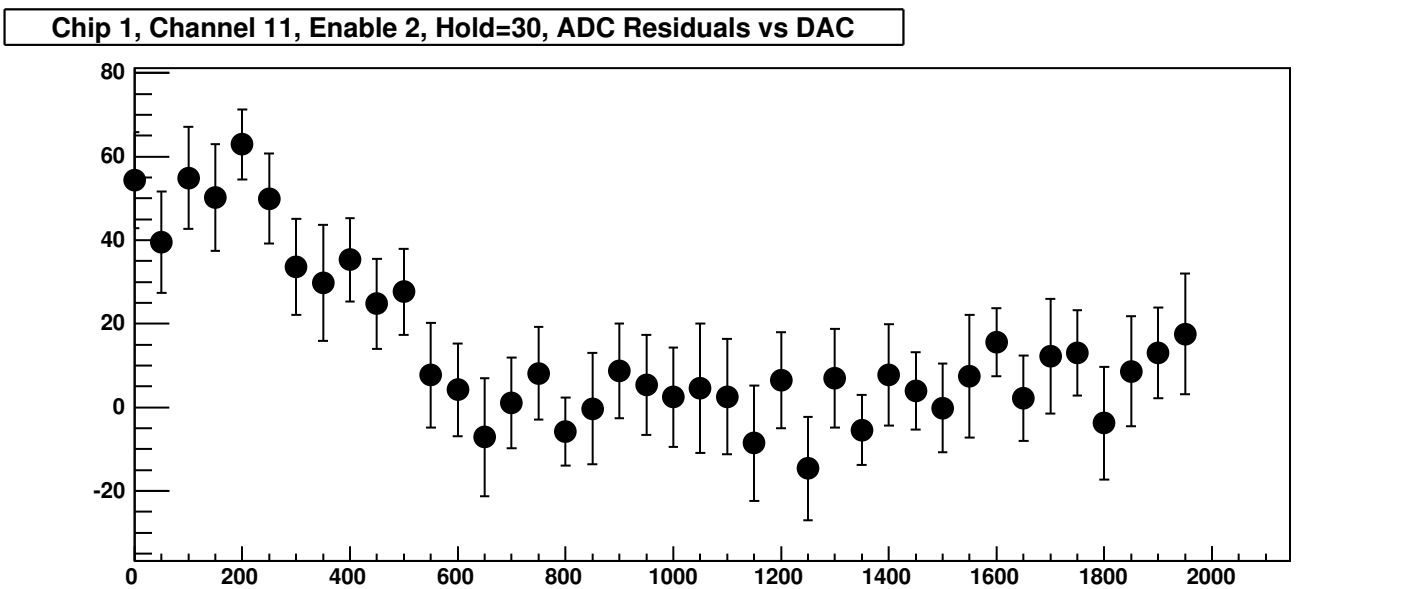

Chip 1, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7.342 / 11
p0 -1571 ± 18.04
p1 0.0493 ± 0.01621
-1440
-1460
-1480
-1500
-1520
-1540
-1560
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

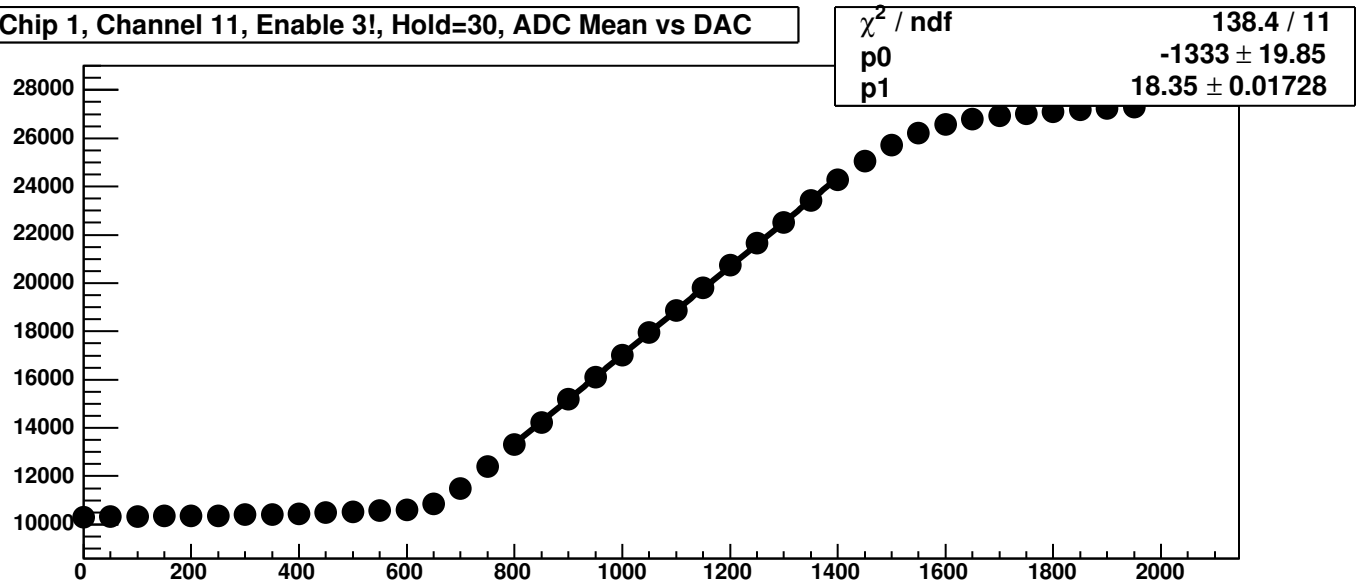

Chip 1, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4.782 / 11
p0 -1551 ± 16.97
p1 0.08286 ± 0.01522
-1360
-1380
-1400
-1420
-1440
-1460
-1480
-1500
-1520
0
200
400
600
800
1000
1200
1400
1600
1800
2000

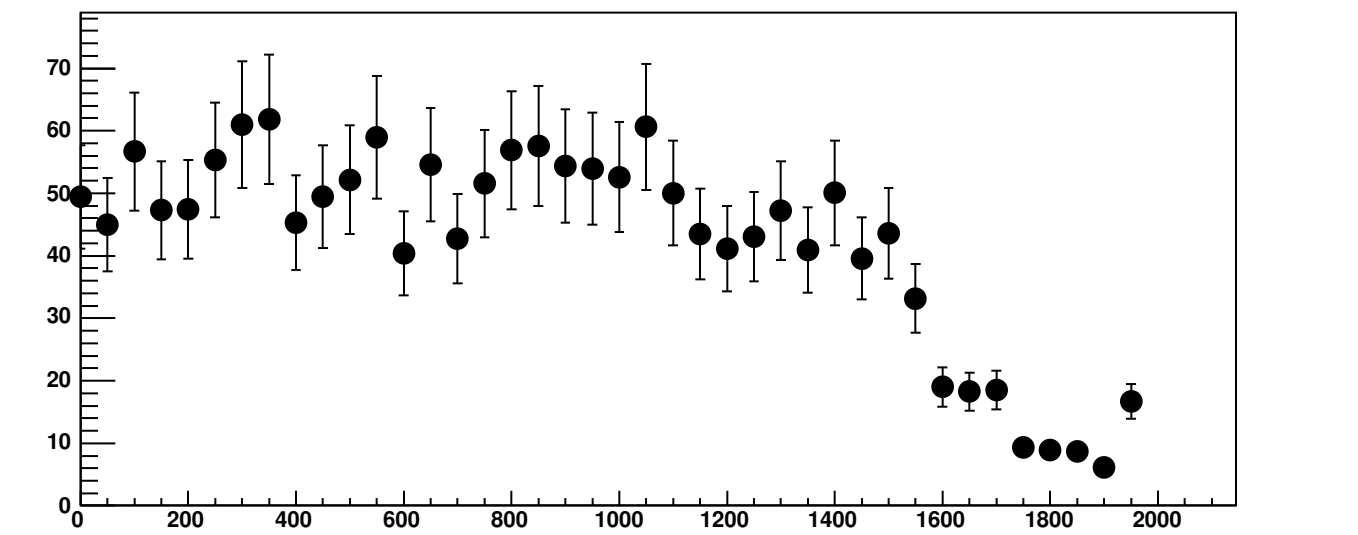

Chip 1, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

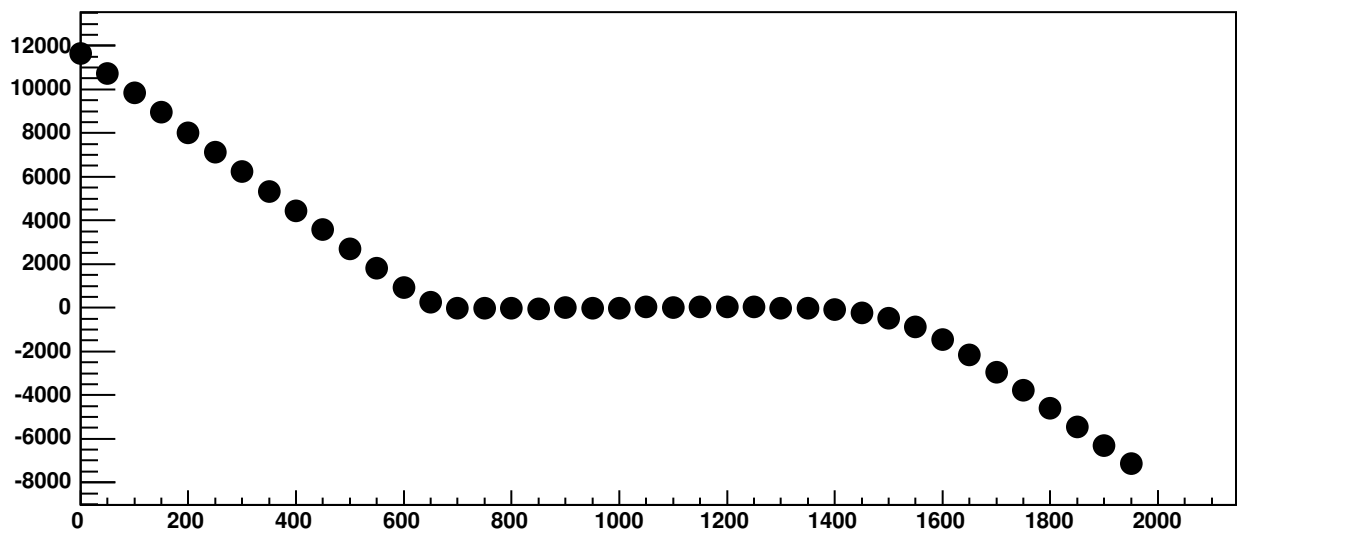
Chip 1, Channel 11, Enable 3!, Hold=30, ADC Mean vs DAC

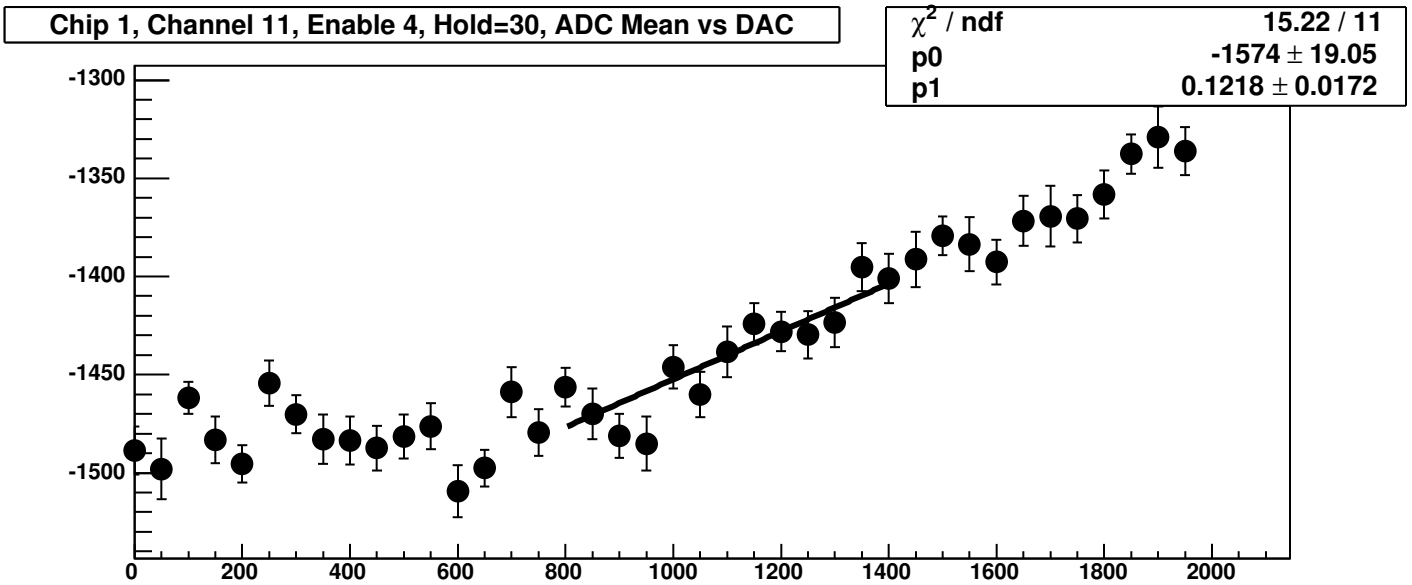


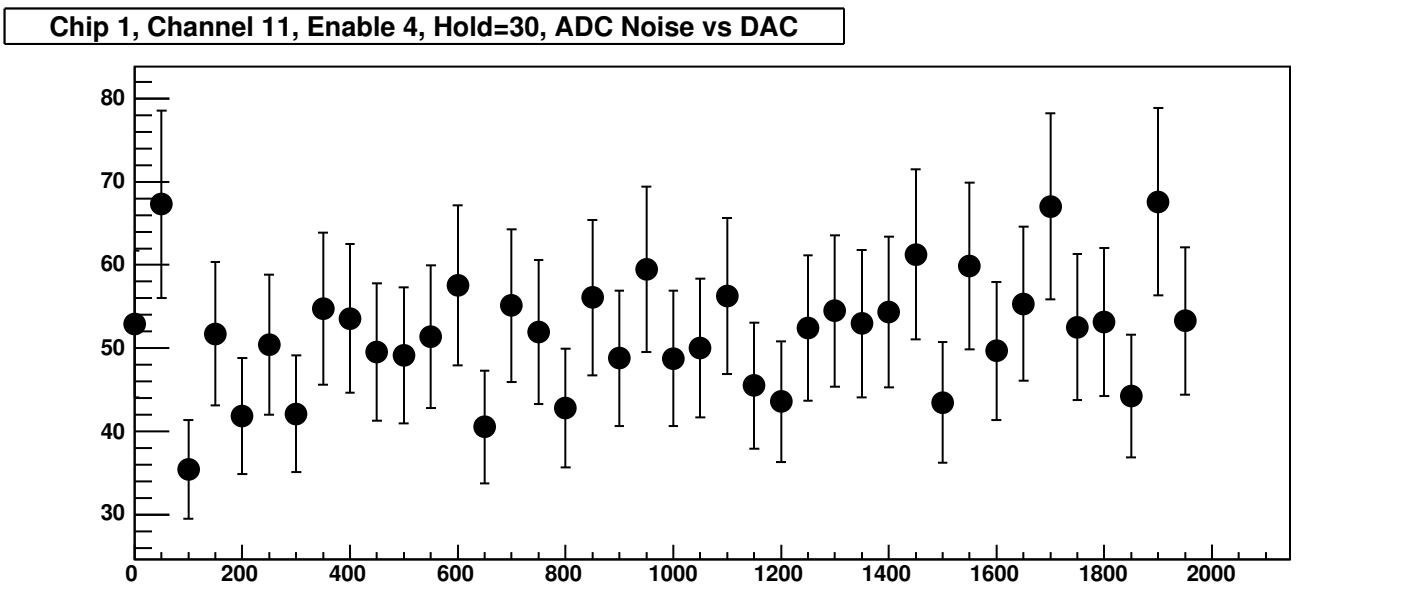
Chip 1, Channel 11, Enable 3!, Hold=30, ADC Noise vs DAC

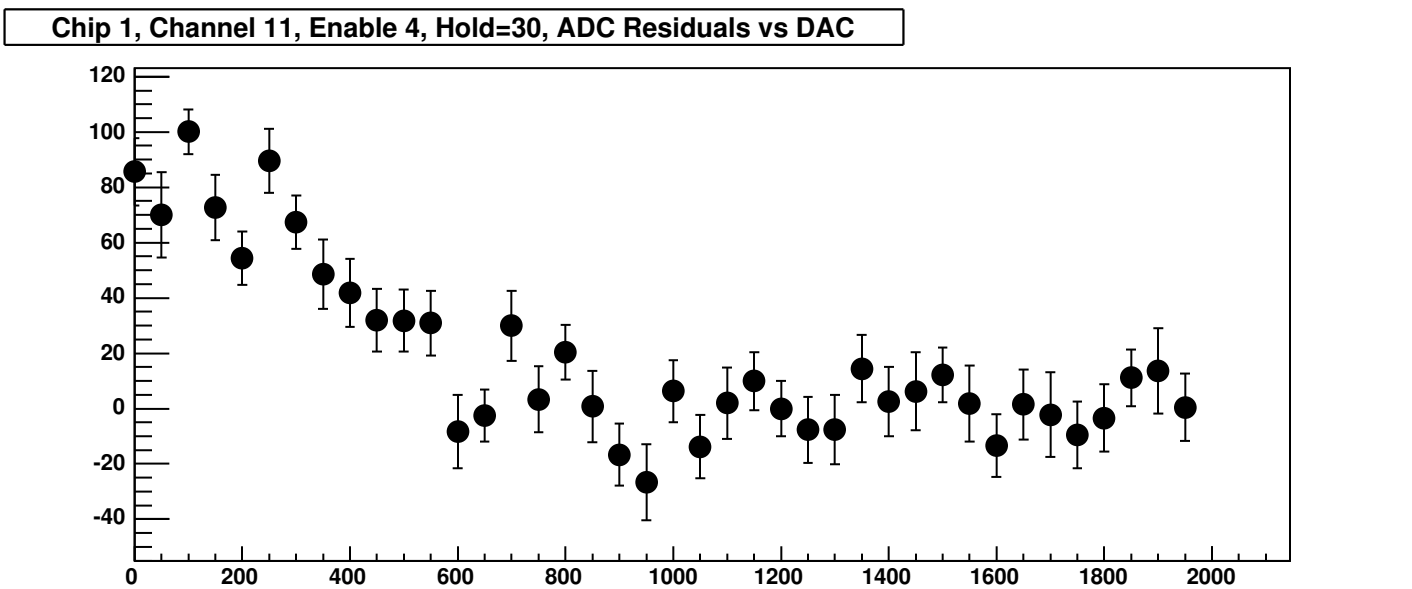


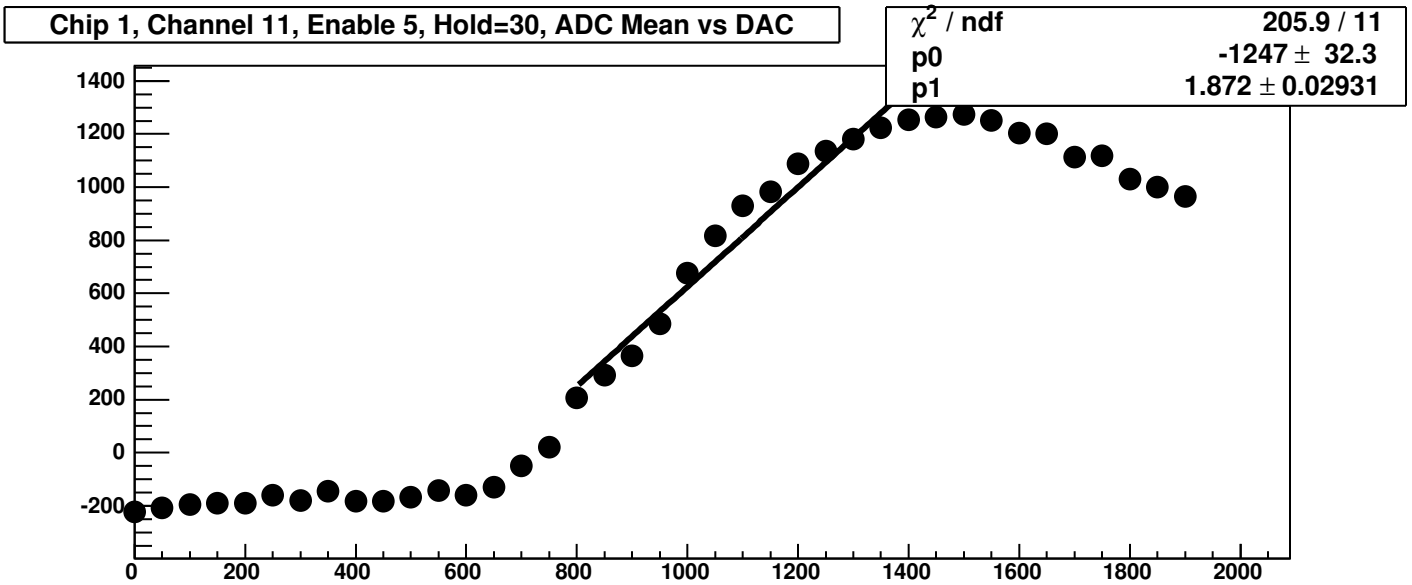
Chip 1, Channel 11, Enable 3!, Hold=30, ADC Residuals vs DAC

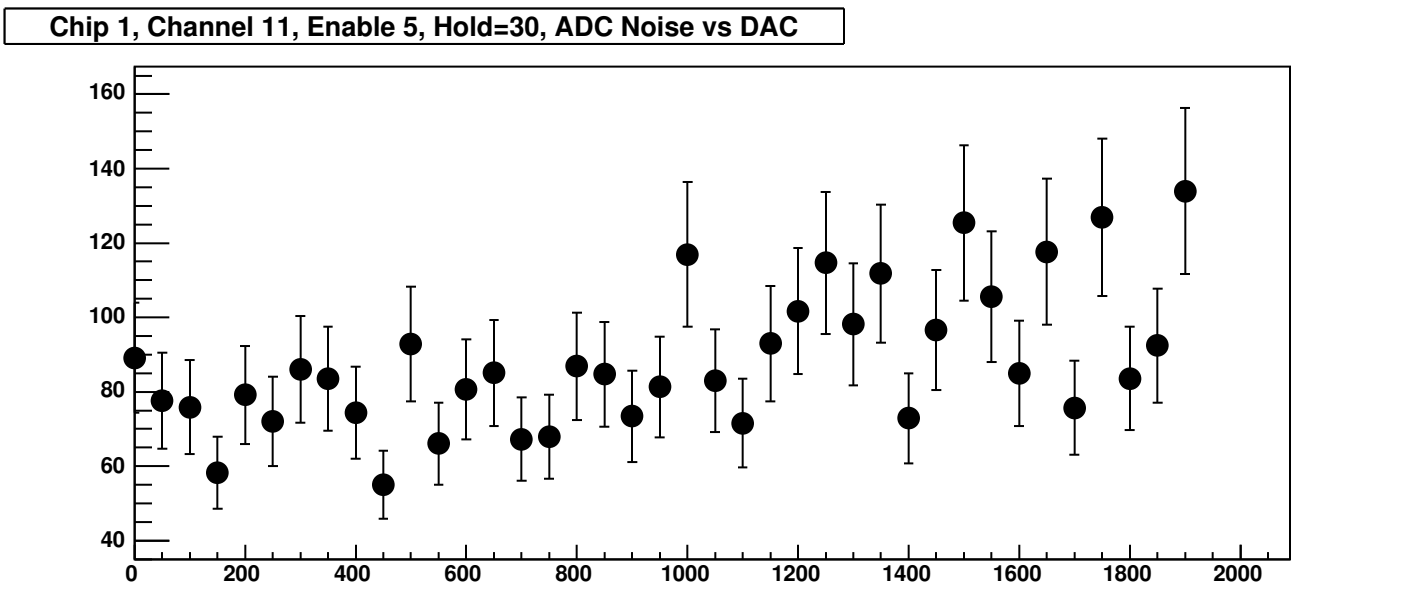


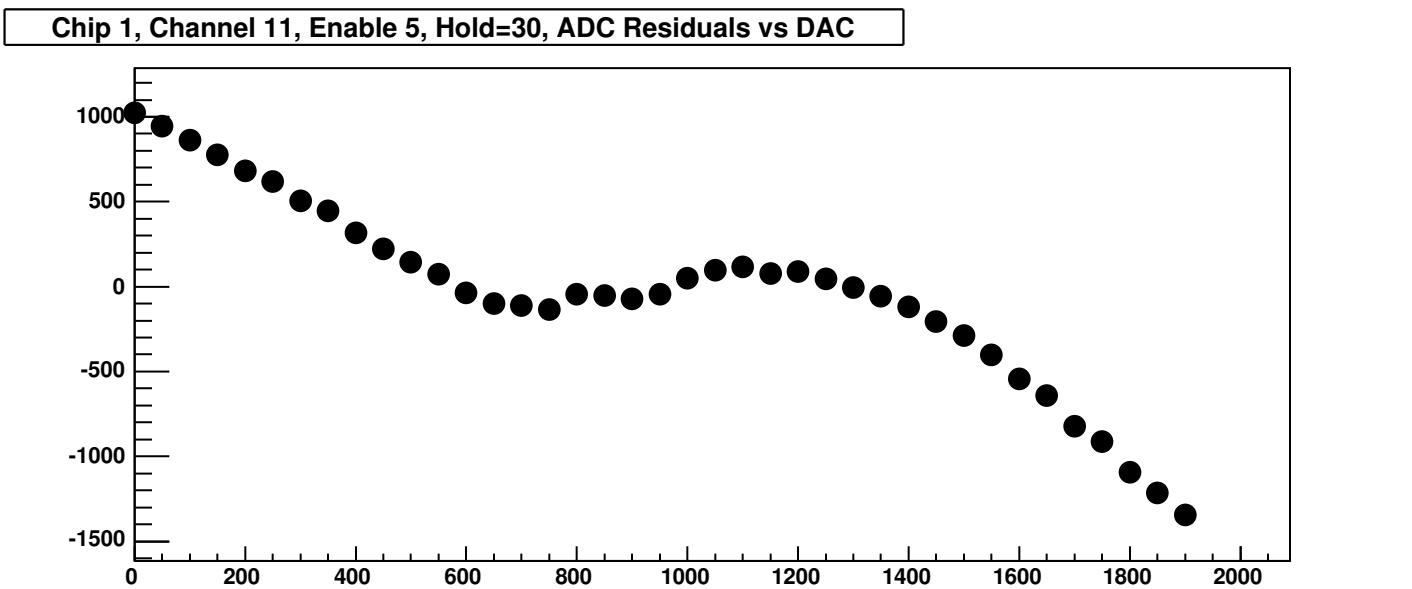

Chip 1, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 15.22 / 11
p0 -1574 ± 19.05
p1 0.1218 ± 0.0172
-1300
-1350
-1400
-1450
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

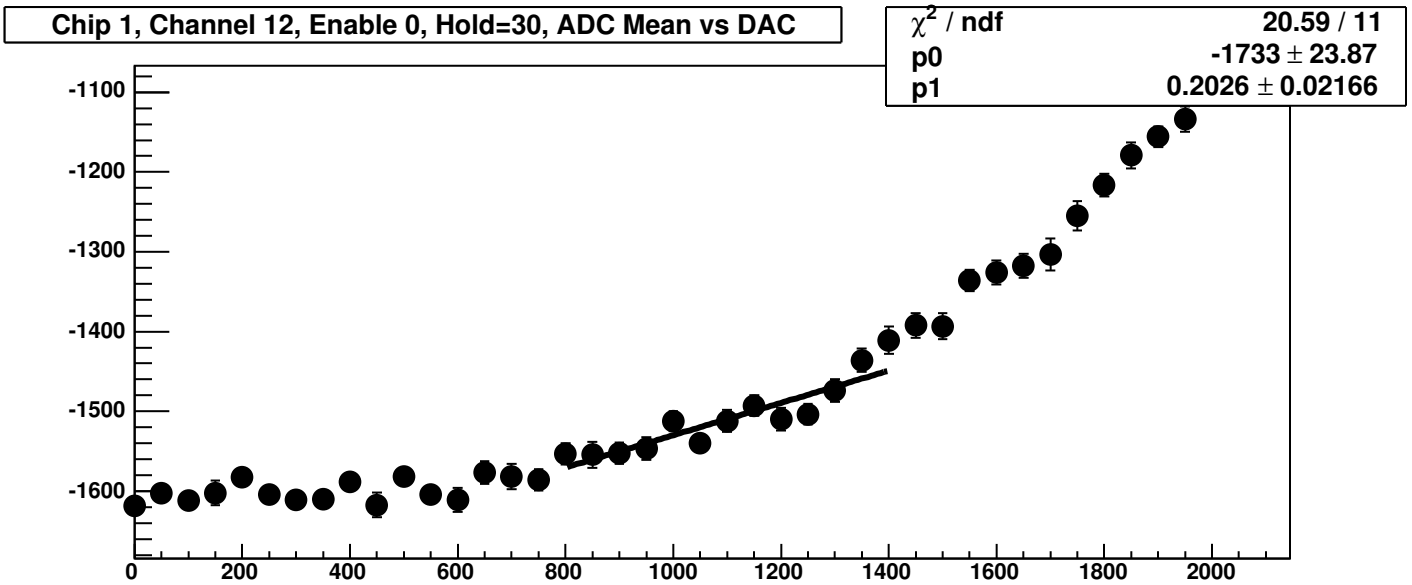

Chip 1, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

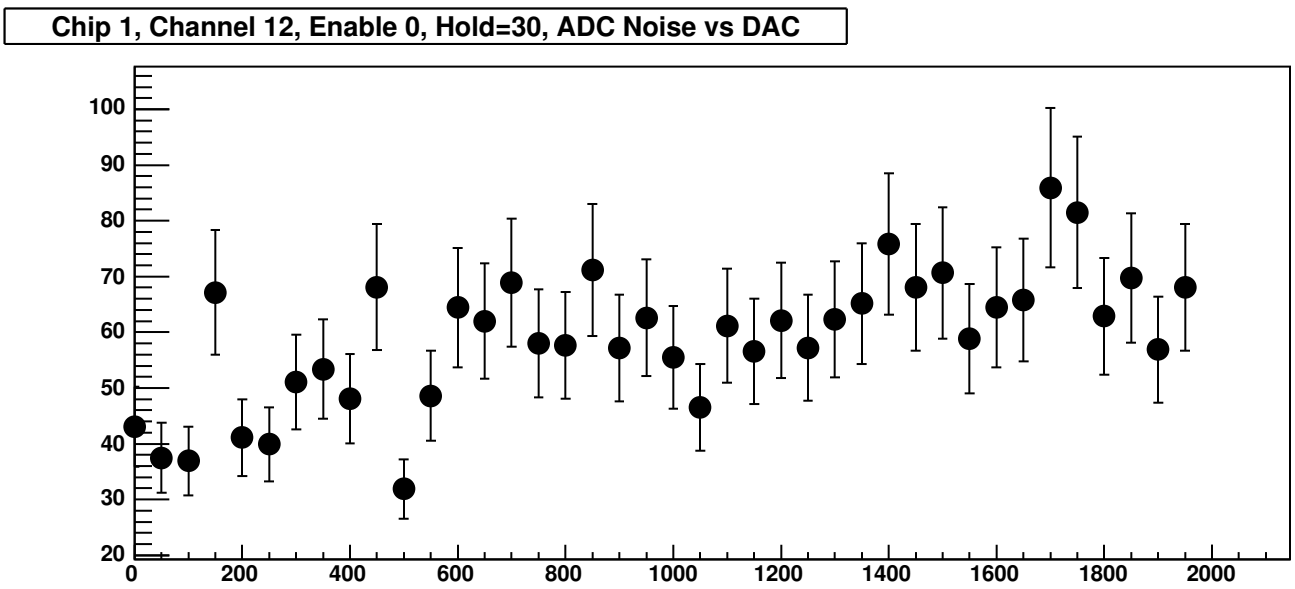

Chip 1, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

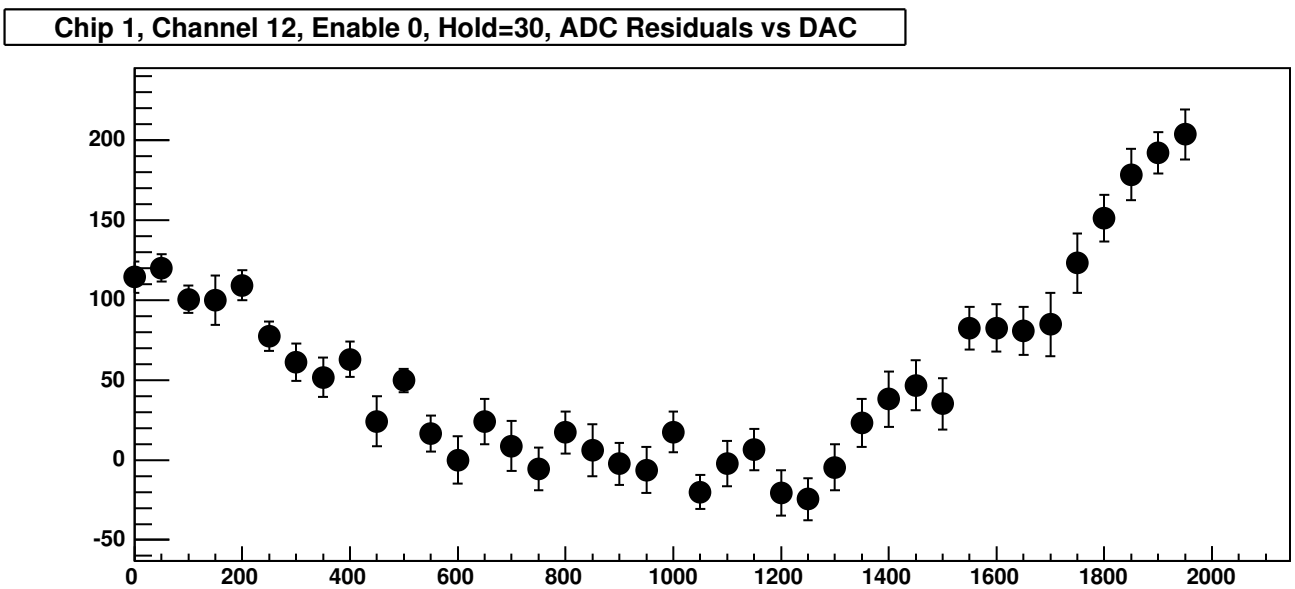

Chip 1, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 205.9 / 11
p0 -1247 ± 32.3
p1 1.872 ± 0.02931
1400
1200
1000
800
600
400
200
0
-200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

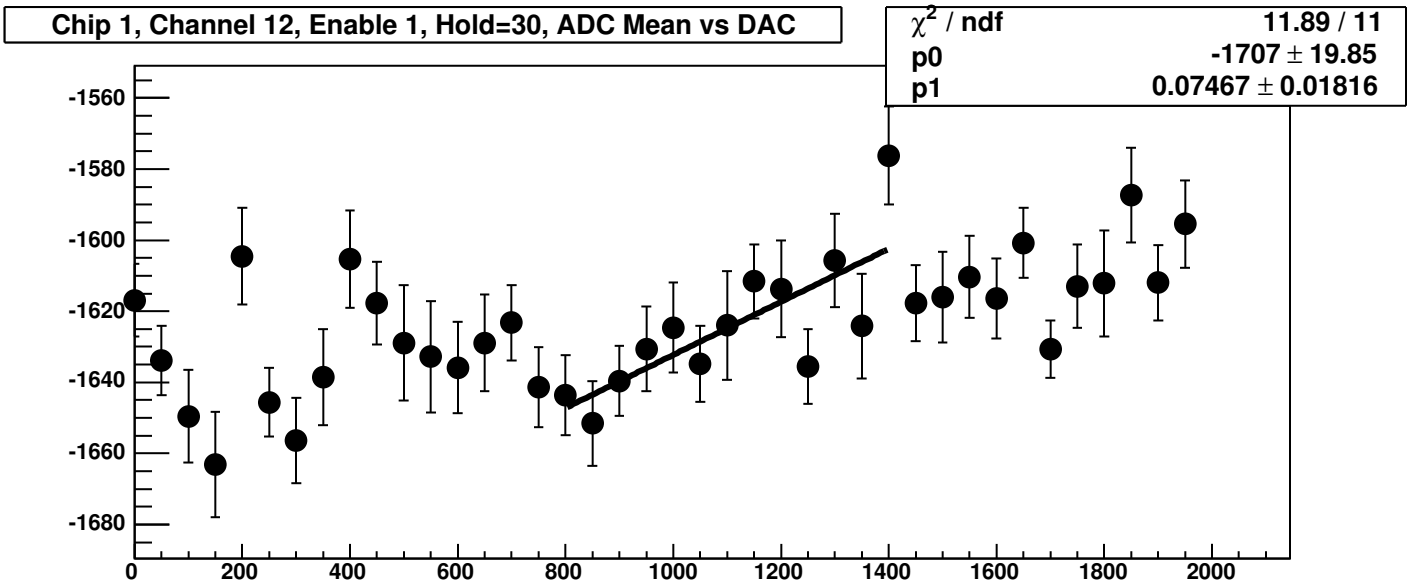

Chip 1, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

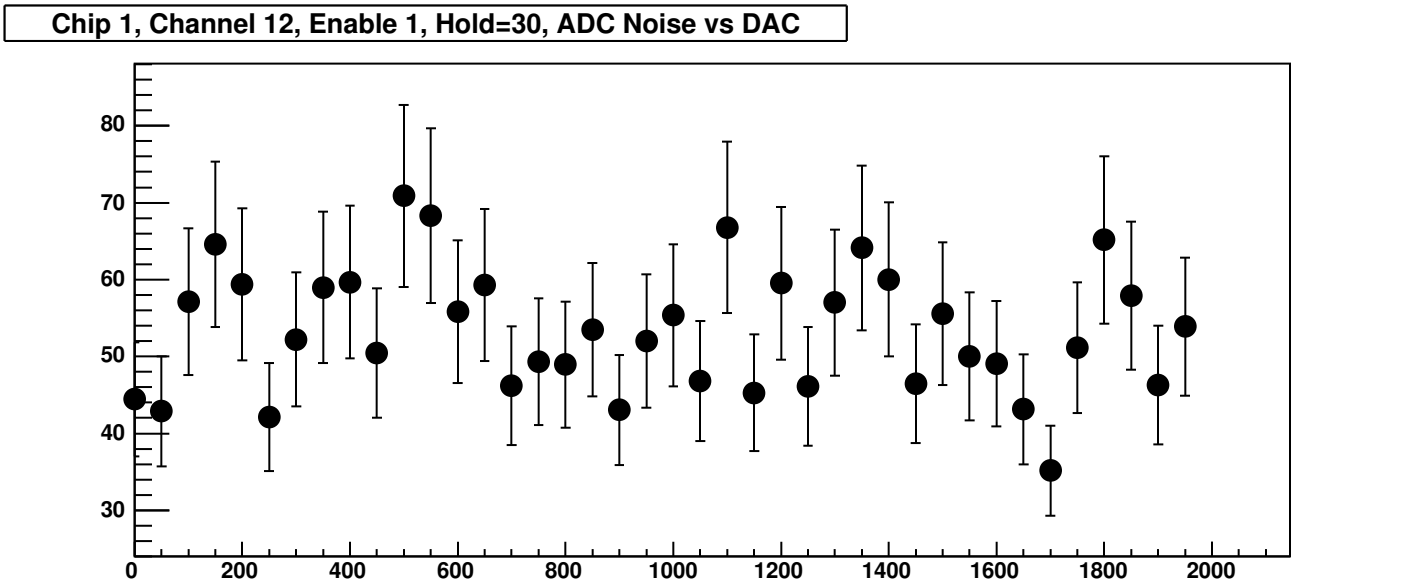

Chip 1, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
1000
500
0
-500
-1000
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

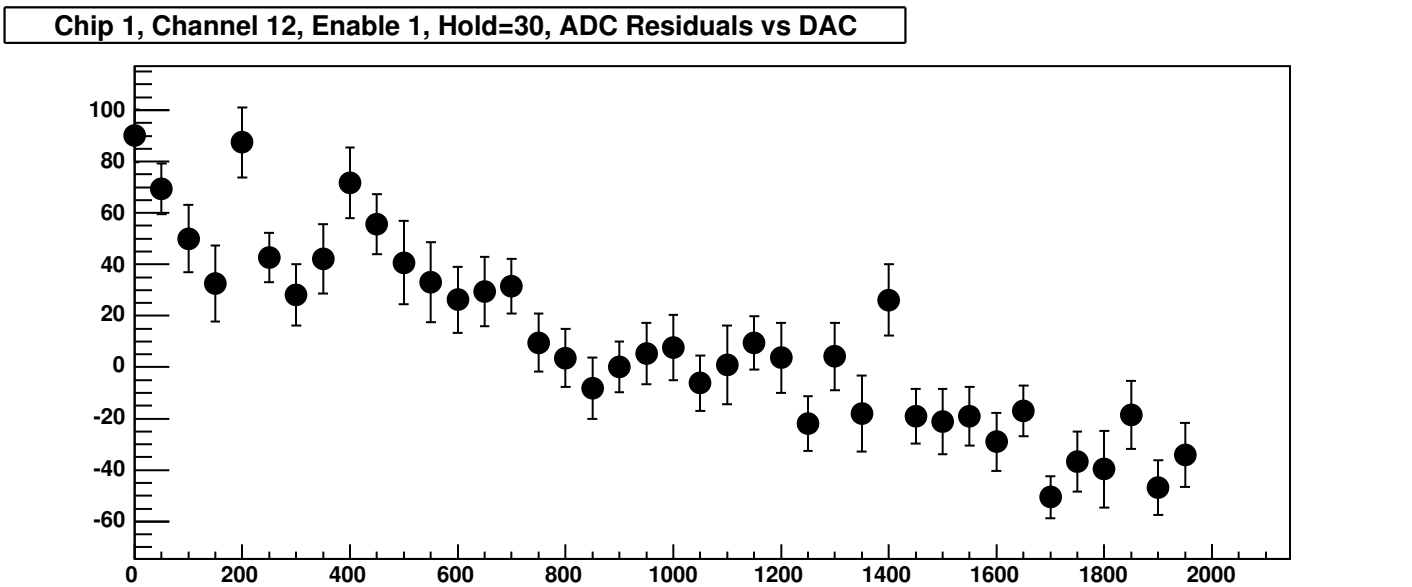

Chip 1, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 20.59 / 11
p0 -1733 ± 23.87
p1 0.2026 ± 0.02166
-1100
-1200
-1300
-1400
-1500
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

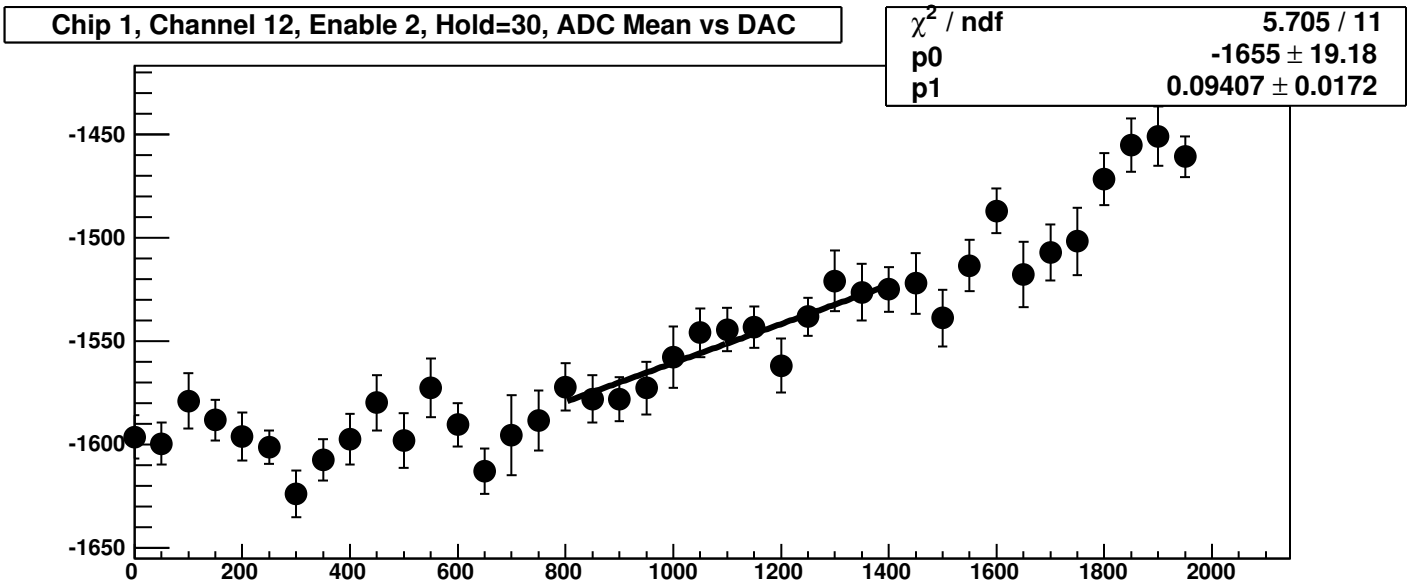

Chip 1, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

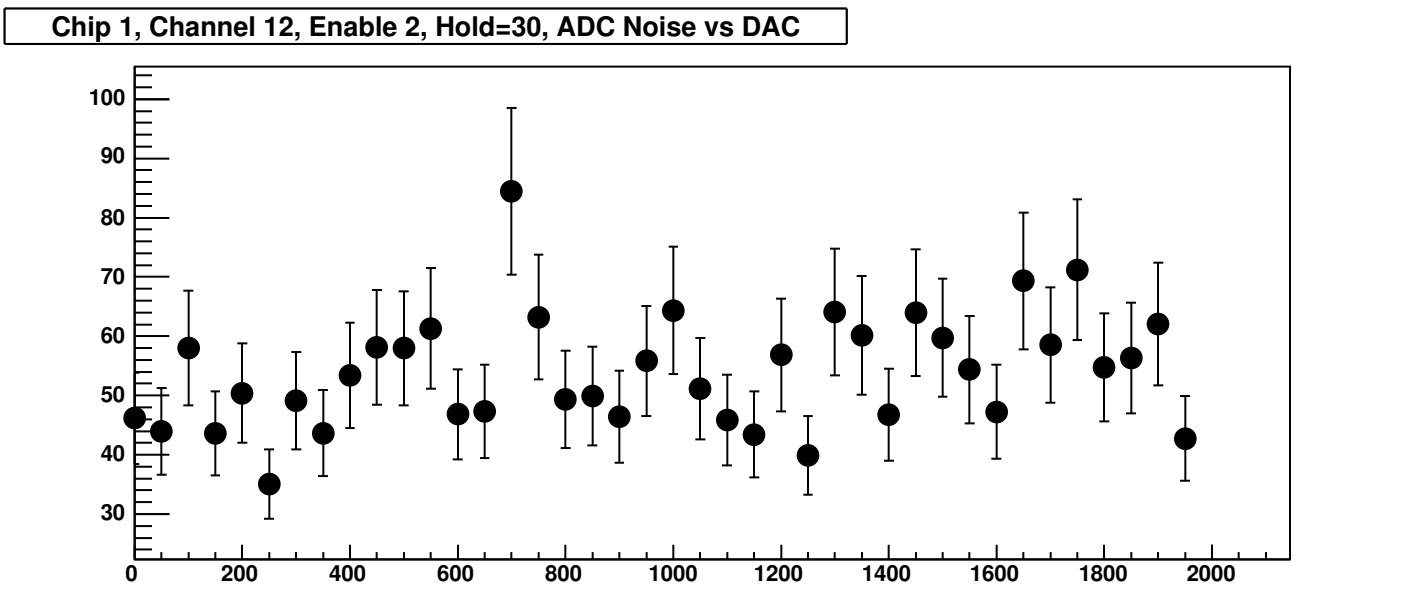

Chip 1, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

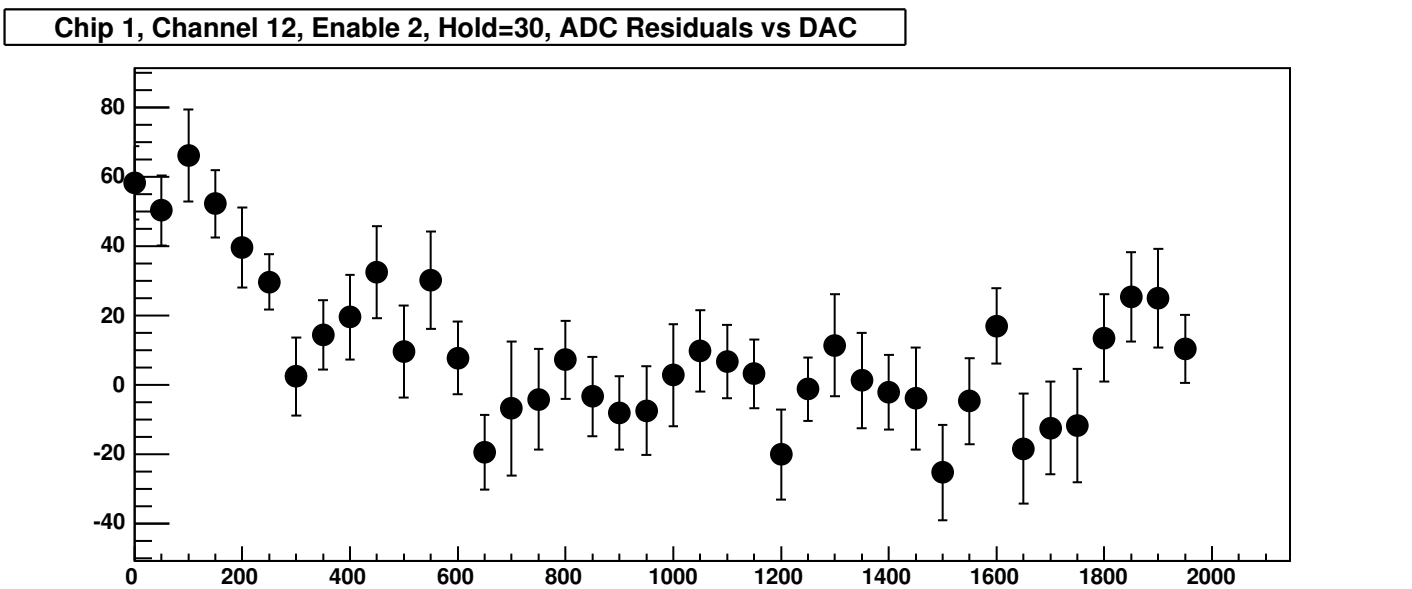

Chip 1, Channel 12, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 11.89 / 11
p0 -1707 ± 19.85
p1 0.07467 ± 0.01816
-1560
-1580
-1600
-1620
-1640
-1660
-1680
0
200
400
600
800
1000
1200
1400
1600
1800
2000

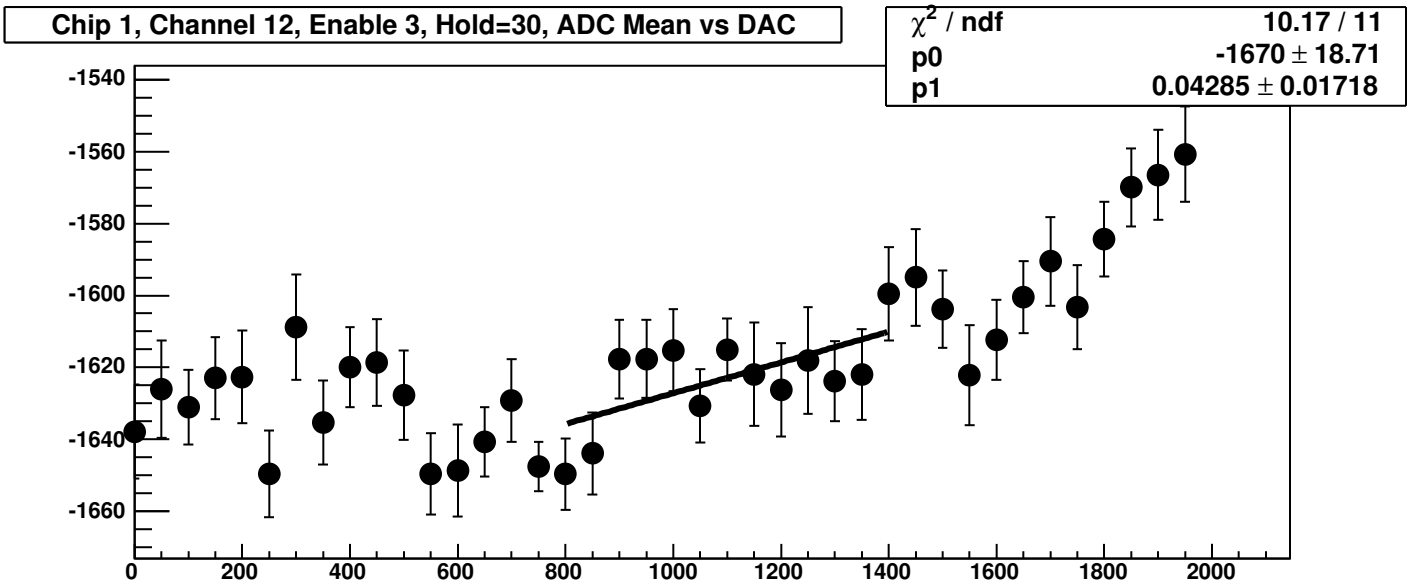

Chip 1, Channel 12, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

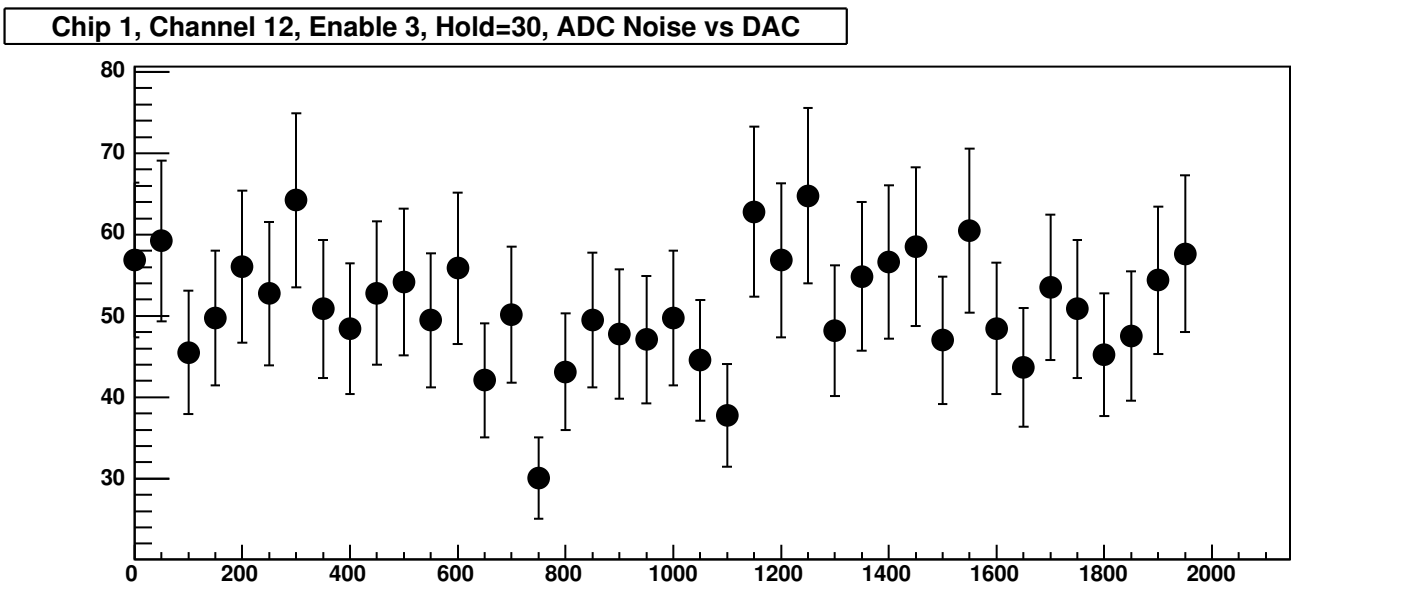

Chip 1, Channel 12, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

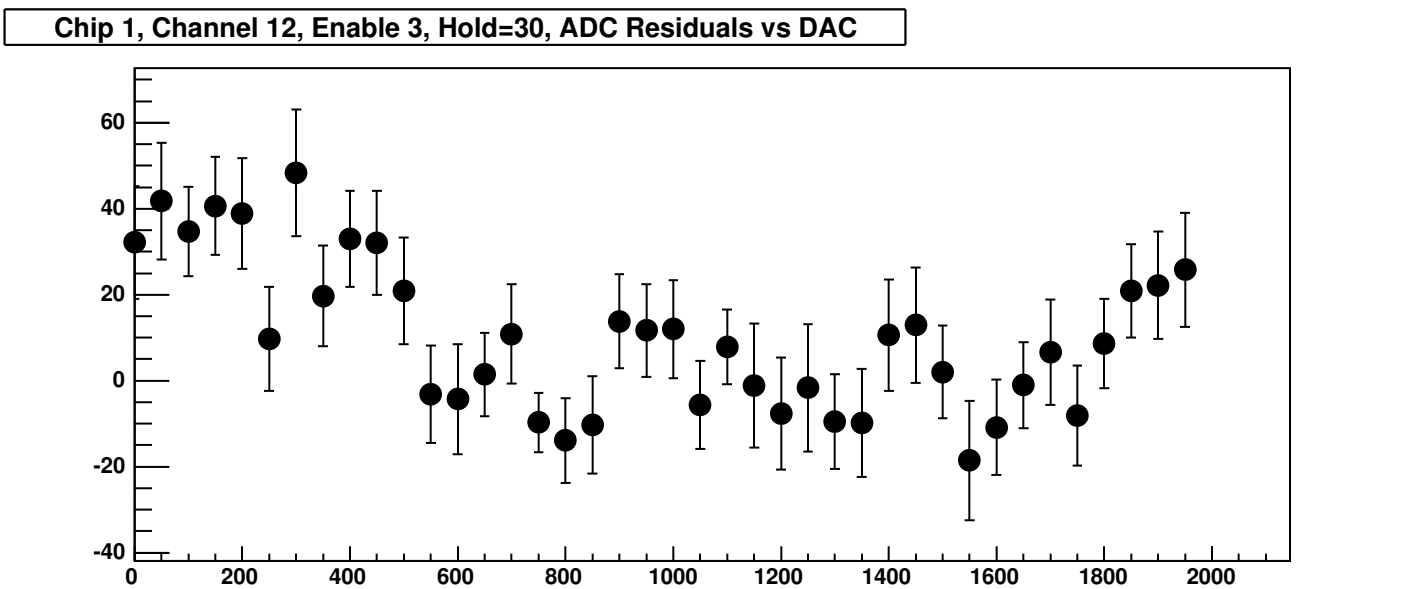

Chip 1, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 5.705 / 11
p0 -1655 ± 19.18
p1 0.09407 ± 0.0172
-1450
-1500
-1550
-1600
-1650
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

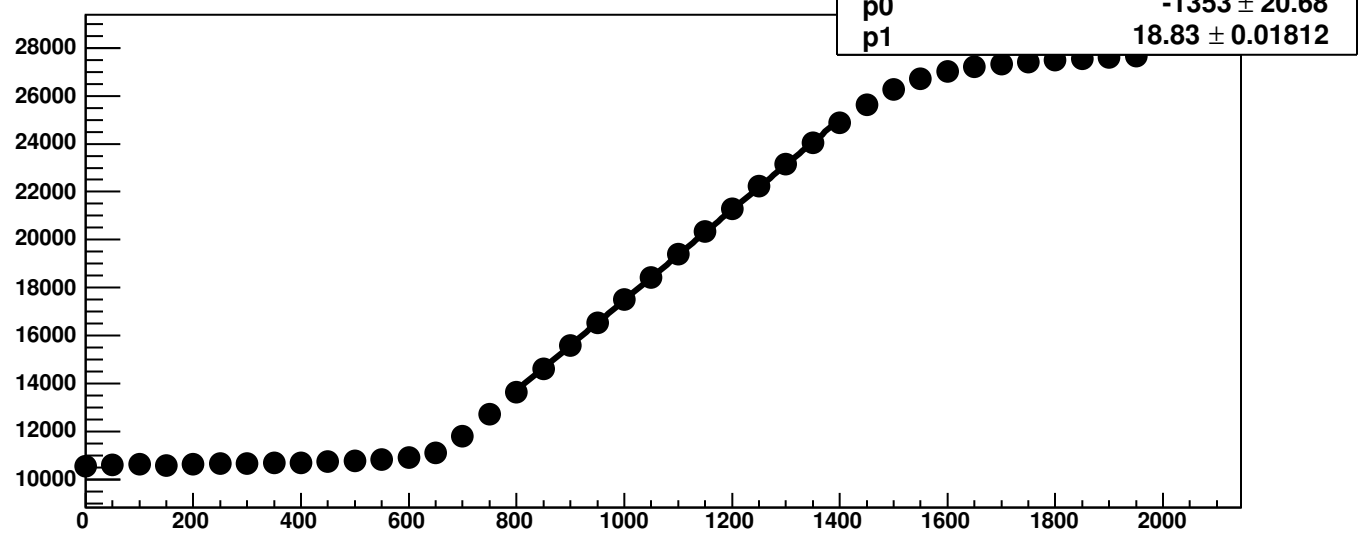

Chip 1, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 10.17 / 11
p0 -1670 ± 18.71
p1 0.04285 ± 0.01718
-1540
-1560
-1580
-1600
-1620
-1640
-1660
0
200
400
600
800
1000
1200
1400
1600
1800
2000

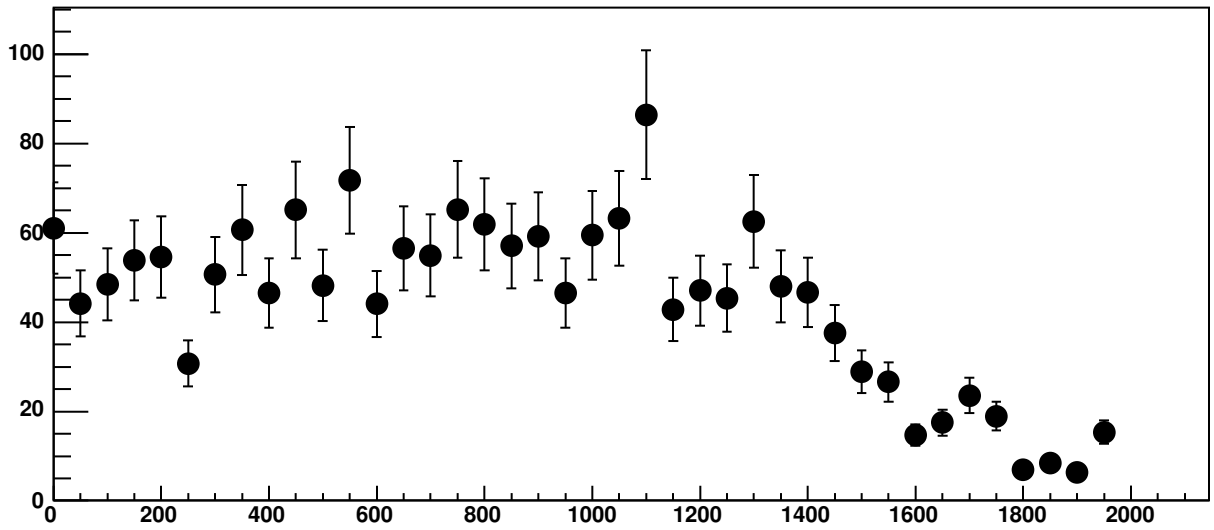

Chip 1, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

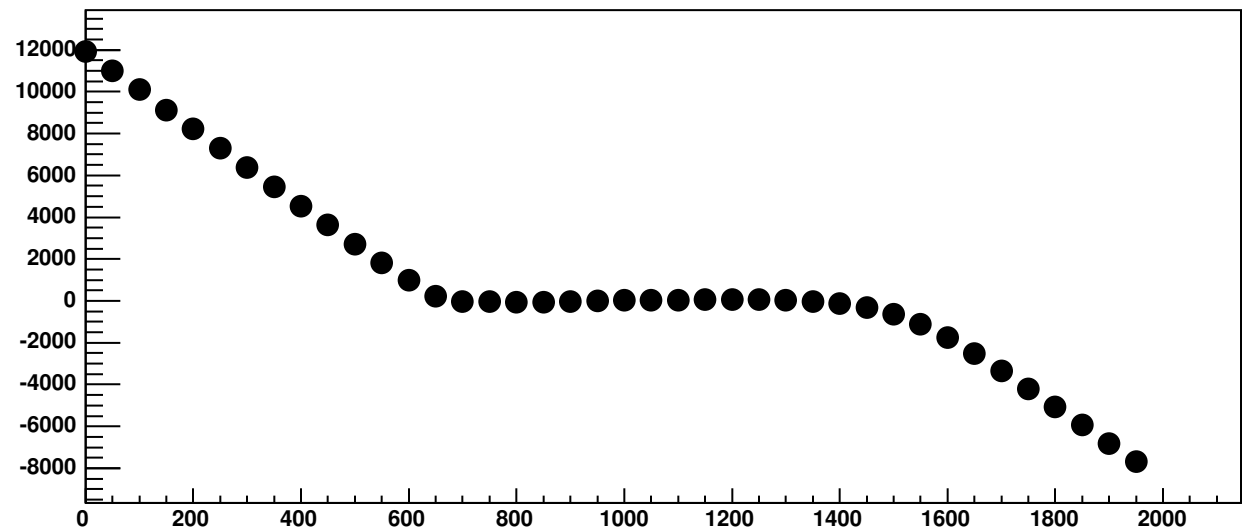
Chip 1, Channel 12, Enable 4!, Hold=30, ADC Mean vs DAC

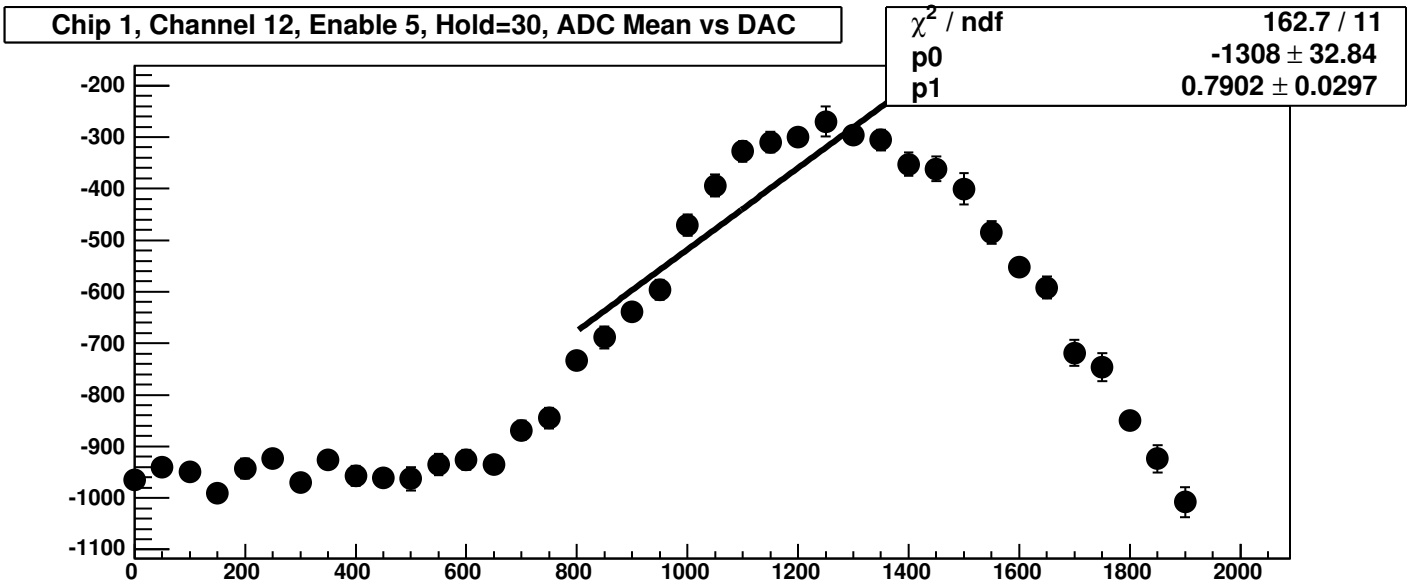


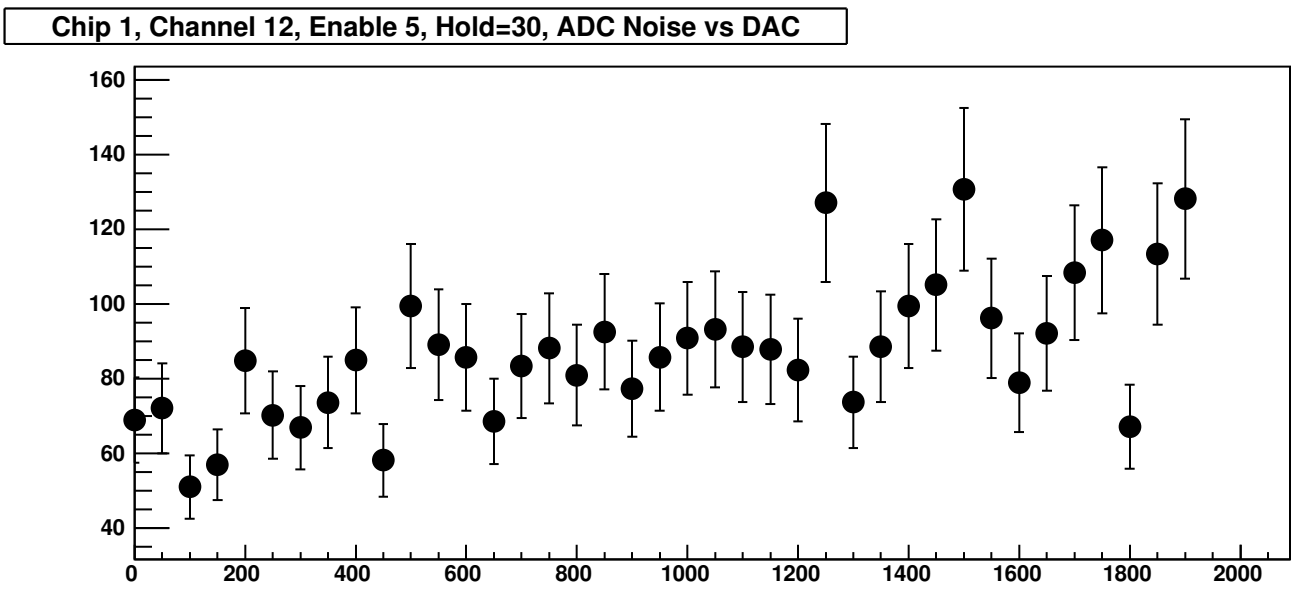
Chip 1, Channel 12, Enable 4!, Hold=30, ADC Noise vs DAC

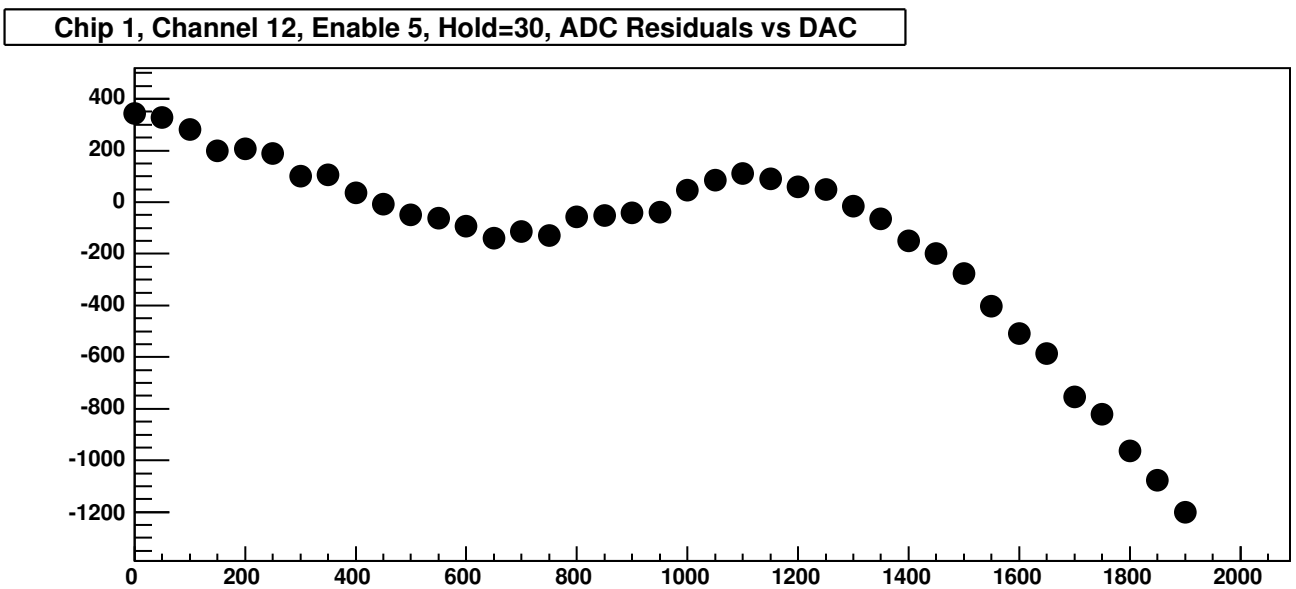


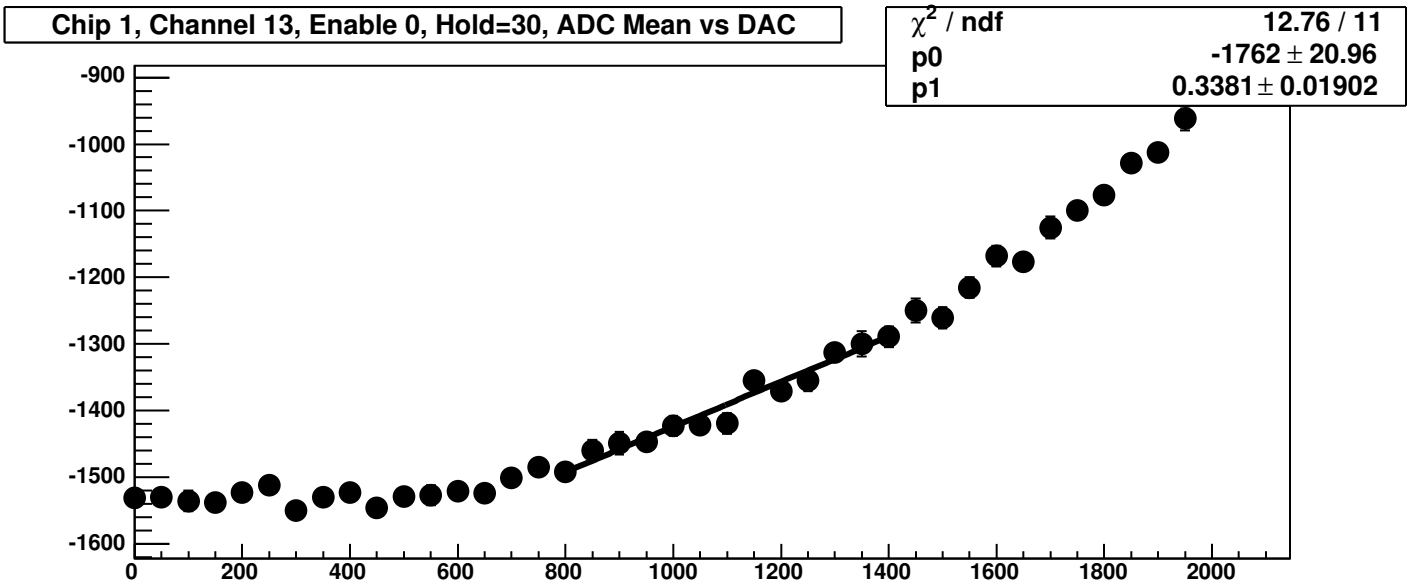
Chip 1, Channel 12, Enable 4!, Hold=30, ADC Residuals vs DAC

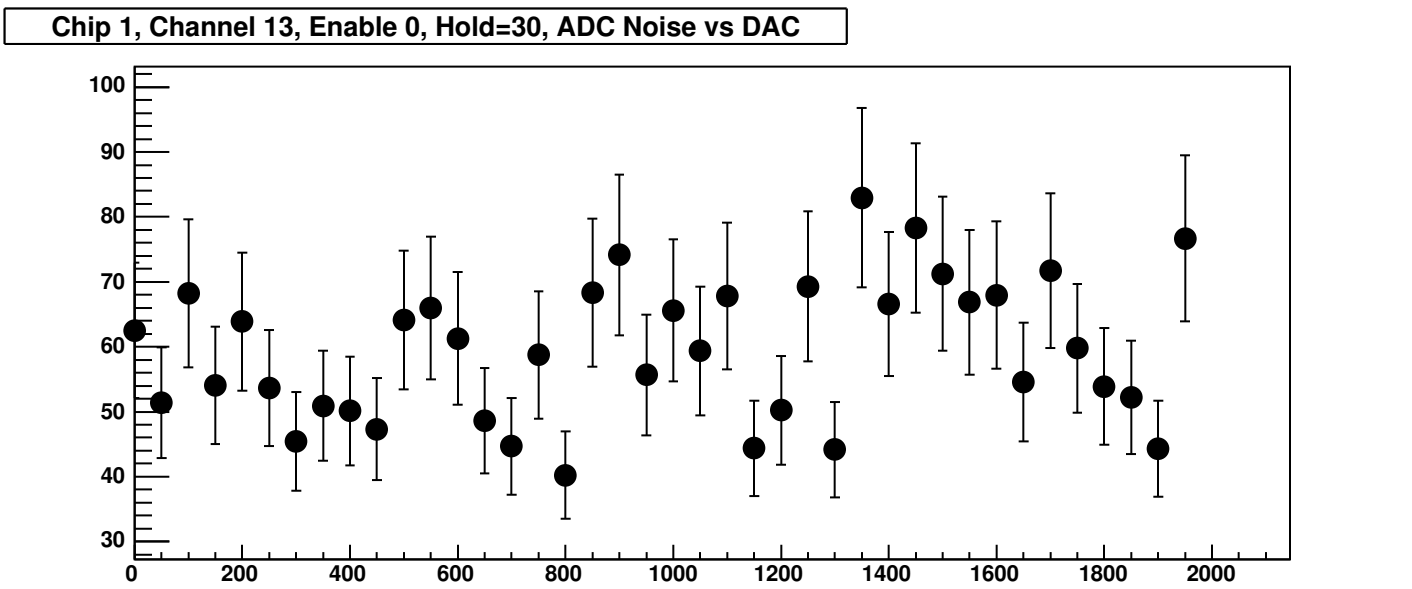


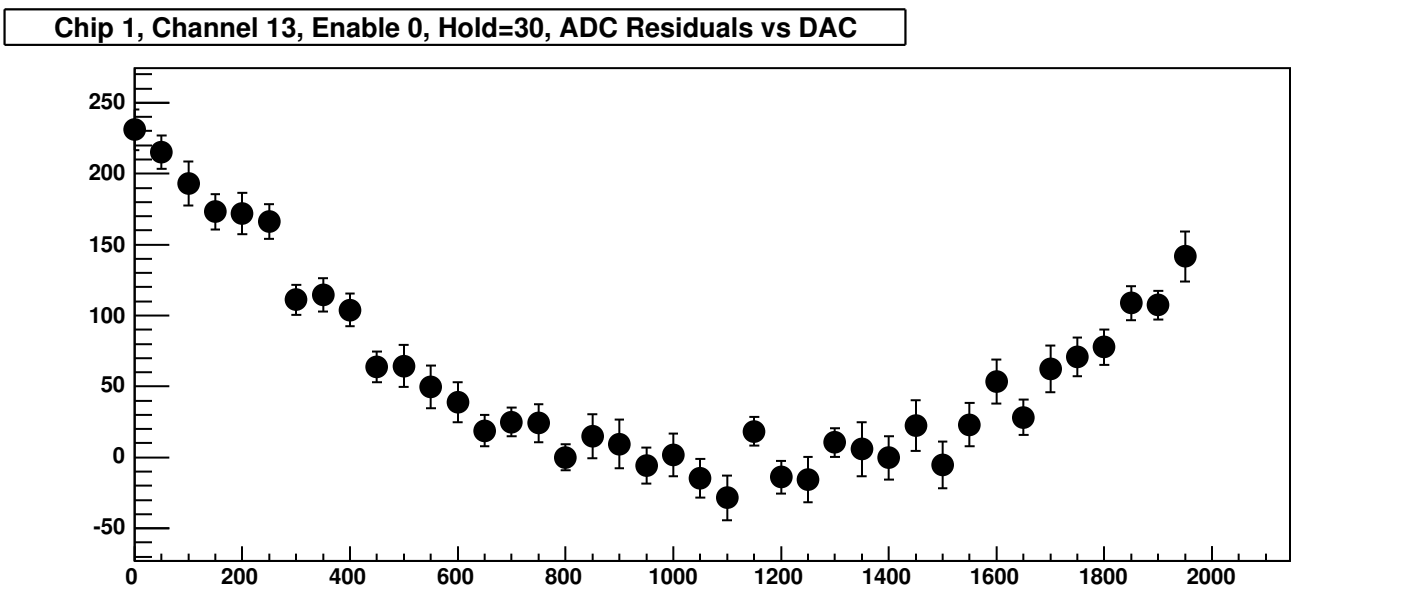

Chip 1, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 162.7 / 11
p0 -1308 ± 32.84
p1 0.7902 ± 0.0297
-200
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

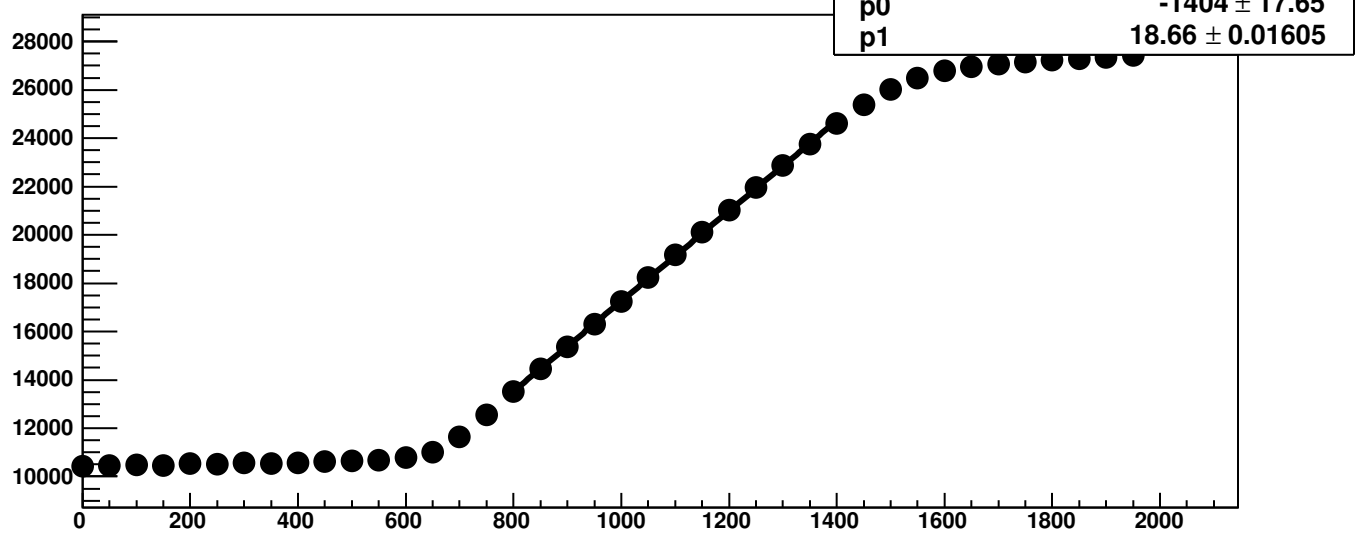

Chip 1, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 12.76 / 11
p0 -1762 ± 20.96
p1 0.3381 ± 0.01902
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

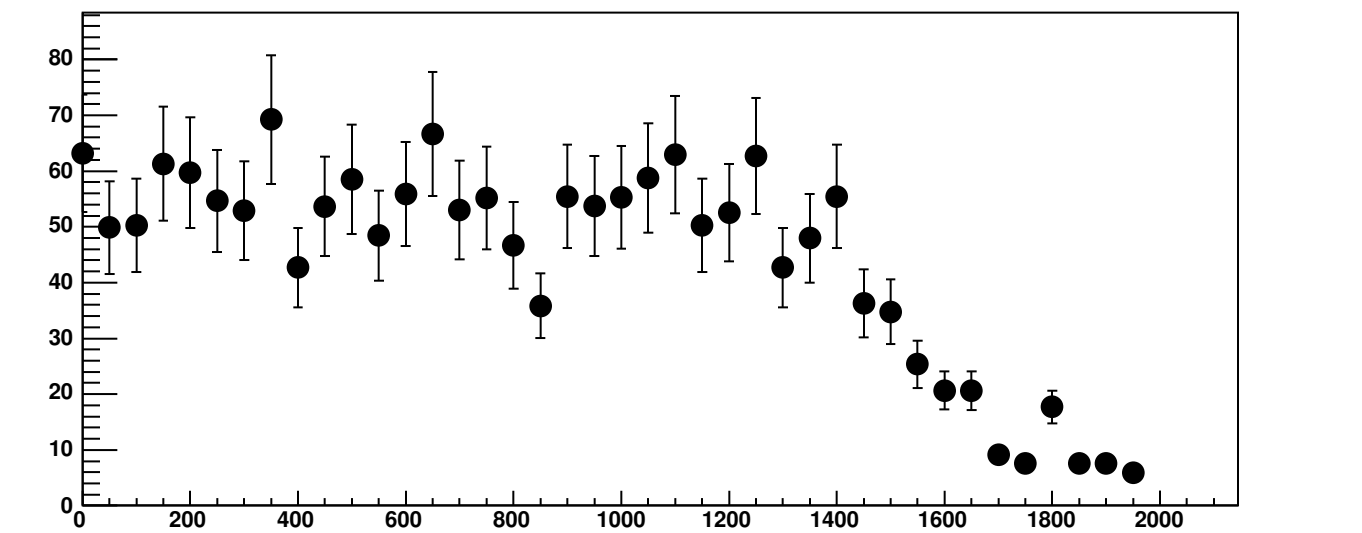

Chip 1, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

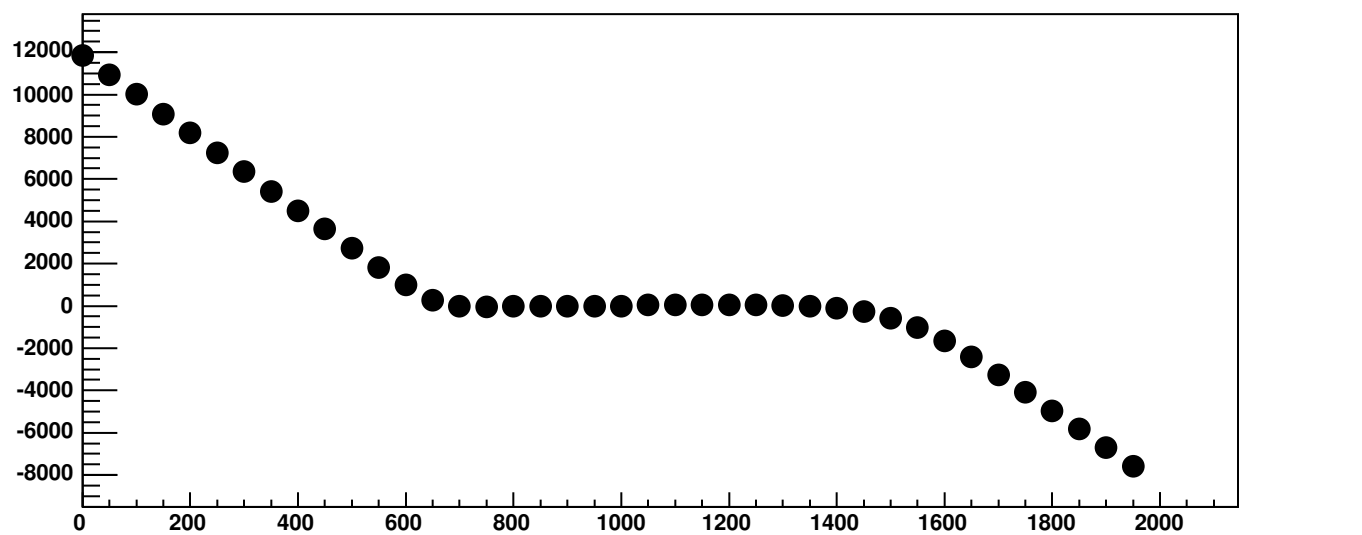
Chip 1, Channel 13, Enable 1!, Hold=30, ADC Mean vs DAC

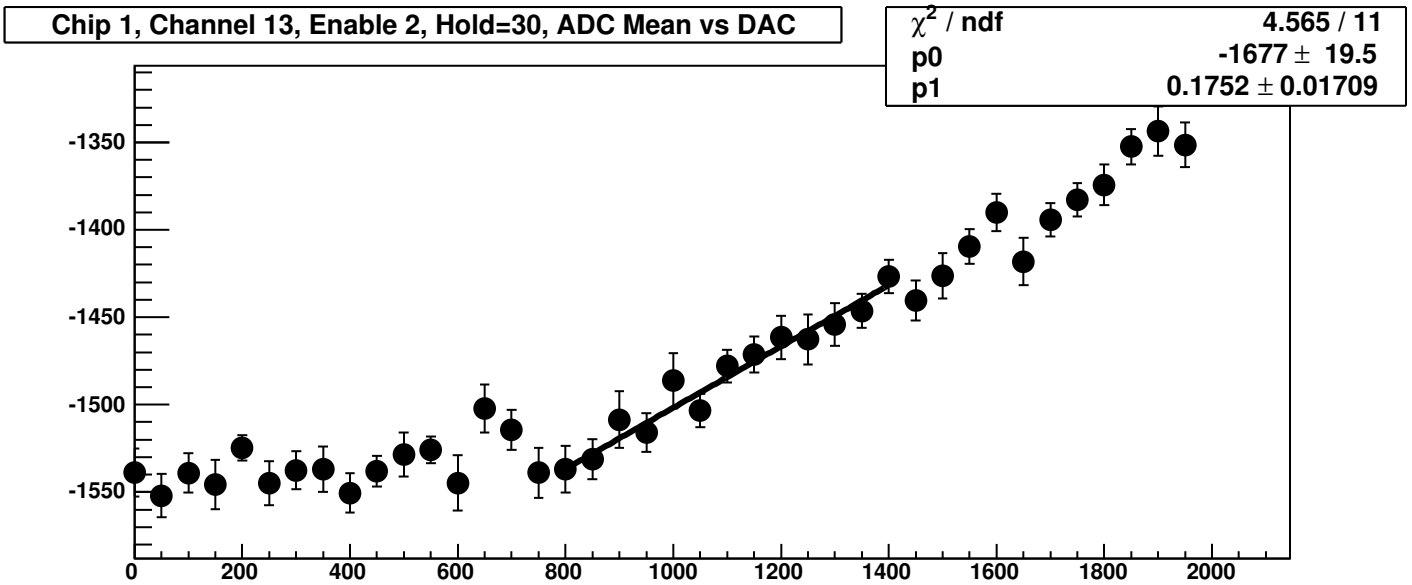


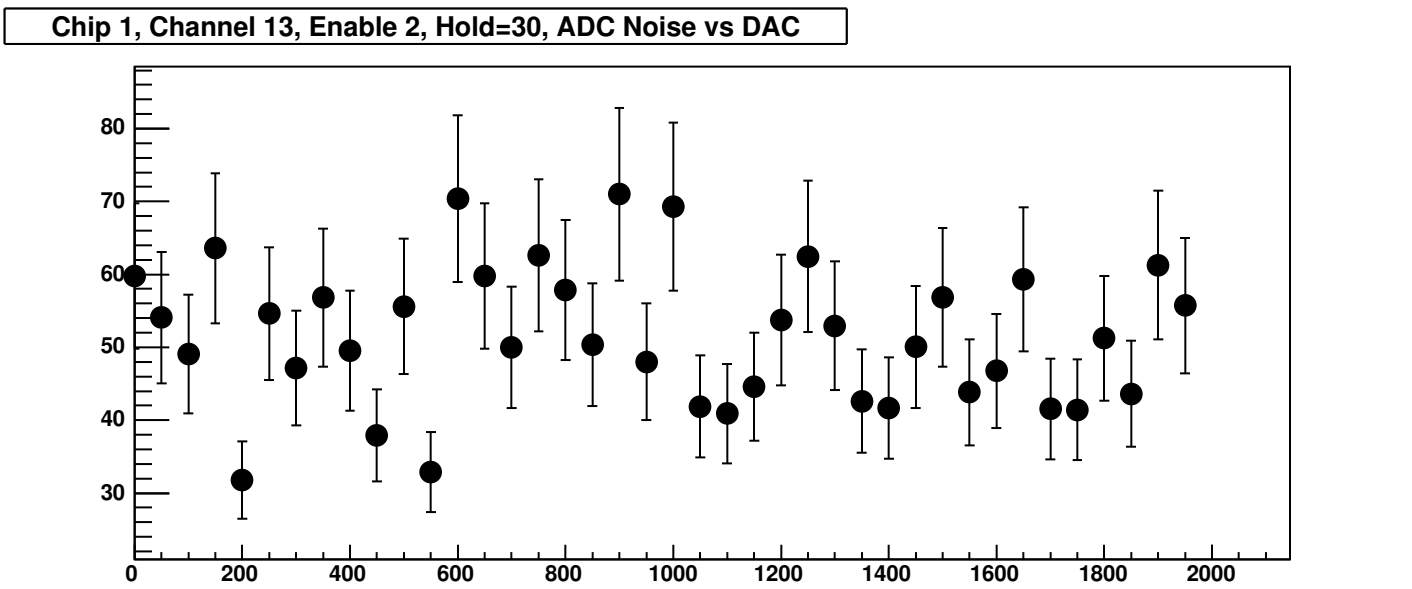
Chip 1, Channel 13, Enable 1!, Hold=30, ADC Noise vs DAC

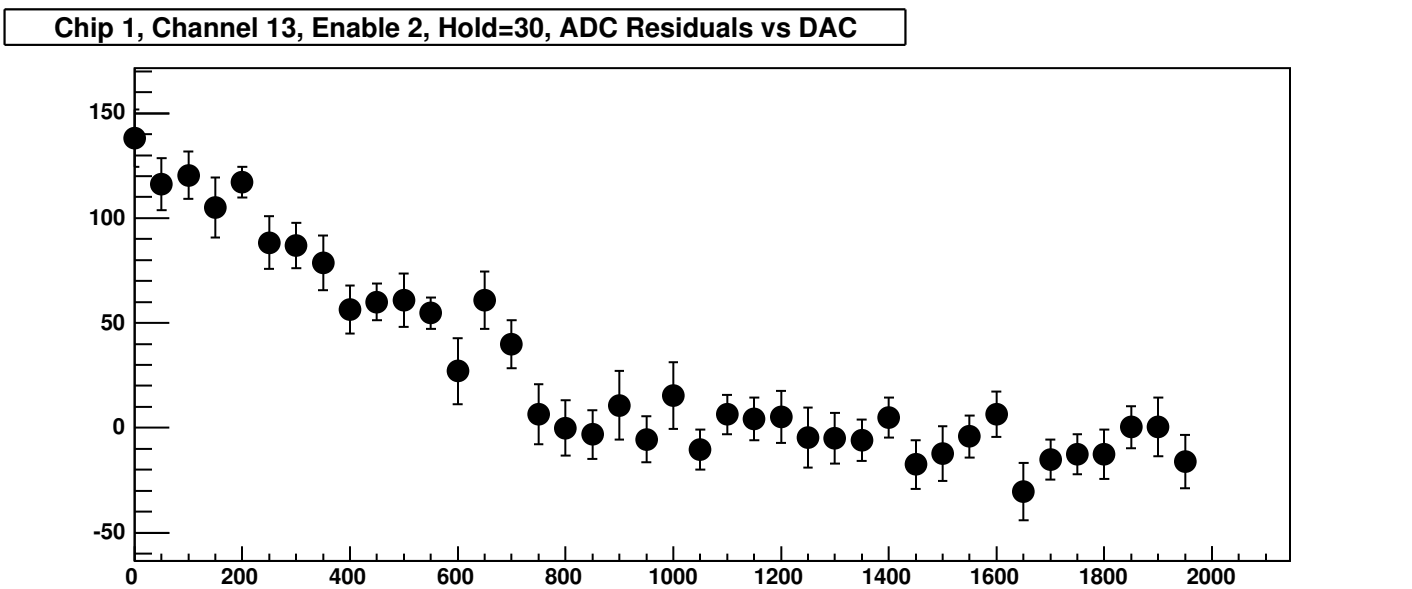


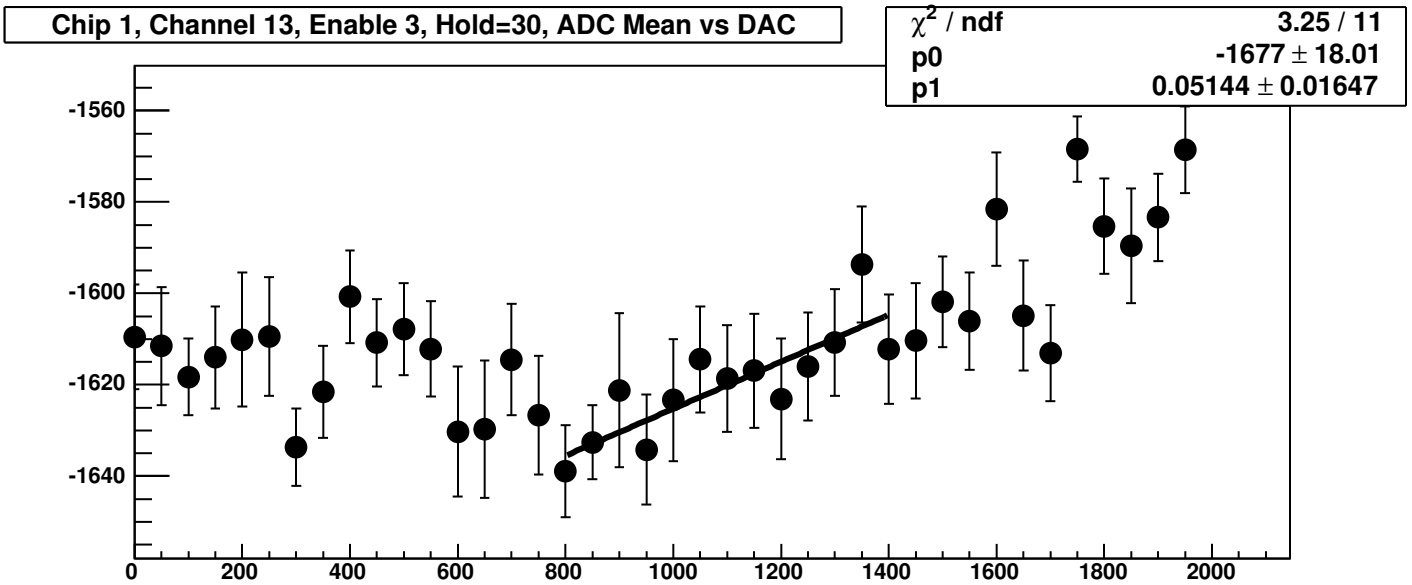
Chip 1, Channel 13, Enable 1!, Hold=30, ADC Residuals vs DAC

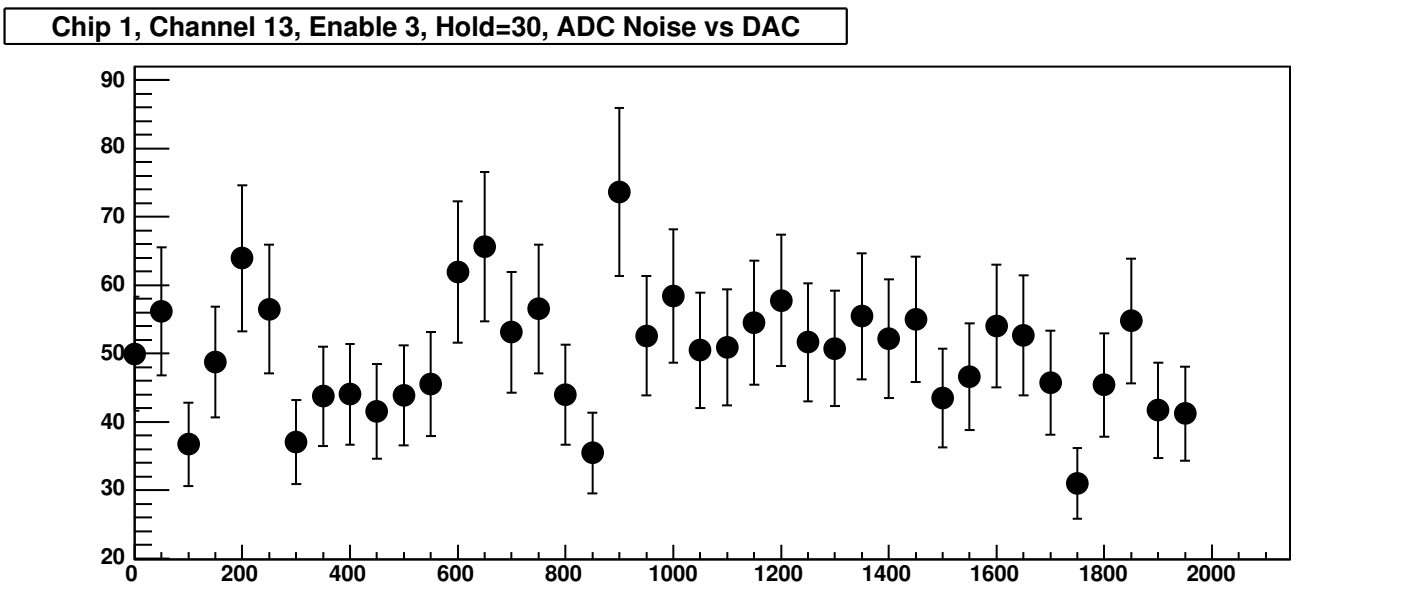


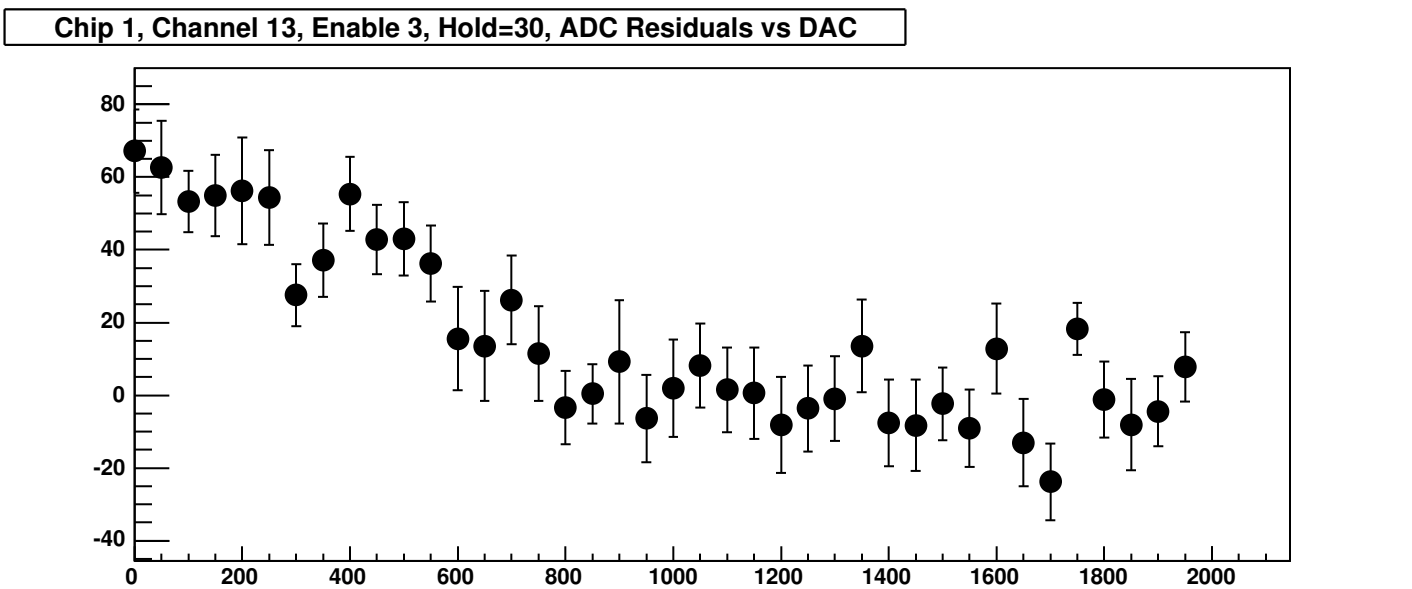

Chip 1, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4.565 / 11
p0 -1677 ± 19.5
p1 0.1752 ± 0.01709
-1350
-1400
-1450
-1500
-1550
0
200
400
600
800
1000
1200
1400
1600
1800
2000

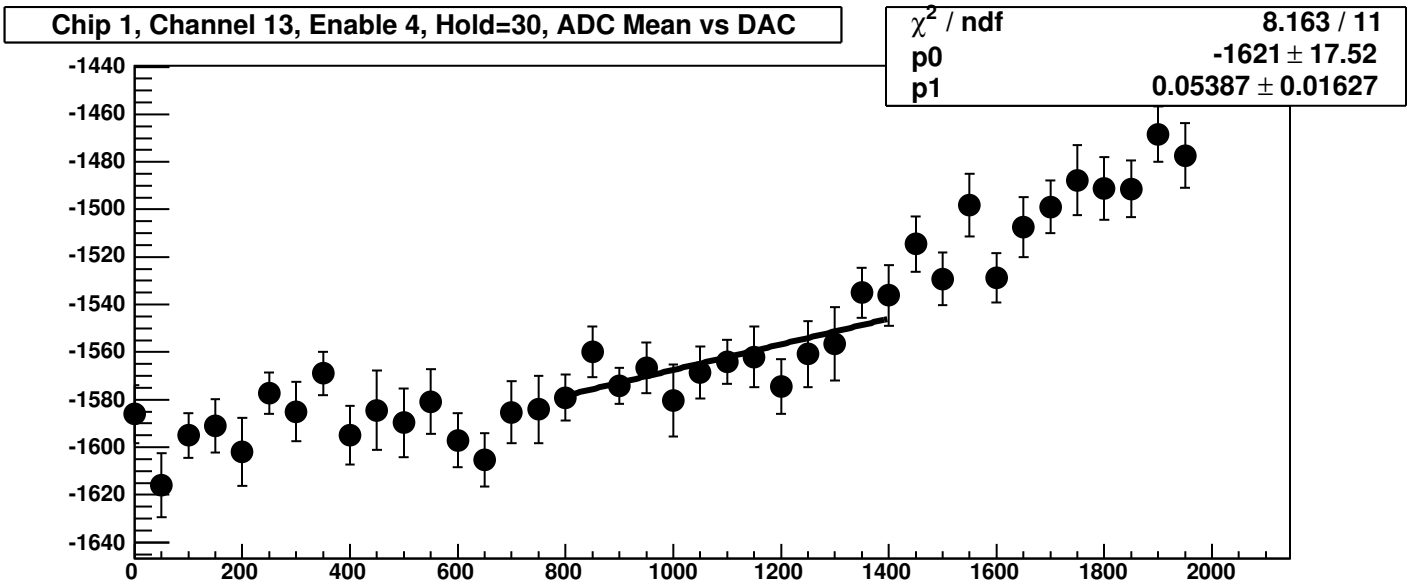

Chip 1, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

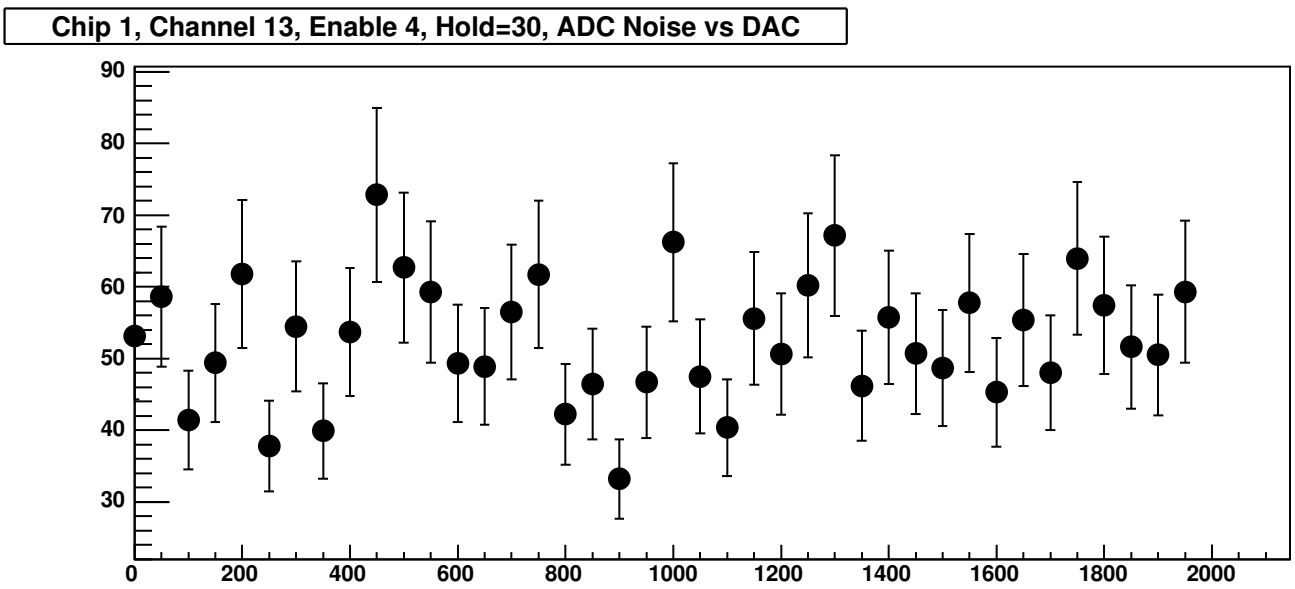

Chip 1, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

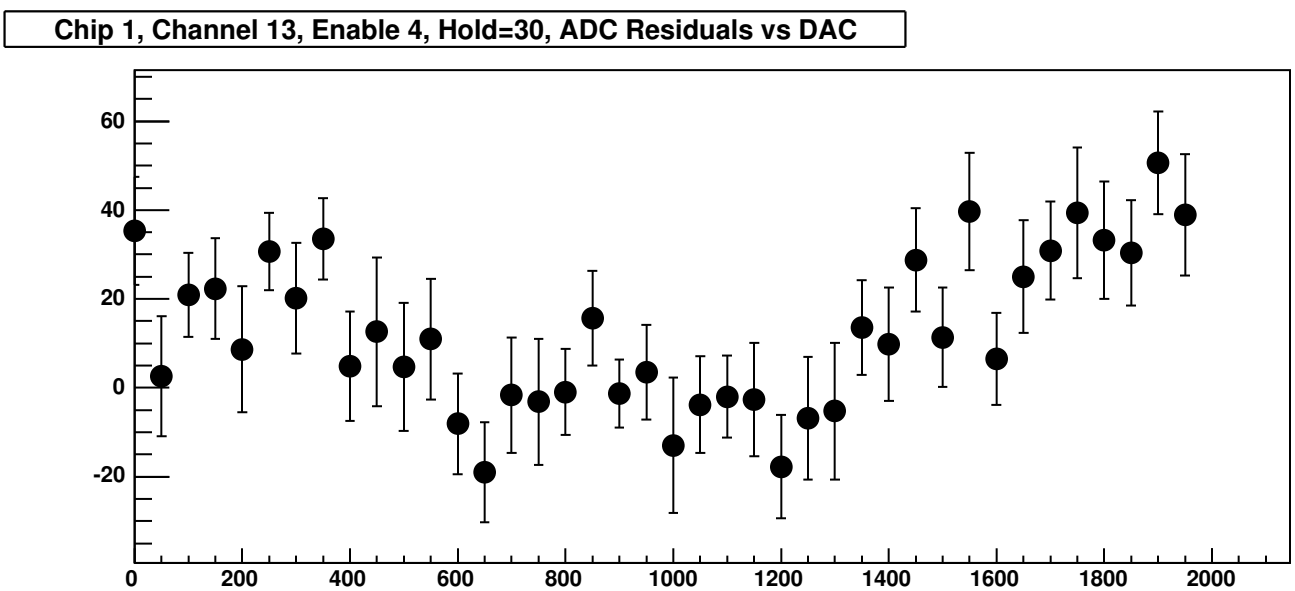

Chip 1, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3.25 / 11
p0 -1677 ± 18.01
p1 0.05144 ± 0.01647
-1560
-1580
-1600
-1620
-1640
0
200
400
600
800
1000
1200
1400
1600
1800
2000

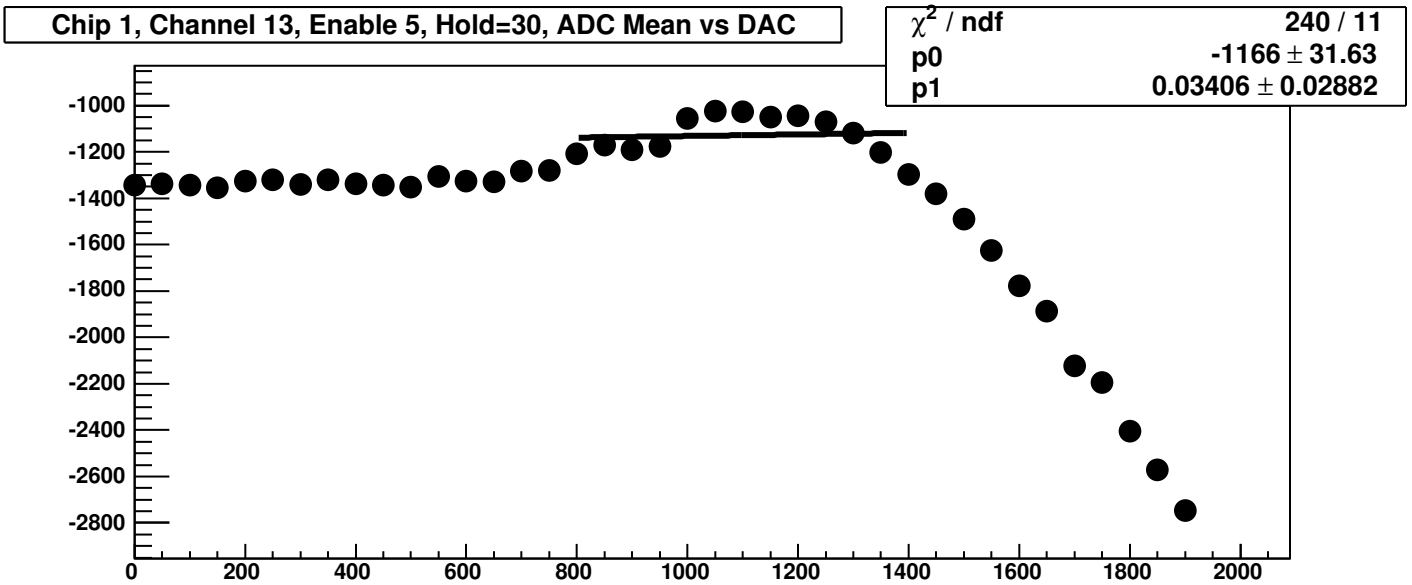

Chip 1, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

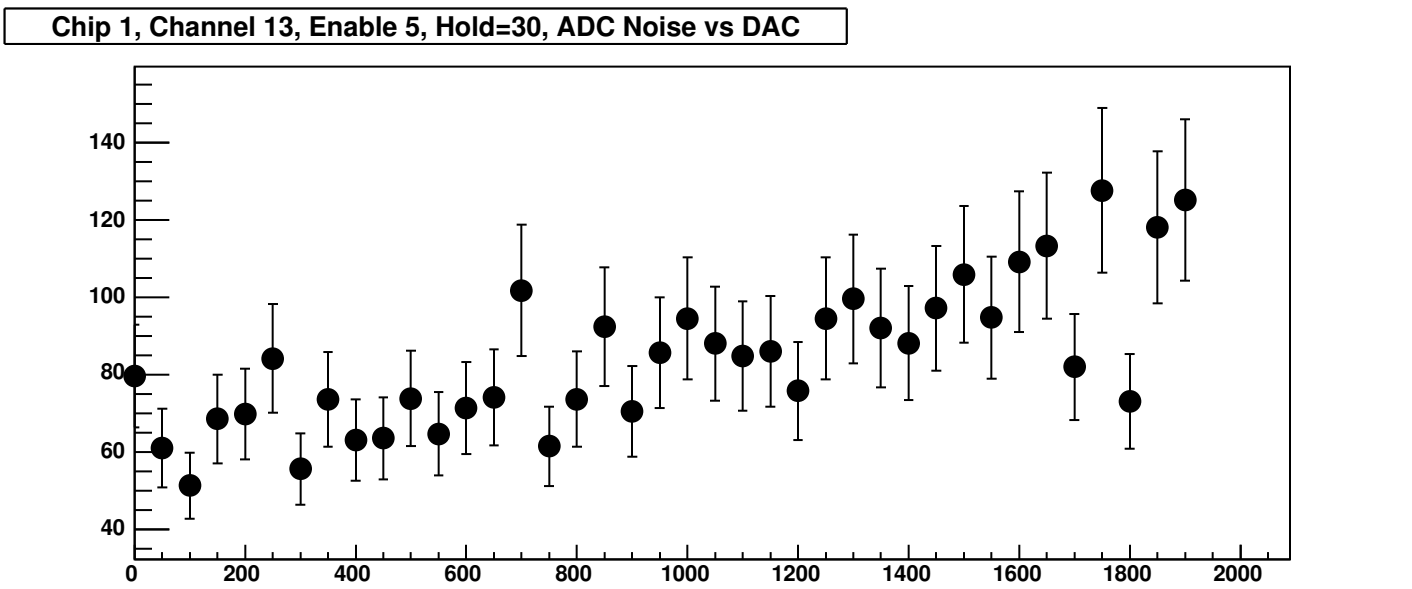

Chip 1, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

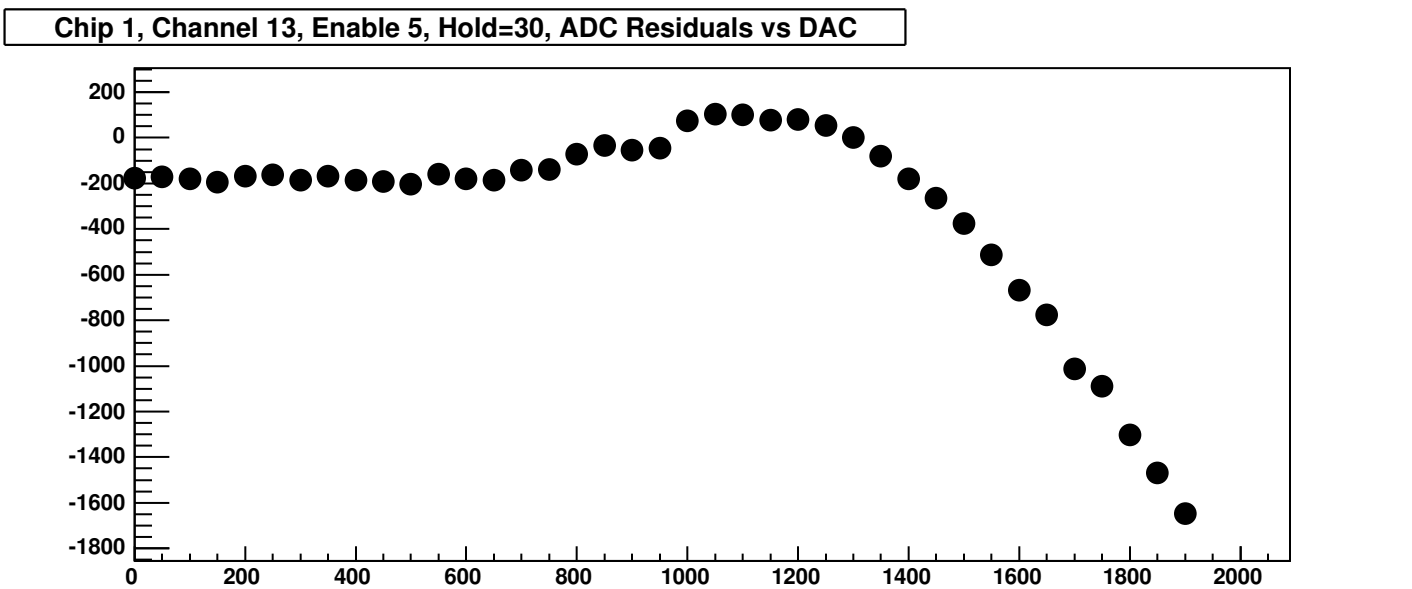

Chip 1, Channel 13, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 8.163 / 11
p0 -1621 ± 17.52
p1 0.05387 ± 0.01627
-1440
-1460
-1480
-1500
-1520
-1540
-1560
-1580
-1600
-1620
-1640
0
200
400
600
800
1000
1200
1400
1600
1800
2000

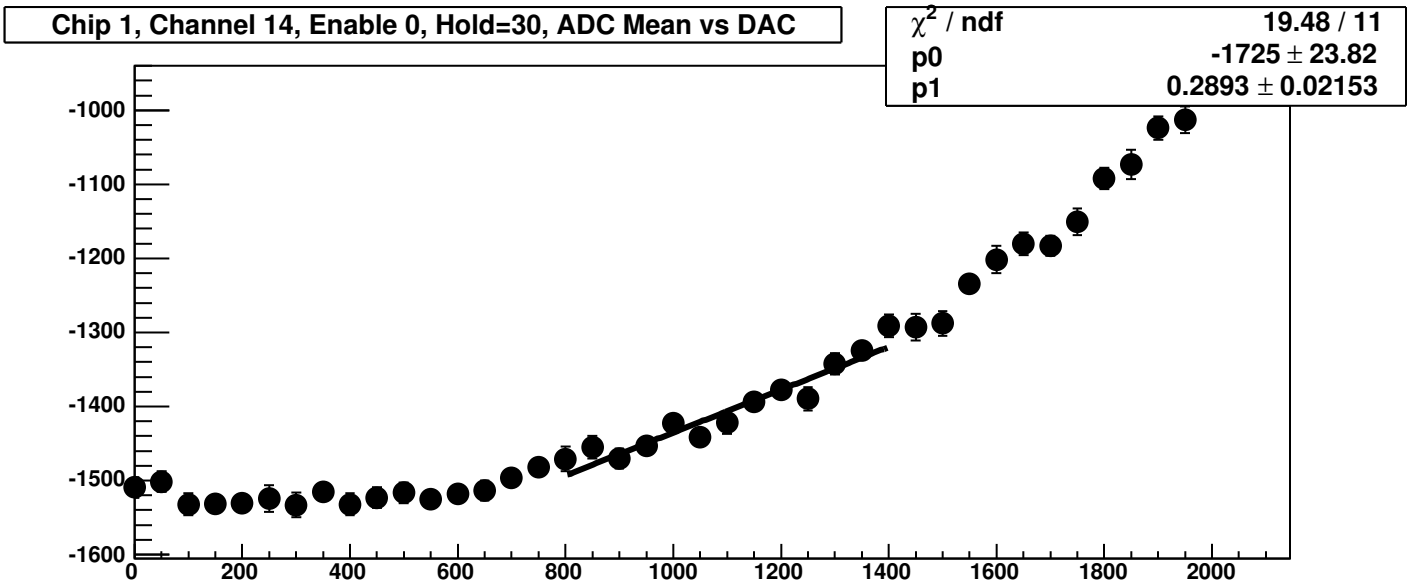

Chip 1, Channel 13, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

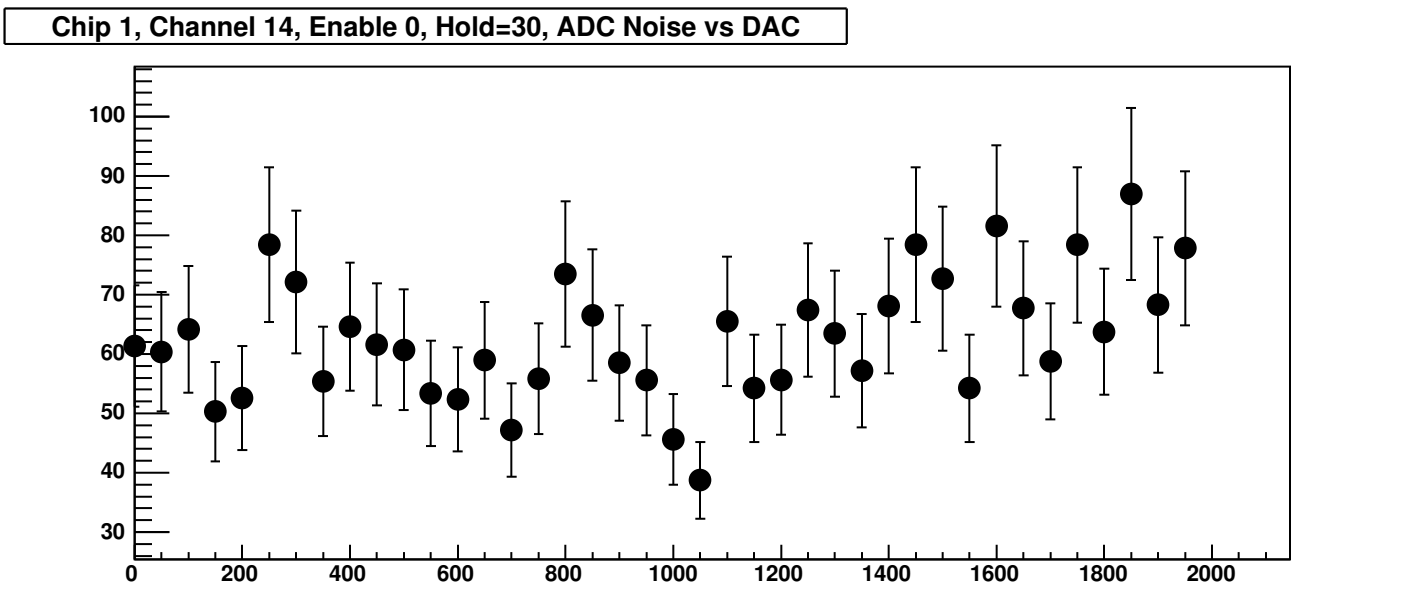

Chip 1, Channel 13, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

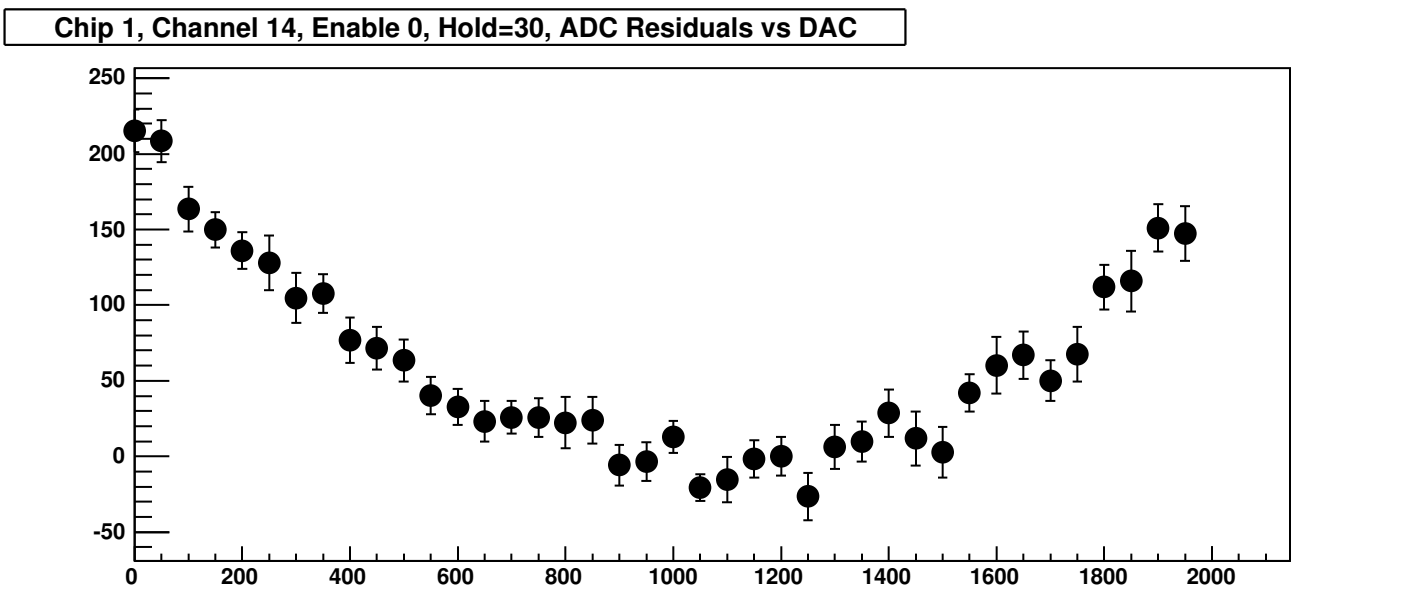

Chip 1, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 240 / 11
p0 -1166 ± 31.63
p1 0.03406 ± 0.02882
-1000
-1200
-1400
-1600
-1800
-2000
-2200
-2400
-2600
-2800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

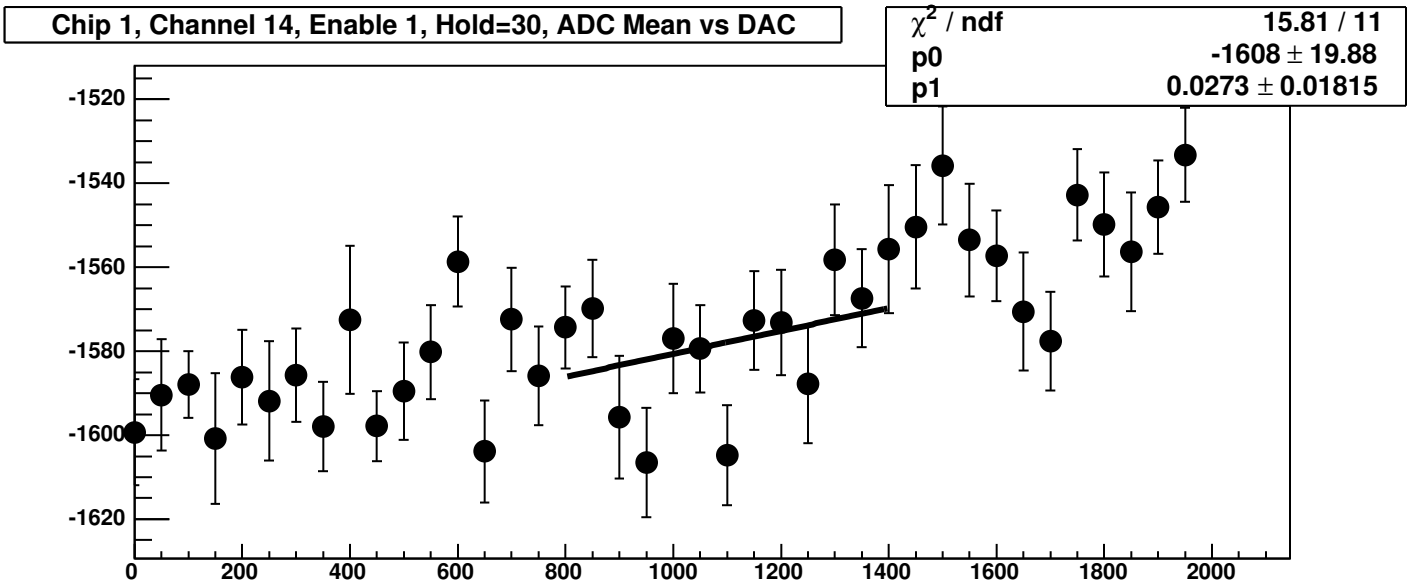

Chip 1, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

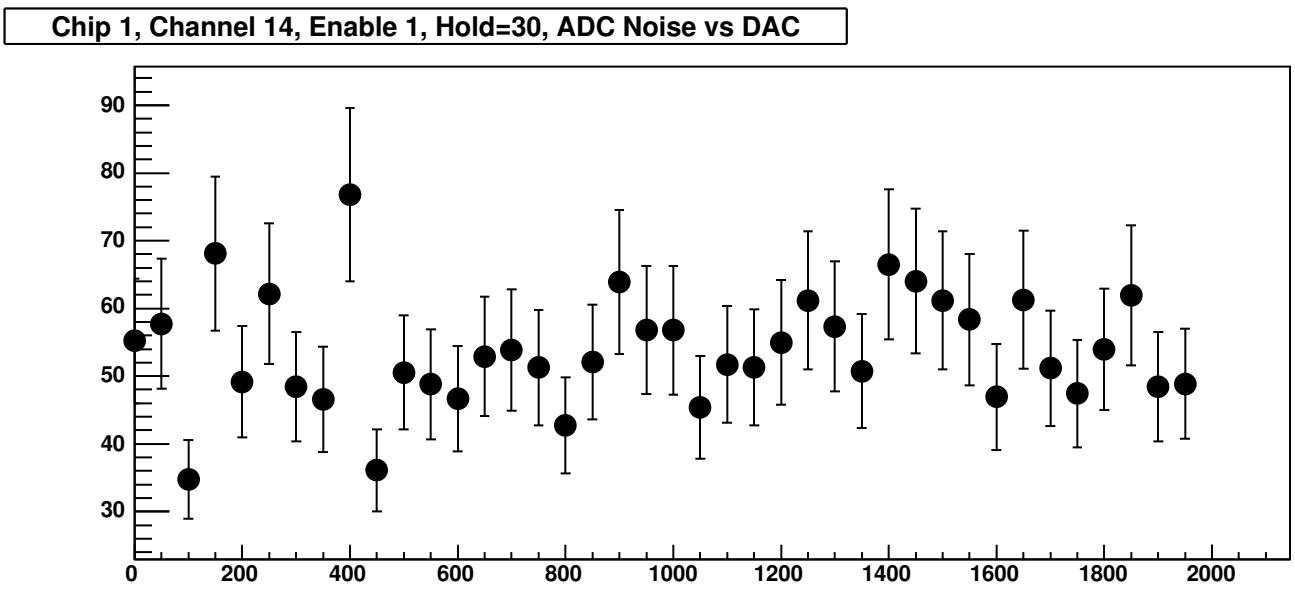

Chip 1, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

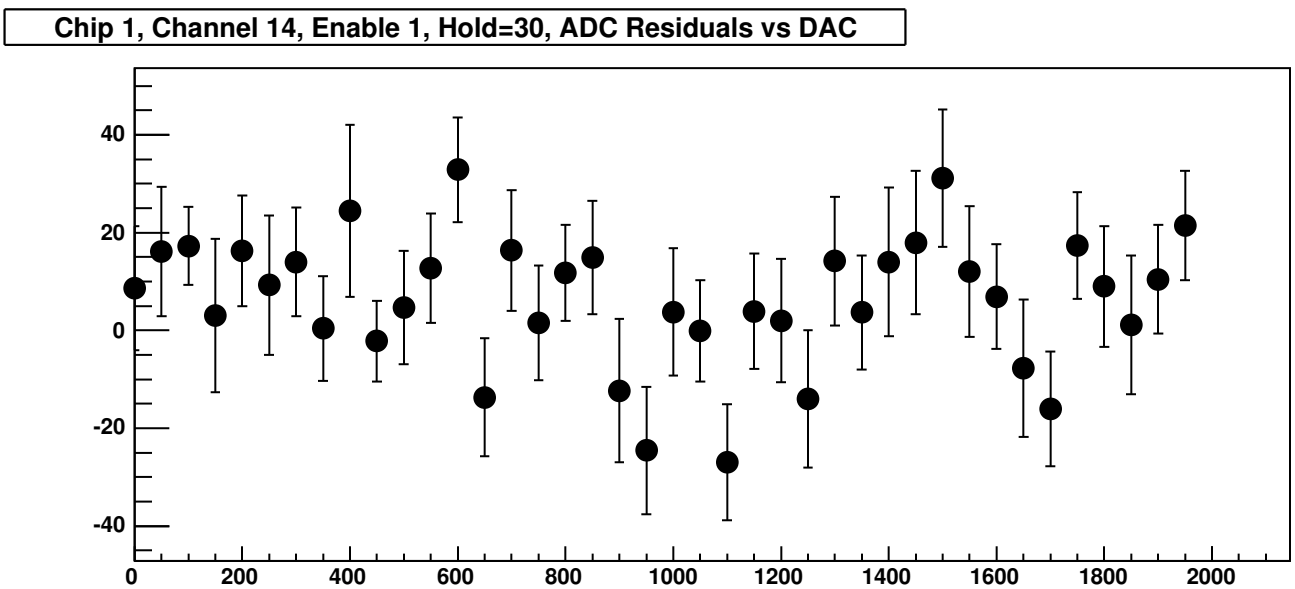

Chip 1, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 19.48 / 11
p0 -1725 ± 23.82
p1 0.2893 ± 0.02153
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

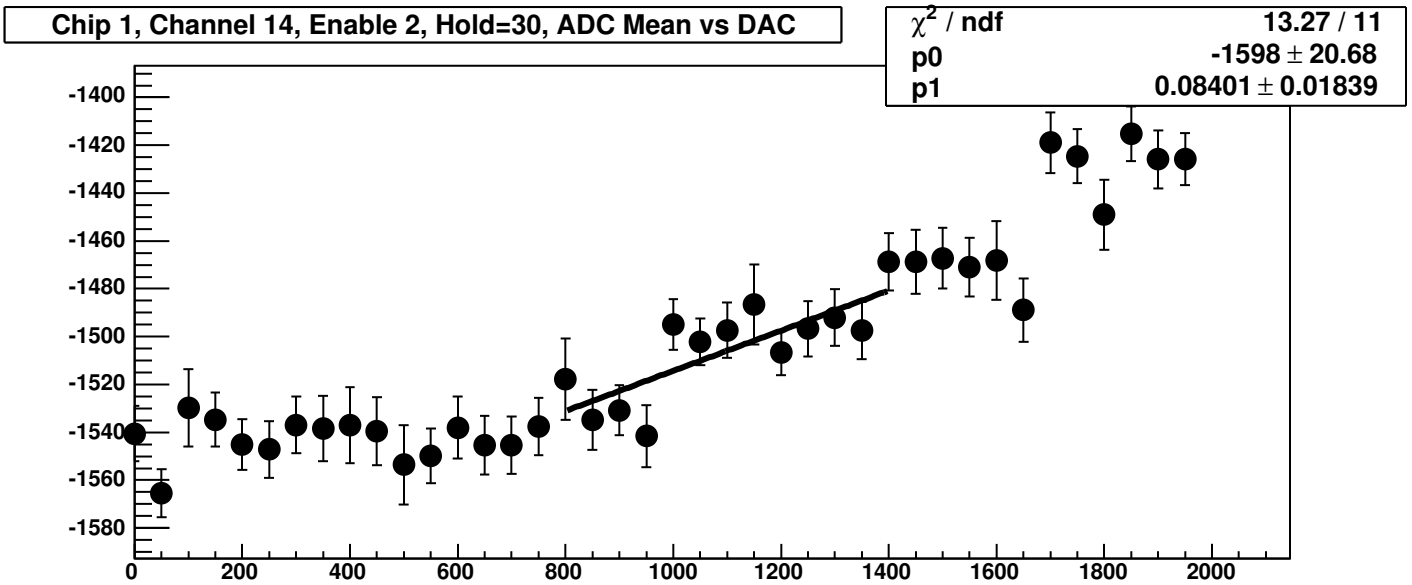

Chip 1, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

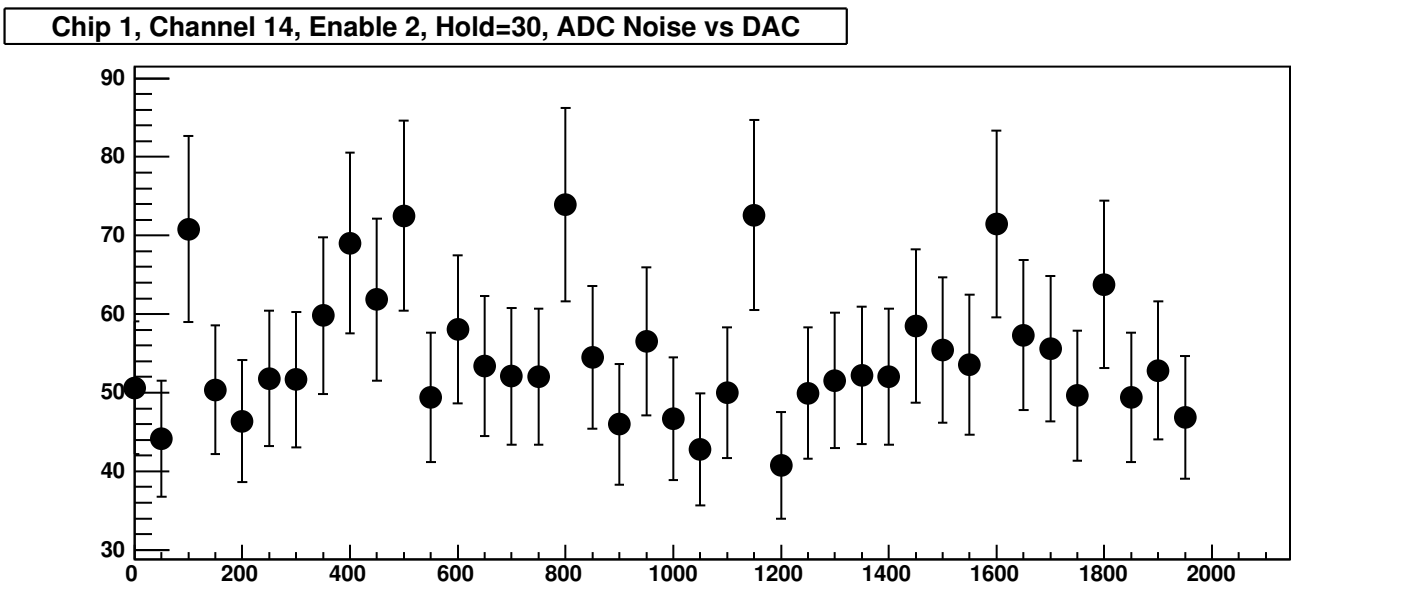

Chip 1, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

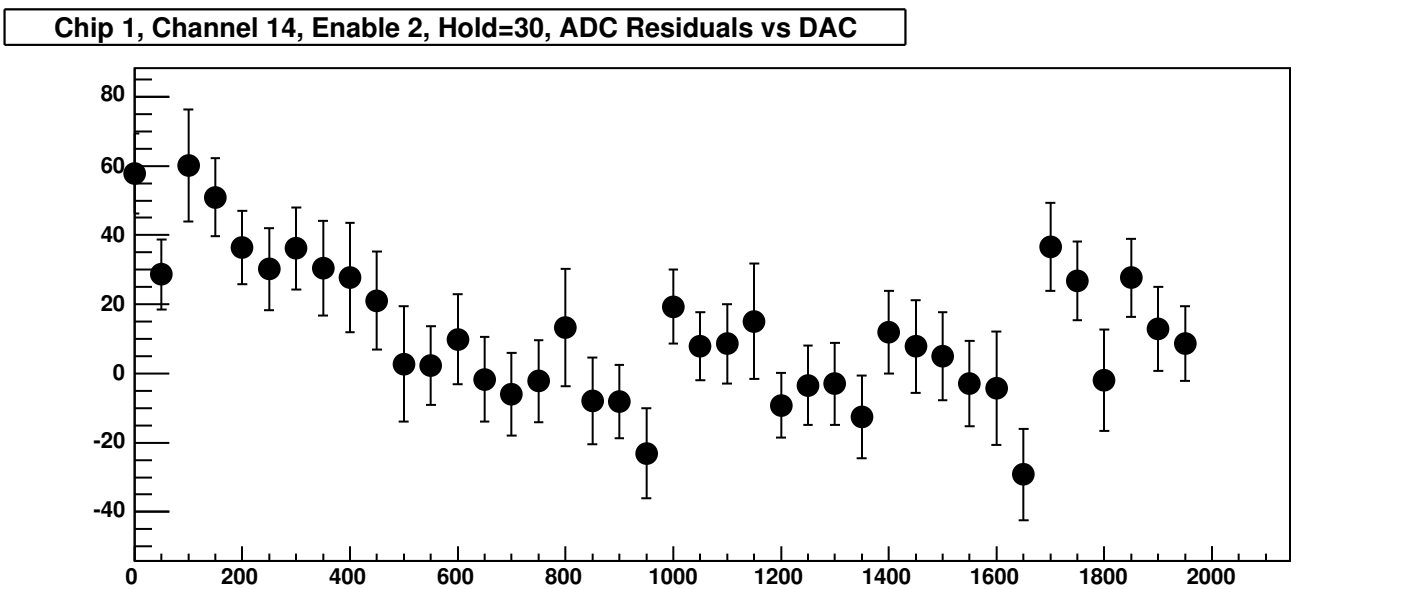

Chip 1, Channel 14, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 15.81 / 11
p0 -1608 ± 19.88
p1 0.0273 ± 0.01815
-1520
-1540
-1560
-1580
-1600
-1620
0
200
400
600
800
1000
1200
1400
1600
1800
2000

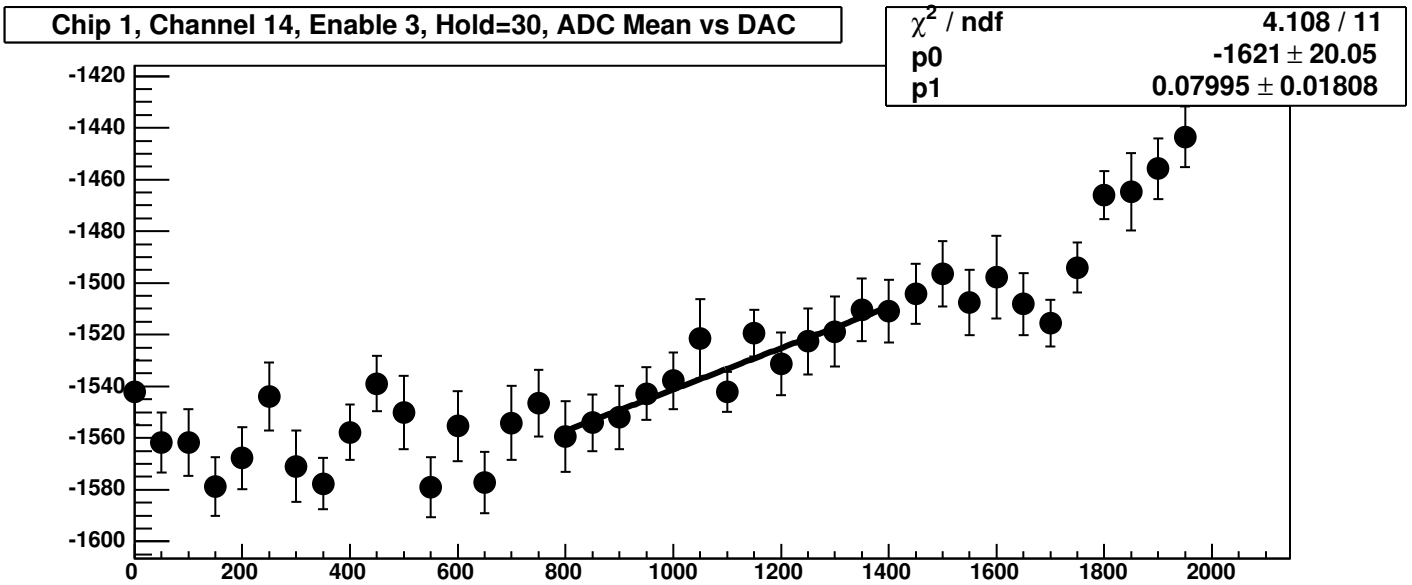

Chip 1, Channel 14, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

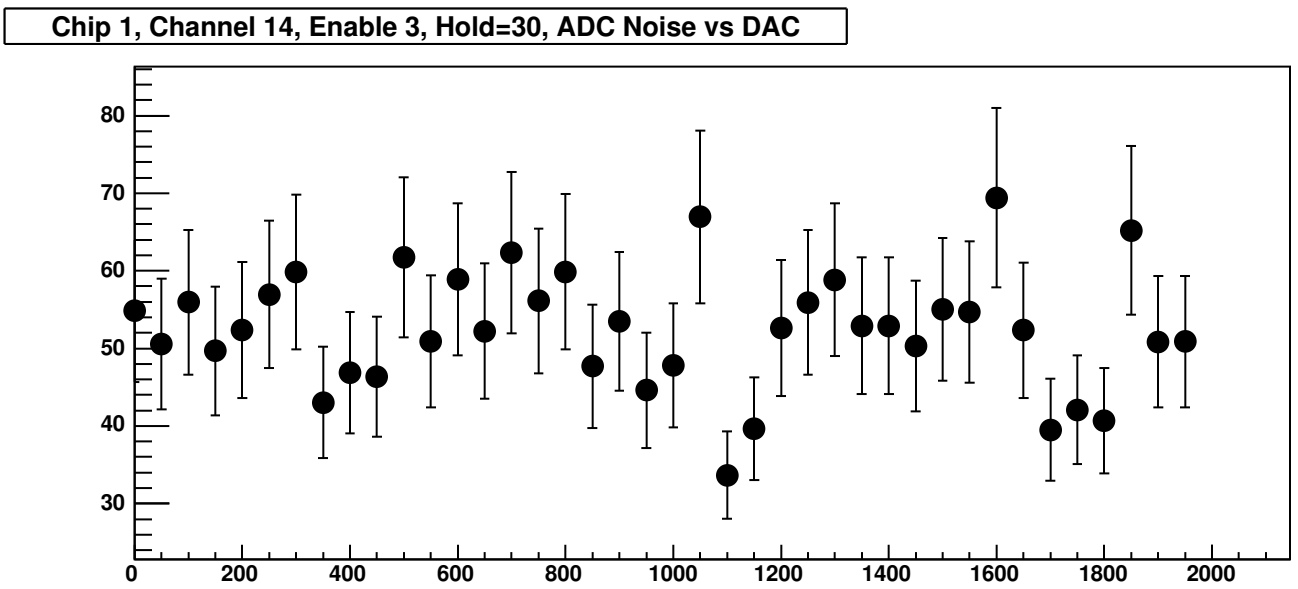

Chip 1, Channel 14, Enable 1, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

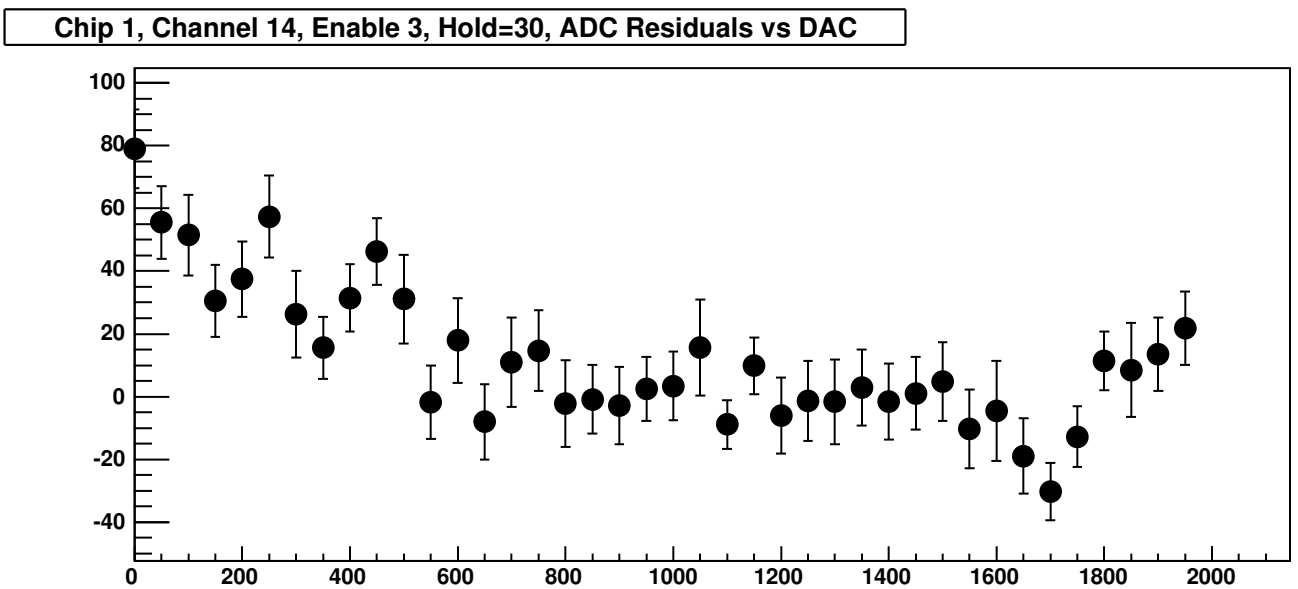

Chip 1, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 13.27 / 11
p0 -1598 ± 20.68
p1 0.08401 ± 0.01839
-1400
-1420
-1440
-1460
-1480
-1500
-1520
-1540
-1560
-1580
0
200
400
600
800
1000
1200
1400
1600
1800
2000

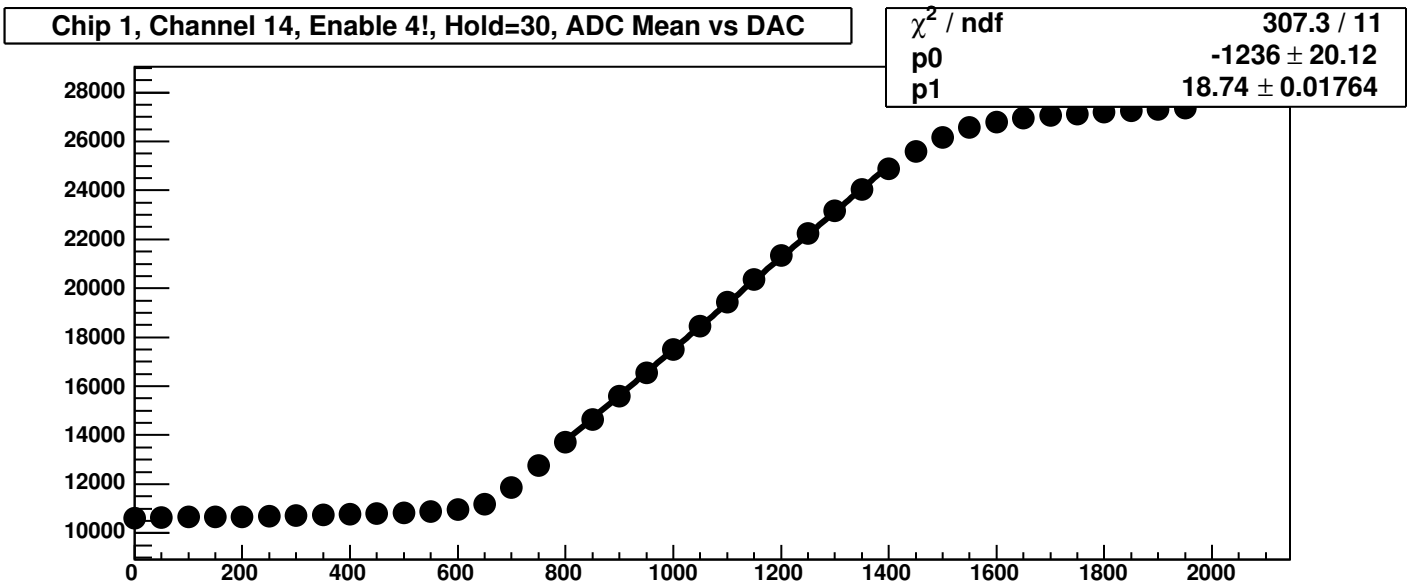

Chip 1, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

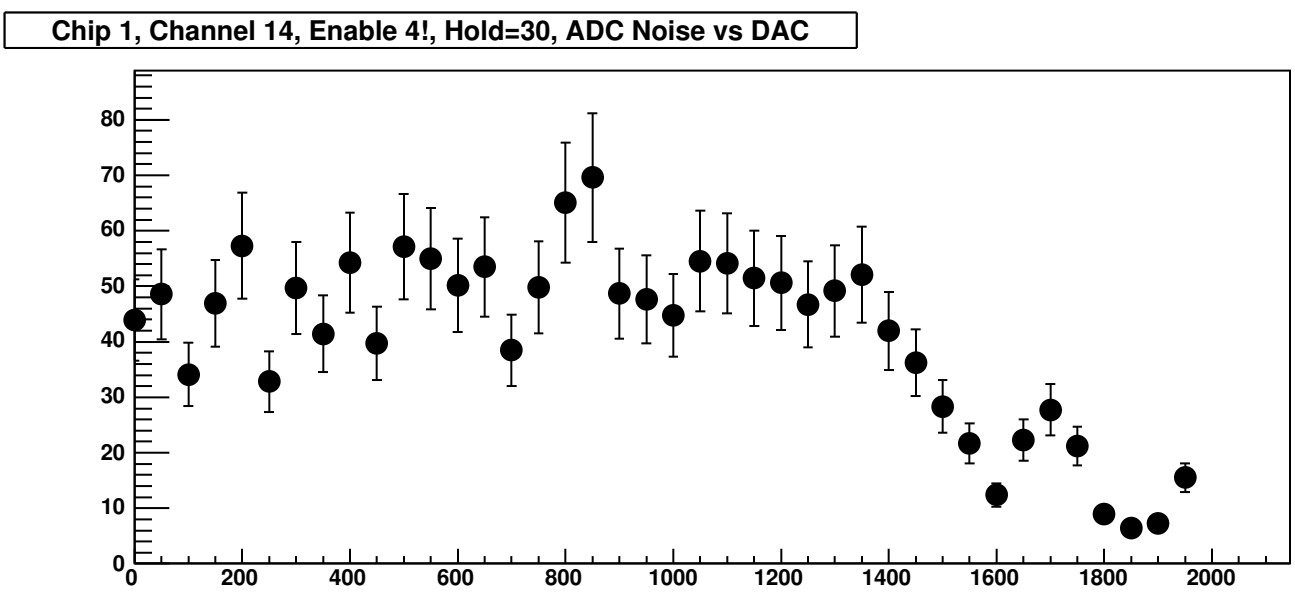

Chip 1, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

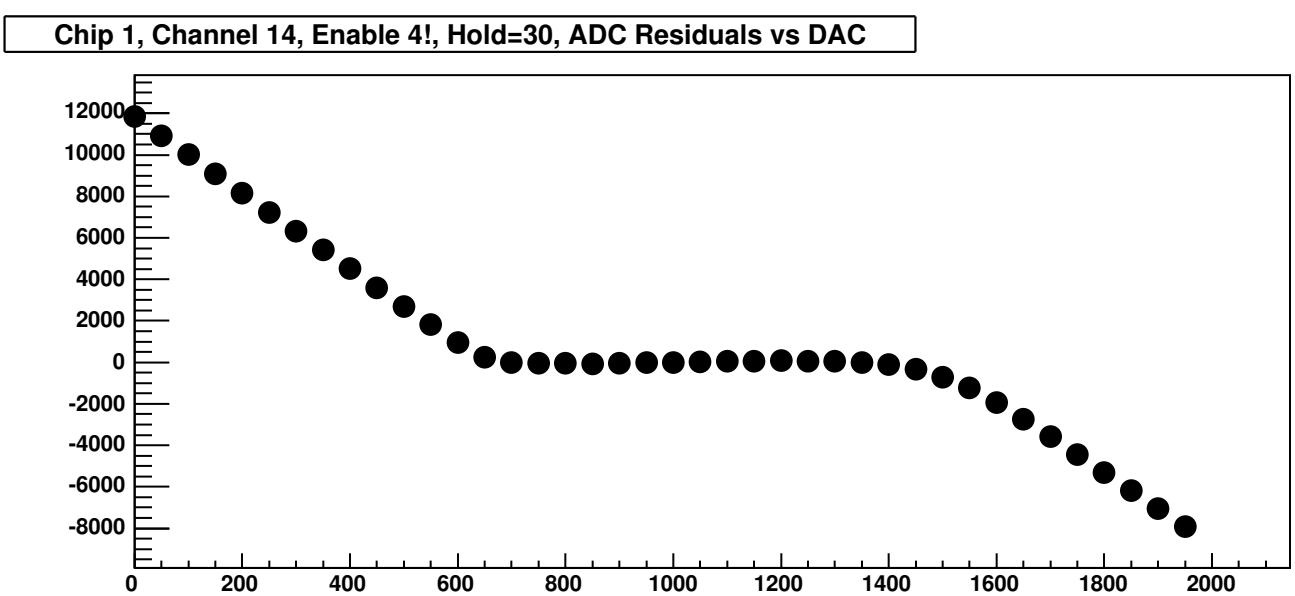

Chip 1, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4.108 / 11
p0 -1621 ± 20.05
p1 0.07995 ± 0.01808
-1420
-1440
-1460
-1480
-1500
-1520
-1540
-1560
-1580
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

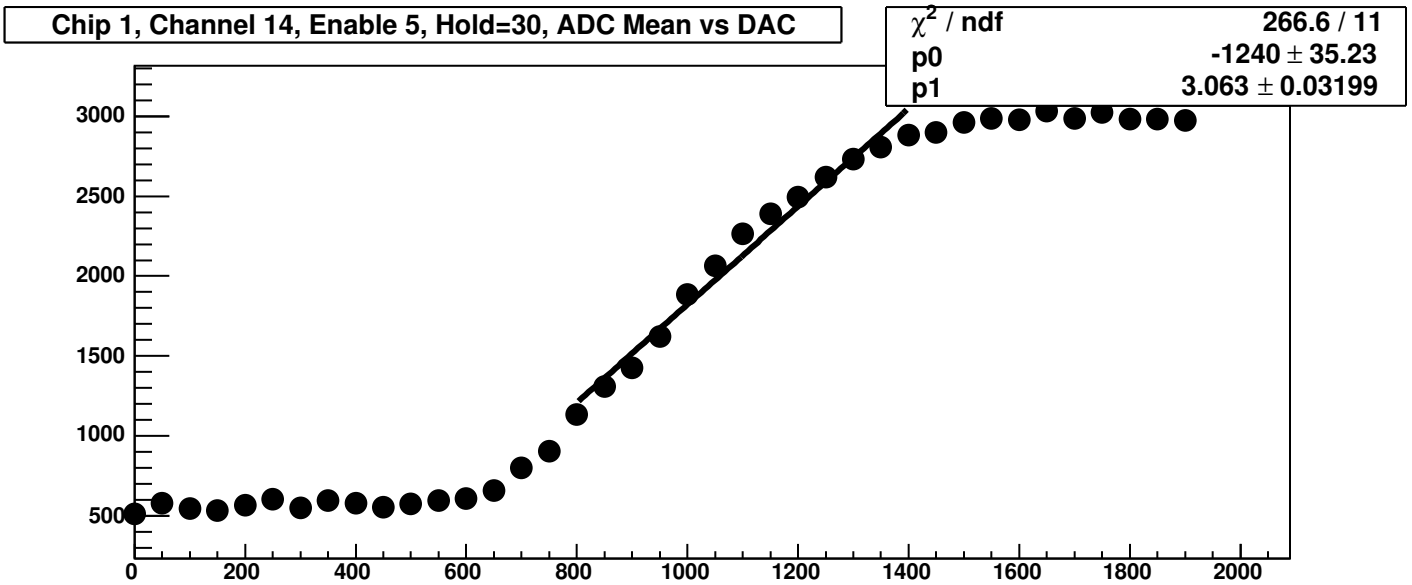

Chip 1, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

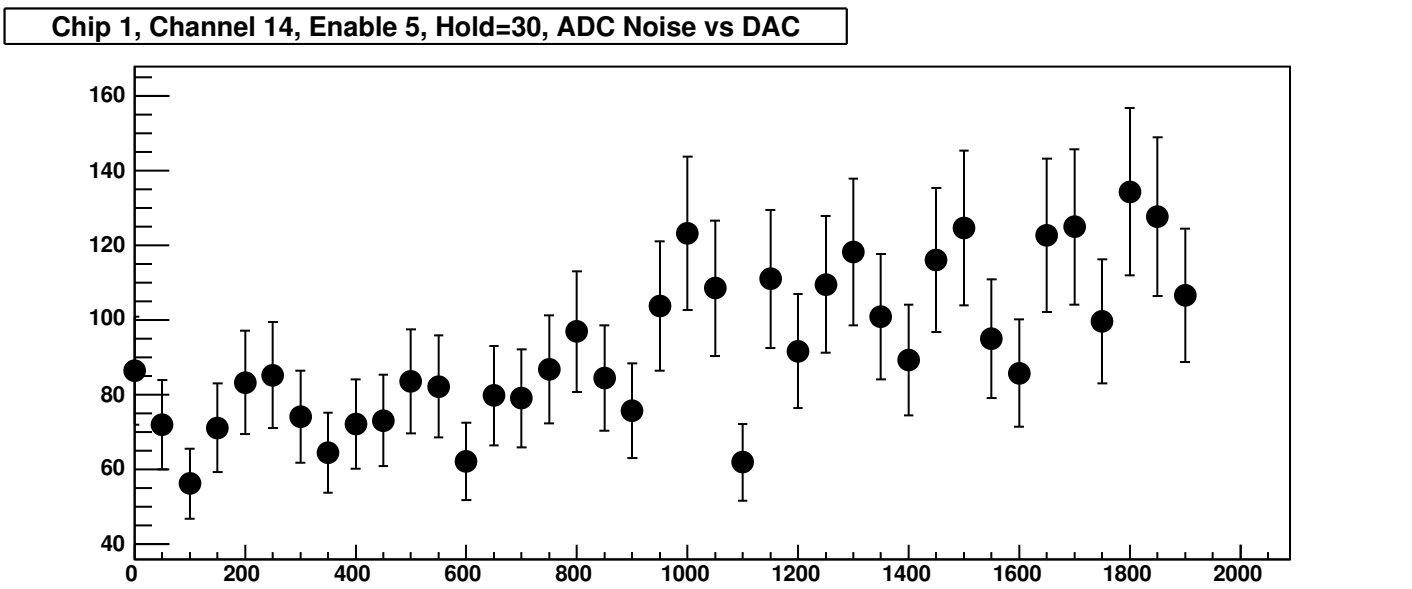

Chip 1, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

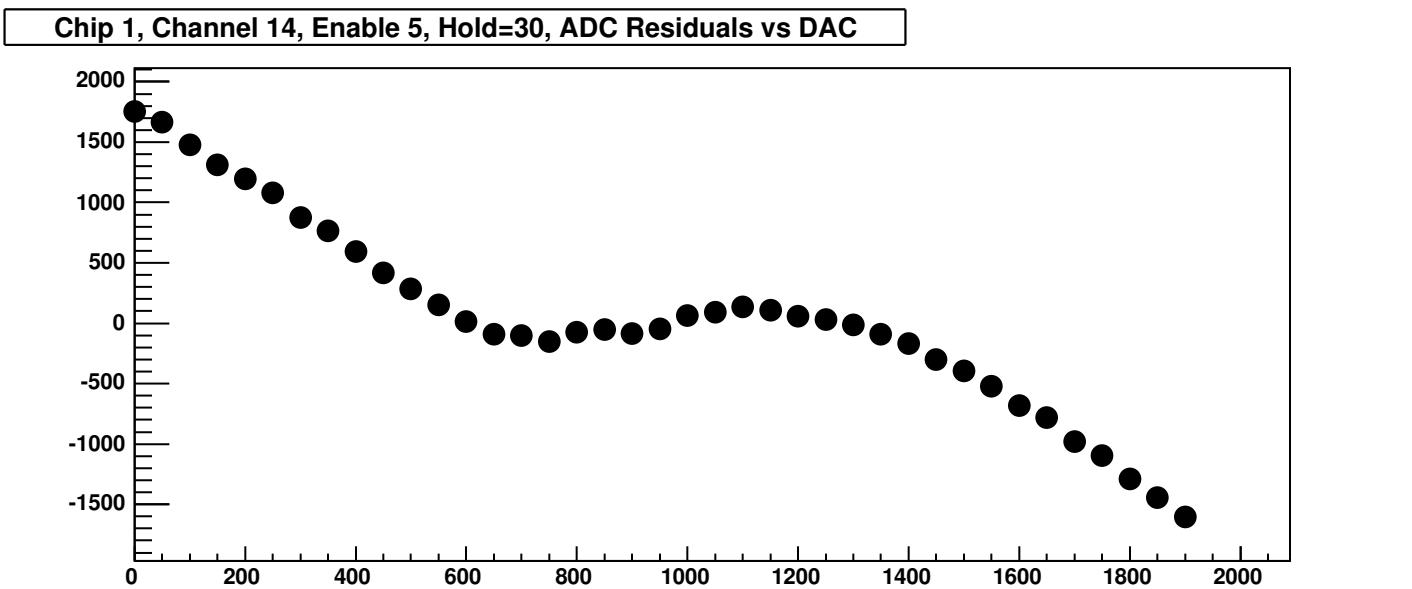

Chip 1, Channel 14, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 307.3 / 11
p0 -1236 ± 20.12
p1 18.74 ± 0.01764
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

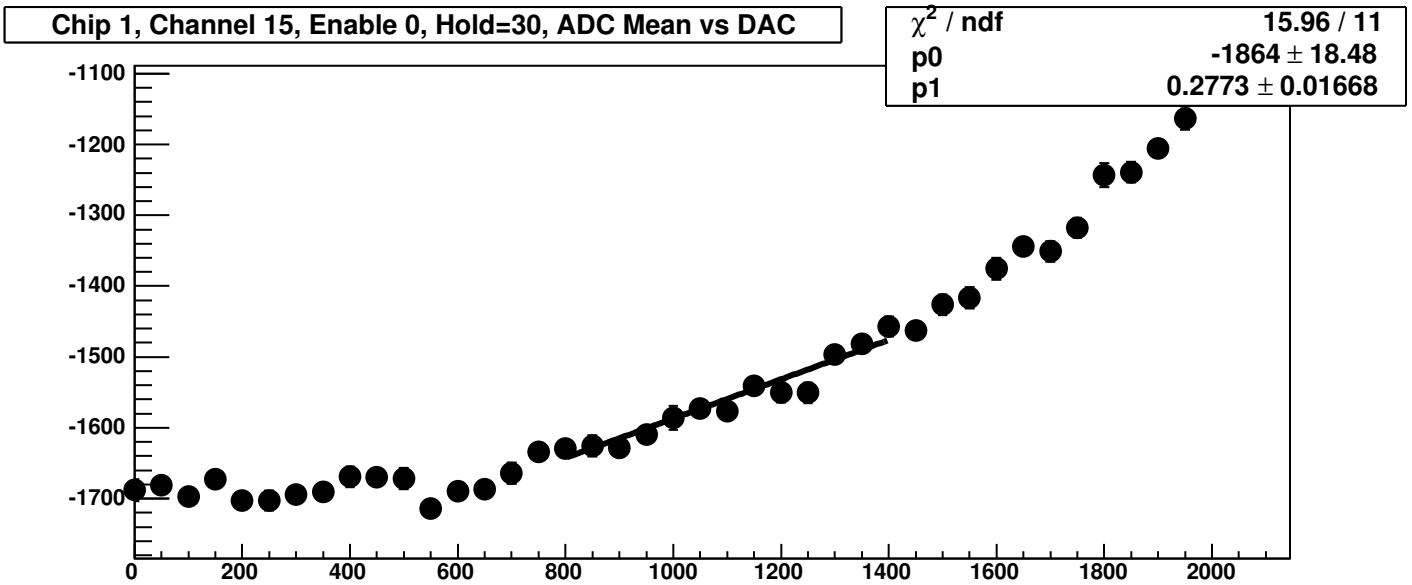

Chip 1, Channel 14, Enable 4!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

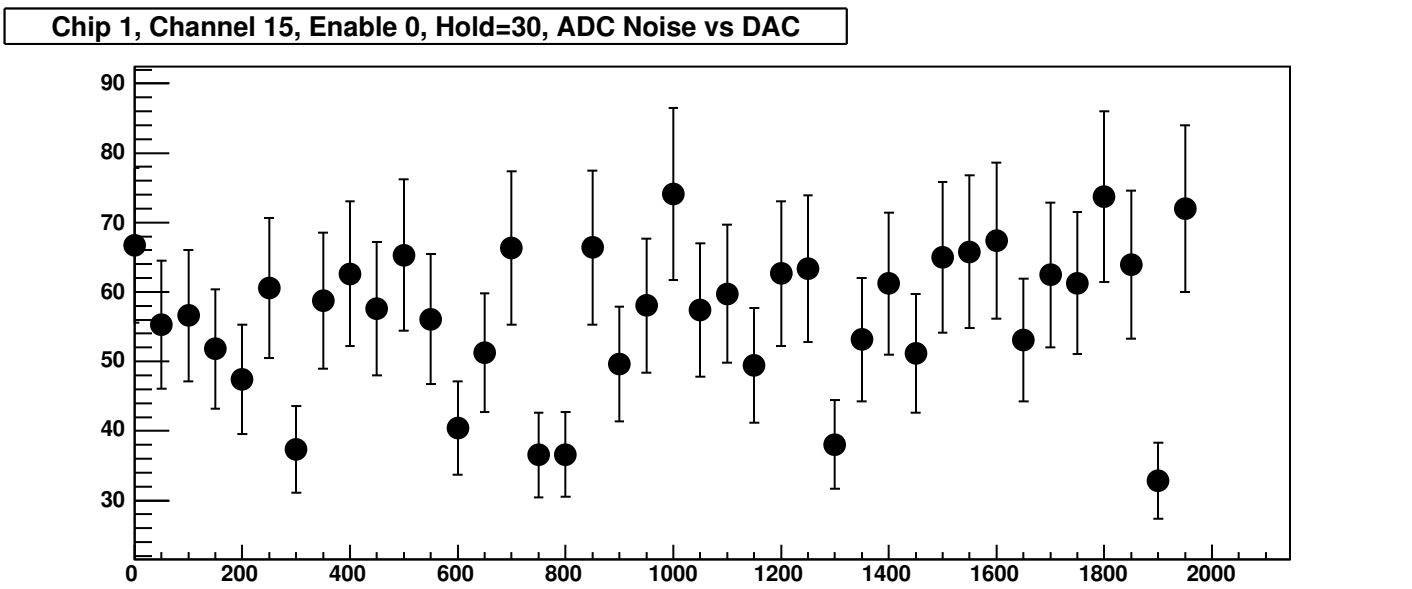

Chip 1, Channel 14, Enable 4!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

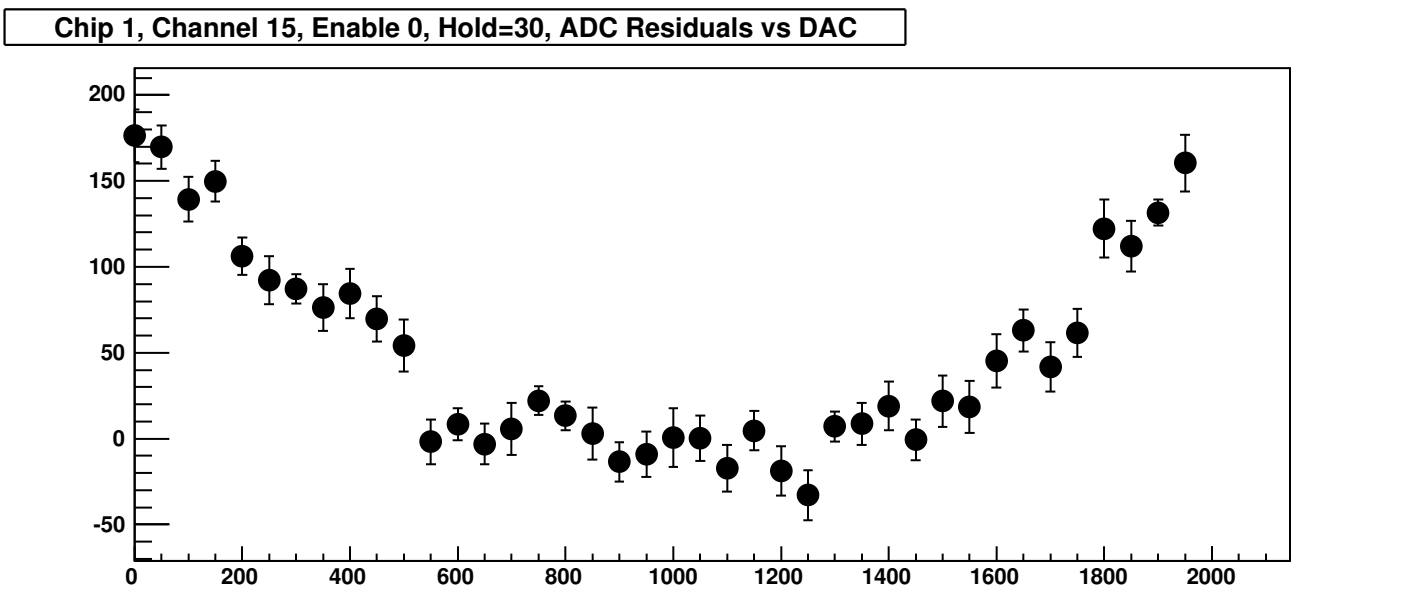

Chip 1, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 266.6 / 11
p0 -1240 ± 35.23
p1 3.063 ± 0.03199
3000
2500
2000
1500
1000
500
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

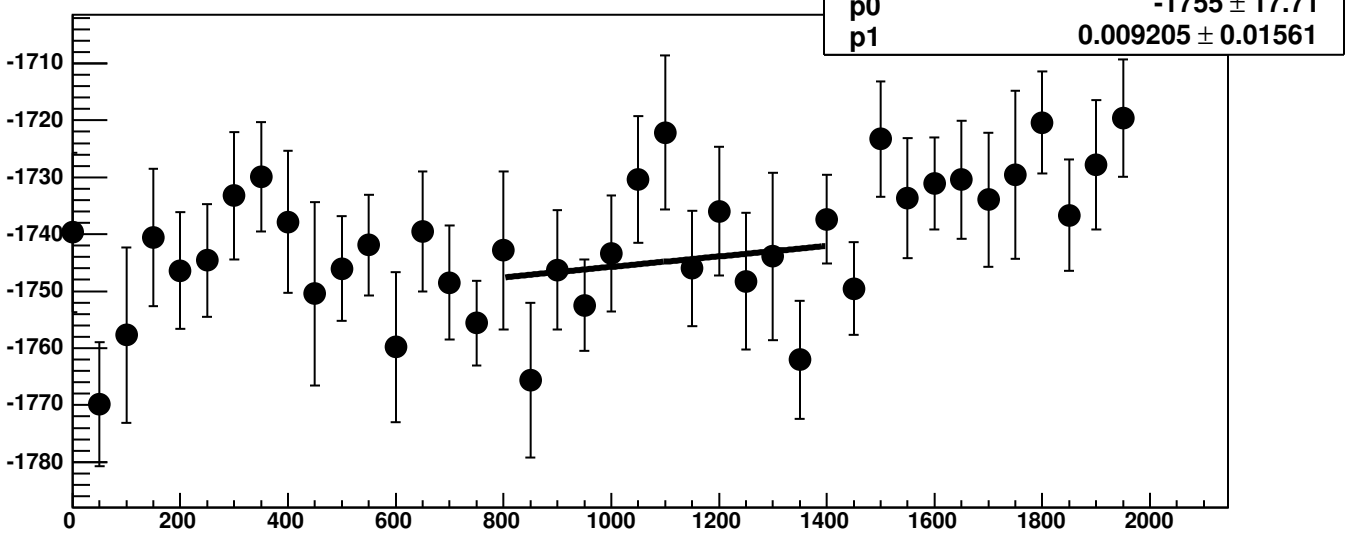

Chip 1, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
-1000
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 15.96 / 11
p0 -1864 ± 18.48
p1 0.2773 ± 0.01668
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

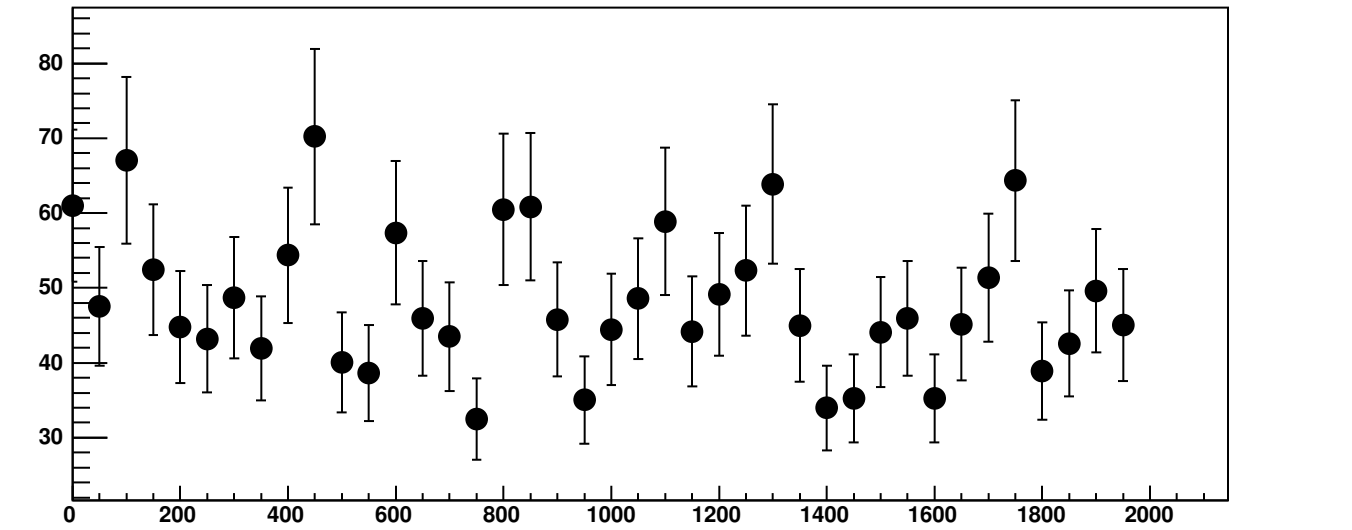

Chip 1, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 1, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

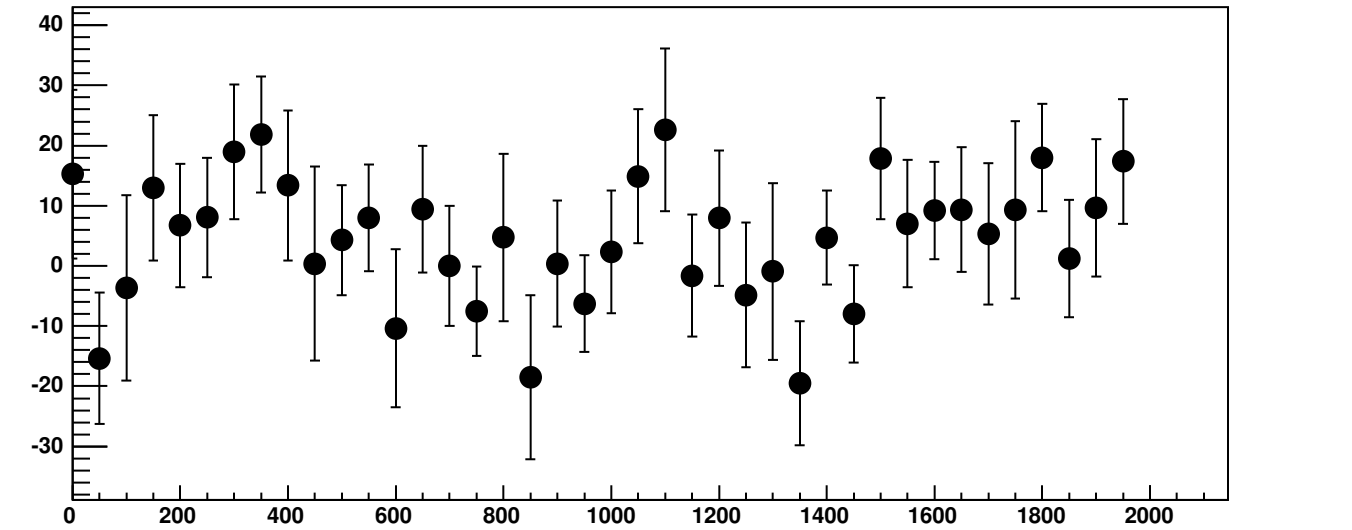
Chip 1, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC

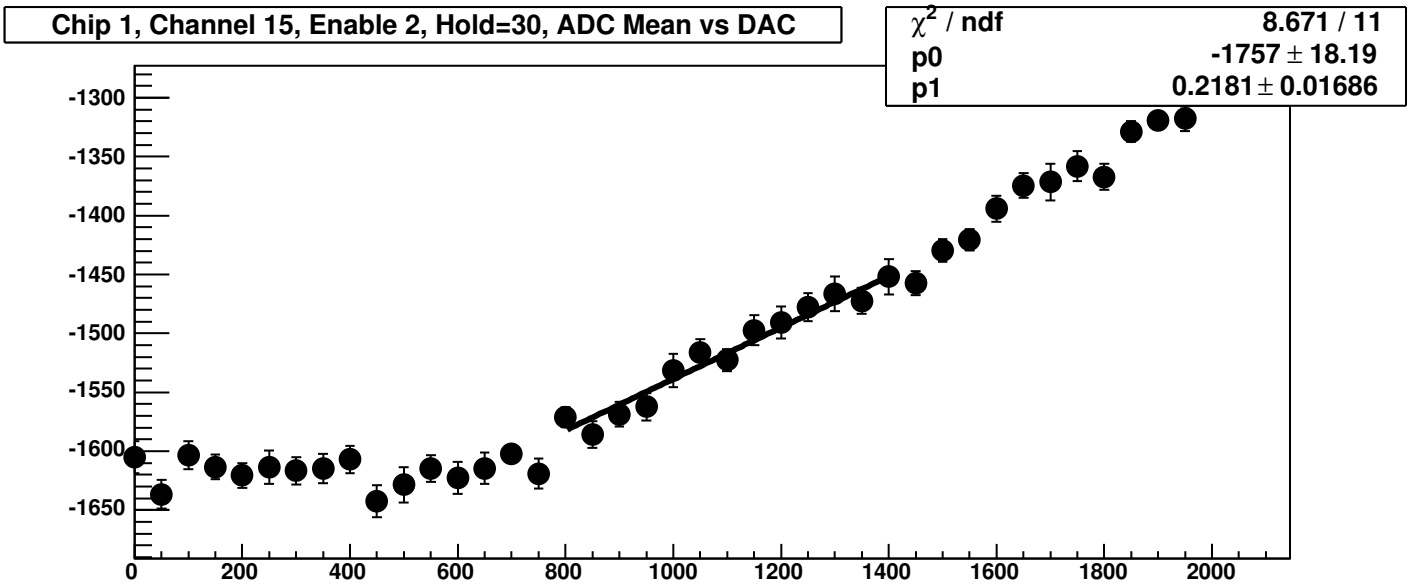


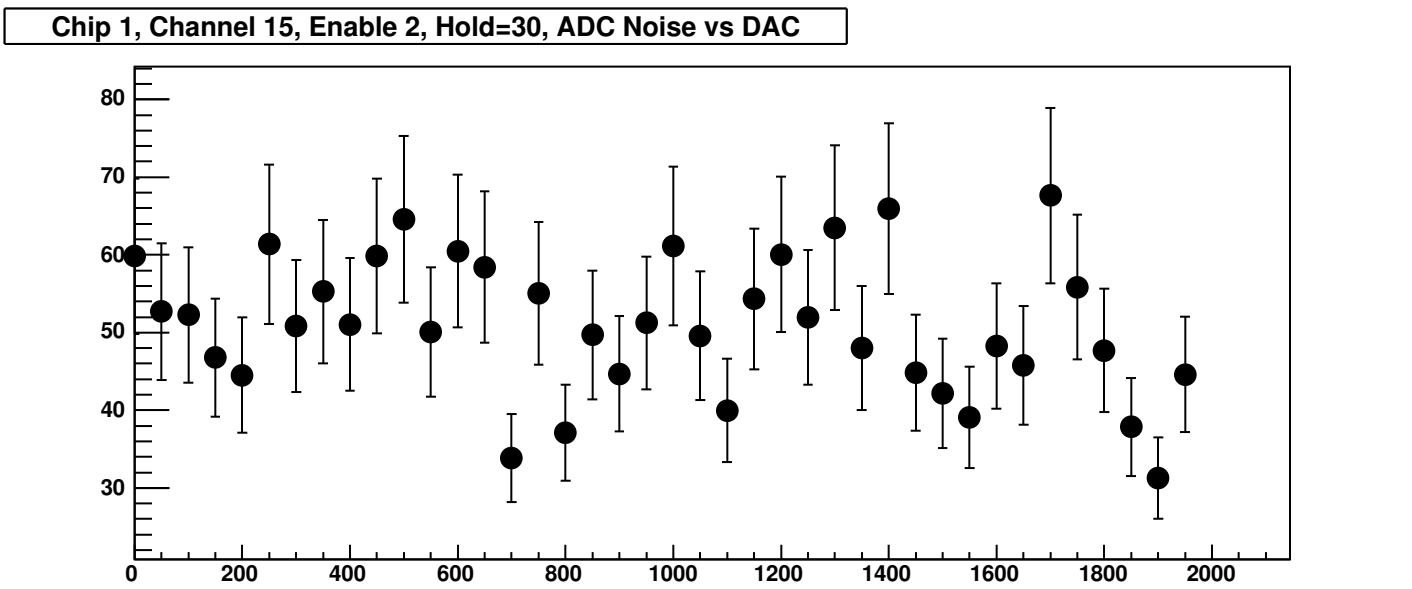
Chip 1, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC

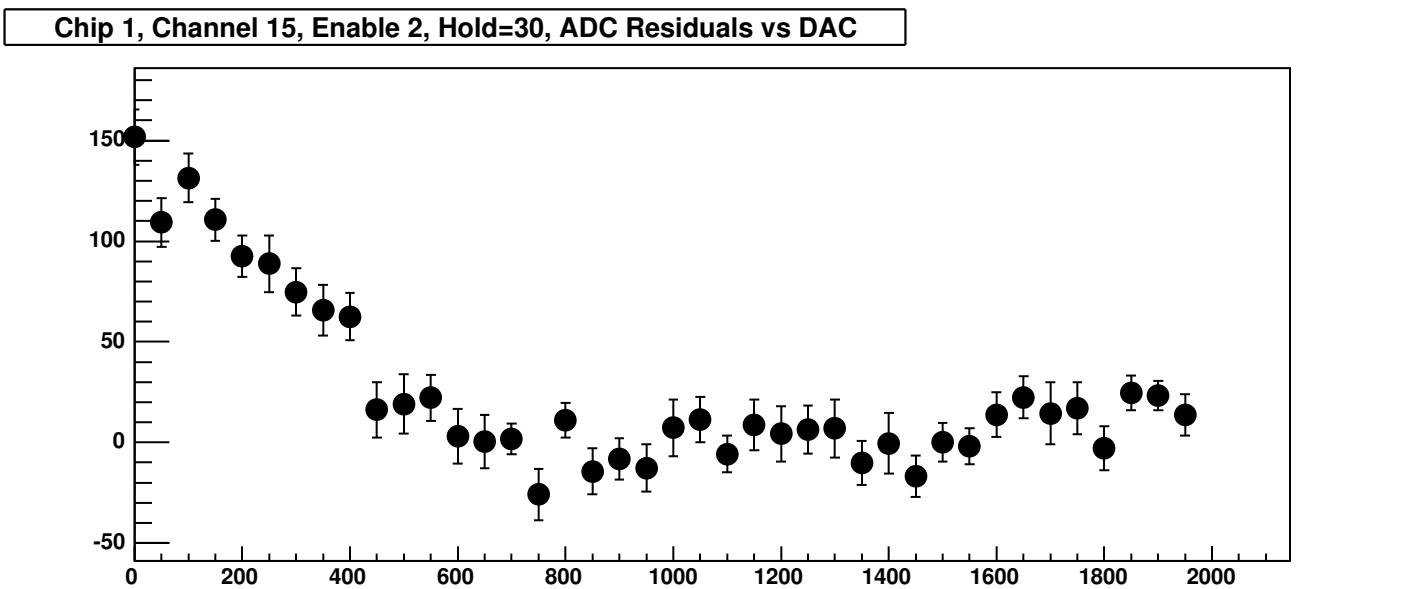


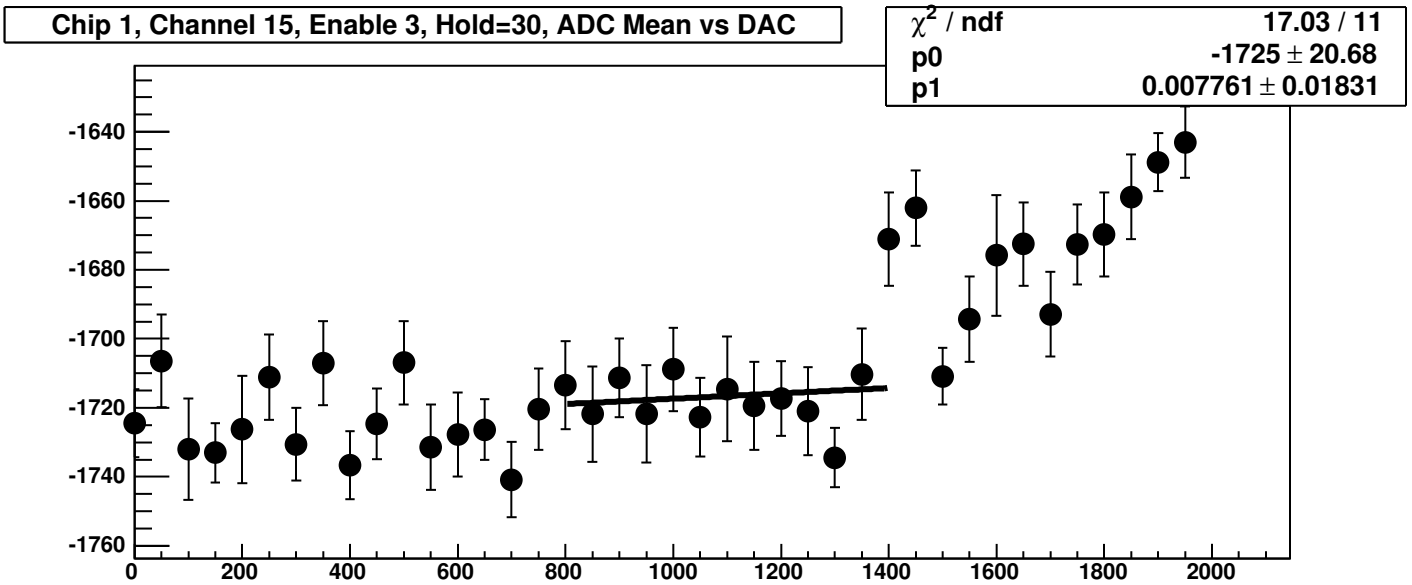
Chip 1, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC

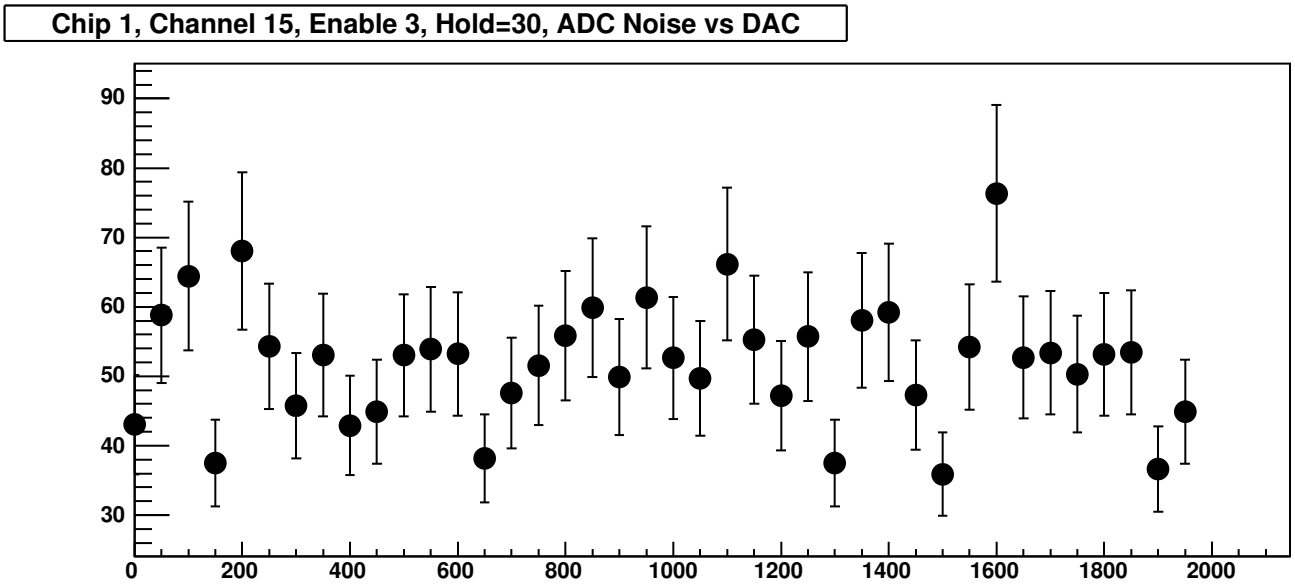


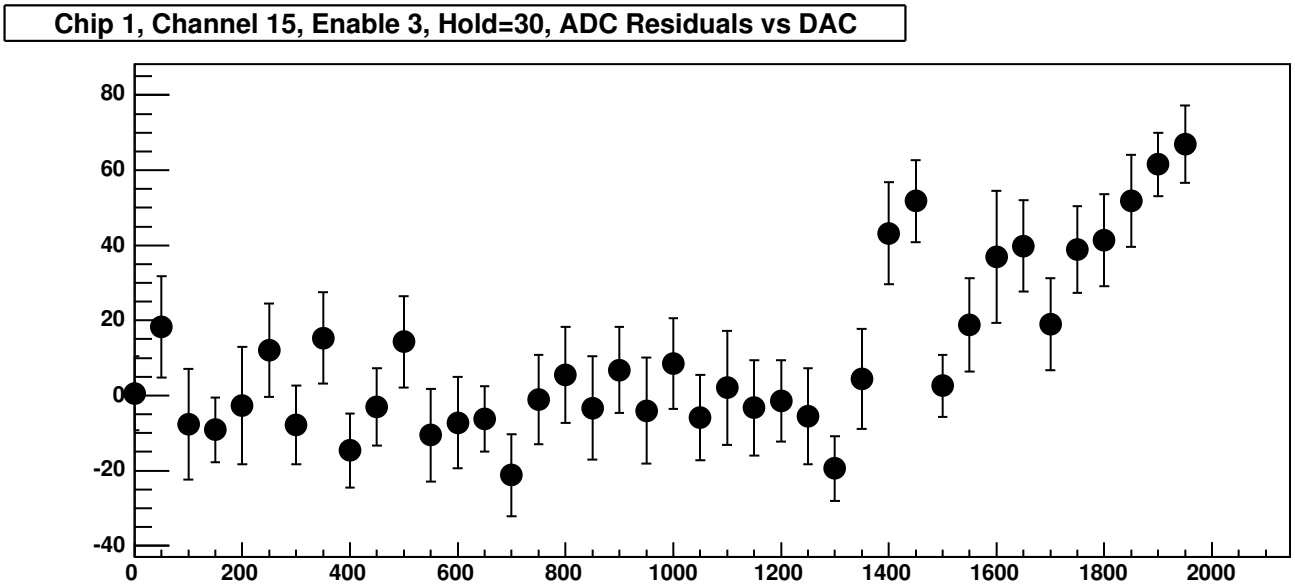

Chip 1, Channel 15, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 8.671 / 11
p0 -1757 ± 18.19
p1 0.2181 ± 0.01686
-1300
-1350
-1400
-1450
-1500
-1550
-1600
-1650
0
200
400
600
800
1000
1200
1400
1600
1800
2000

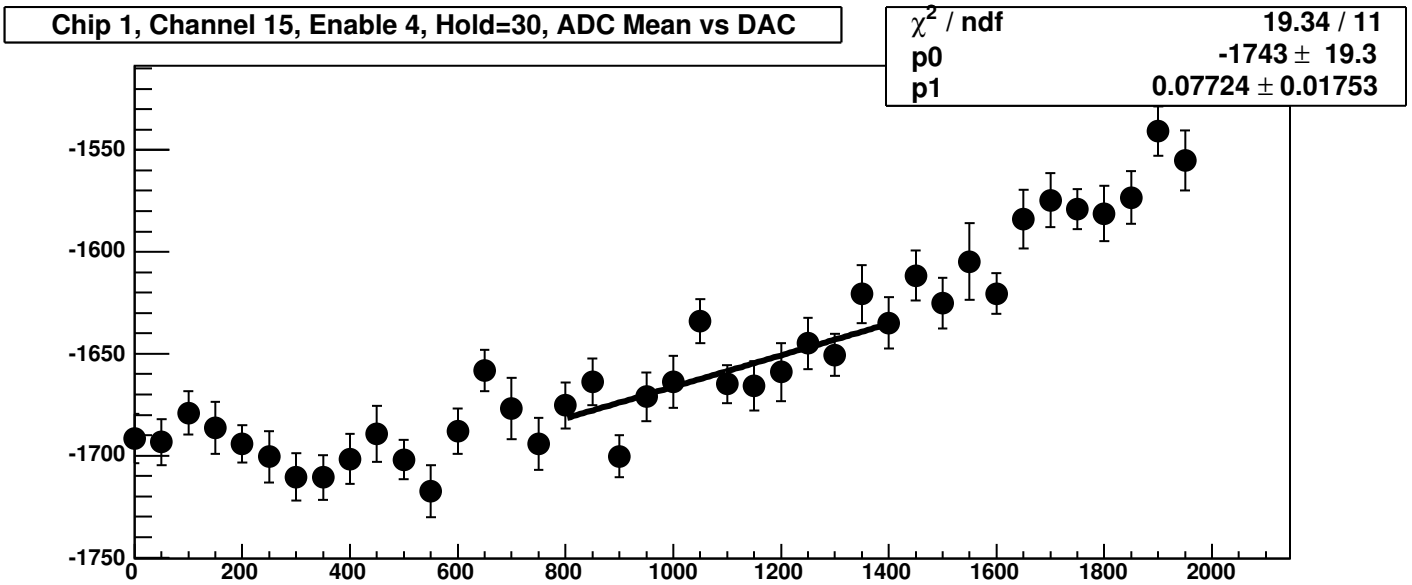

Chip 1, Channel 15, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

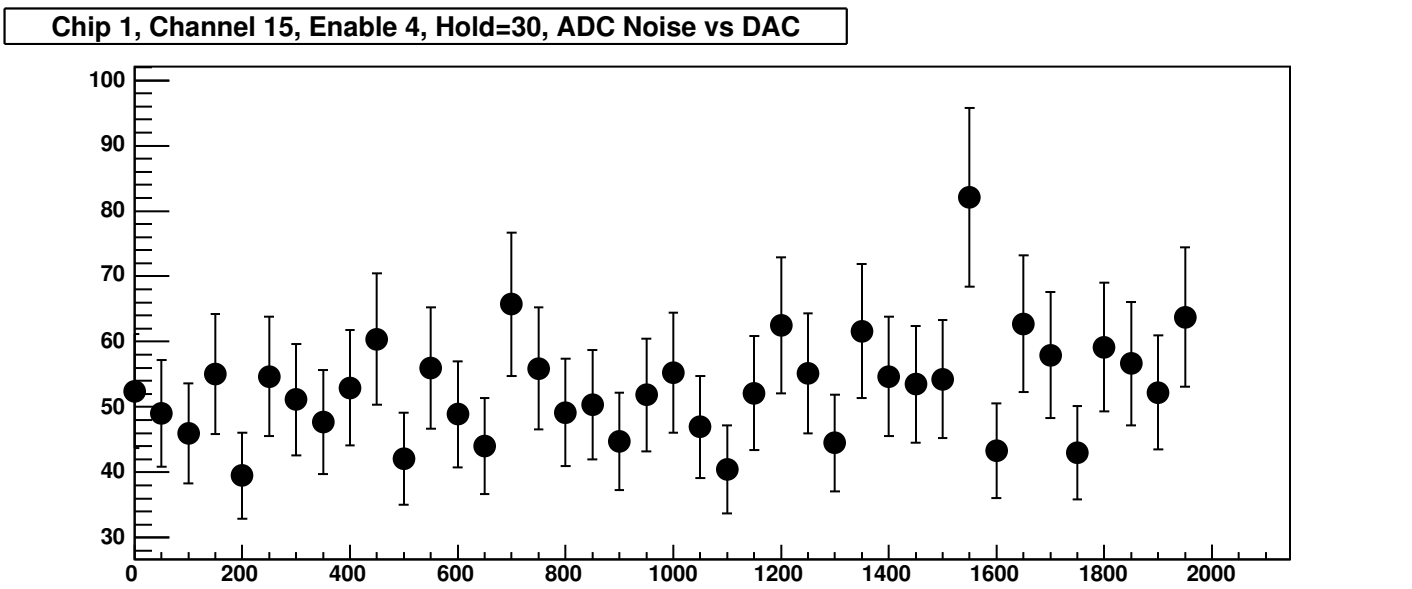

Chip 1, Channel 15, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

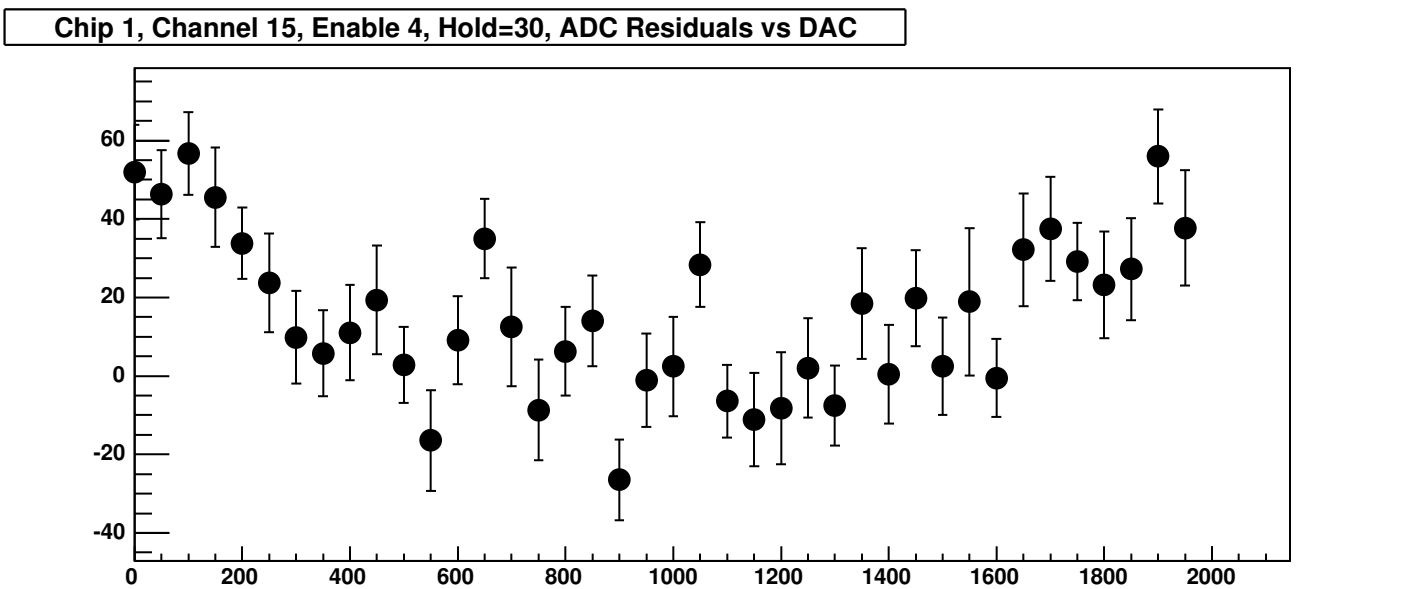

Chip 1, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 17.03 / 11
p0 -1725 ± 20.68
p1 0.007761 ± 0.01831
-1640
-1660
-1680
-1700
-1720
-1740
-1760
0
200
400
600
800
1000
1200
1400
1600
1800
2000

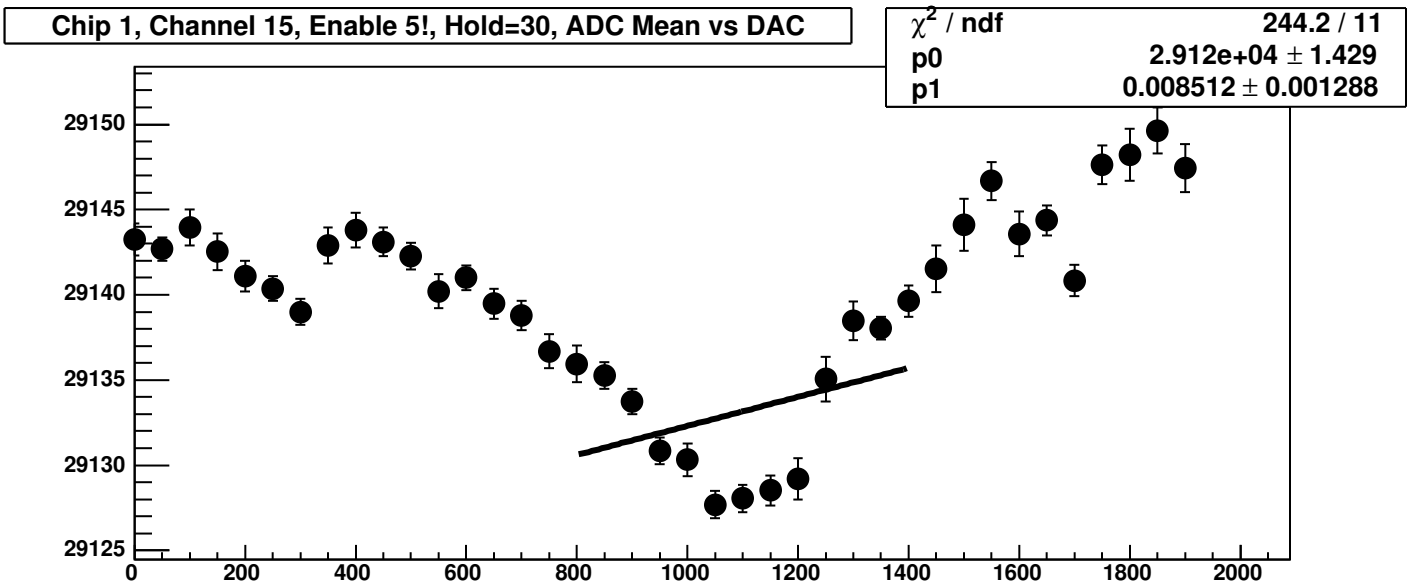

Chip 1, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

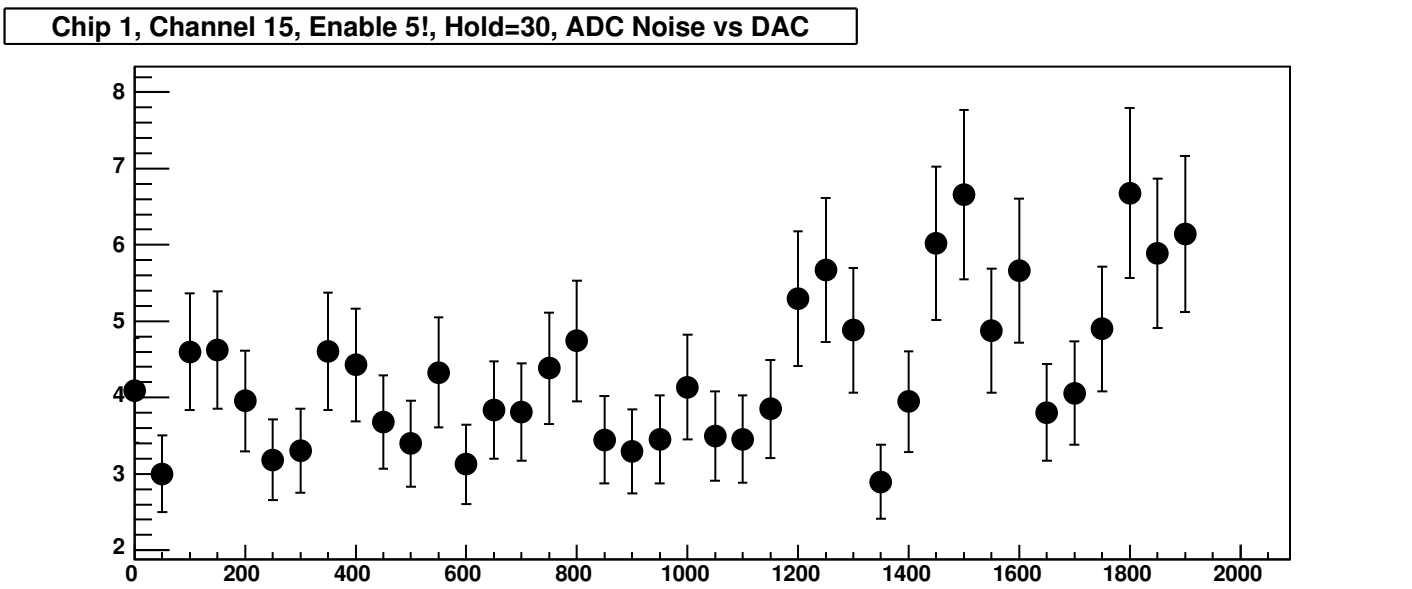

Chip 1, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

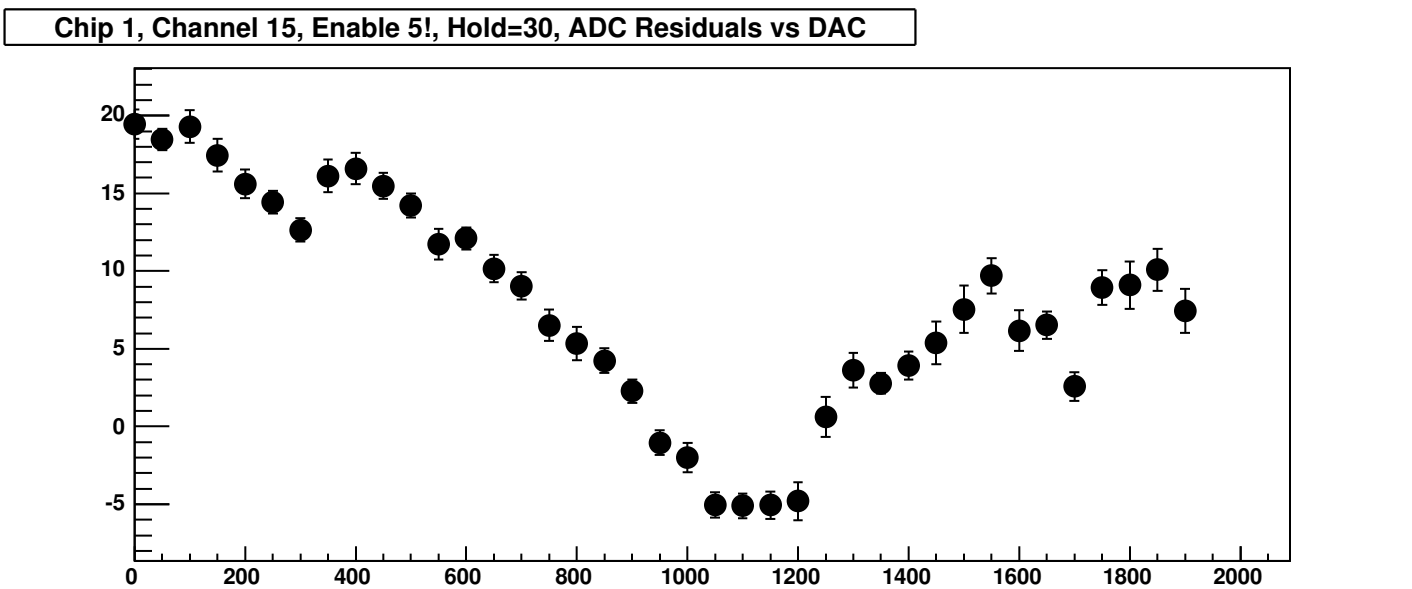

Chip 1, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 19.34 / 11
p0 -1743 ± 19.3
p1 0.07724 ± 0.01753
-1550
-1600
-1650
-1700
-1750
0
200
400
600
800
1000
1200
1400
1600
1800
2000

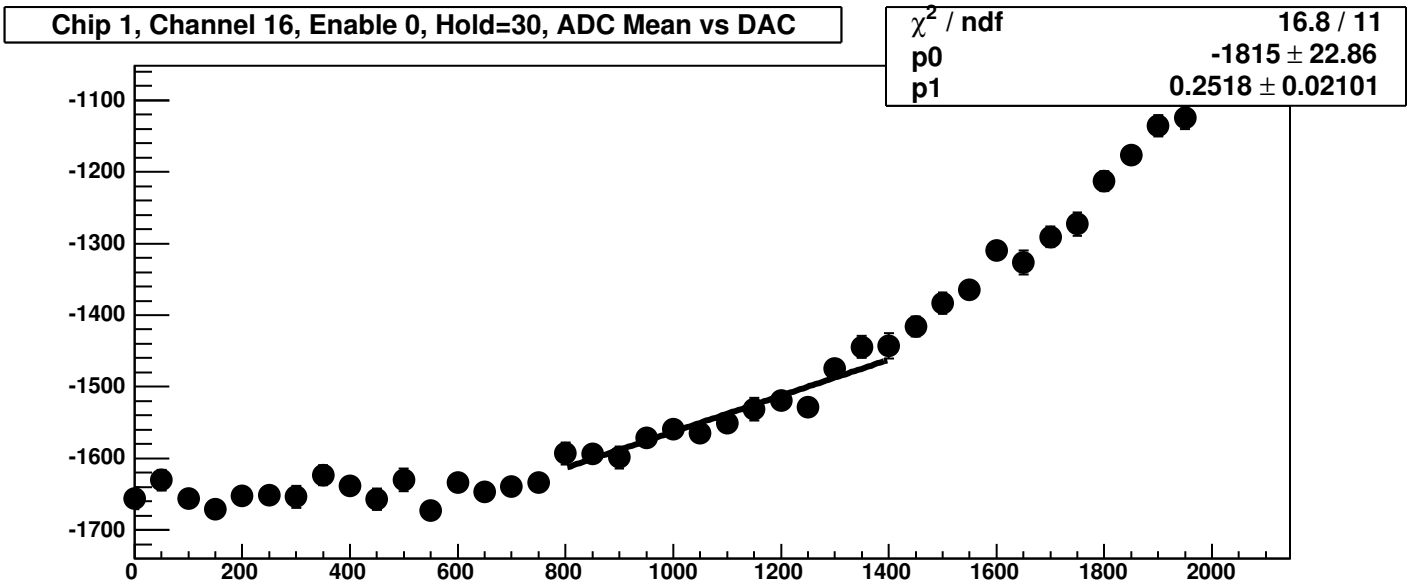

Chip 1, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

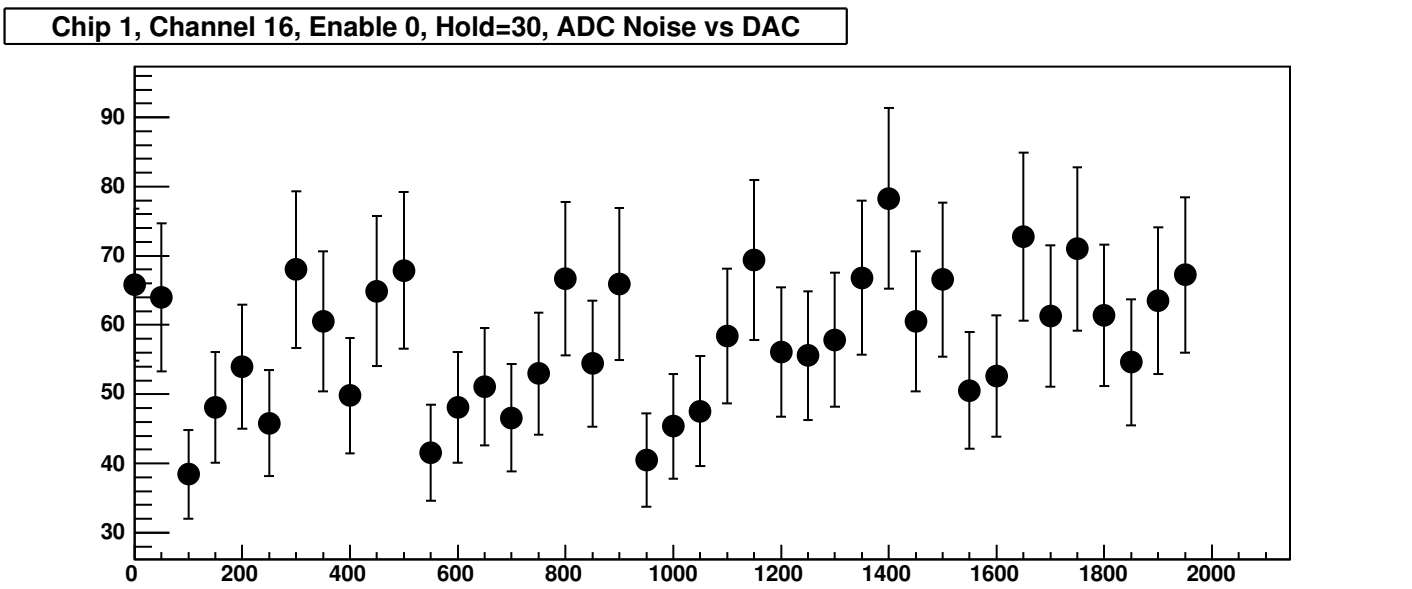

Chip 1, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

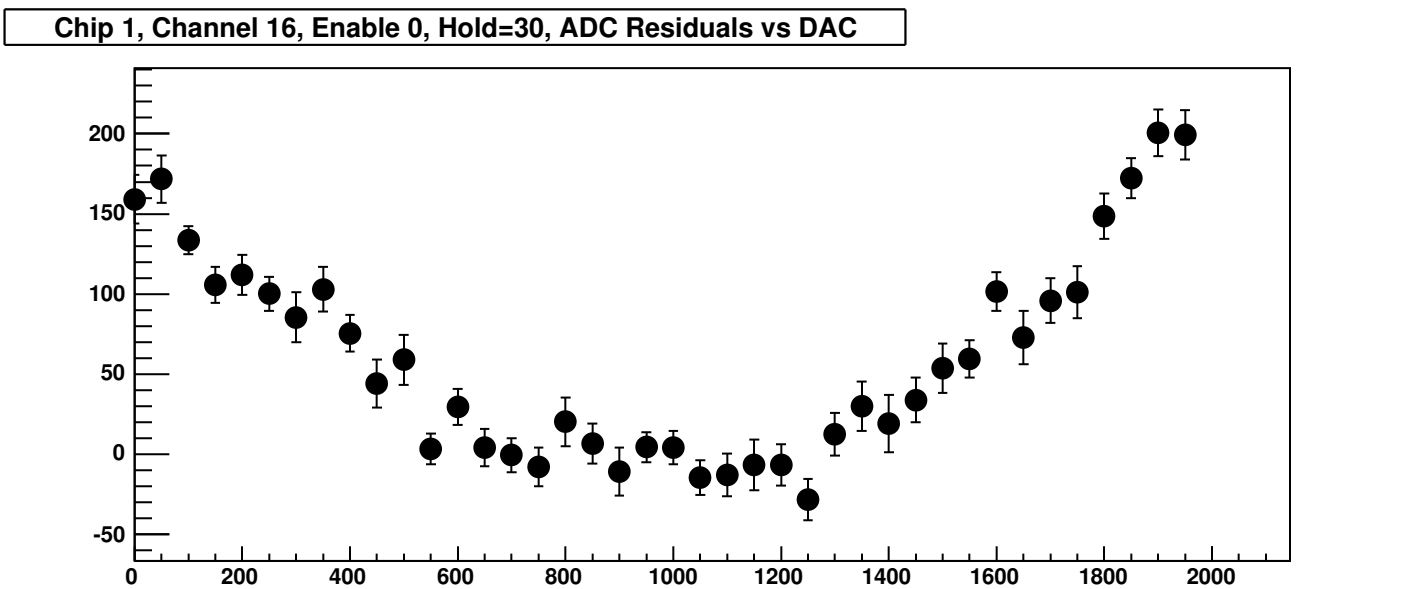

Chip 1, Channel 15, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 244.2 / 11
p0 2.912e+04 ± 1.429
p1 0.008512 ± 0.001288
29150
29145
29140
29135
29130
29125
0
200
400
600
800
1000
1200
1400
1600
1800
2000

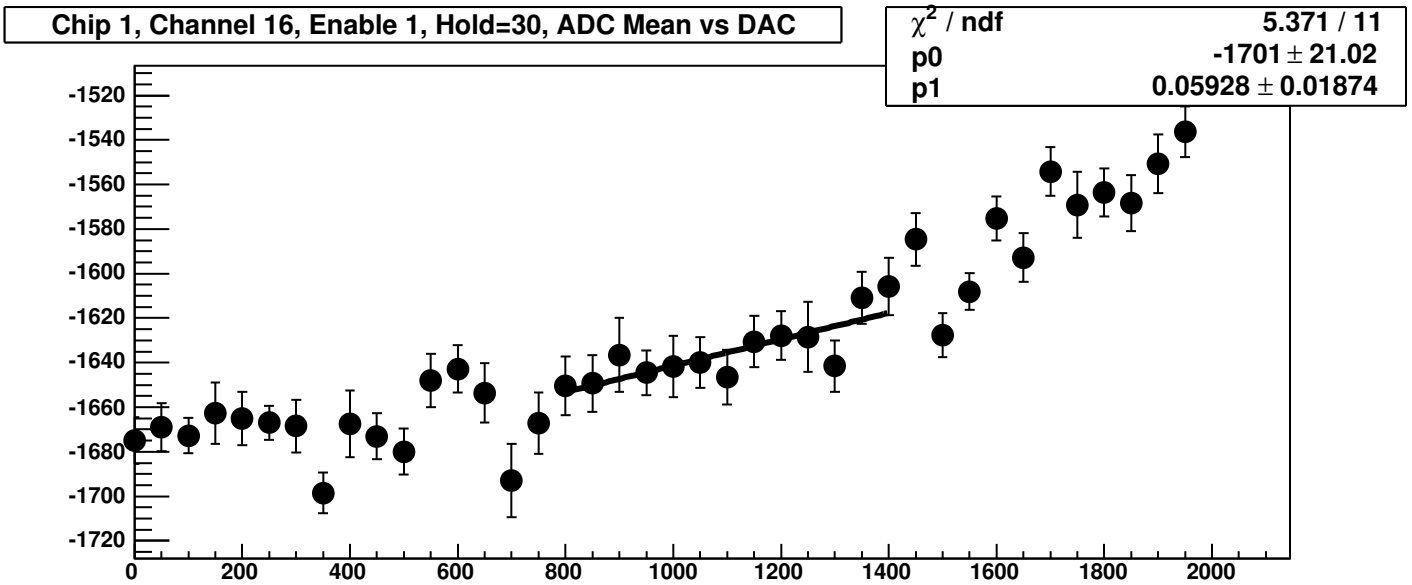

Chip 1, Channel 15, Enable 5!, Hold=30, ADC Noise vs DAC
8
7
6
5
4
3
2
0
200
400
600
800
1000
1200
1400
1600
1800
2000

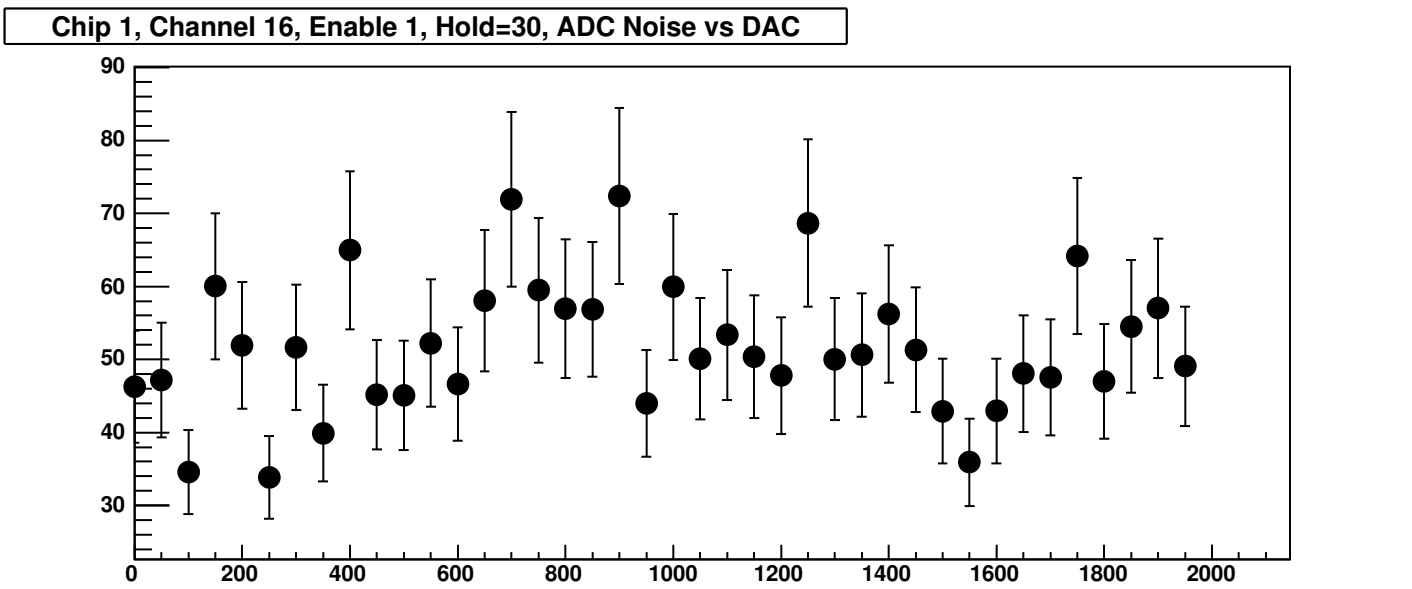

Chip 1, Channel 15, Enable 5!, Hold=30, ADC Residuals vs DAC
20
15
10
5
0
-5
0
200
400
600
800
1000
1200
1400
1600
1800
2000

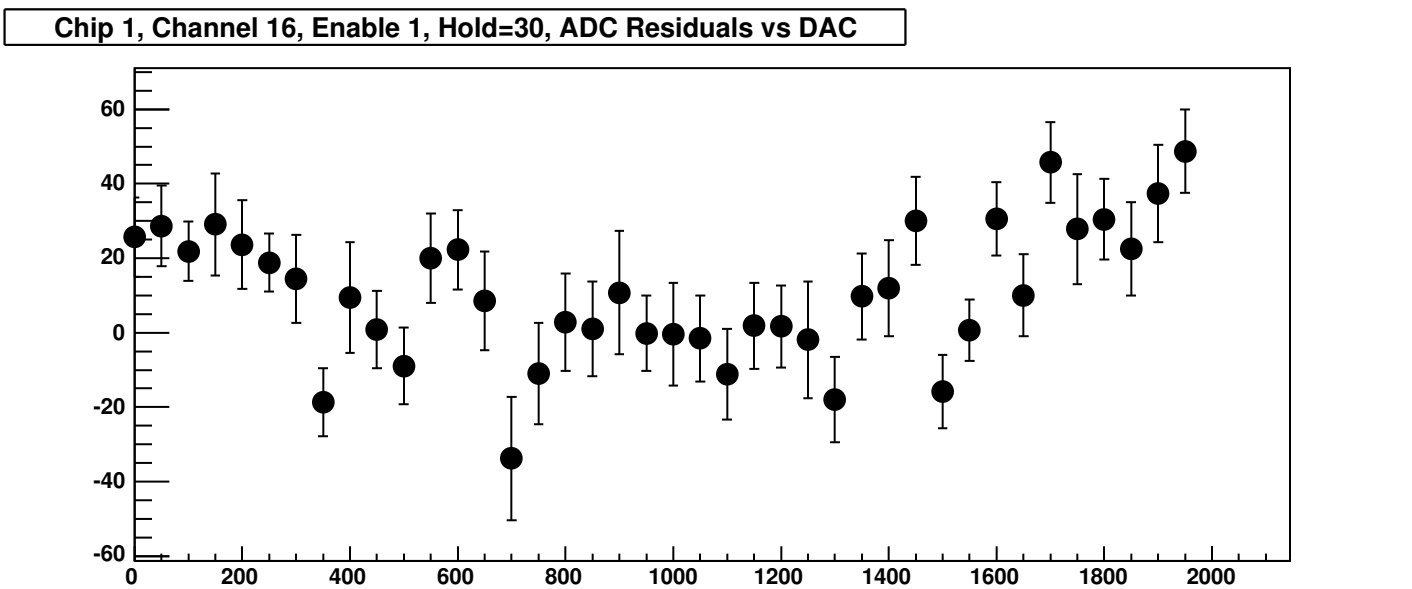

Chip 1, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 16.8 / 11
p0 -1815 ± 22.86
p1 0.2518 ± 0.02101
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

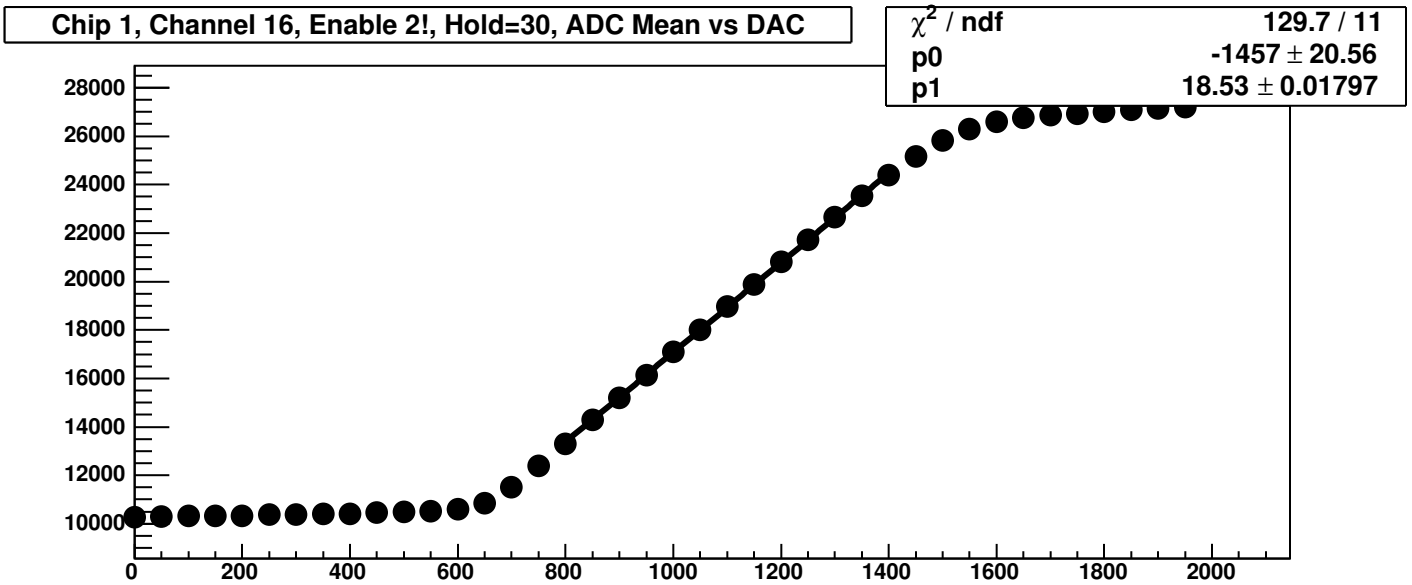

Chip 1, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

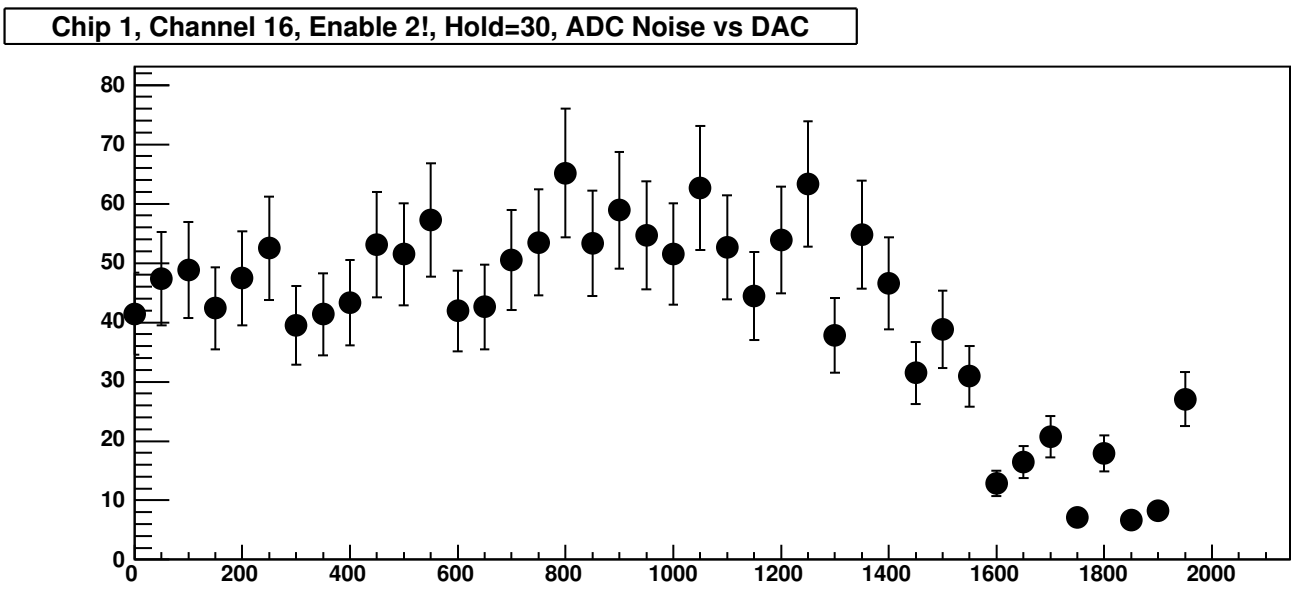

Chip 1, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

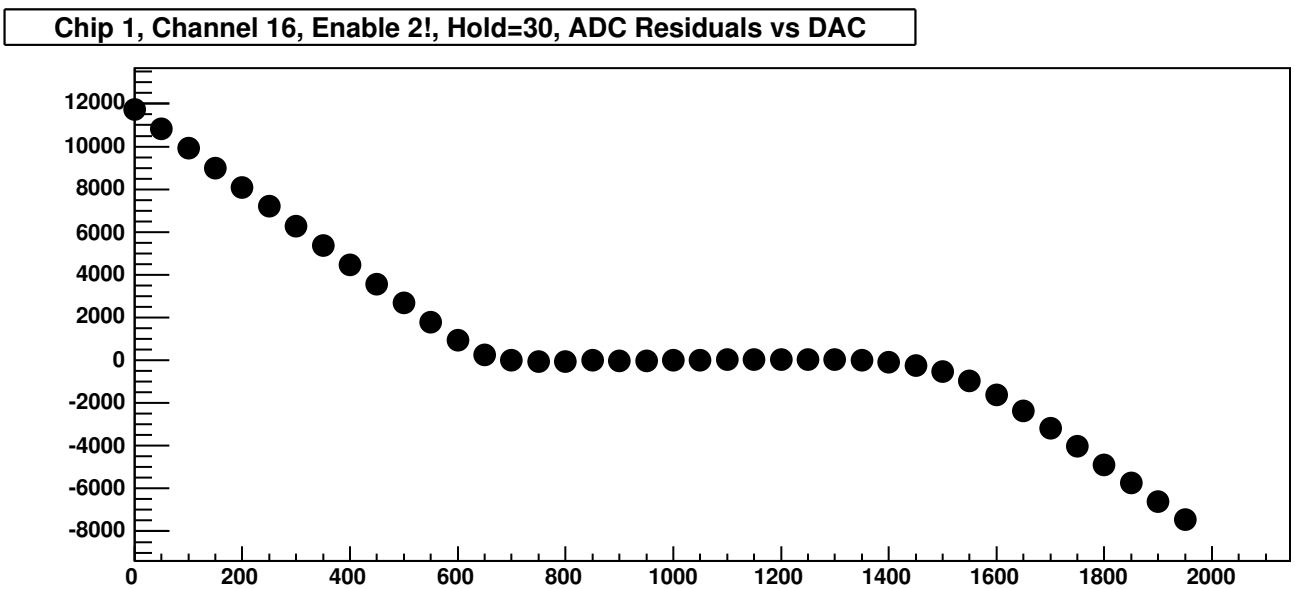

Chip 1, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 5.371 / 11
p0 -1701 ± 21.02
p1 0.05928 ± 0.01874
-1520
-1540
-1560
-1580
-1600
-1620
-1640
-1660
-1680
-1700
-1720
0
200
400
600
800
1000
1200
1400
1600
1800
2000

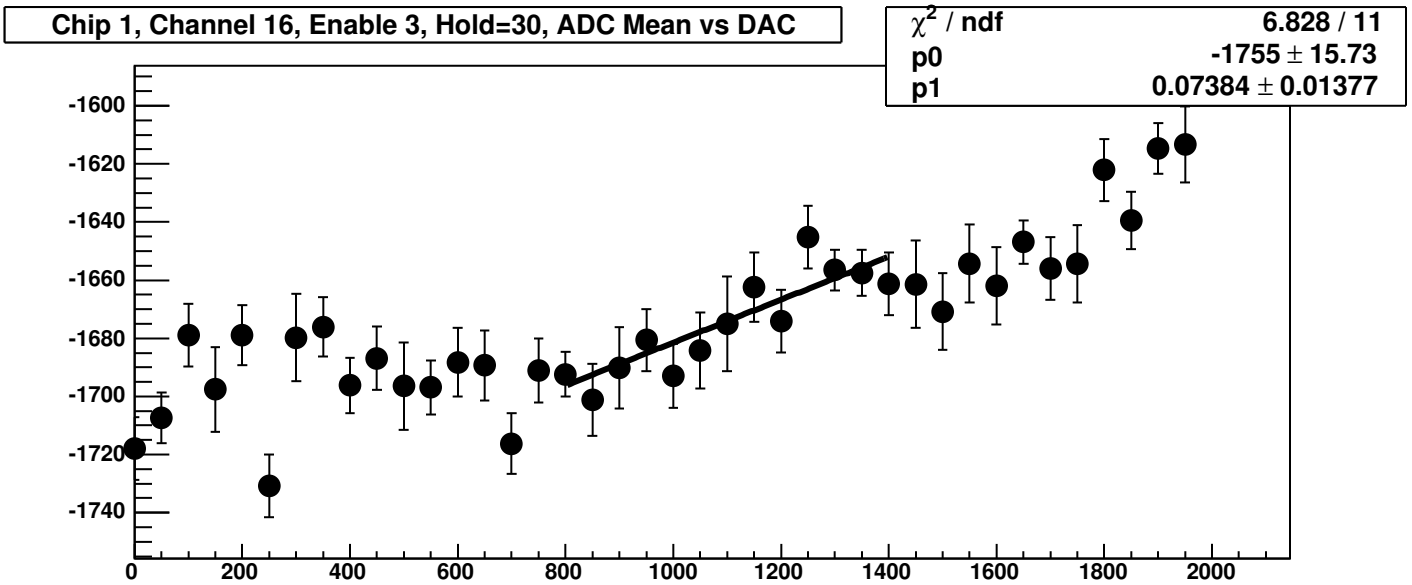

Chip 1, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

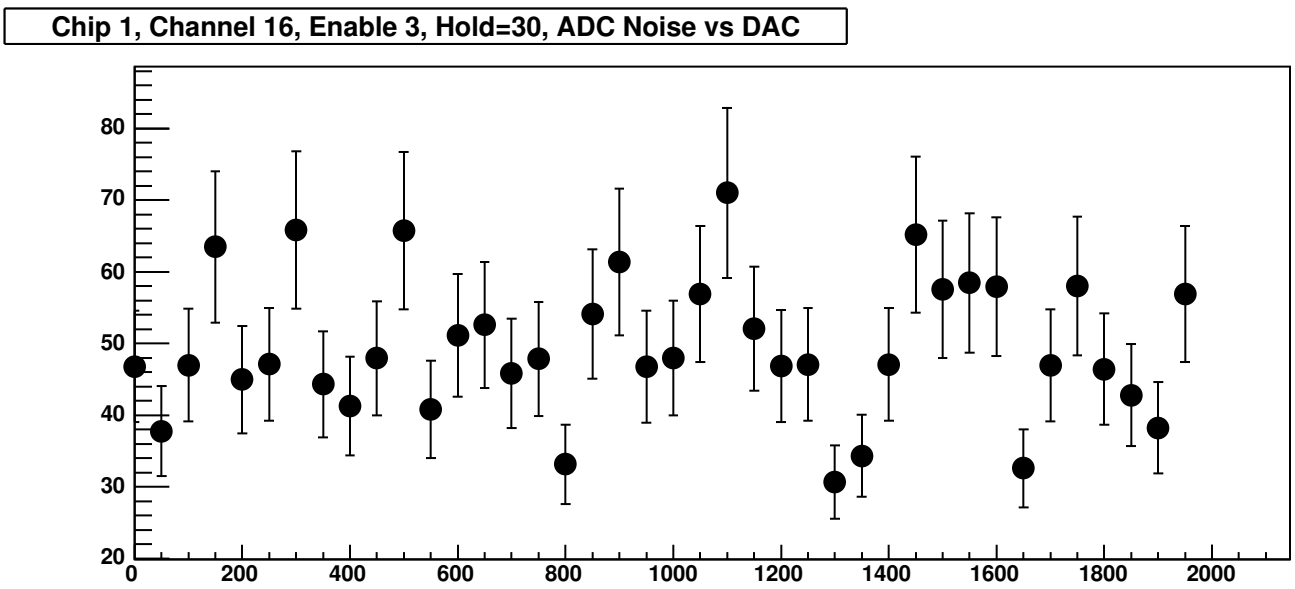

Chip 1, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

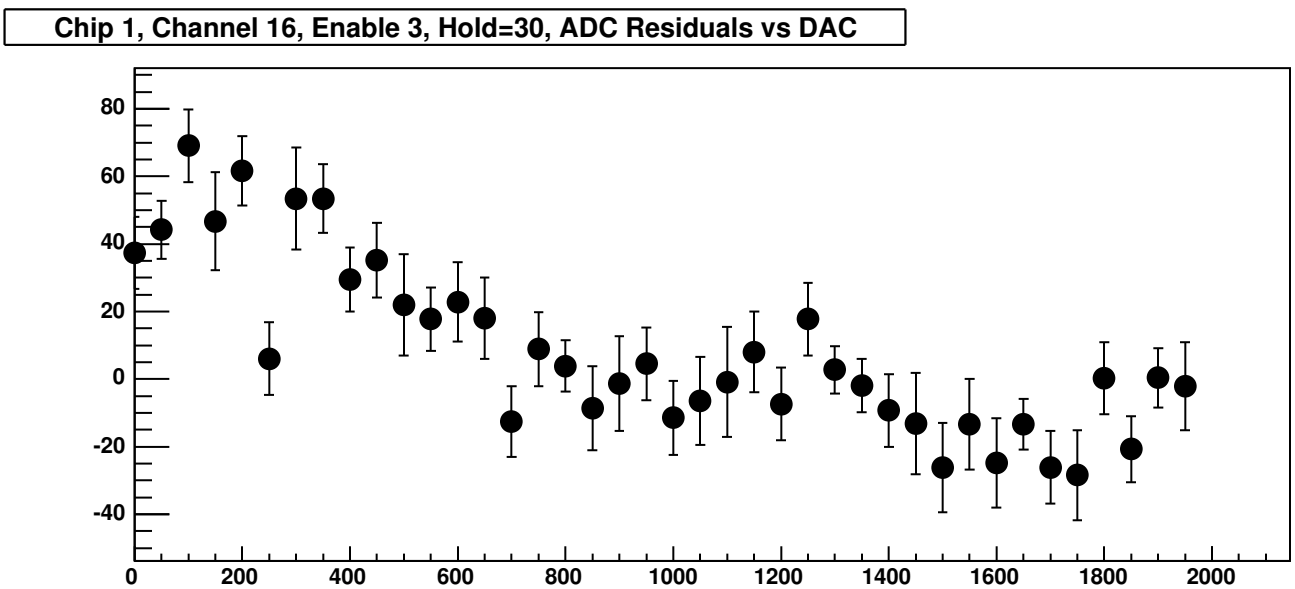

Chip 1, Channel 16, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 129.7 / 11
p0 -1457 ± 20.56
p1 18.53 ± 0.01797
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

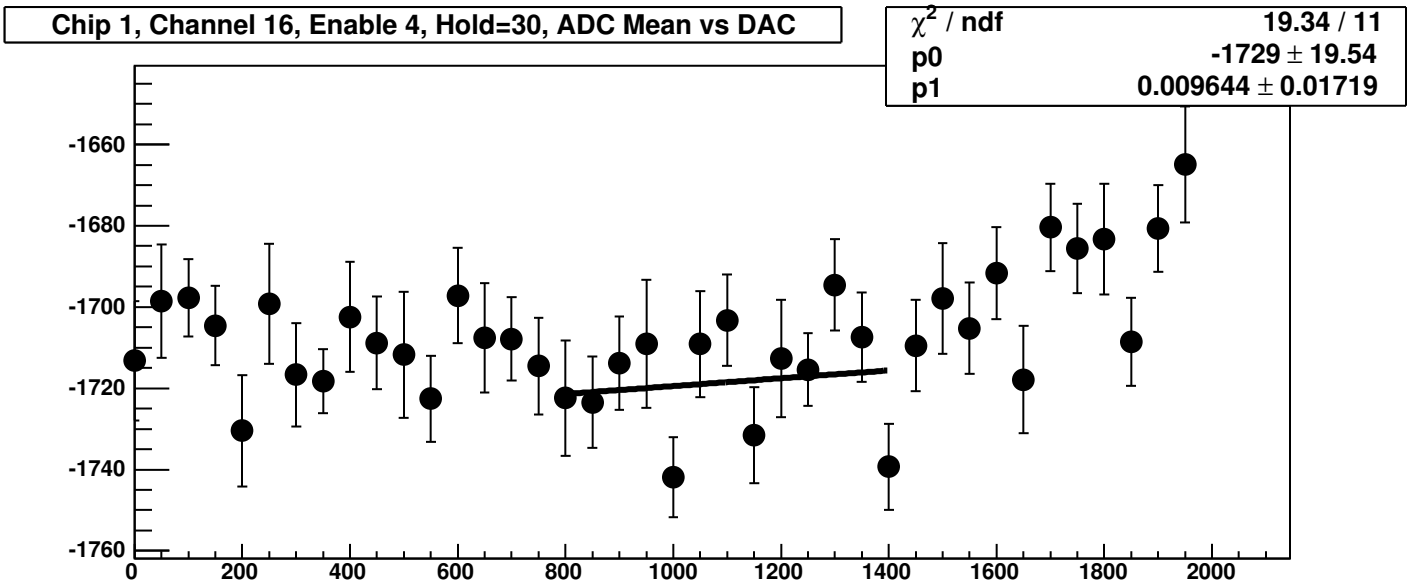

Chip 1, Channel 16, Enable 2!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

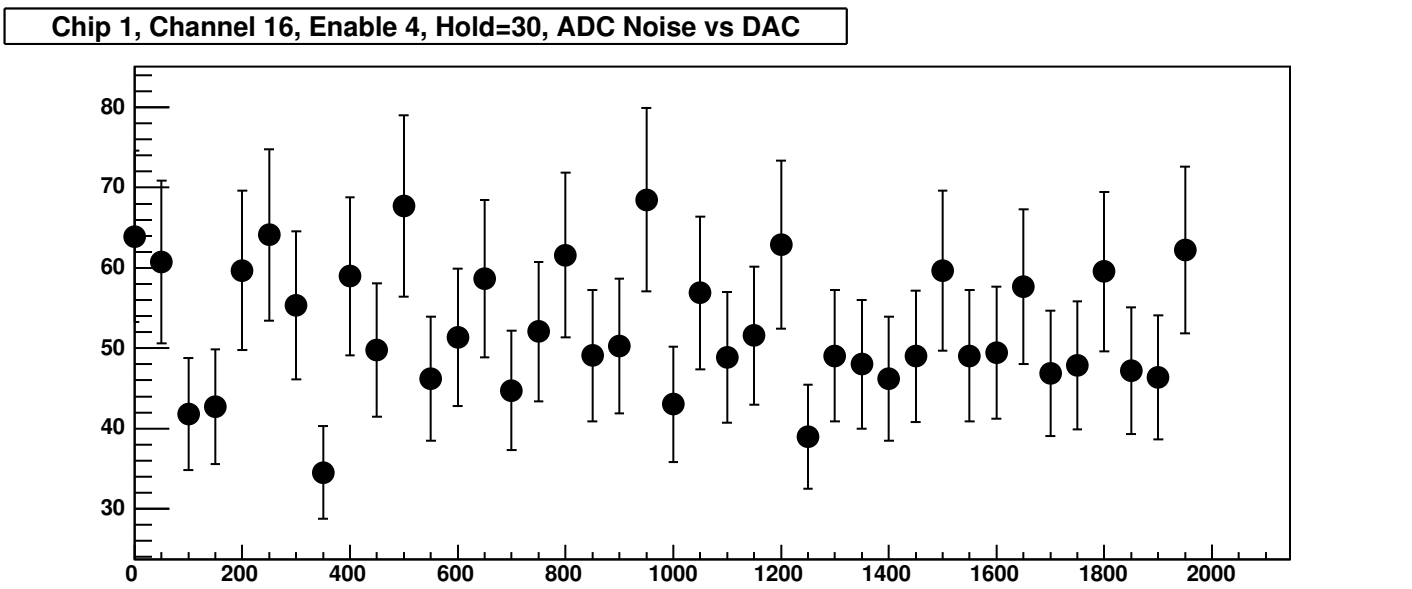

Chip 1, Channel 16, Enable 2!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

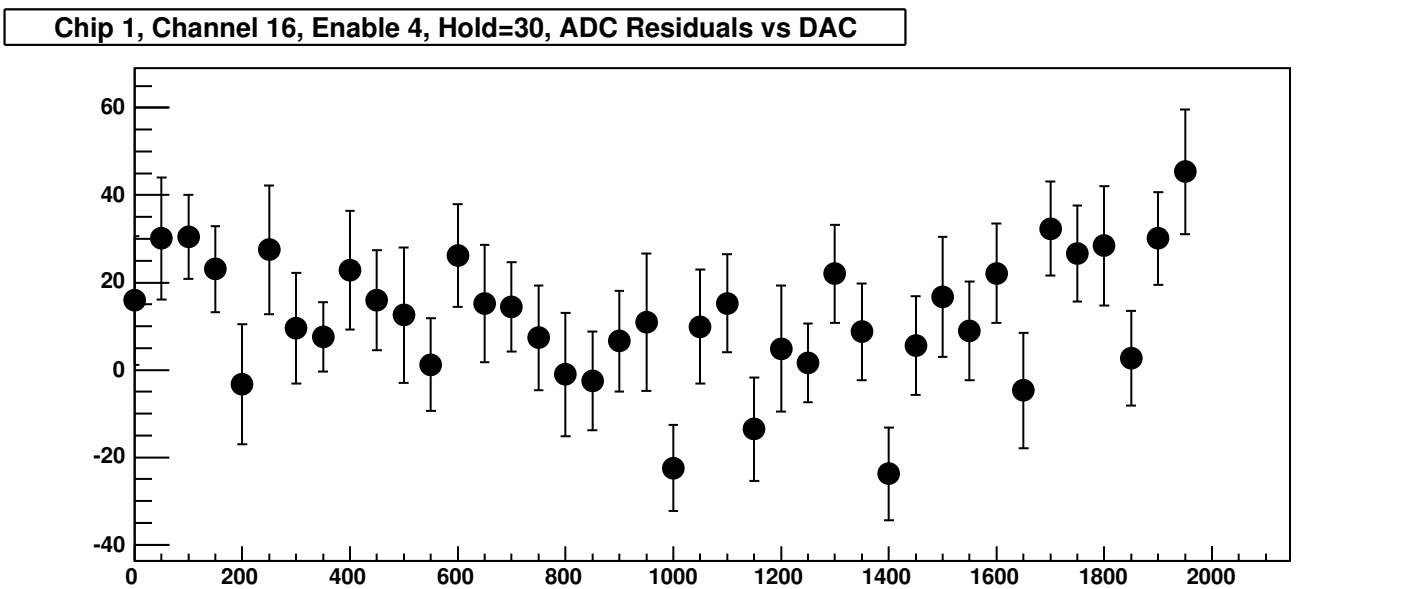

Chip 1, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 6.828 / 11
p0 -1755 ± 15.73
p1 0.07384 ± 0.01377
-1600
-1620
-1640
-1660
-1680
-1700
-1720
-1740
0
200
400
600
800
1000
1200
1400
1600
1800
2000

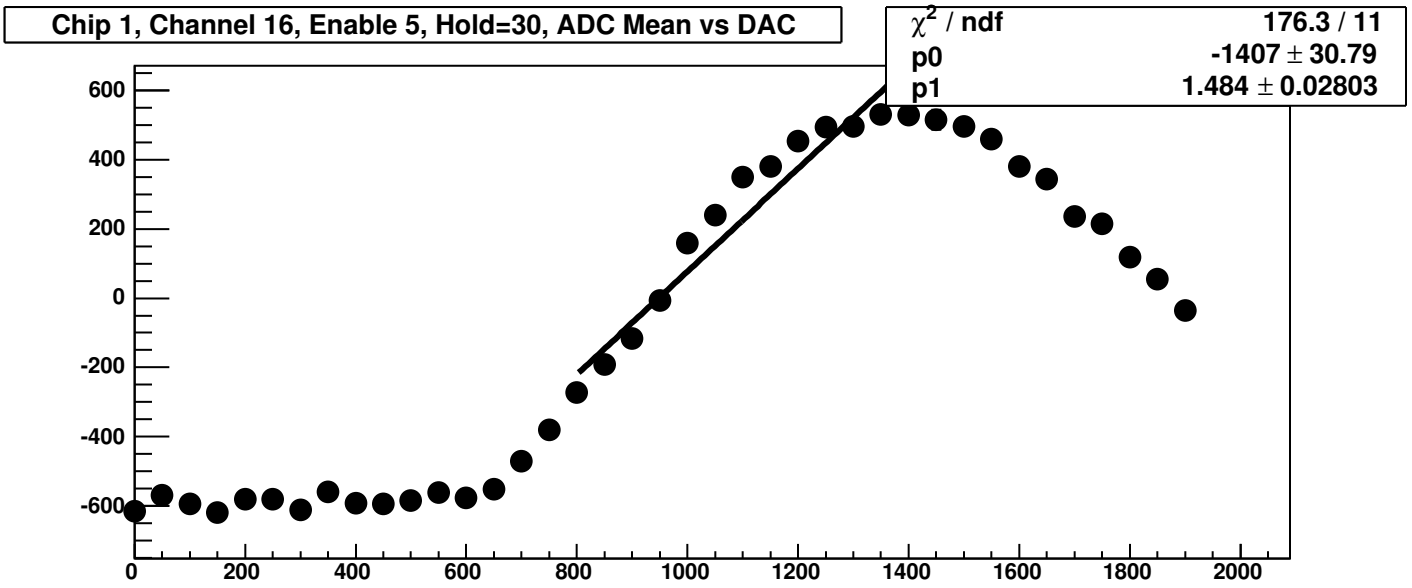

Chip 1, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

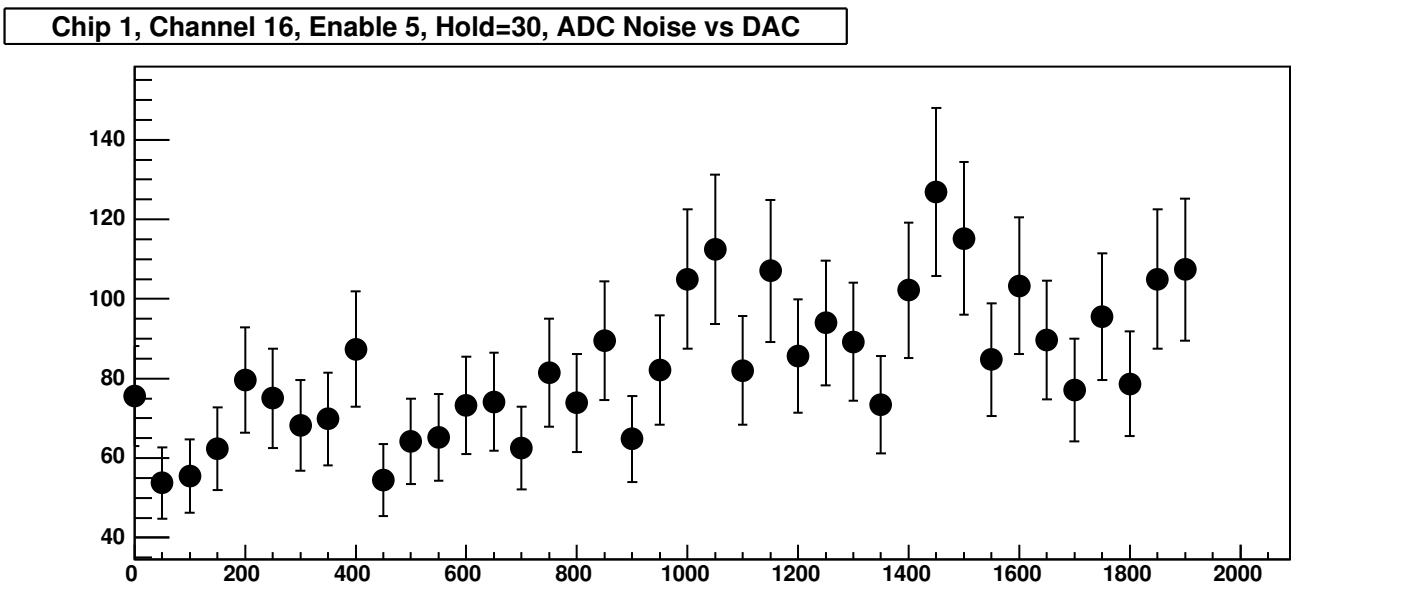

Chip 1, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

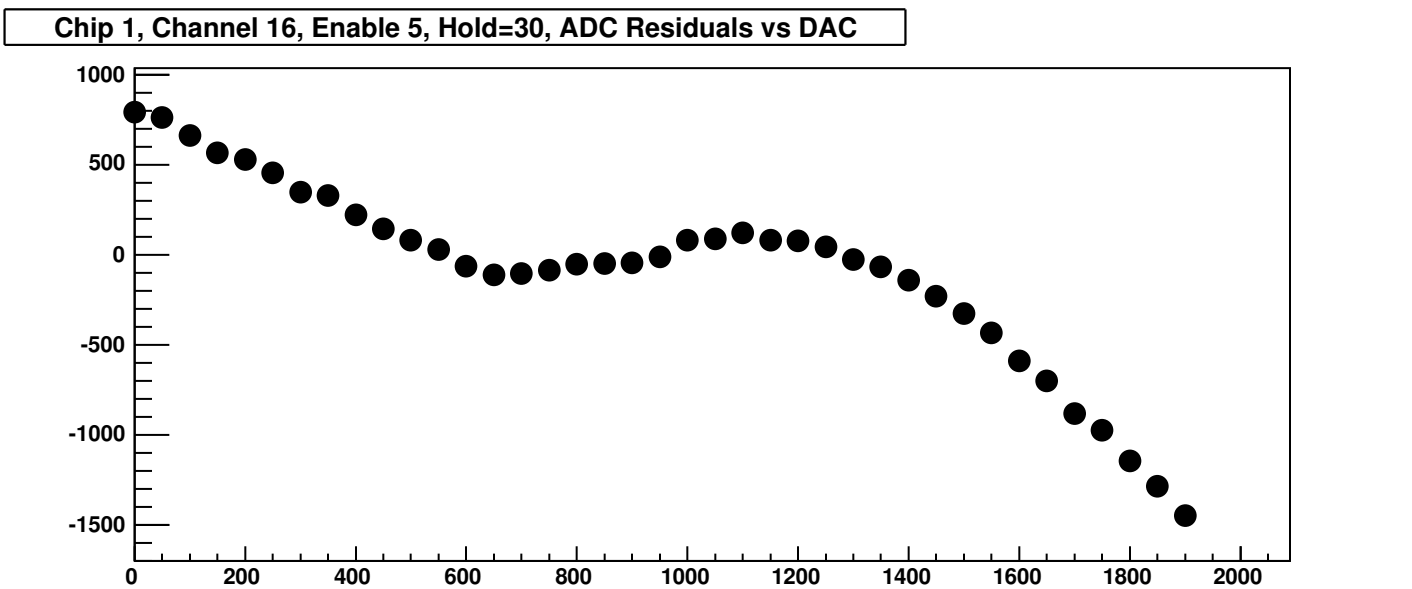

Chip 1, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 19.34 / 11
p0 -1729 ± 19.54
p1 0.009644 ± 0.01719
-1660
-1680
-1700
-1720
-1740
-1760
0
200
400
600
800
1000
1200
1400
1600
1800
2000

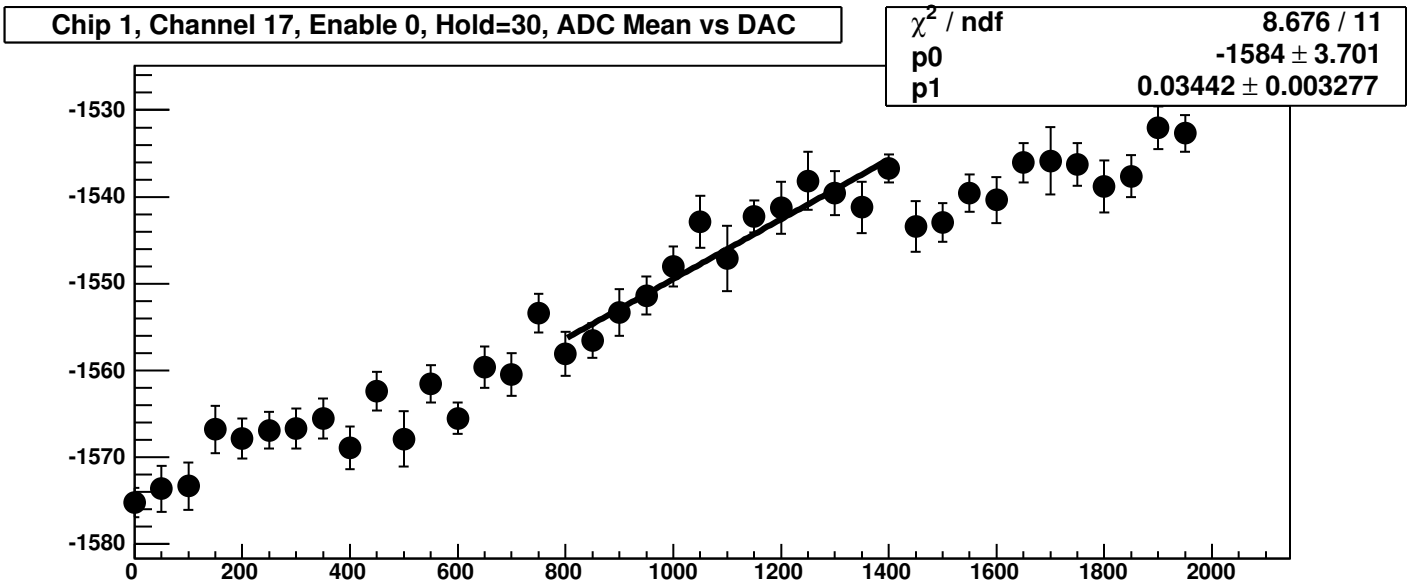

Chip 1, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

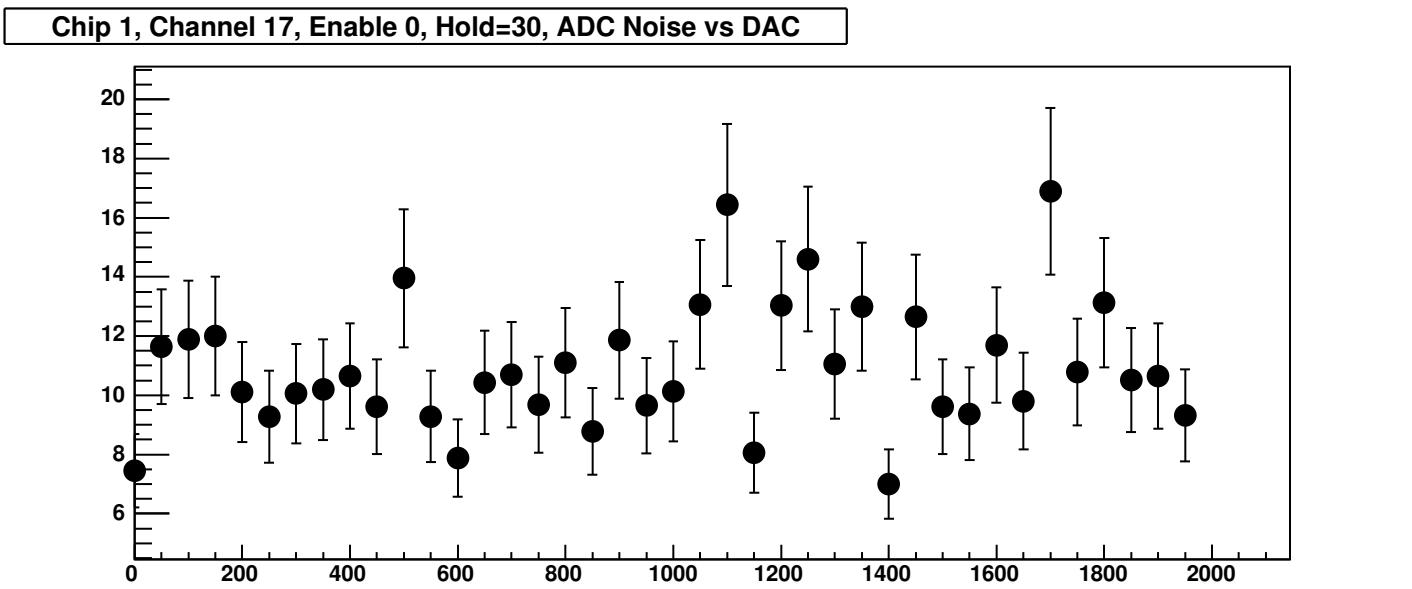

Chip 1, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

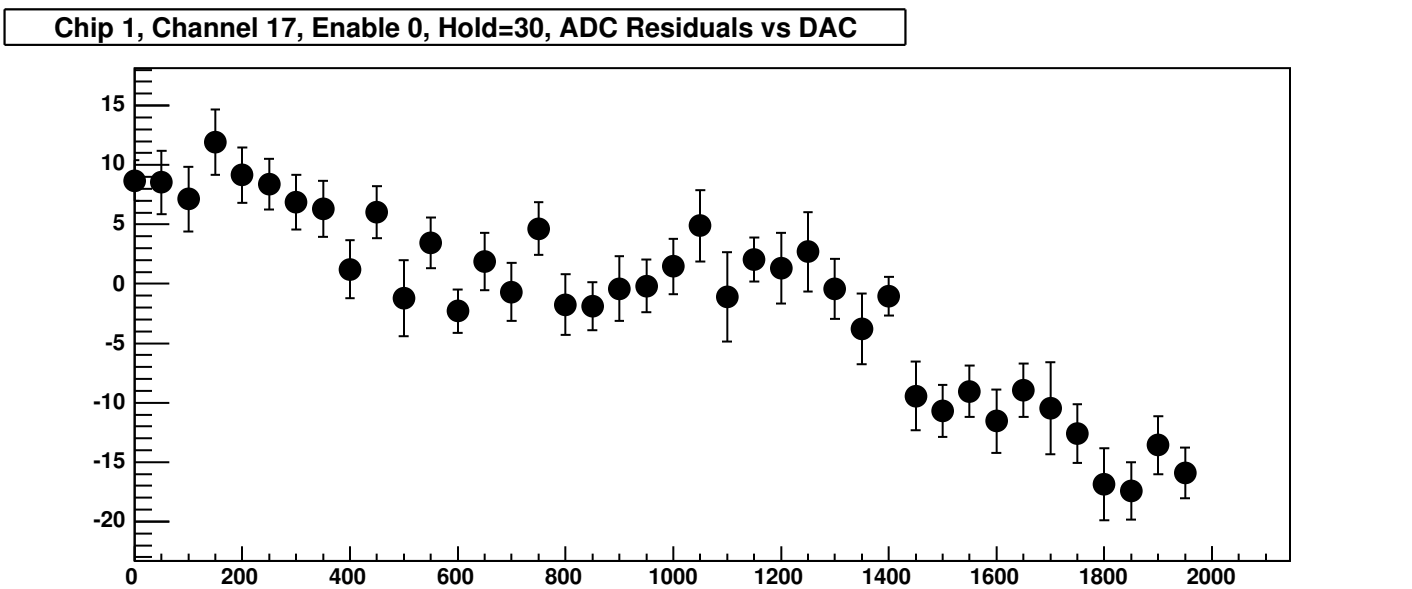

Chip 1, Channel 16, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 176.3 / 11
p0 -1407 ± 30.79
p1 1.484 ± 0.02803
600
400
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

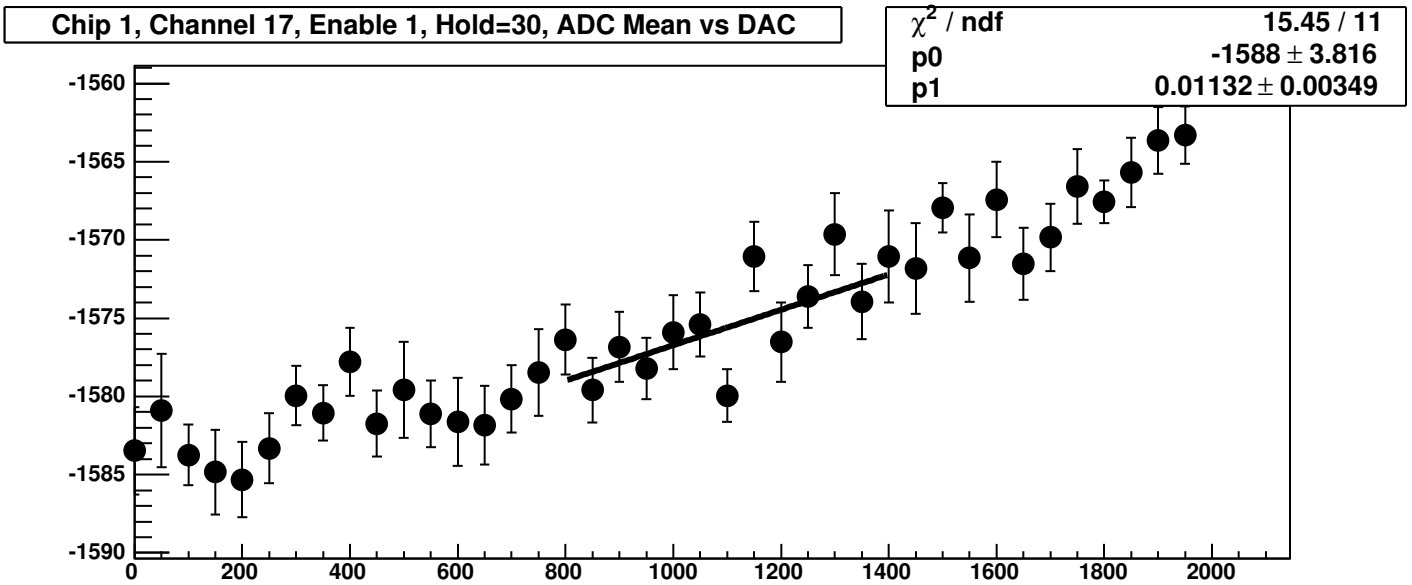

Chip 1, Channel 16, Enable 5, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

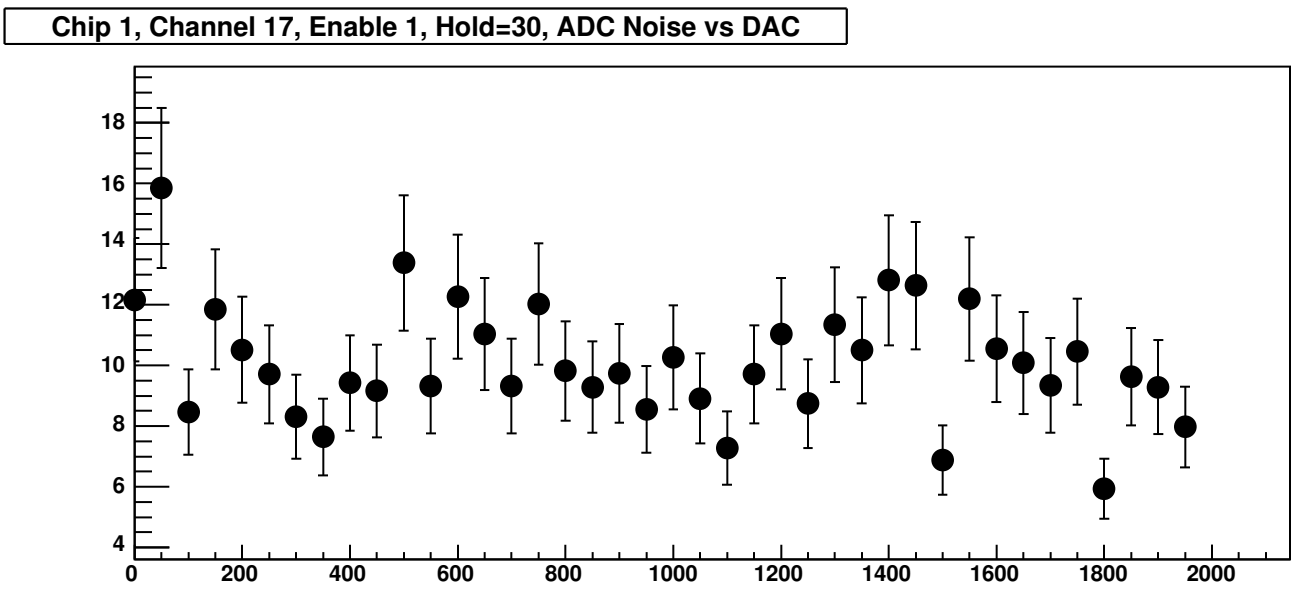

Chip 1, Channel 16, Enable 5, Hold=30, ADC Residuals vs DAC
1000
500
0
-500
-1000
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

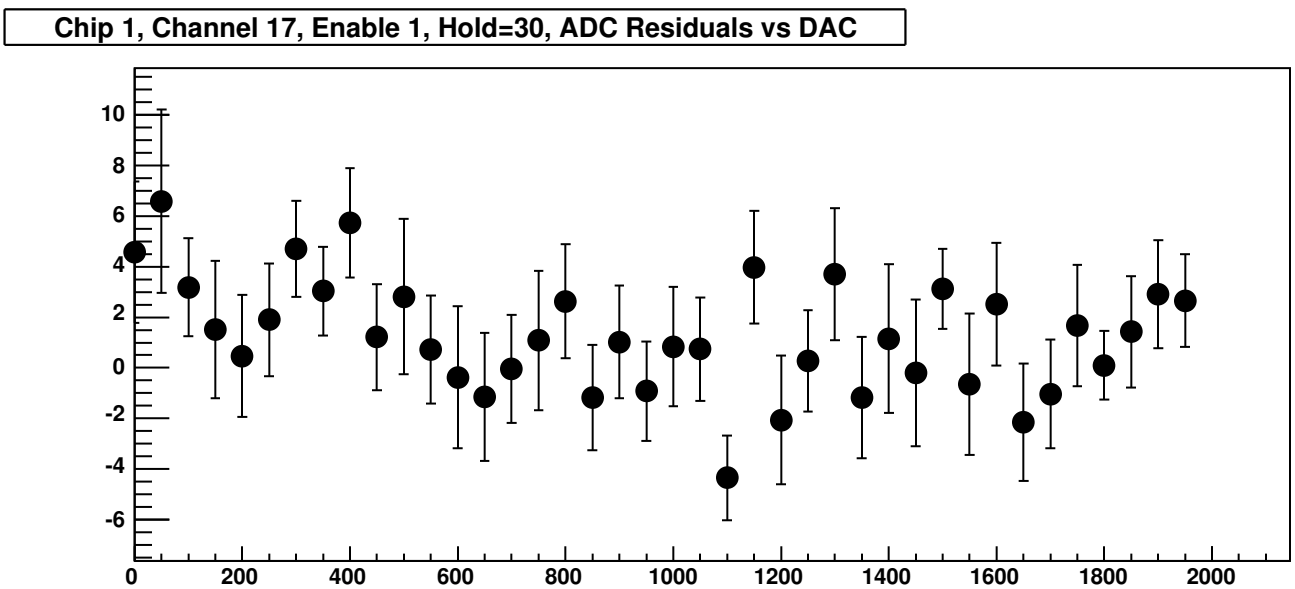

Chip 1, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 8.676 / 11
p0 -1584 ± 3.701
p1 0.03442 ± 0.003277
-1530
-1540
-1550
-1560
-1570
-1580
0
200
400
600
800
1000
1200
1400
1600
1800
2000

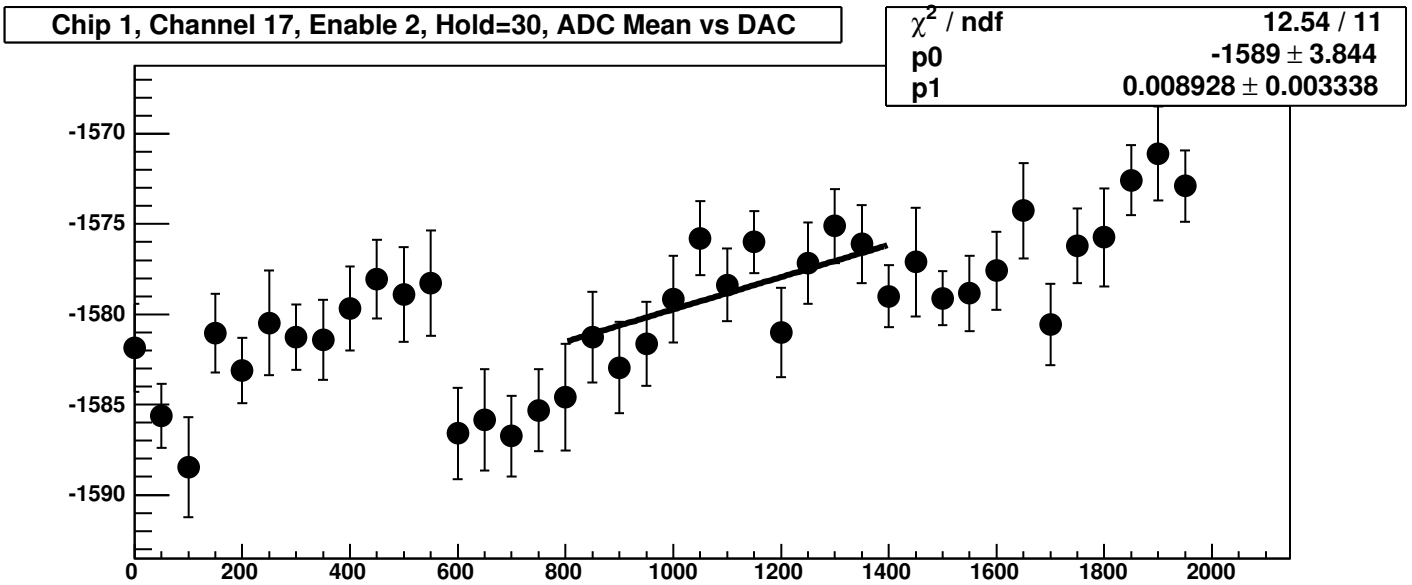

Chip 1, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
20
18
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

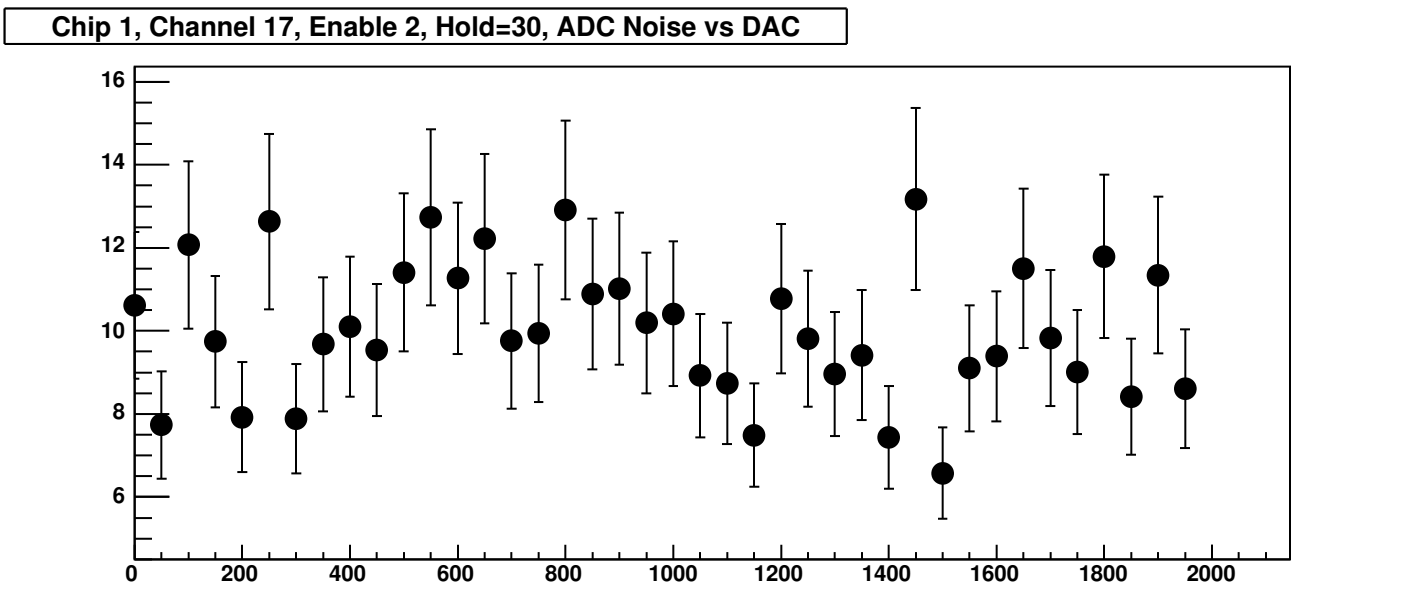

Chip 1, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

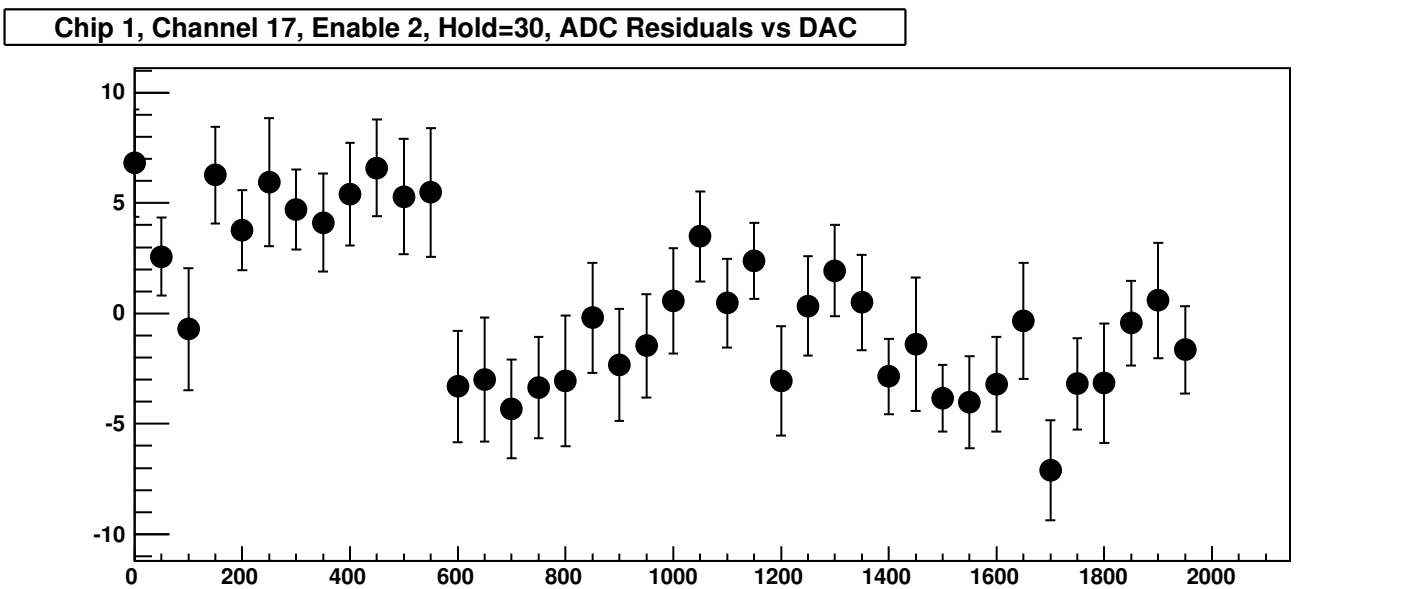

Chip 1, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 15.45 / 11
p0 -1588 ± 3.816
p1 0.01132 ± 0.00349
-1560
-1565
-1570
-1575
-1580
-1585
-1590
0
200
400
600
800
1000
1200
1400
1600
1800
2000

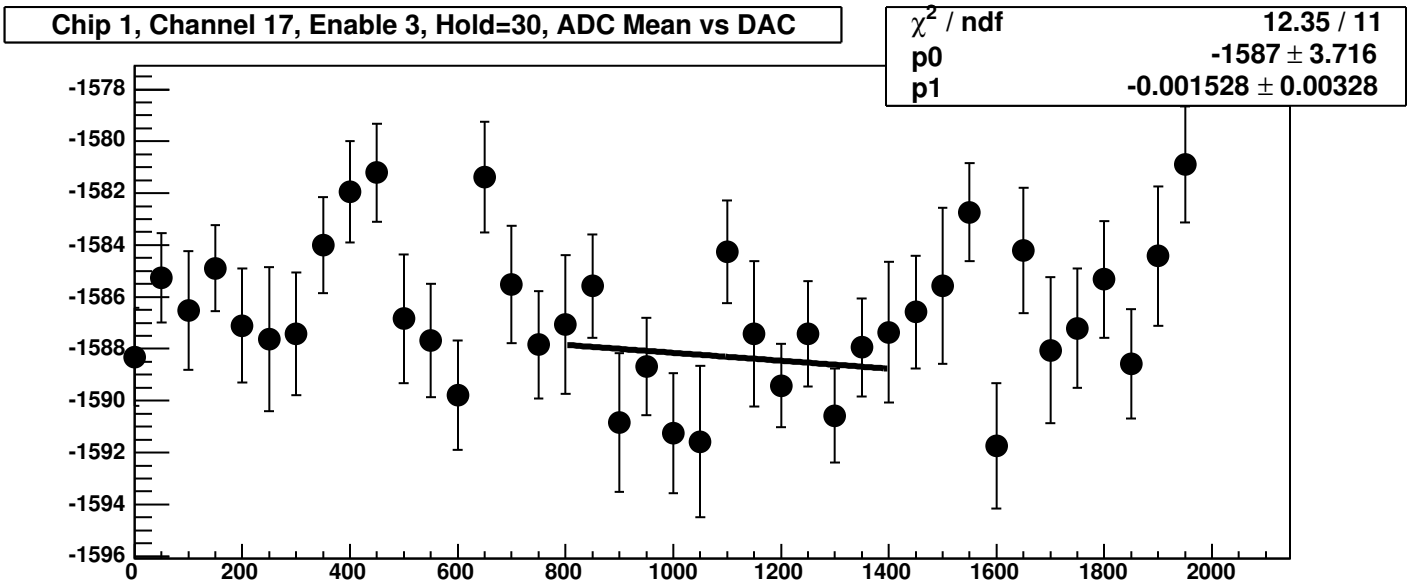

Chip 1, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
200
400
600
800
1000
1200
1400
1600
1800
2000

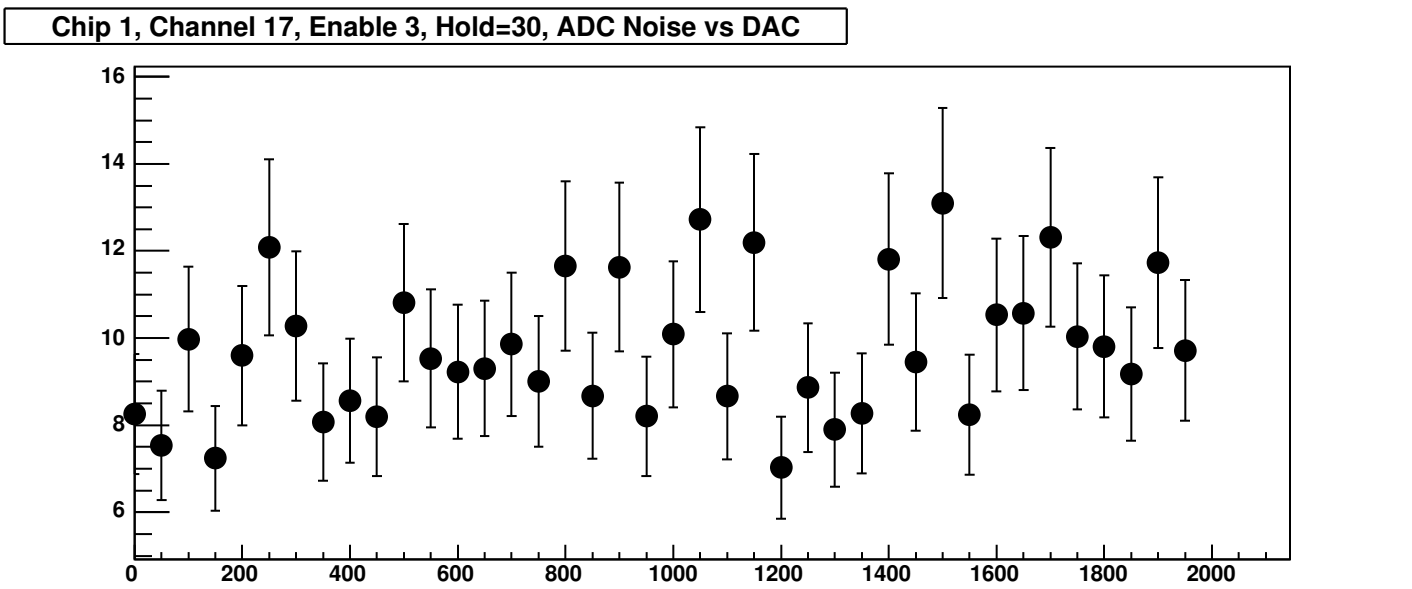

Chip 1, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

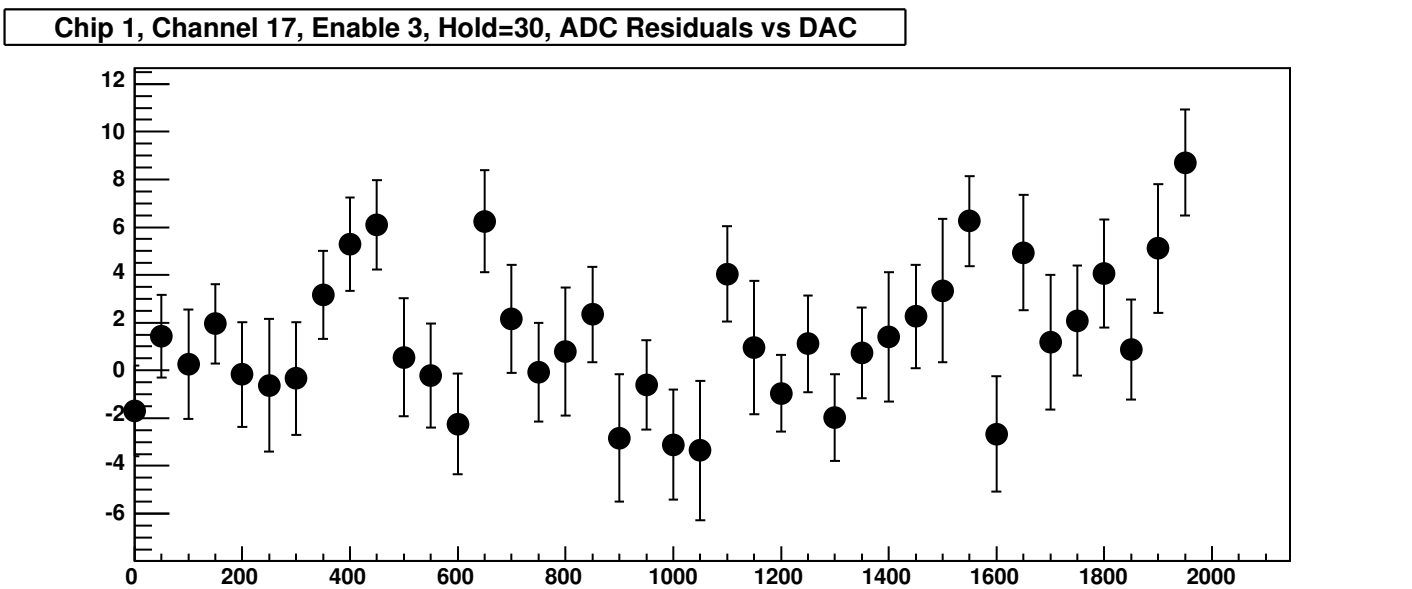

Chip 1, Channel 17, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 12.54 / 11
p0 -1589 ± 3.844
p1 0.008928 ± 0.003338
-1570
-1575
-1580
-1585
-1590
0
200
400
600
800
1000
1200
1400
1600
1800
2000

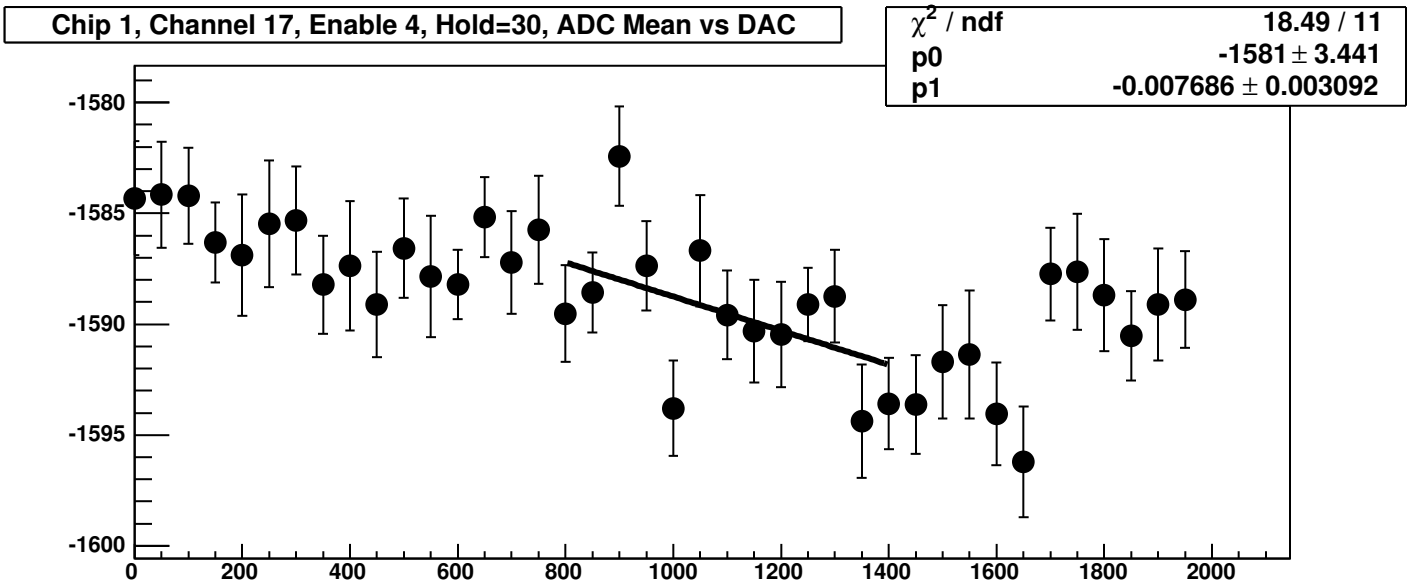

Chip 1, Channel 17, Enable 2, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

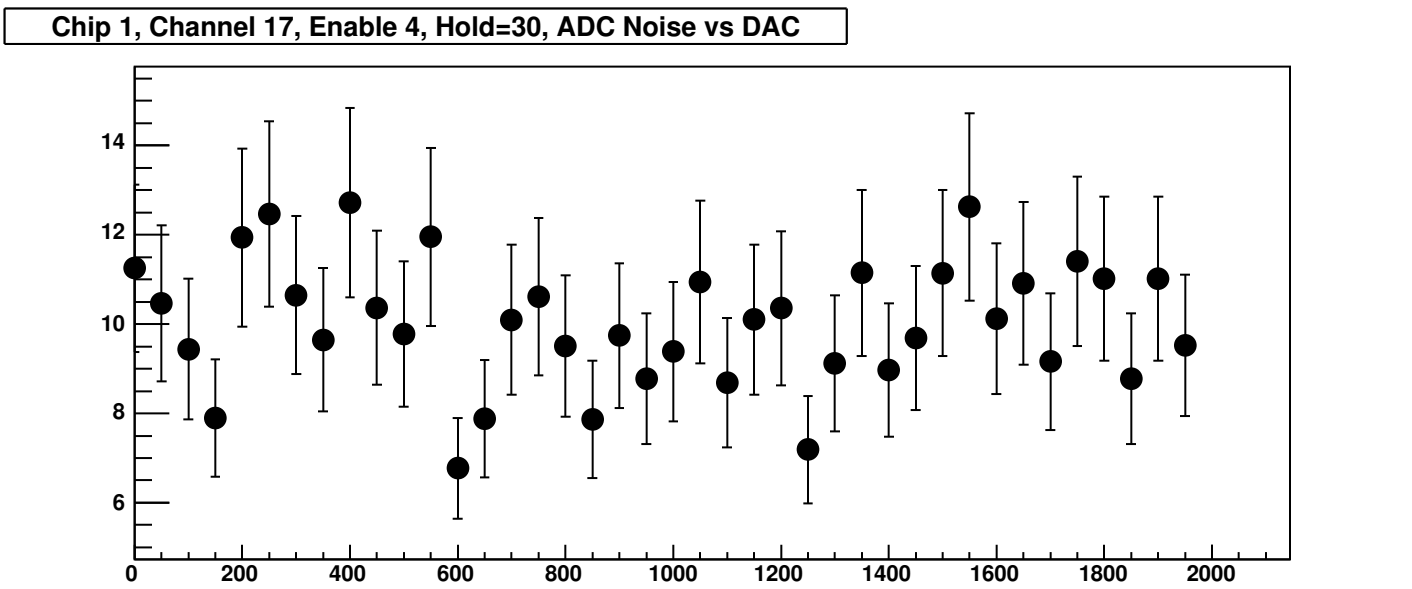

Chip 1, Channel 17, Enable 2, Hold=30, ADC Residuals vs DAC
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

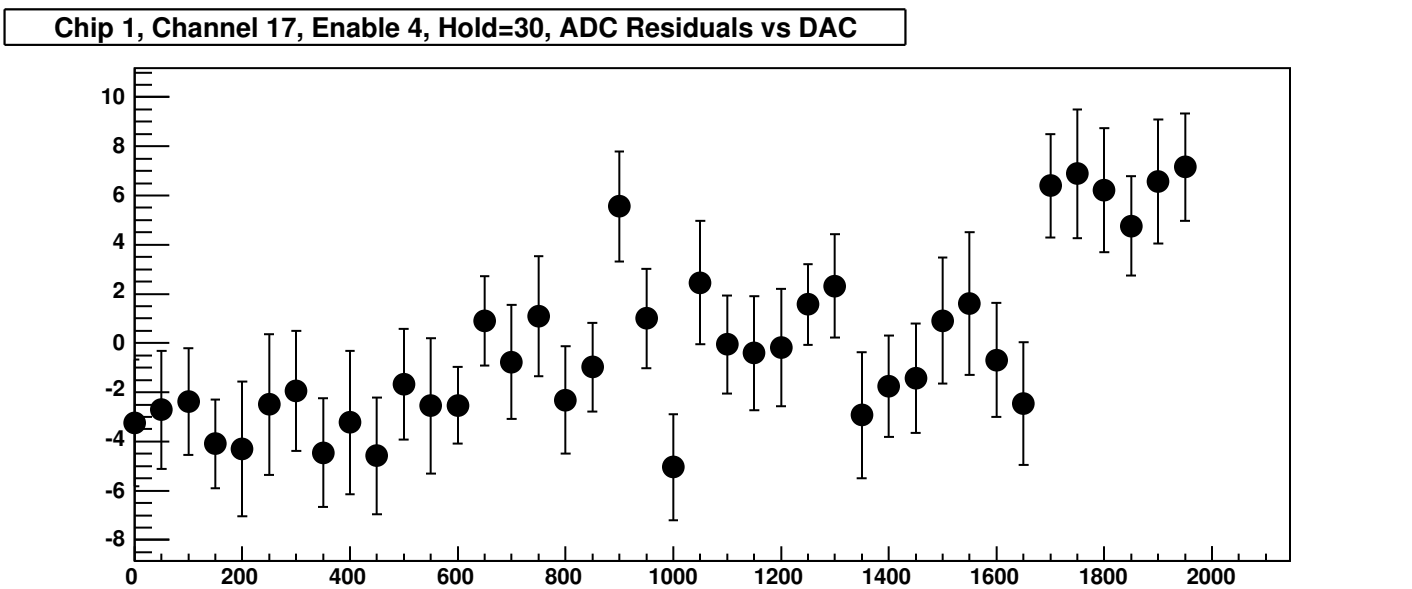

Chip 1, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 12.35 / 11
p0 -1587 ± 3.716
p1 -0.001528 ± 0.00328
-1578
-1580
-1582
-1584
-1586
-1588
-1590
-1592
-1594
-1596
0
200
400
600
800
1000
1200
1400
1600
1800
2000

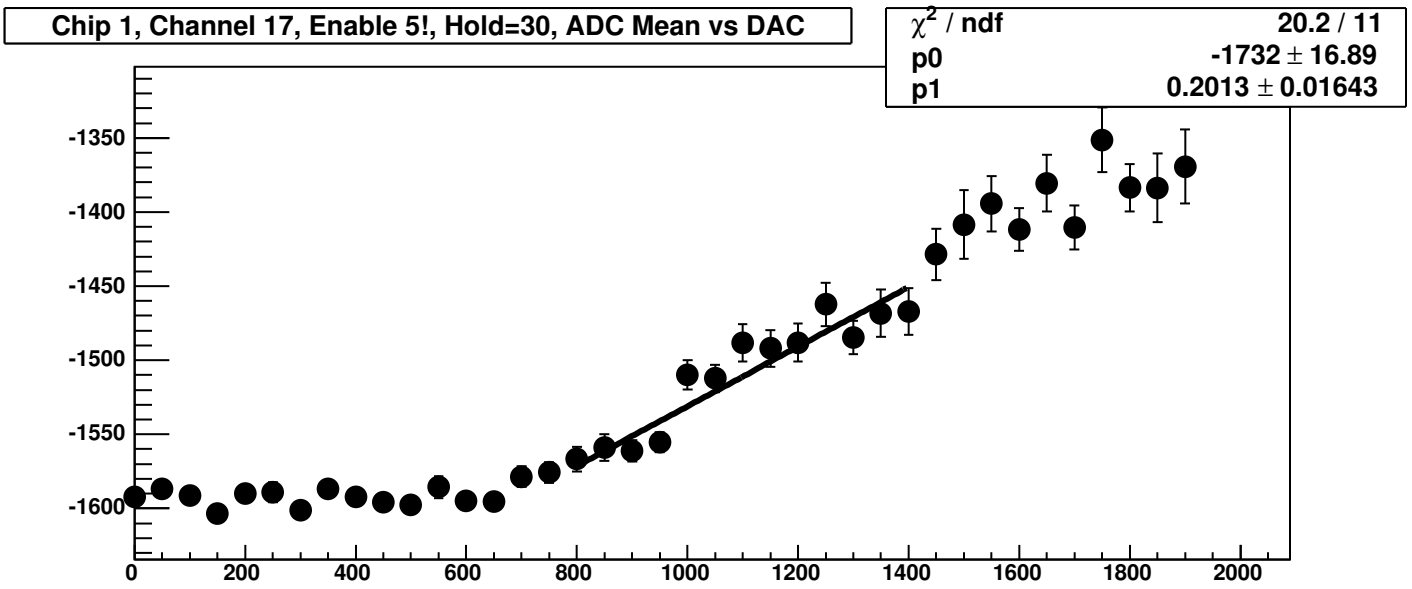

Chip 1, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

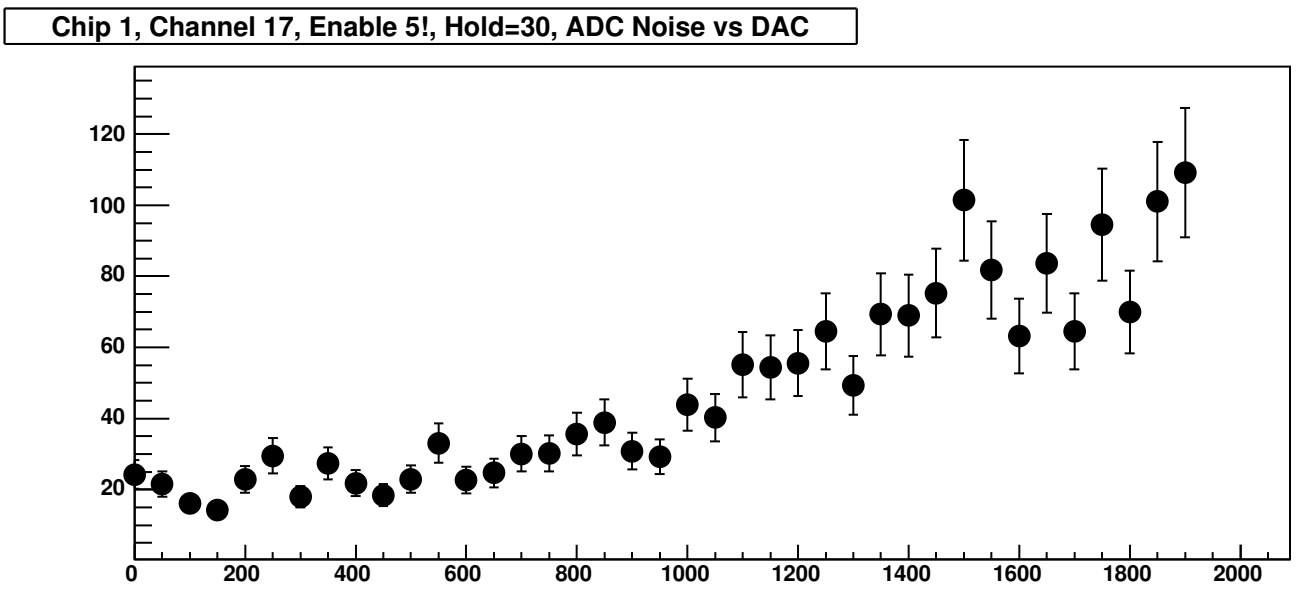

Chip 1, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

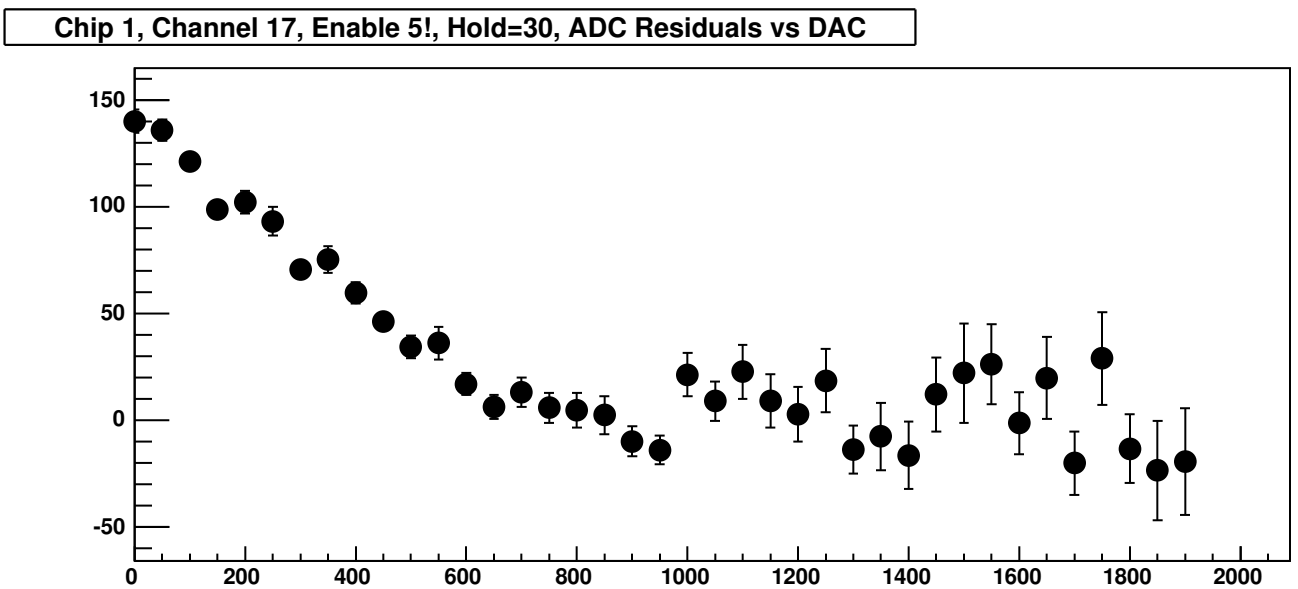

Chip 1, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 18.49 / 11
p0 -1581 ± 3.441
p1 -0.007686 ± 0.003092
-1580
-1585
-1590
-1595
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

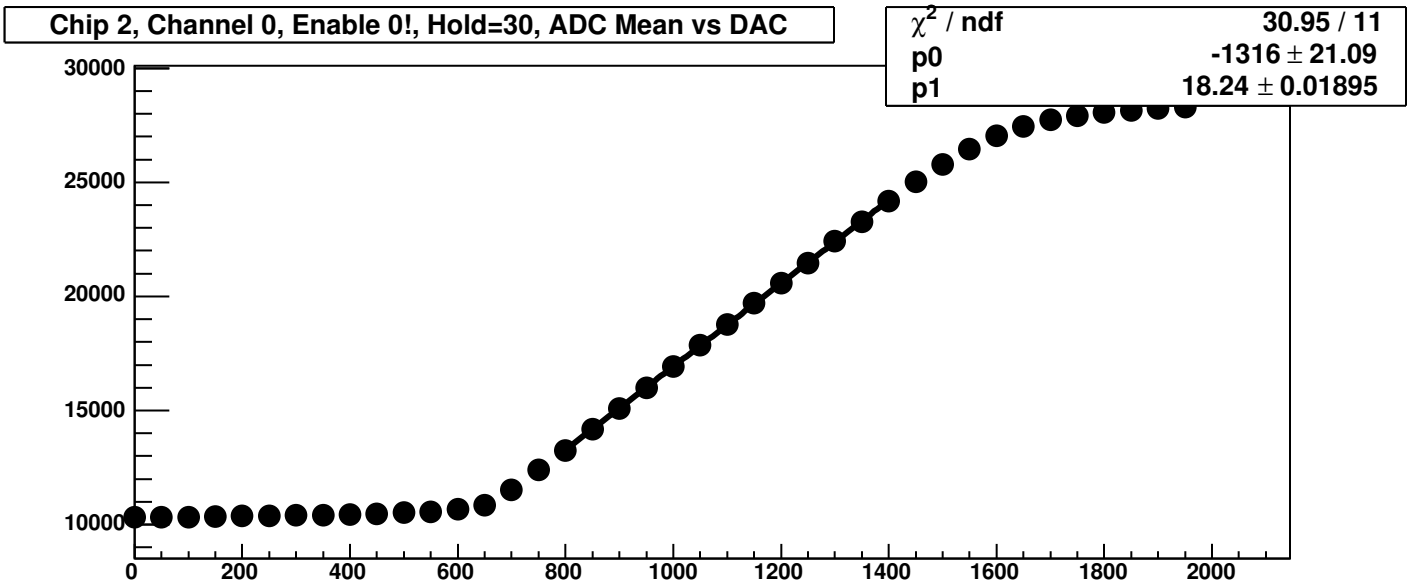

Chip 1, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

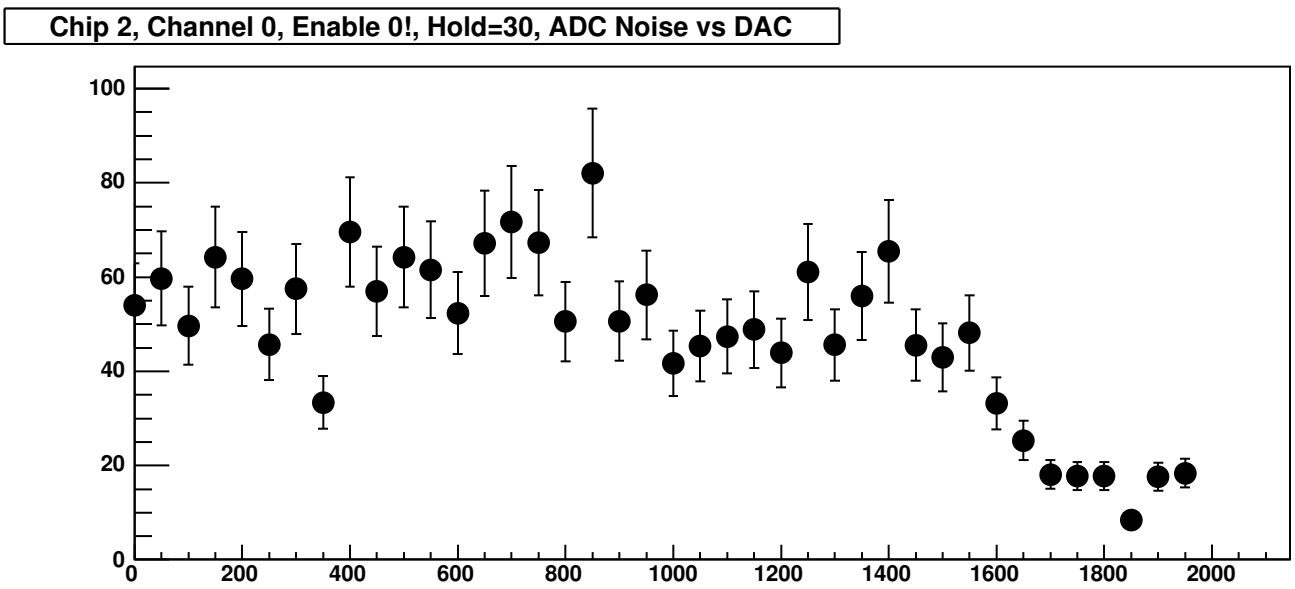

Chip 1, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
200
400
600
800
1000
1200
1400
1600
1800
2000

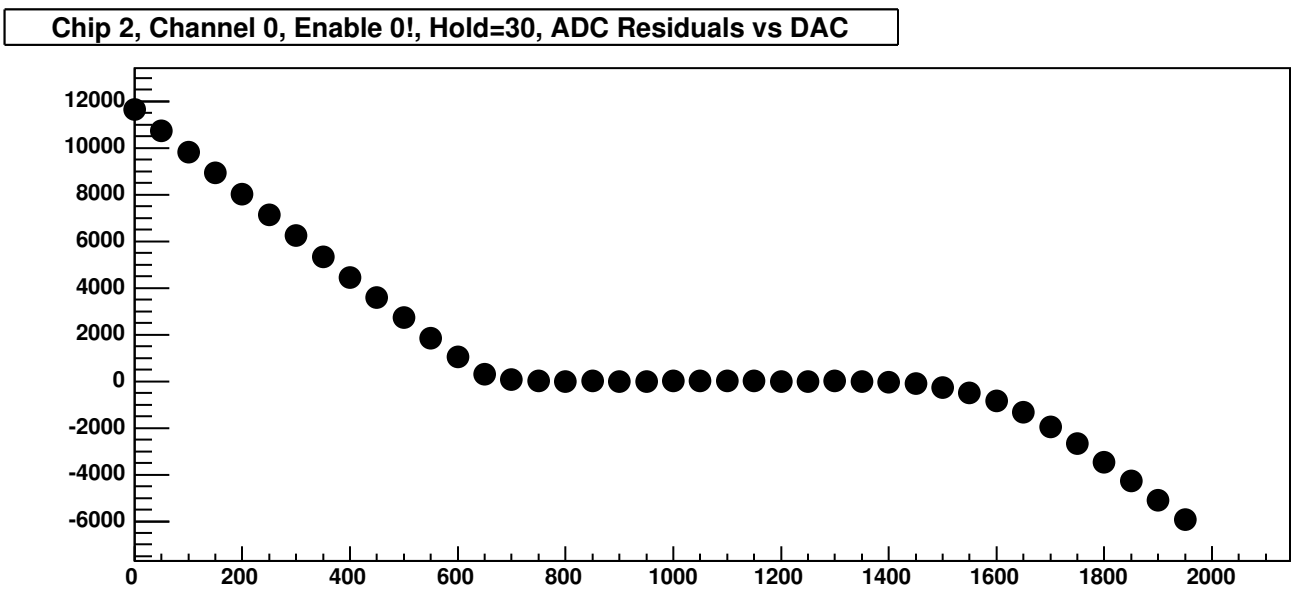

Chip 1, Channel 17, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 20.2 / 11
p0 -1732 ± 16.89
p1 0.2013 ± 0.01643
-1350
-1400
-1450
-1500
-1550
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

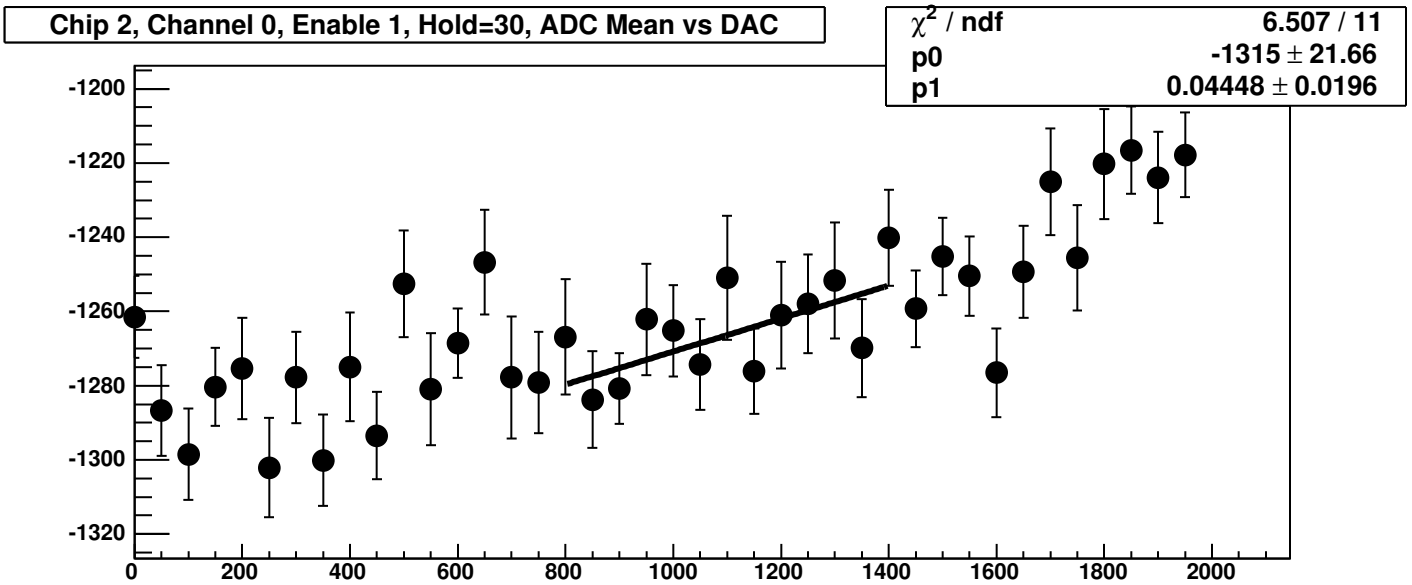

Chip 1, Channel 17, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

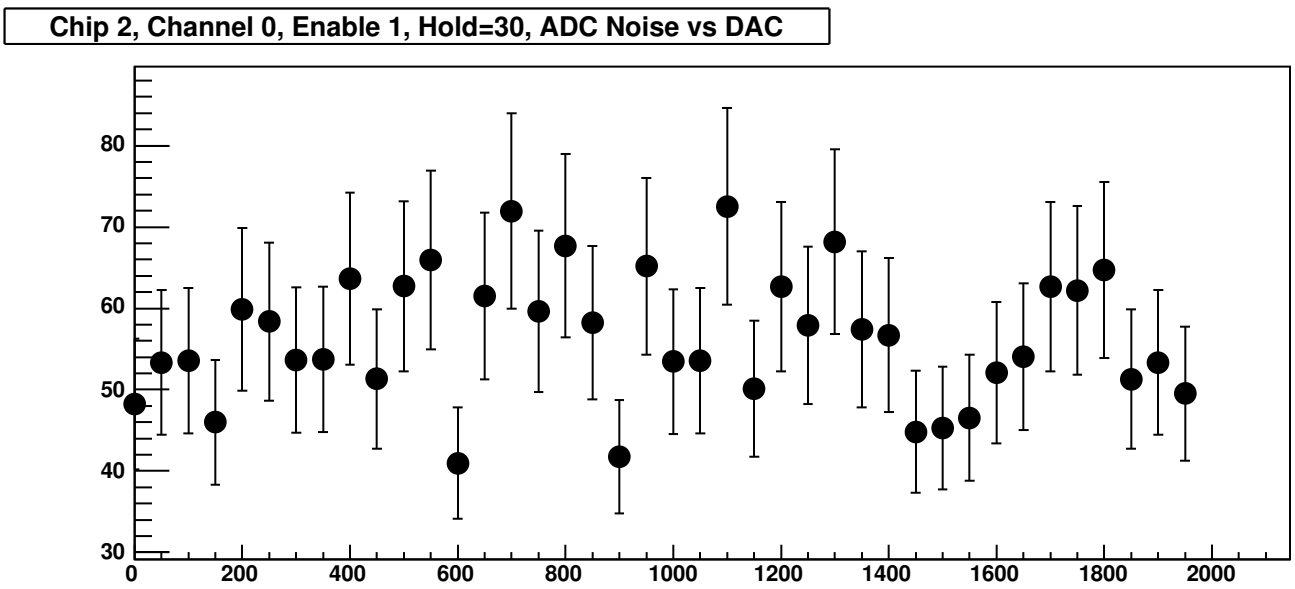

Chip 1, Channel 17, Enable 5!, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

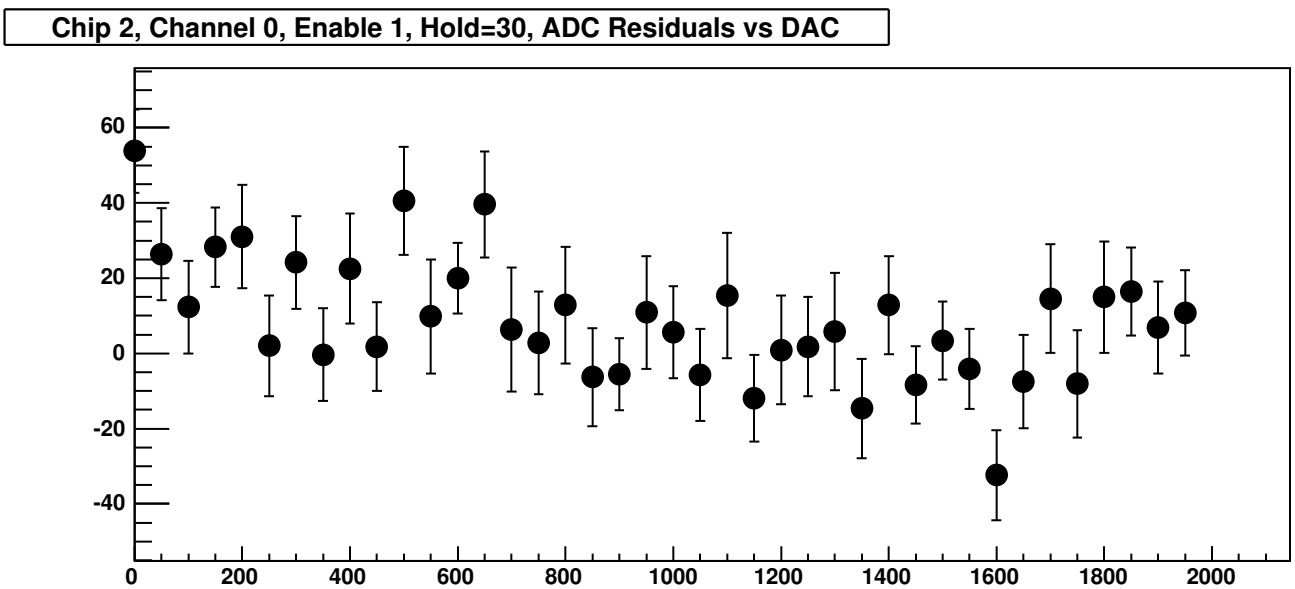

Chip 2, Channel 0, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 30.95 / 11
p0 -1316 ± 21.09
p1 18.24 ± 0.01895
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

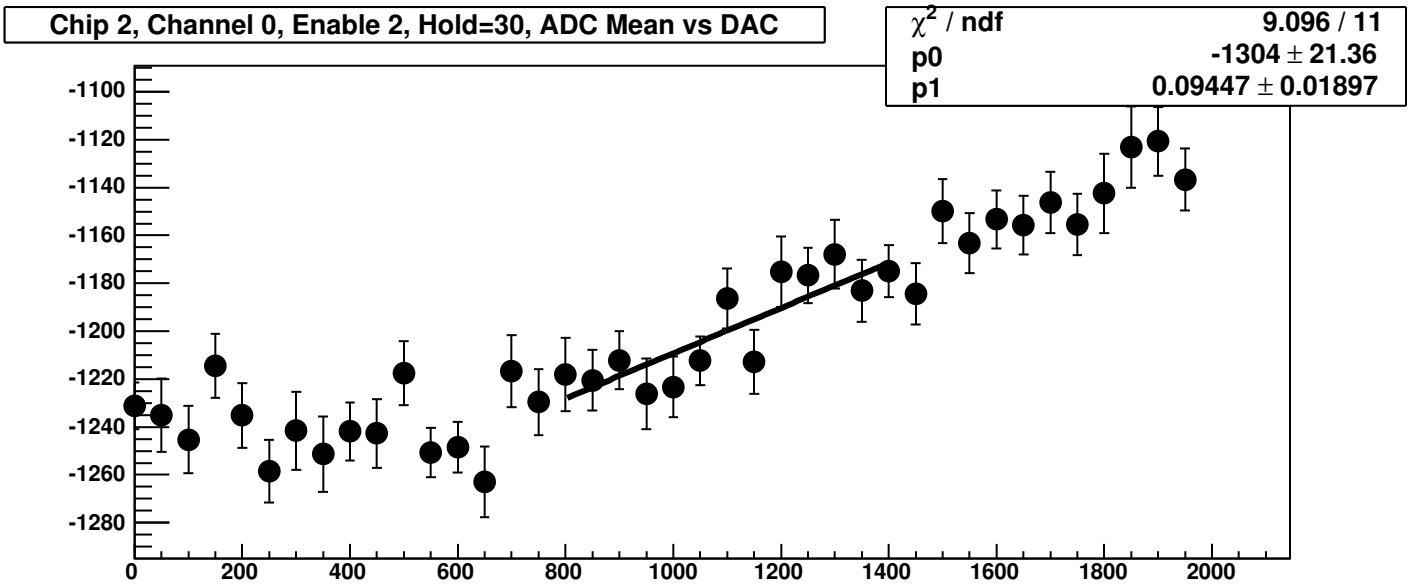

Chip 2, Channel 0, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

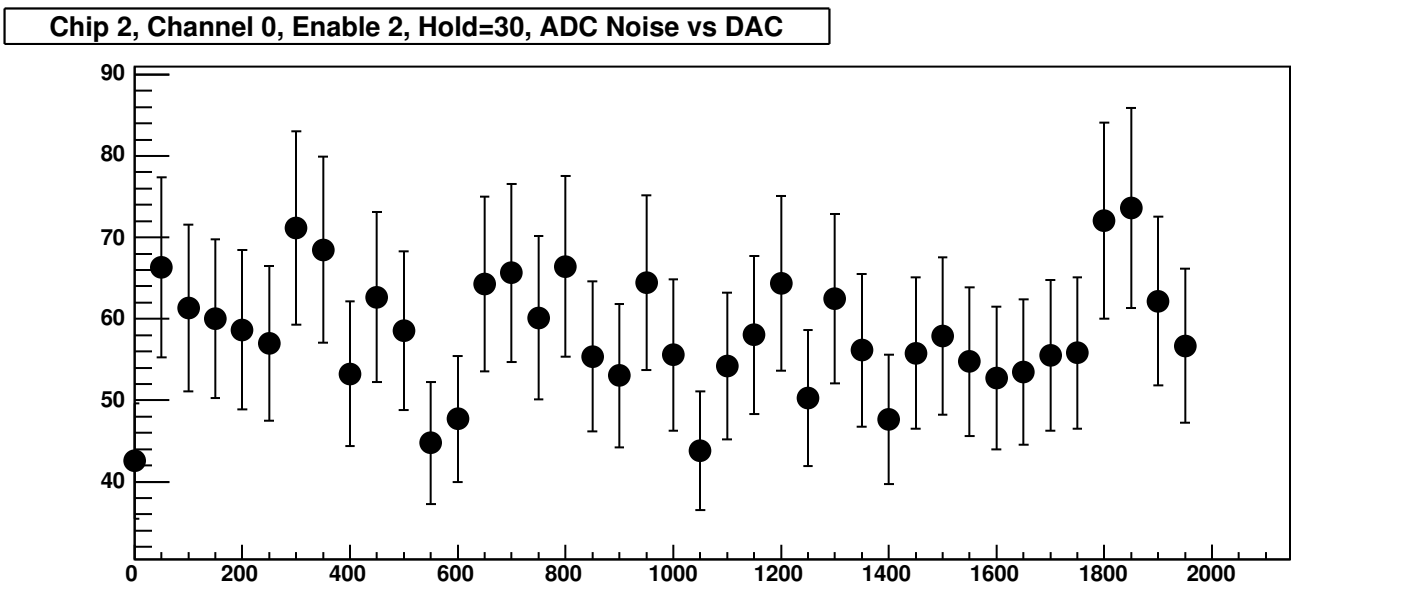

Chip 2, Channel 0, Enable 0!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

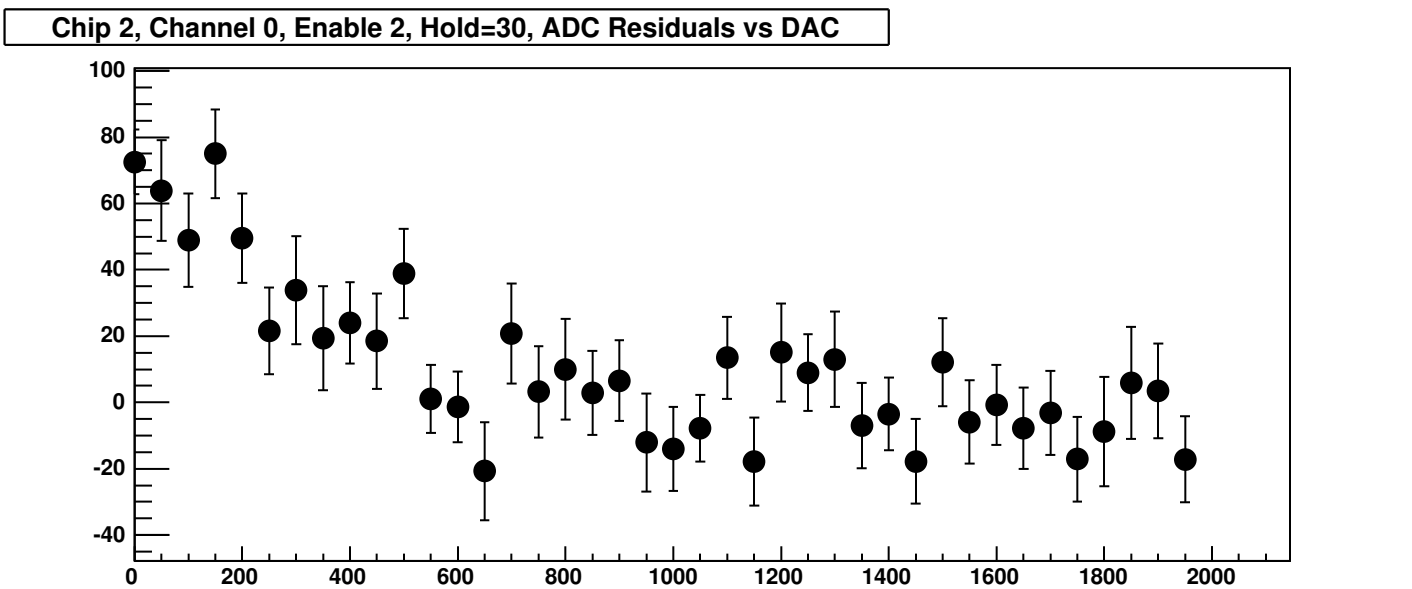

Chip 2, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 6.507 / 11
p0 -1315 ± 21.66
p1 0.04448 ± 0.0196
-1200
-1220
-1240
-1260
-1280
-1300
-1320
0
200
400
600
800
1000
1200
1400
1600
1800
2000

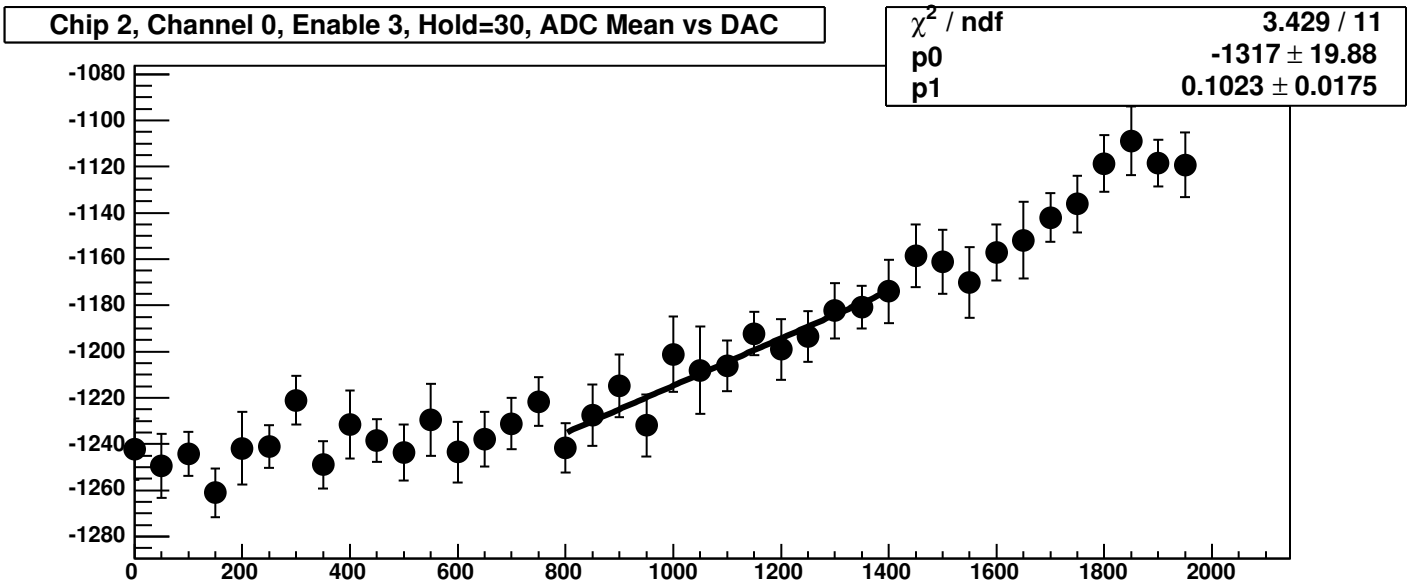

Chip 2, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

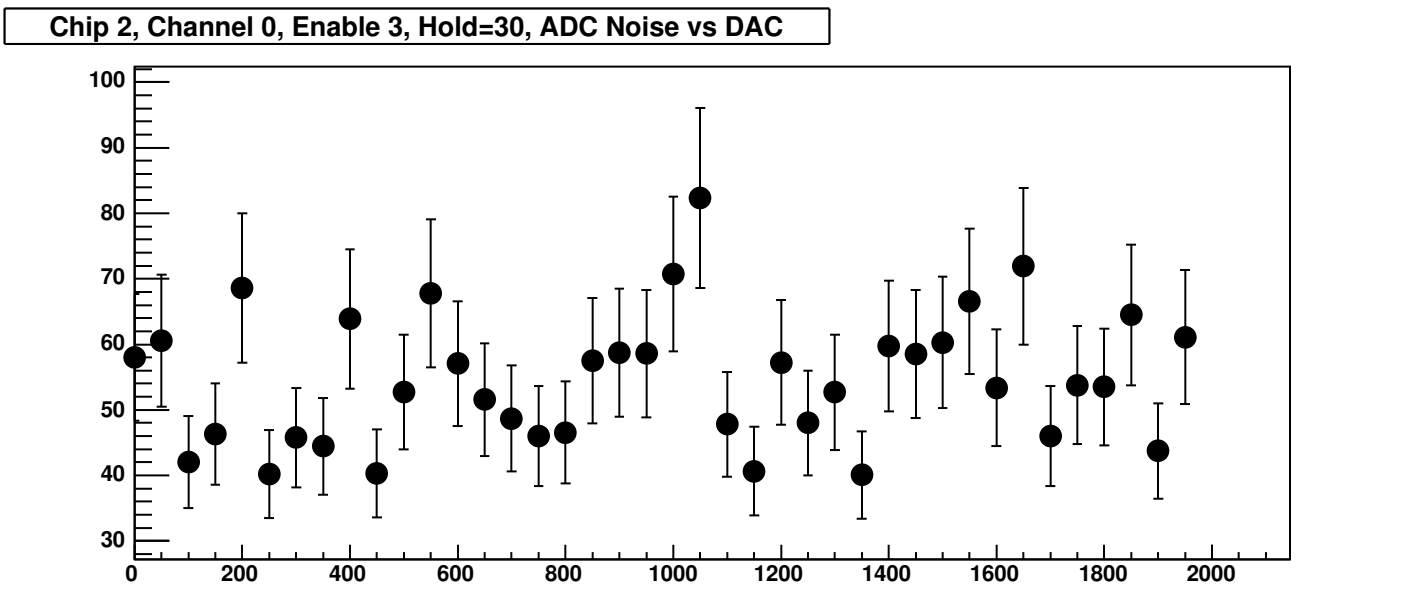

Chip 2, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

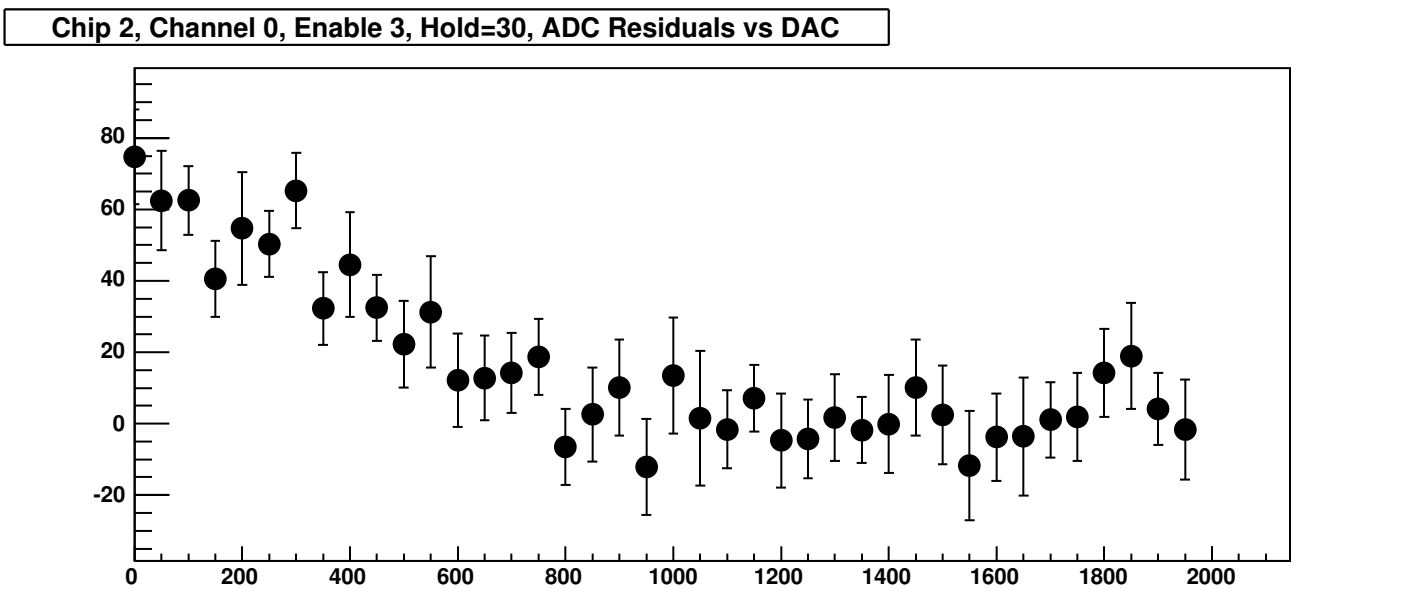

Chip 2, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 9.096 / 11
p0 -1304 ± 21.36
p1 0.09447 ± 0.01897
-1100
-1120
-1140
-1160
-1180
-1200
-1220
-1240
-1260
-1280
0
200
400
600
800
1000
1200
1400
1600
1800
2000

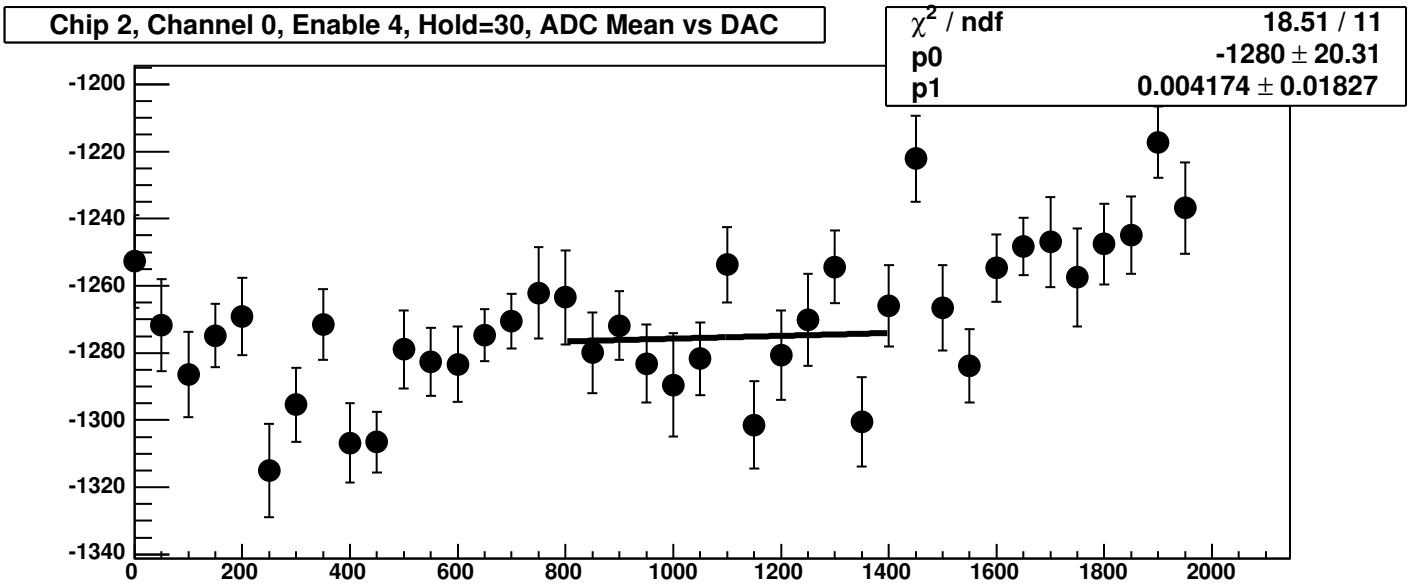

Chip 2, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

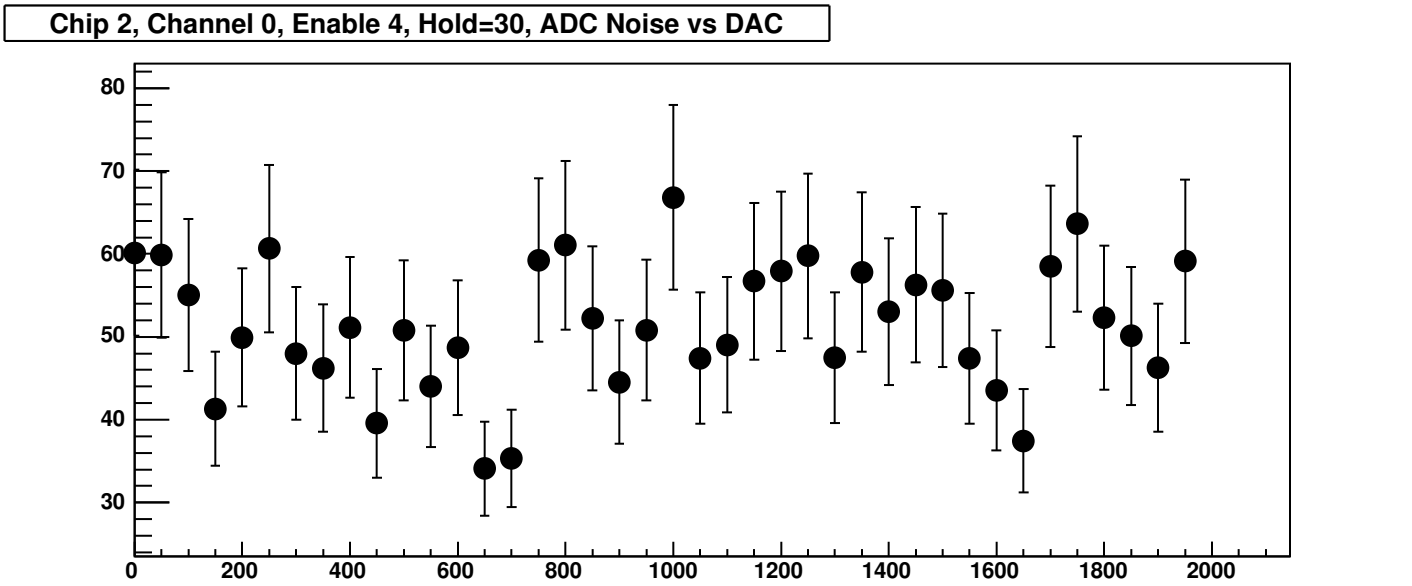

Chip 2, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

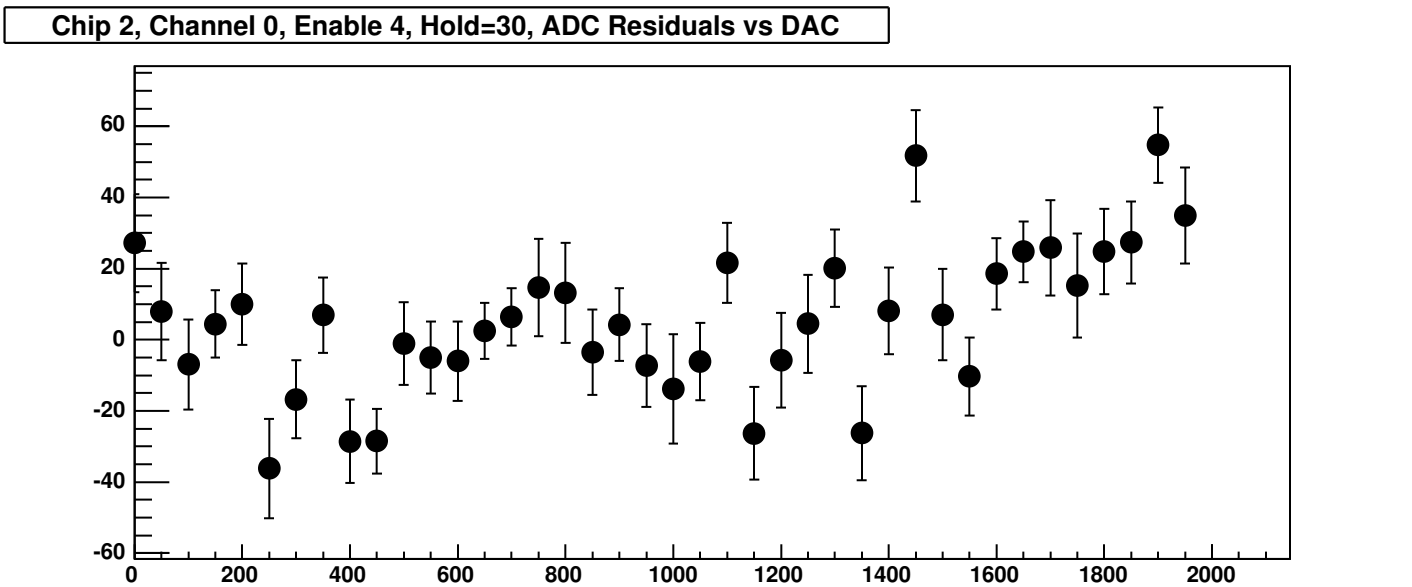

Chip 2, Channel 0, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3.429 / 11
p0 -1317 ± 19.88
p1 0.1023 ± 0.0175
-1080
-1100
-1120
-1140
-1160
-1180
-1200
-1220
-1240
-1260
-1280
0
200
400
600
800
1000
1200
1400
1600
1800
2000

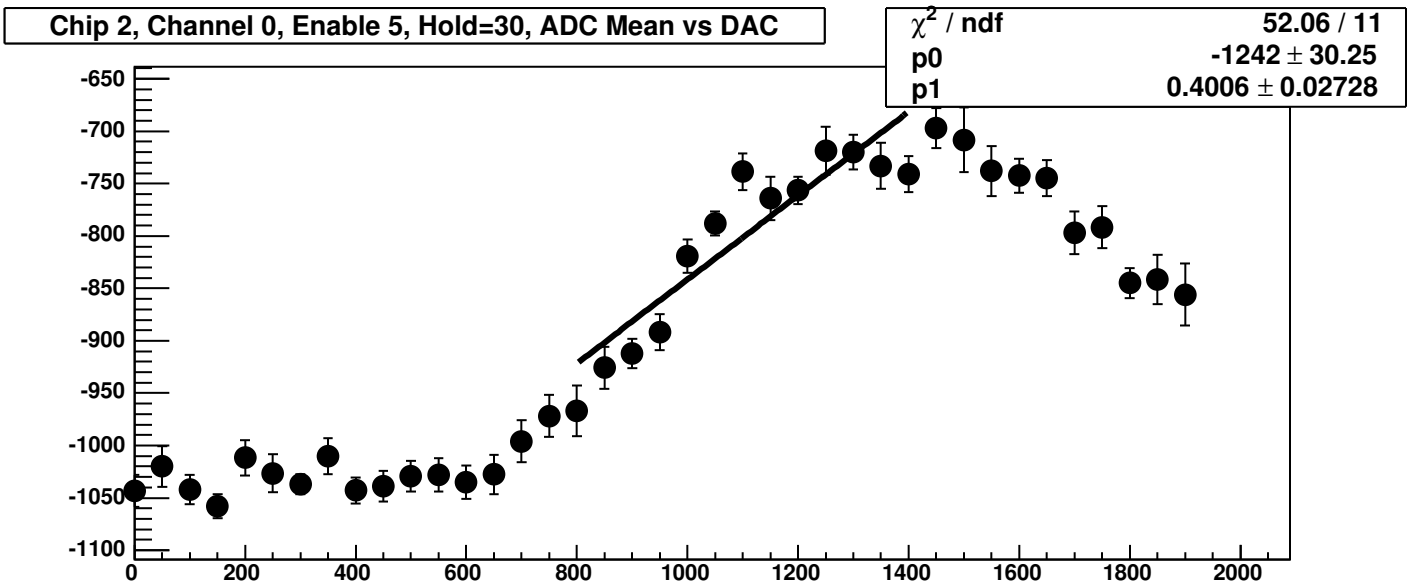

Chip 2, Channel 0, Enable 3, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

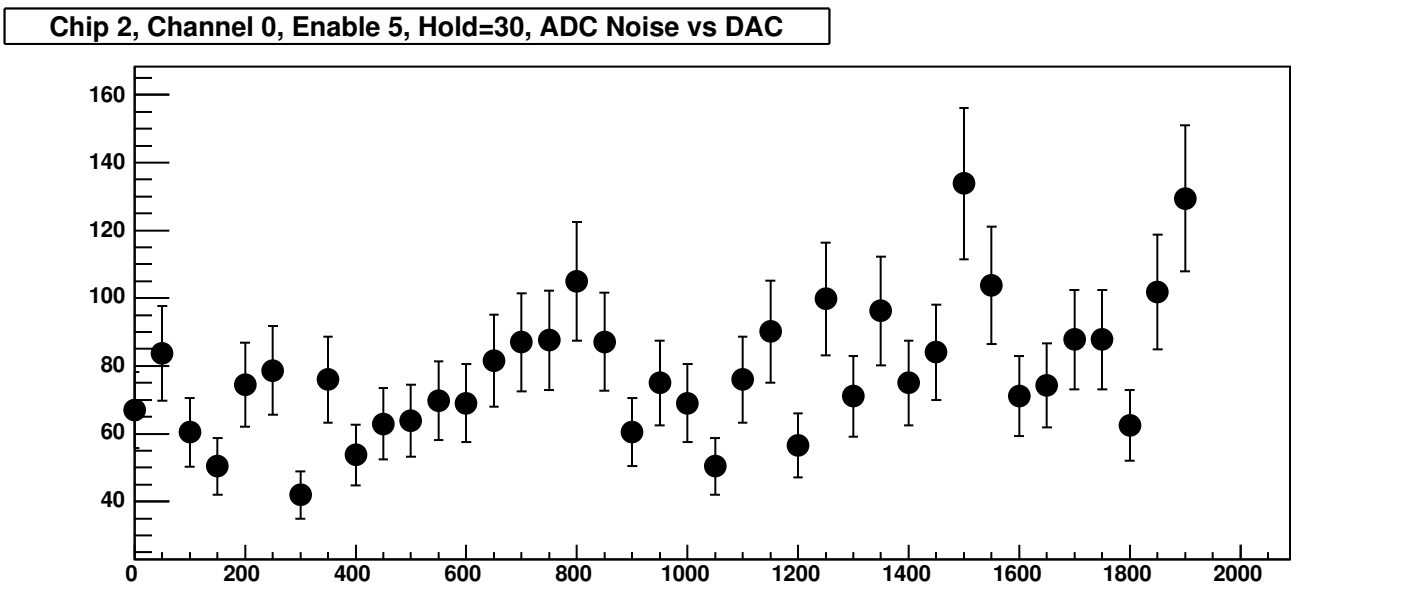

Chip 2, Channel 0, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

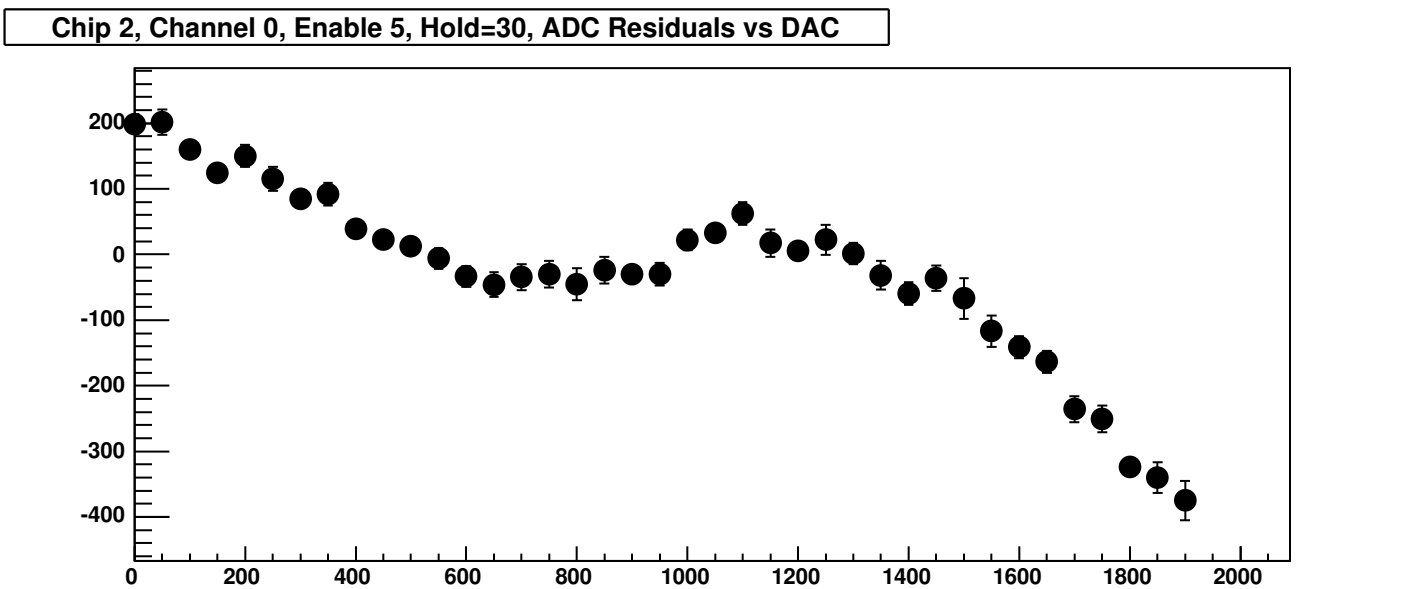

Chip 2, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 18.51 / 11
p0 -1280 ± 20.31
p1 0.004174 ± 0.01827
-1200
-1220
-1240
-1260
-1280
-1300
-1320
-1340
0
200
400
600
800
1000
1200
1400
1600
1800
2000

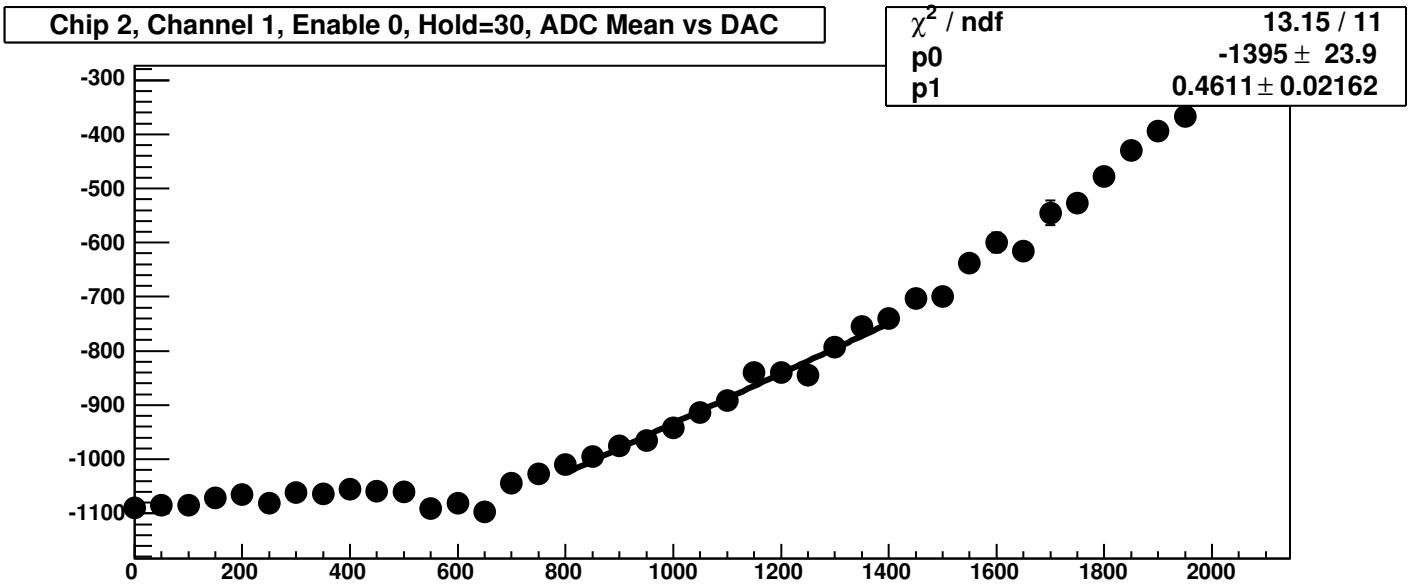

Chip 2, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

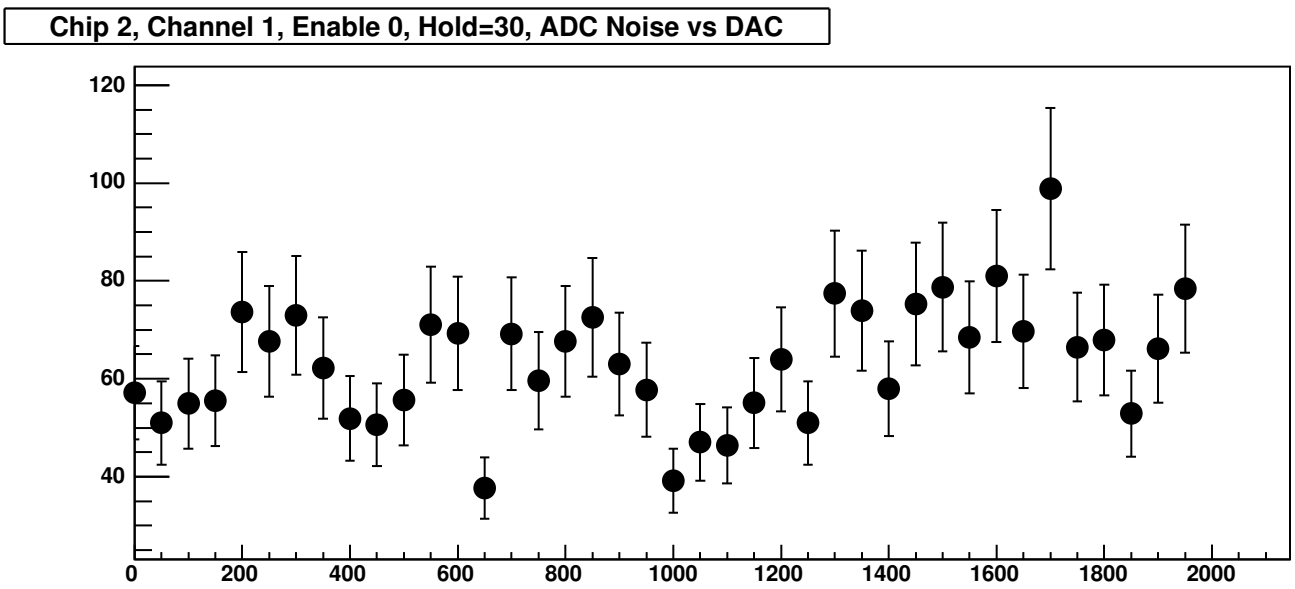

Chip 2, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

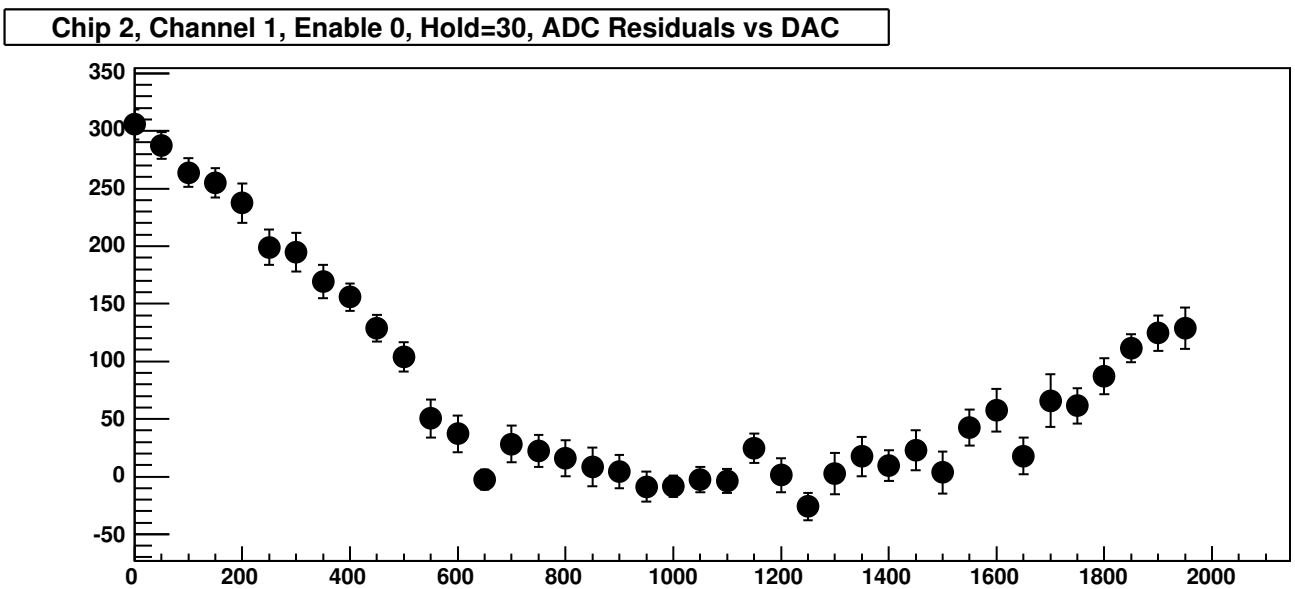

Chip 2, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 52.06 / 11
p0 -1242 ± 30.25
p1 0.4006 ± 0.02728
-650
-700
-750
-800
-850
-900
-950
-1000
-1050
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

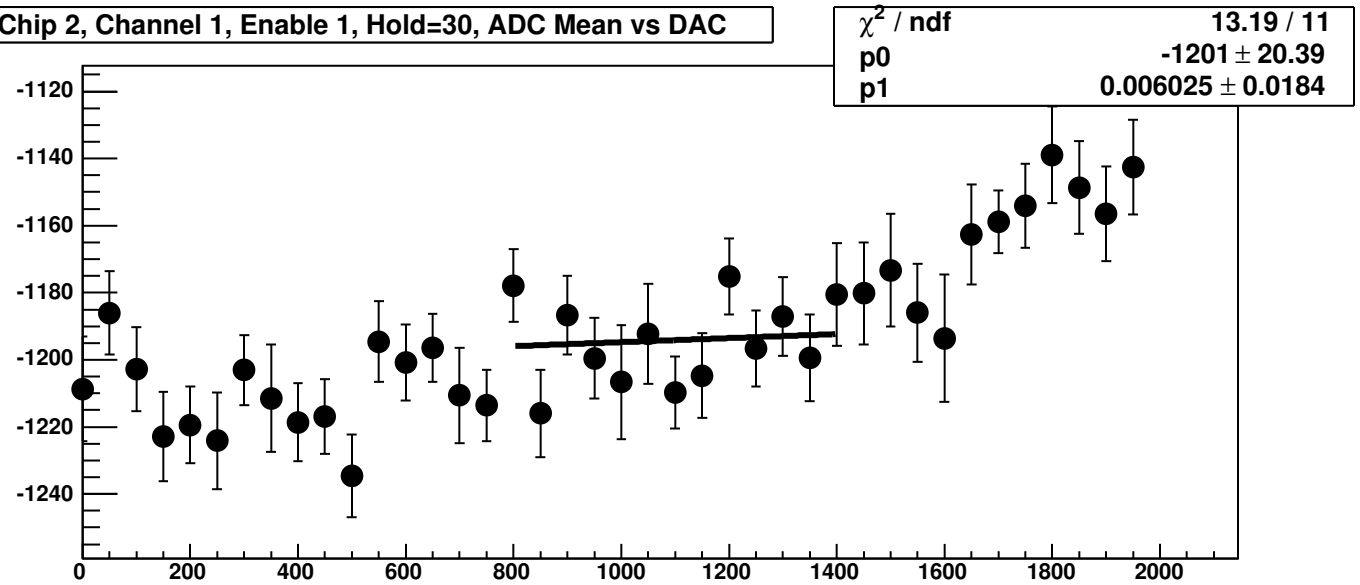

Chip 2, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
200
100
0
-100
-200
-300
-400
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 1, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 13.15 / 11
p0 -1395 ± 23.9
p1 0.4611 ± 0.02162
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

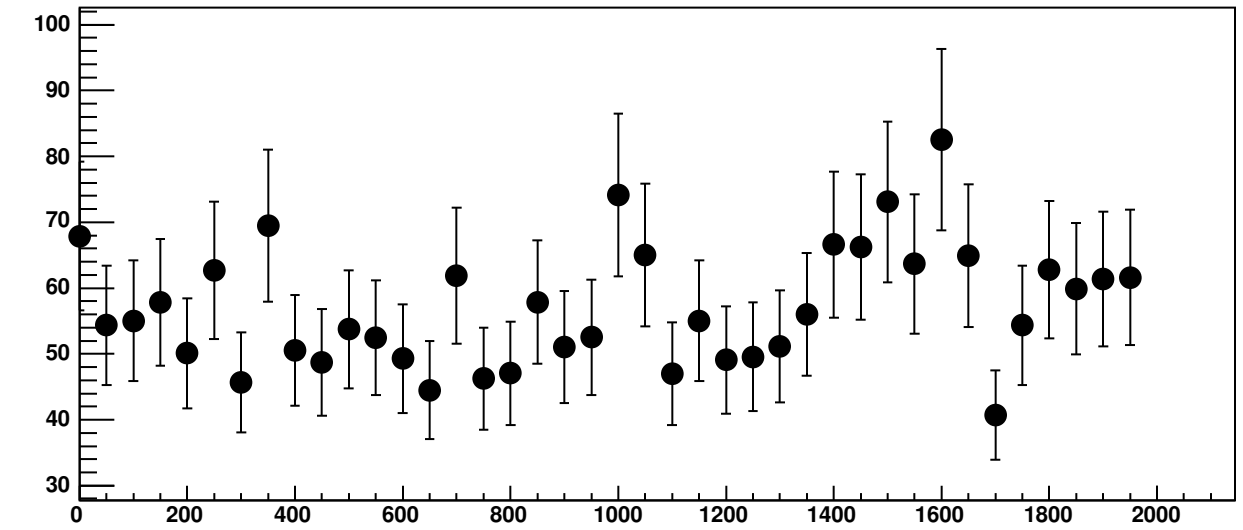

Chip 2, Channel 1, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 1, Enable 0, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

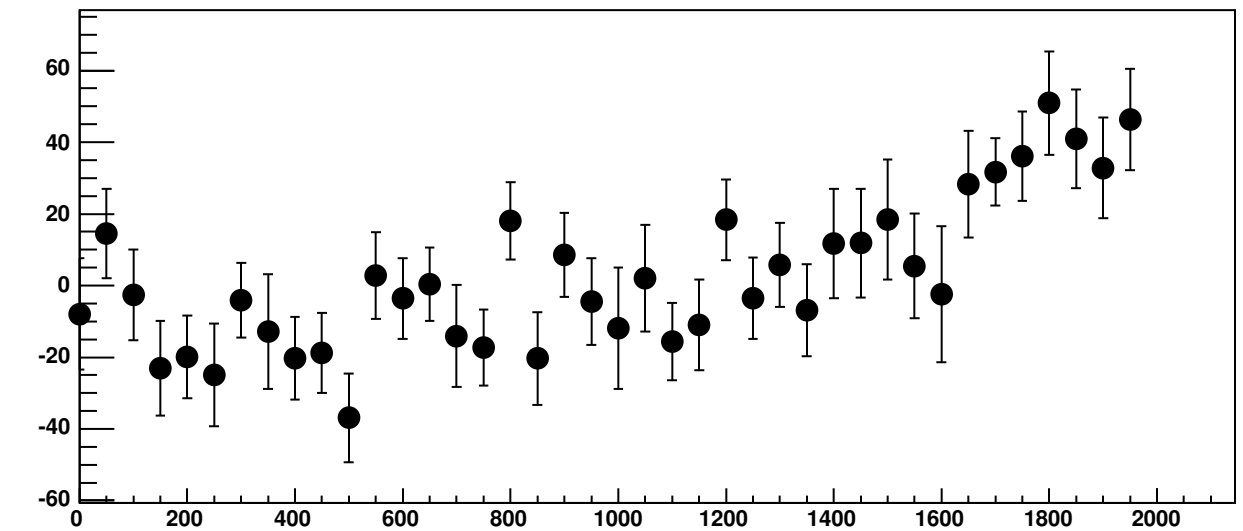
Chip 2, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC

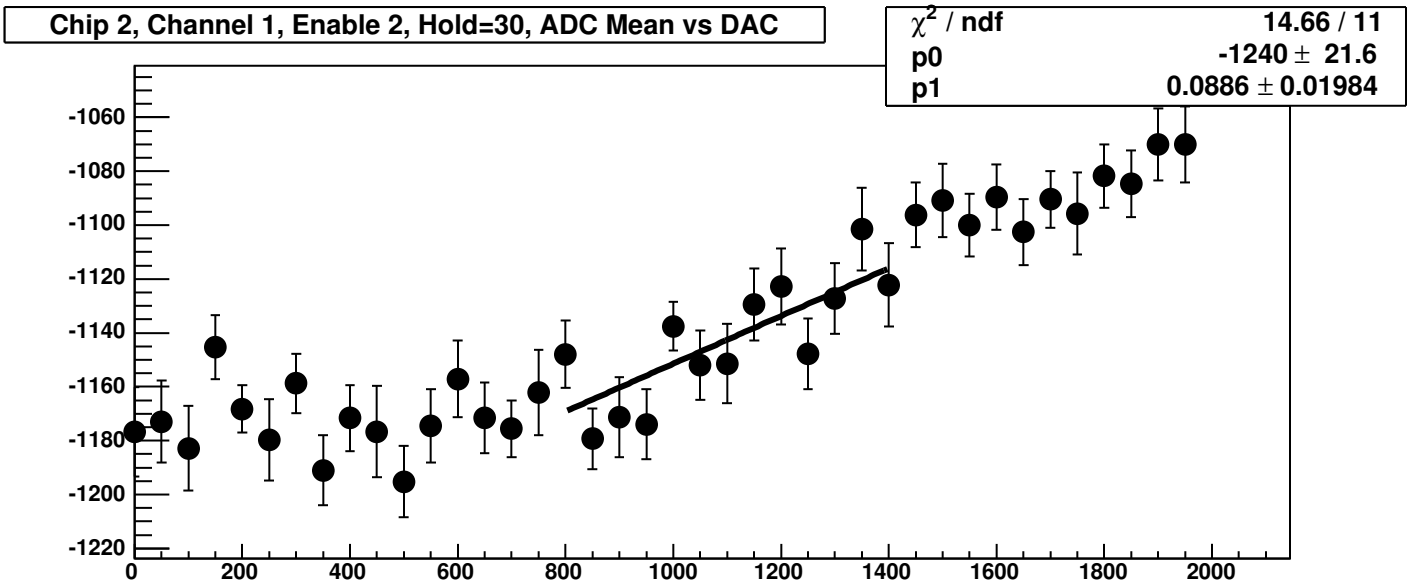


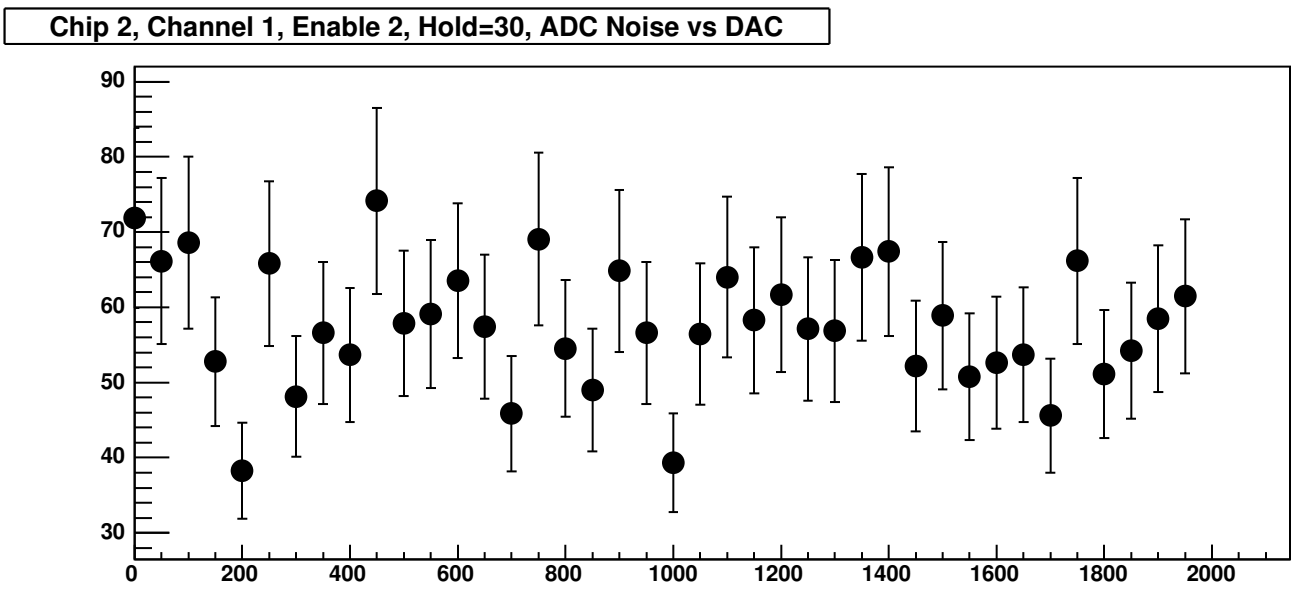
Chip 2, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC

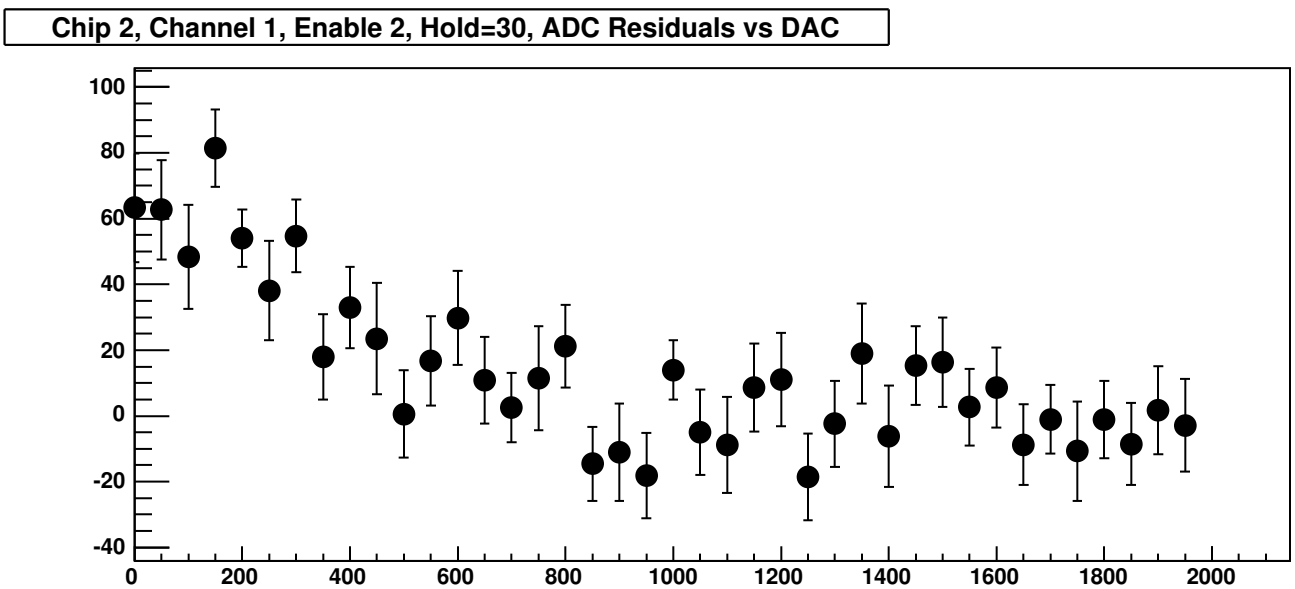


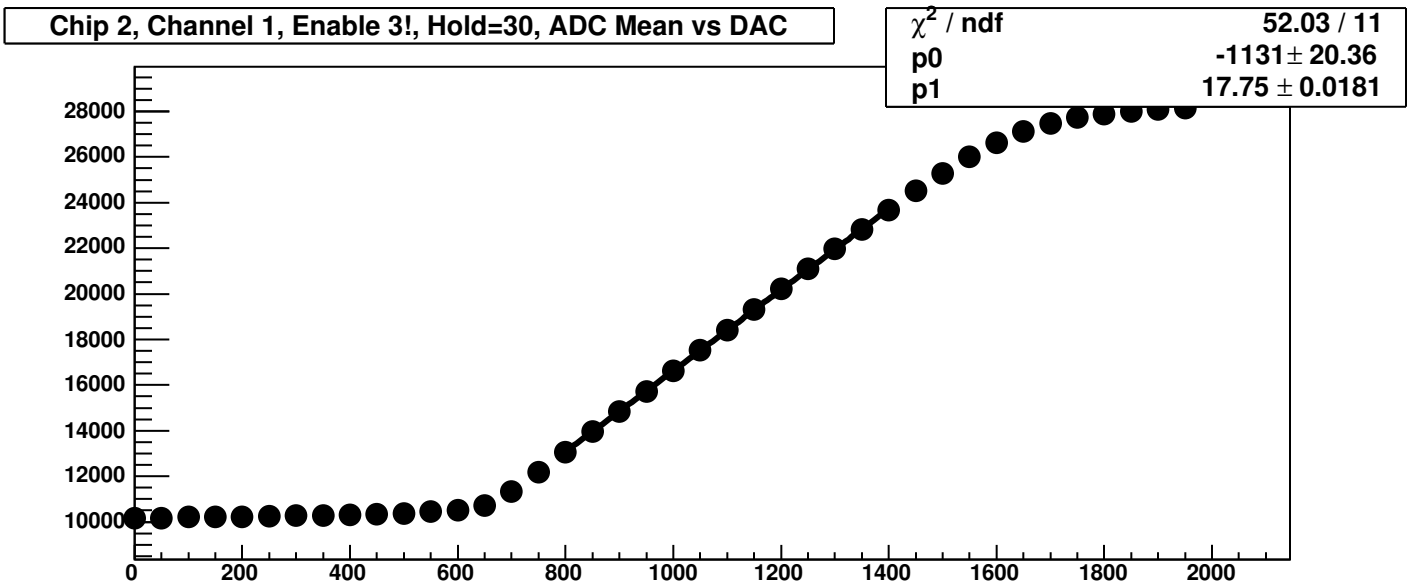
Chip 2, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC

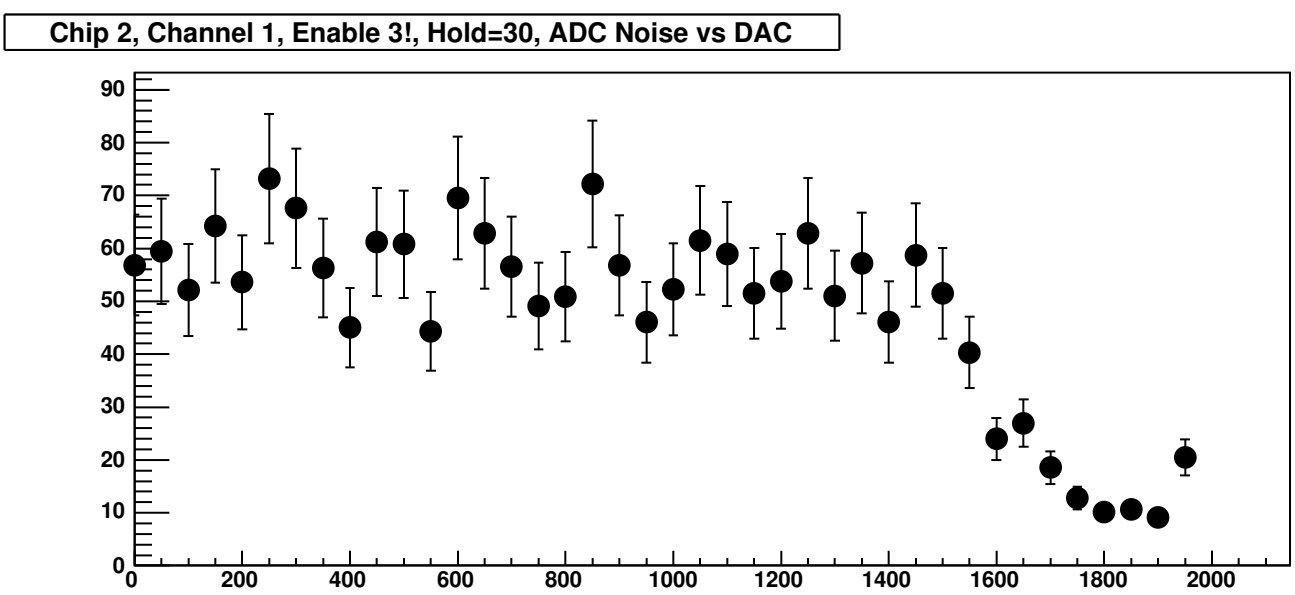


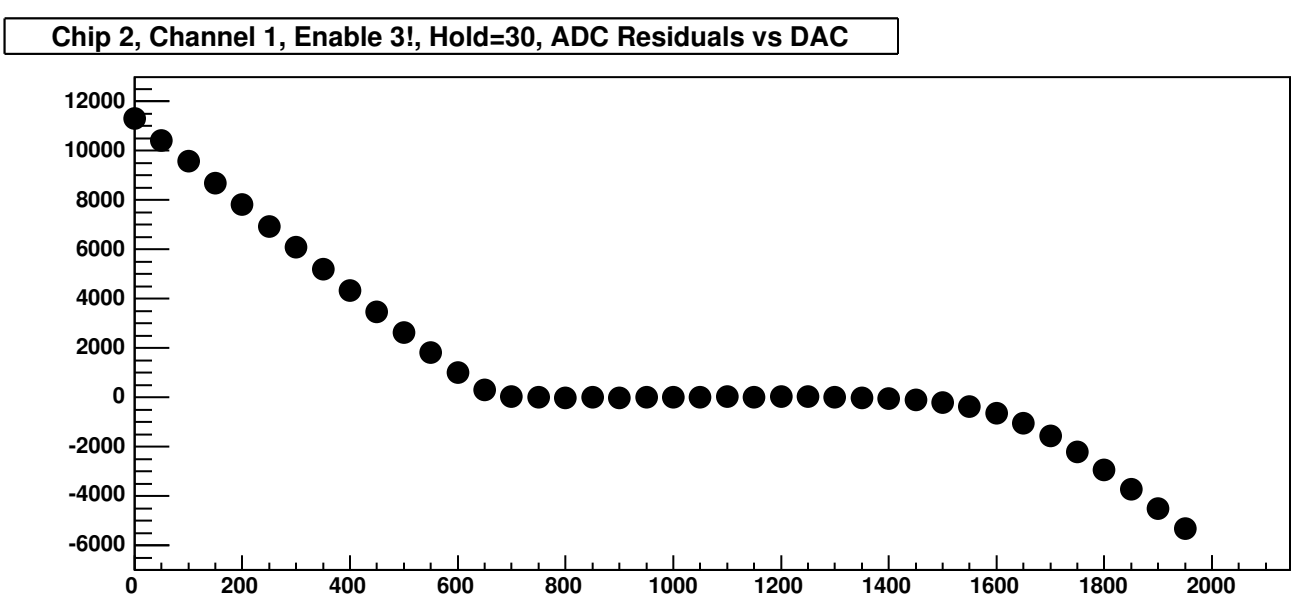

Chip 2, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 14.66 / 11
p0 -1240 ± 21.6
p1 0.0886 ± 0.01984
-1060
-1080
-1100
-1120
-1140
-1160
-1180
-1200
-1220
0
200
400
600
800
1000
1200
1400
1600
1800
2000

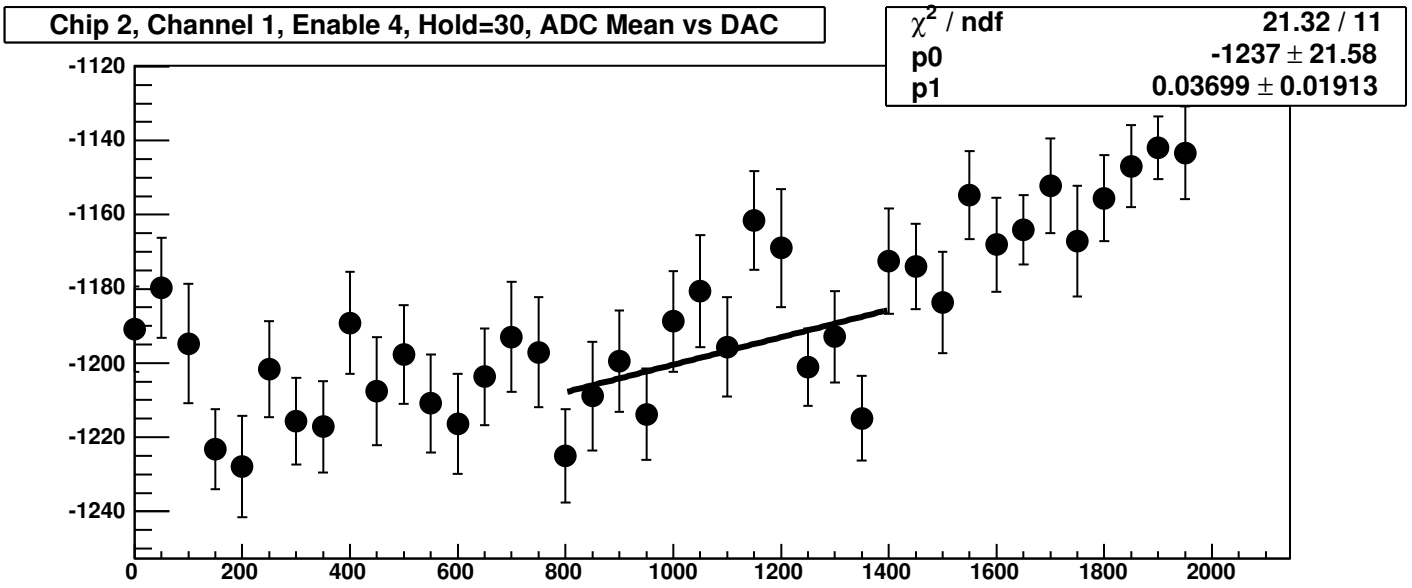

Chip 2, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

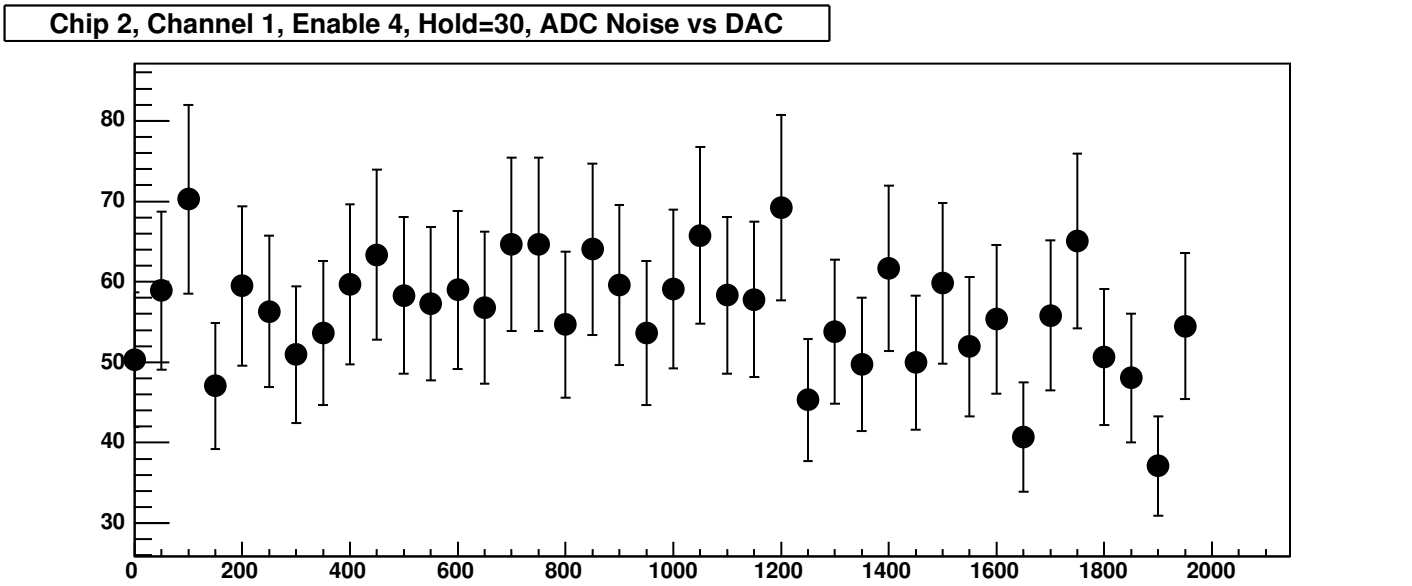

Chip 2, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

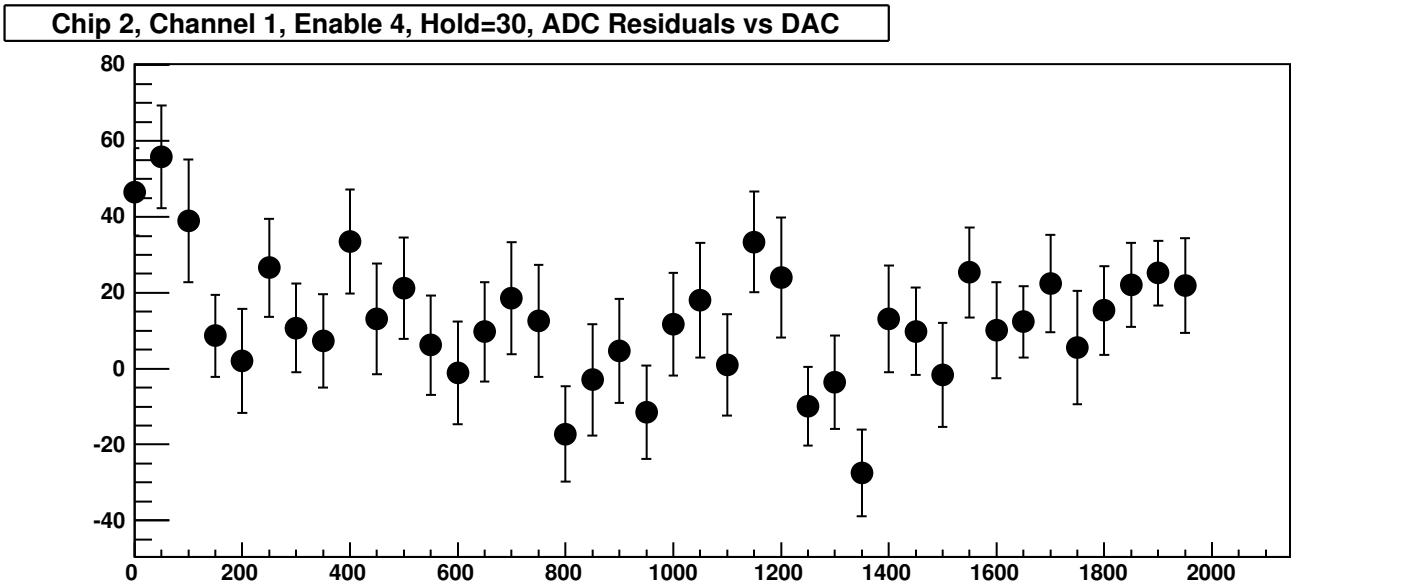

Chip 2, Channel 1, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 52.03 / 11
p0 -1131 ± 20.36
p1 17.75 ± 0.0181
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 1, Enable 3!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

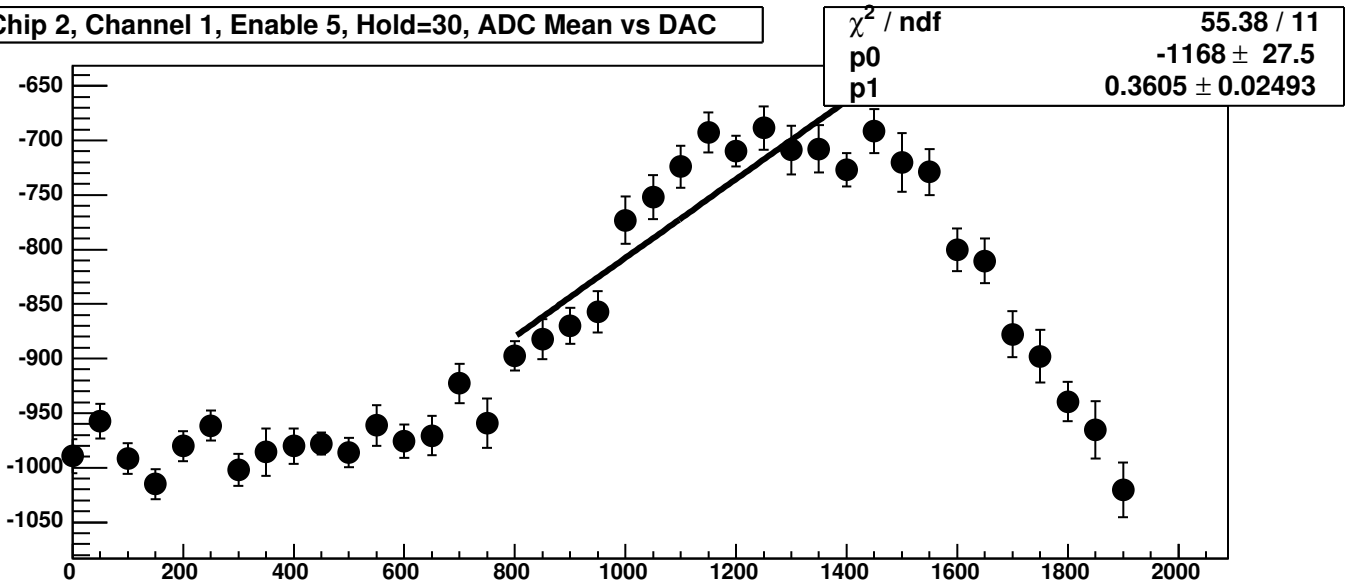

Chip 2, Channel 1, Enable 3!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 21.32 / 11
p0 -1237 ± 21.58
p1 0.03699 ± 0.01913
-1120
-1140
-1160
-1180
-1200
-1220
-1240
0
200
400
600
800
1000
1200
1400
1600
1800
2000

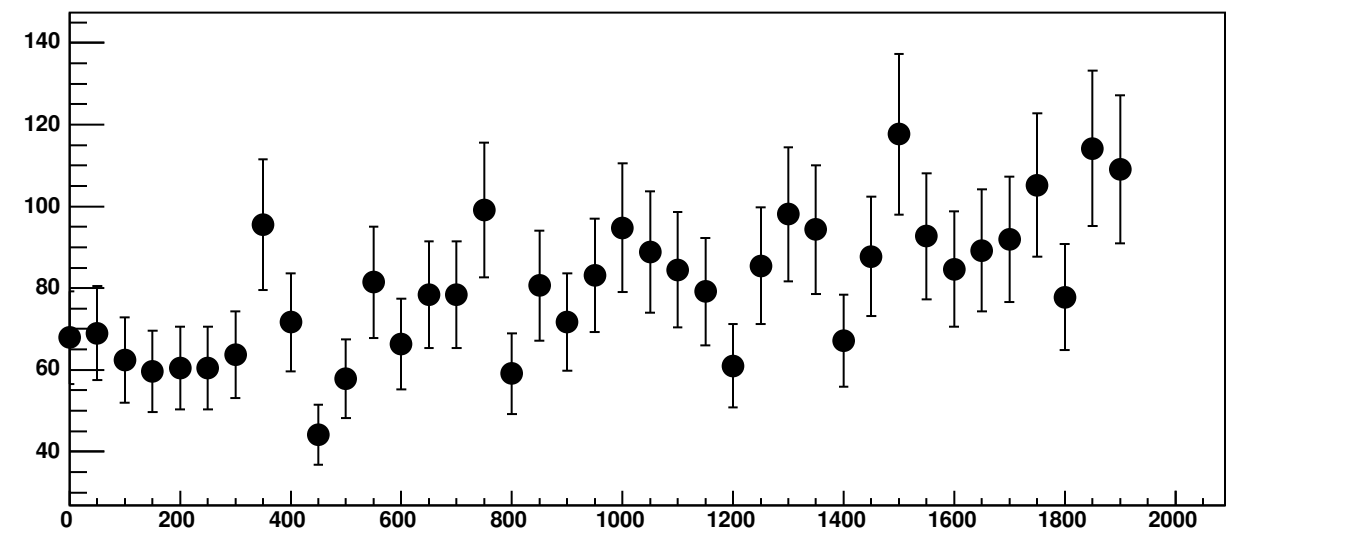

Chip 2, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

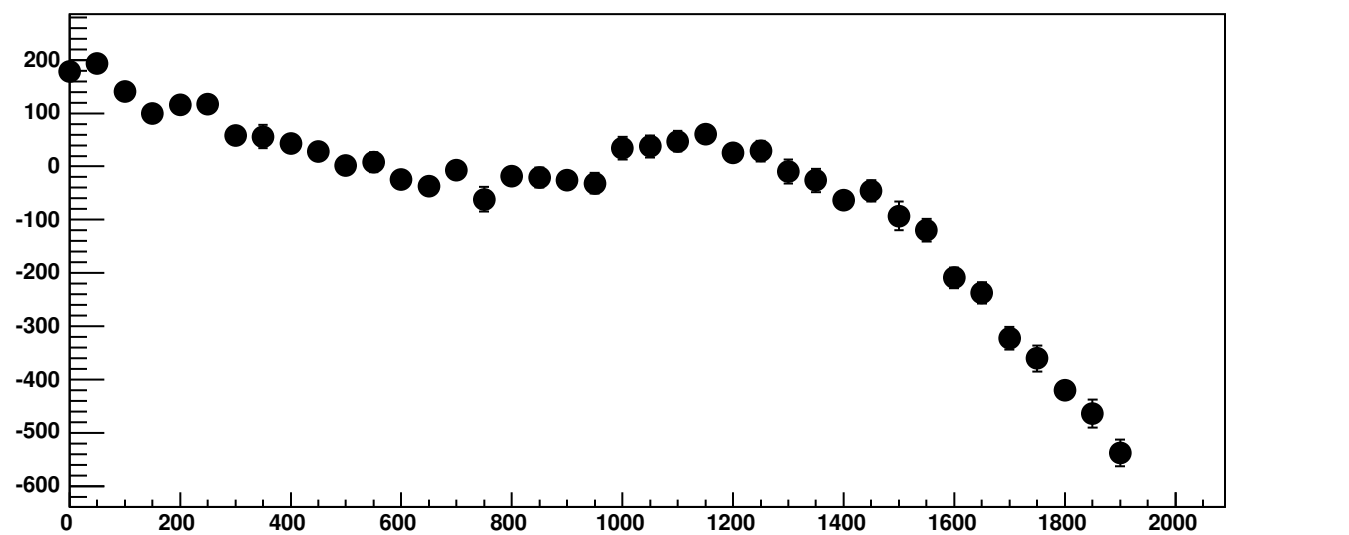
Chip 2, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC

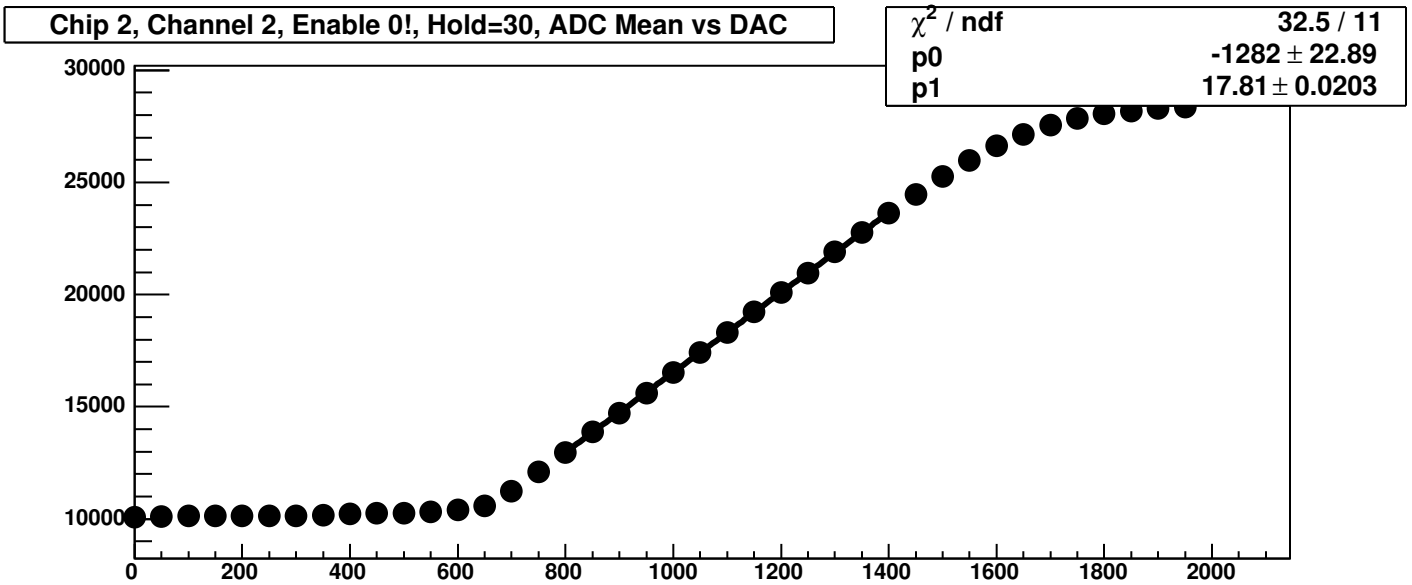


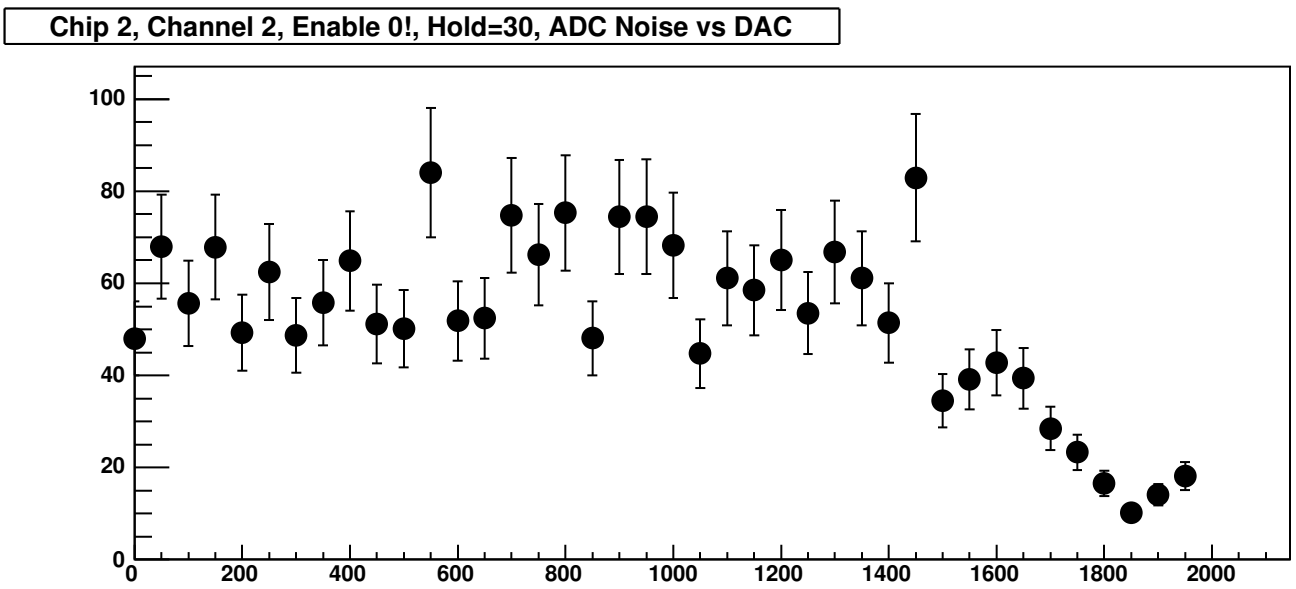
Chip 2, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC

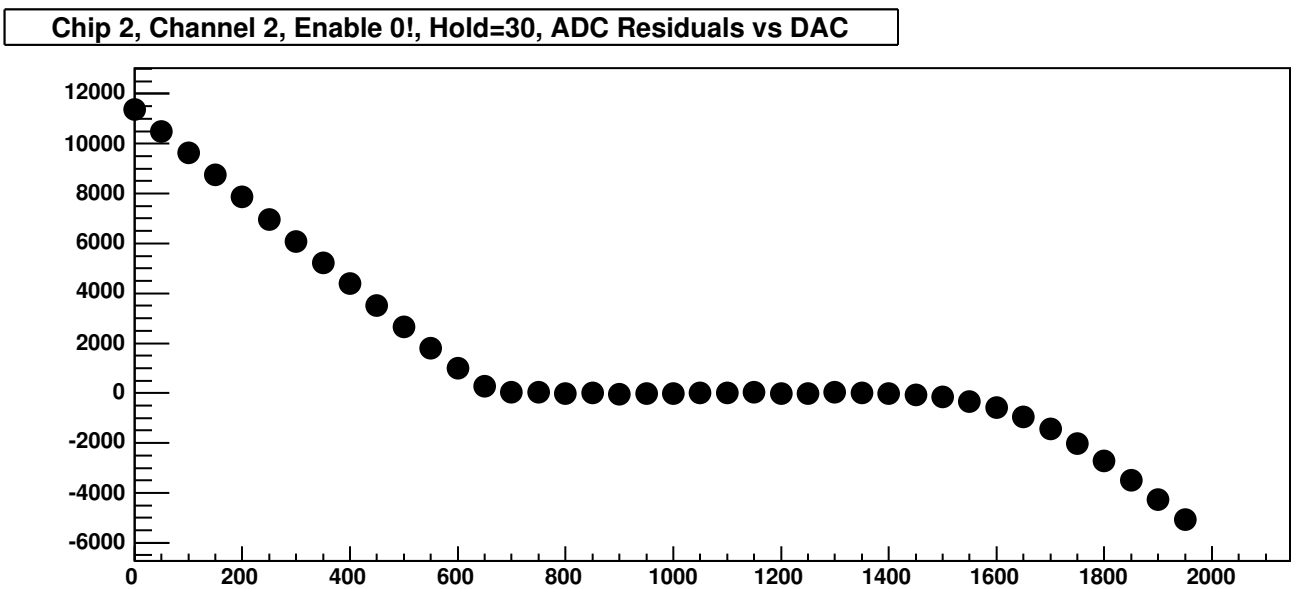


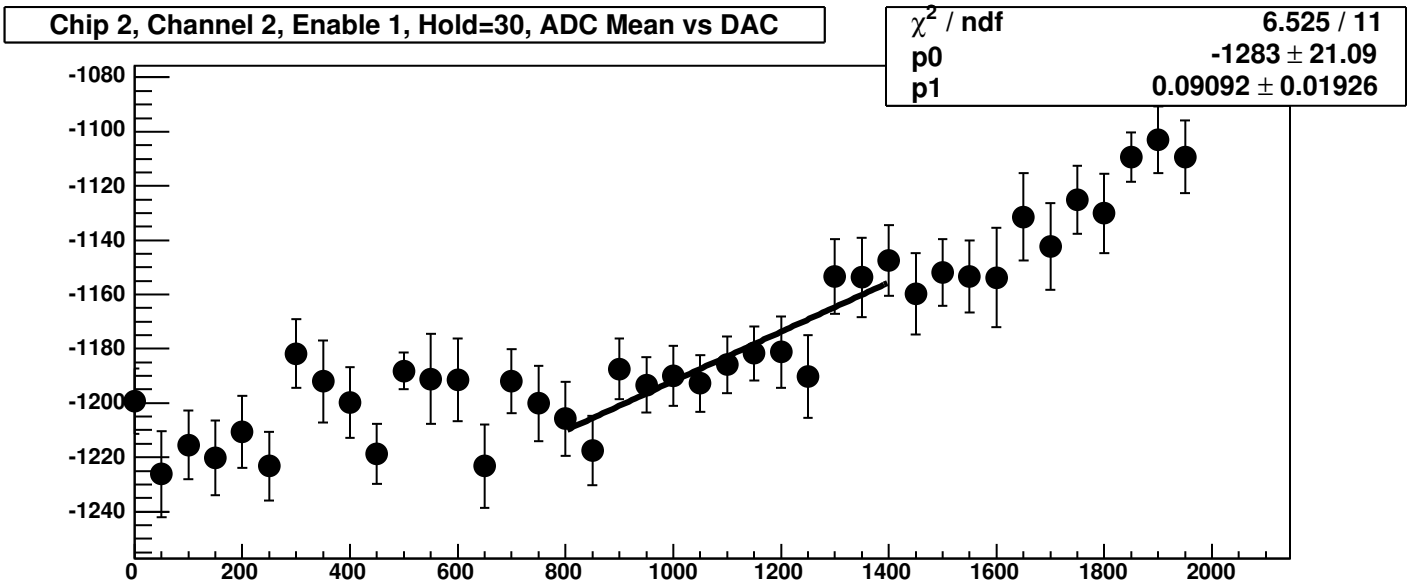
Chip 2, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC

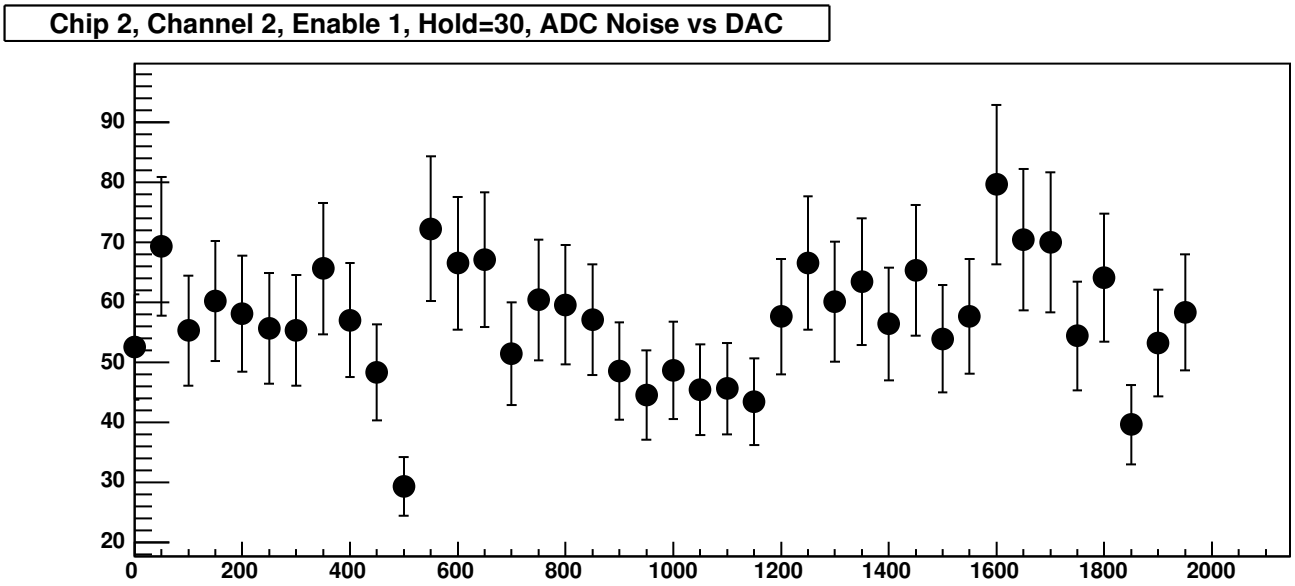


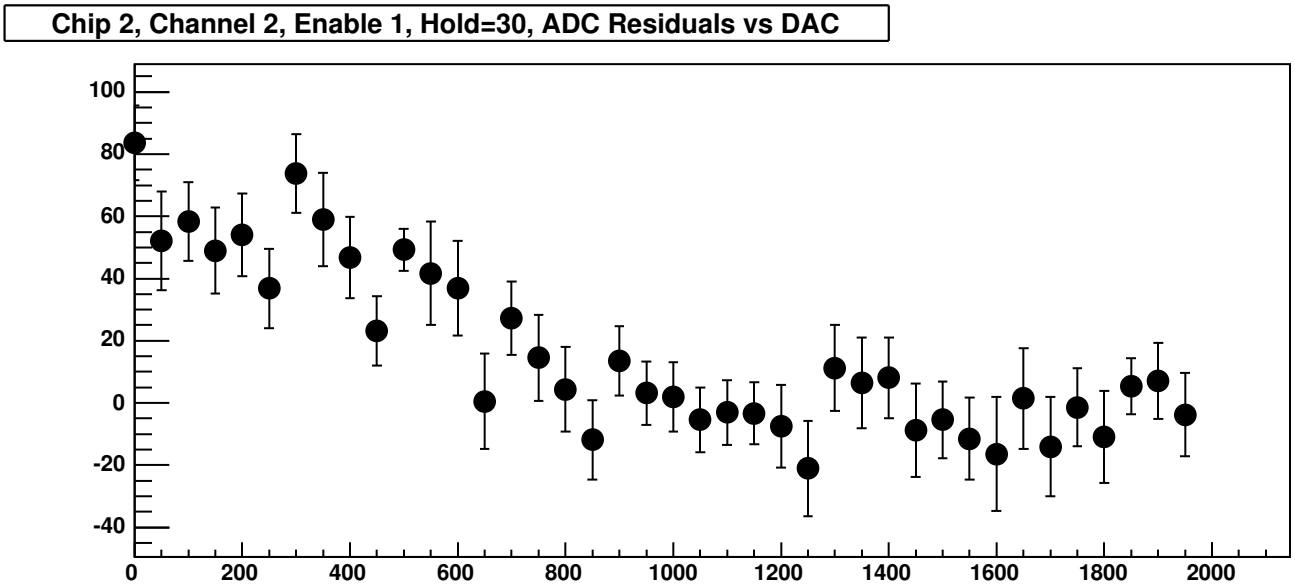

Chip 2, Channel 2, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 32.5 / 11
p0 -1282 ± 22.89
p1 17.81 ± 0.0203
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 2, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

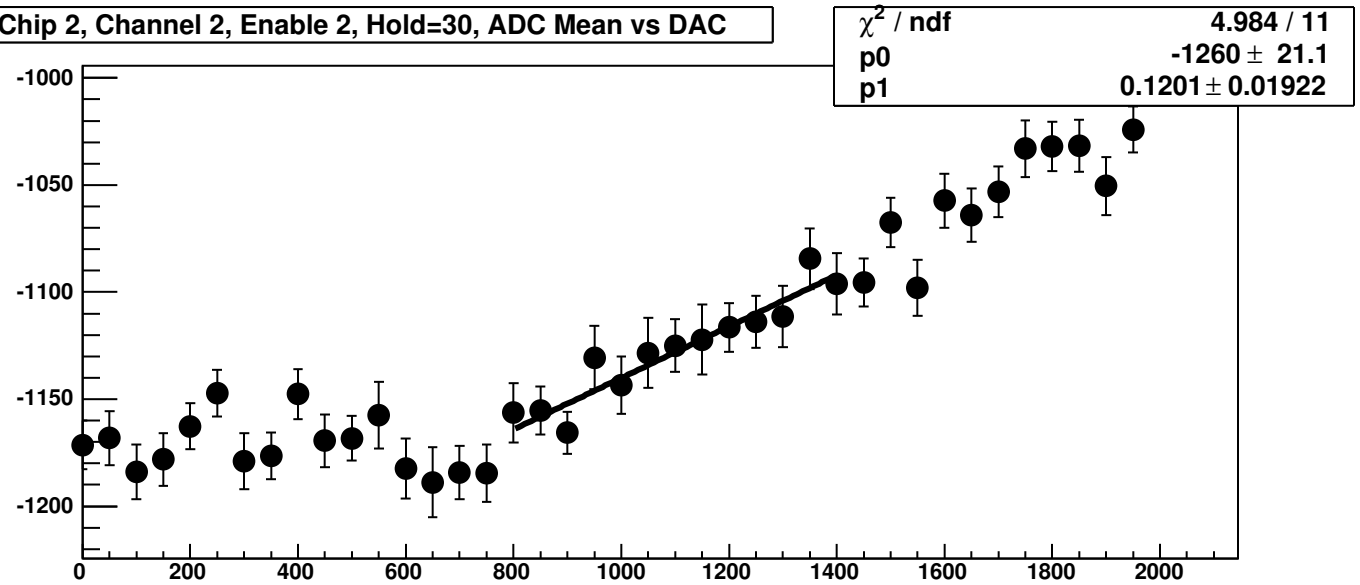

Chip 2, Channel 2, Enable 0!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 6.525 / 11
p0 -1283 ± 21.09
p1 0.09092 ± 0.01926
-1080
-1100
-1120
-1140
-1160
-1180
-1200
-1220
-1240
0
200
400
600
800
1000
1200
1400
1600
1800
2000

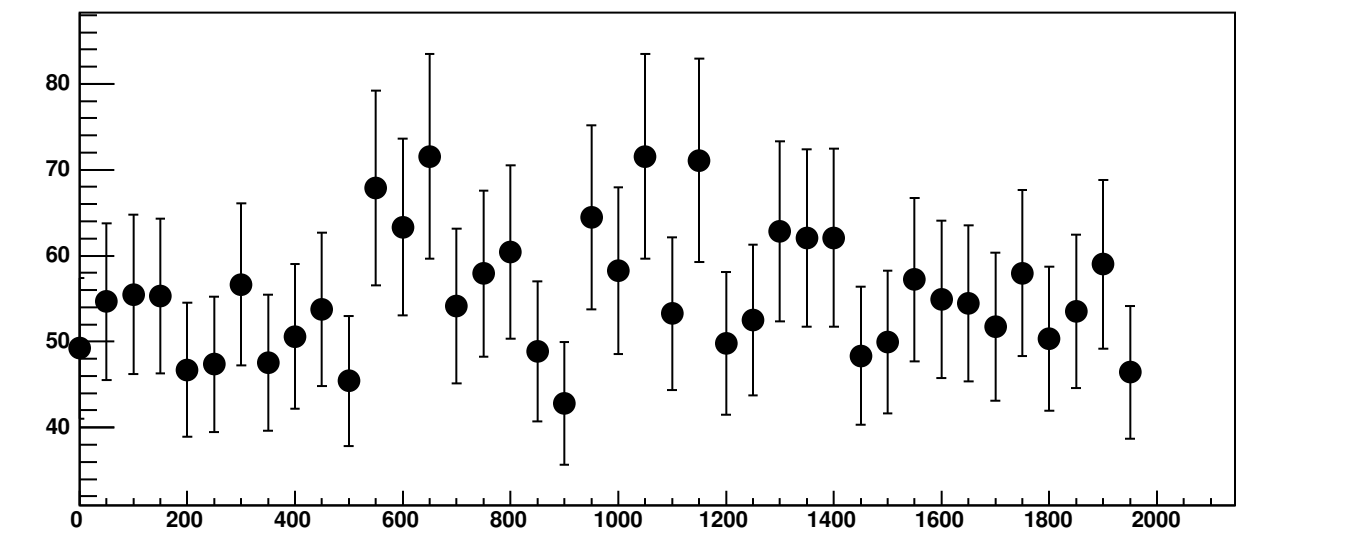

Chip 2, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

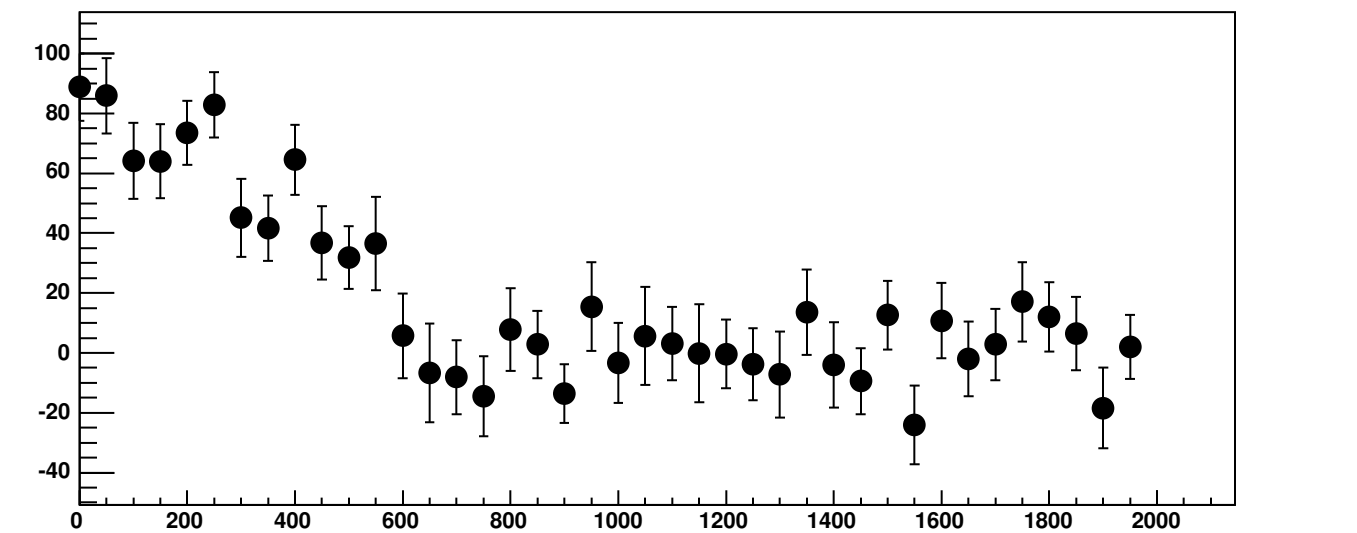
Chip 2, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC

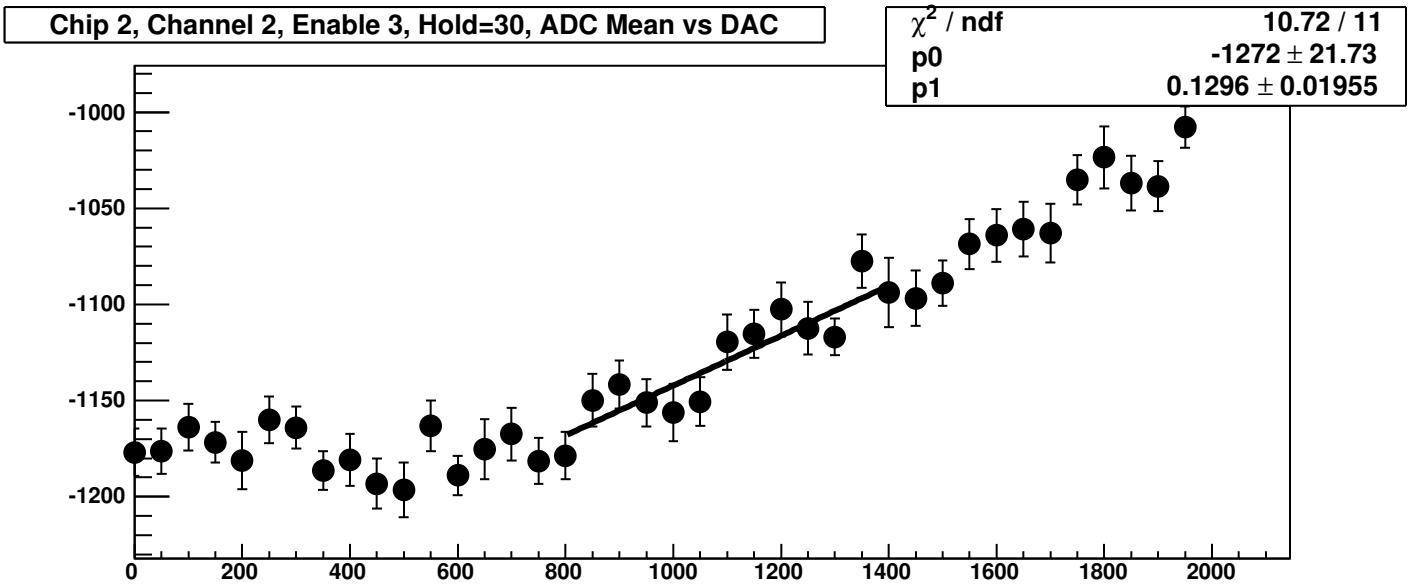


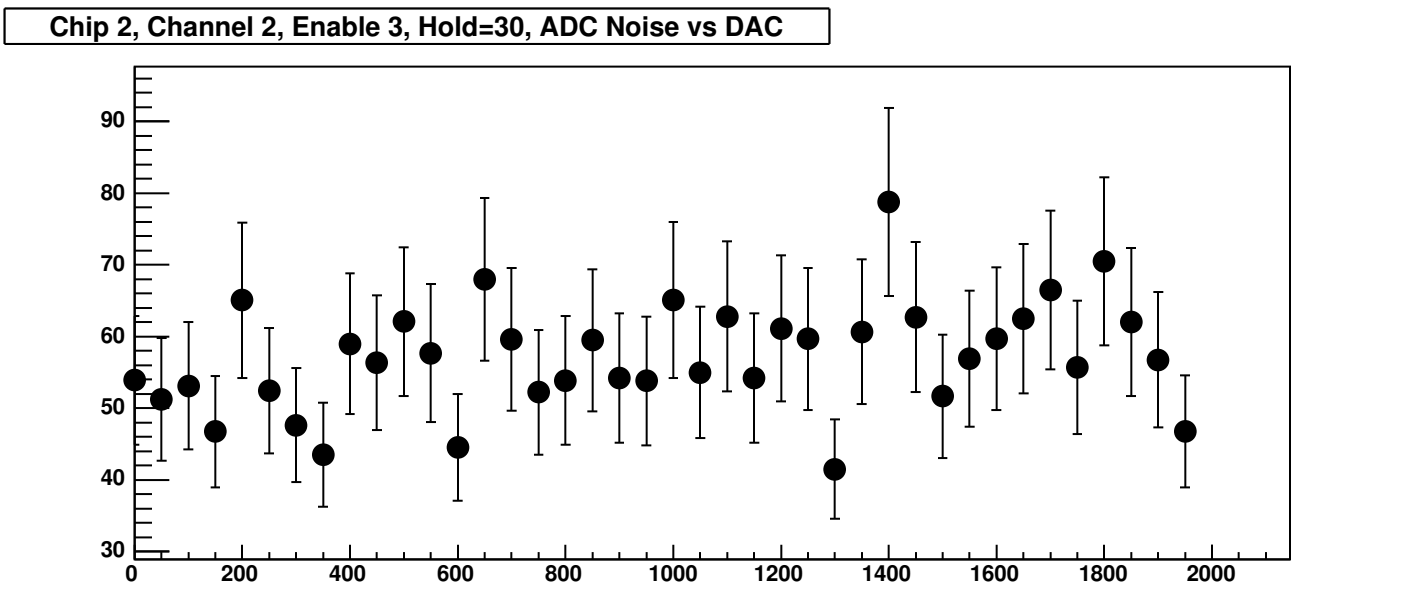
Chip 2, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC

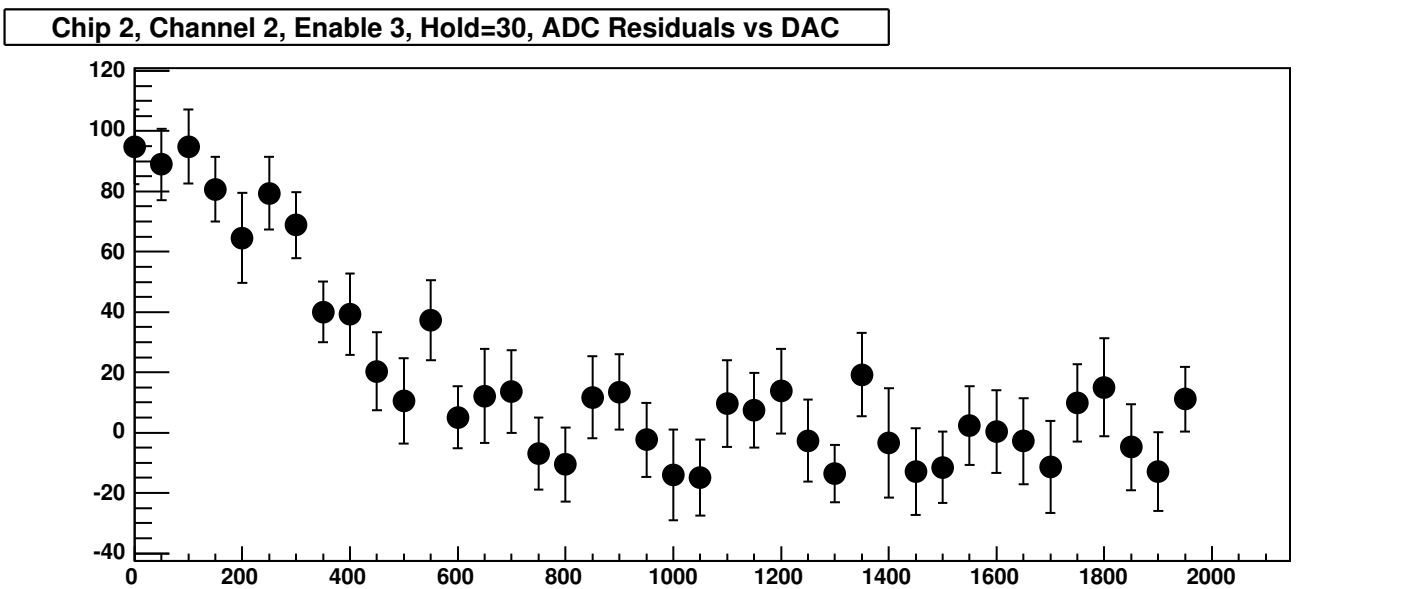


Chip 2, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC

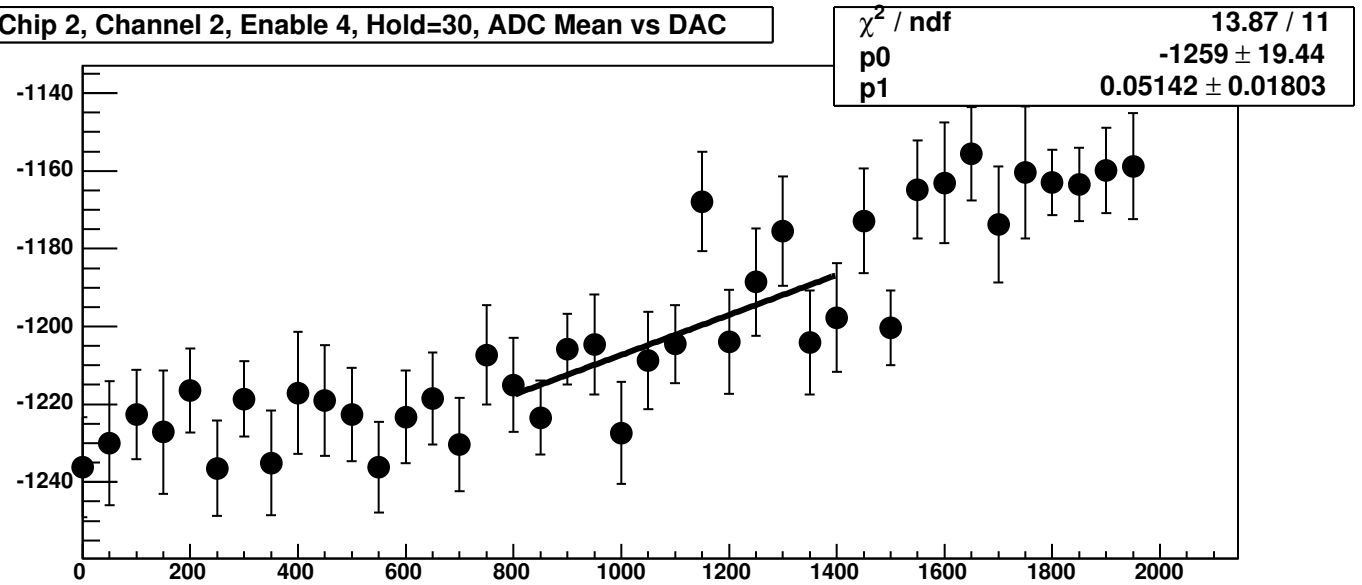



Chip 2, Channel 2, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 10.72 / 11
p0 -1272 ± 21.73
p1 0.1296 ± 0.01955
-1000
-1050
-1100
-1150
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

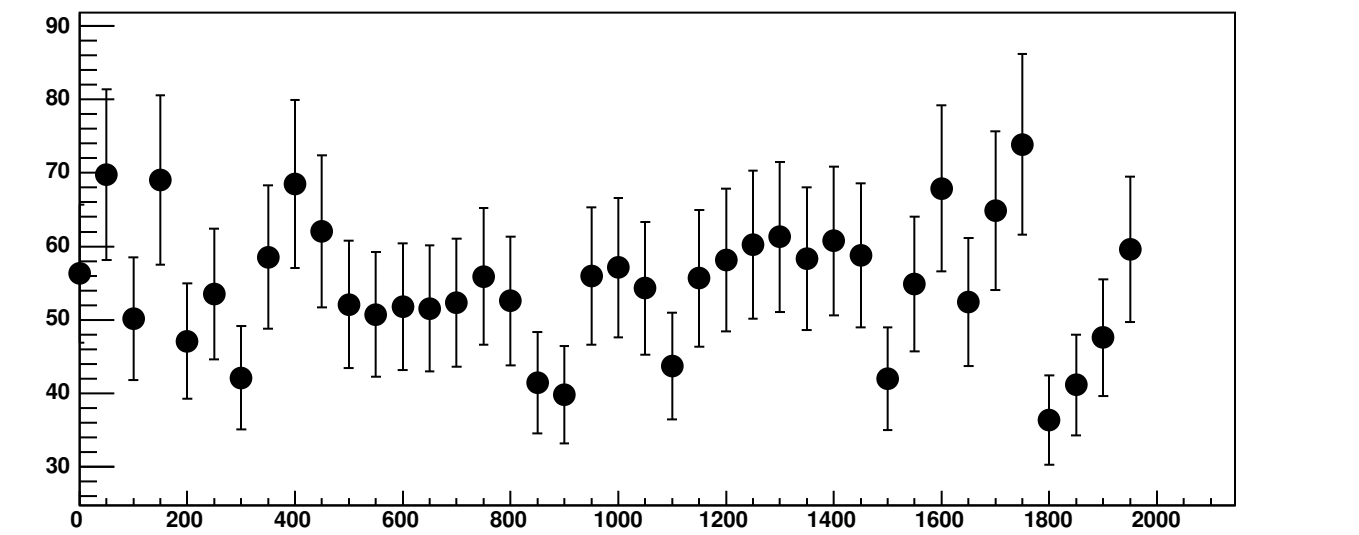

Chip 2, Channel 2, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 2, Enable 3, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

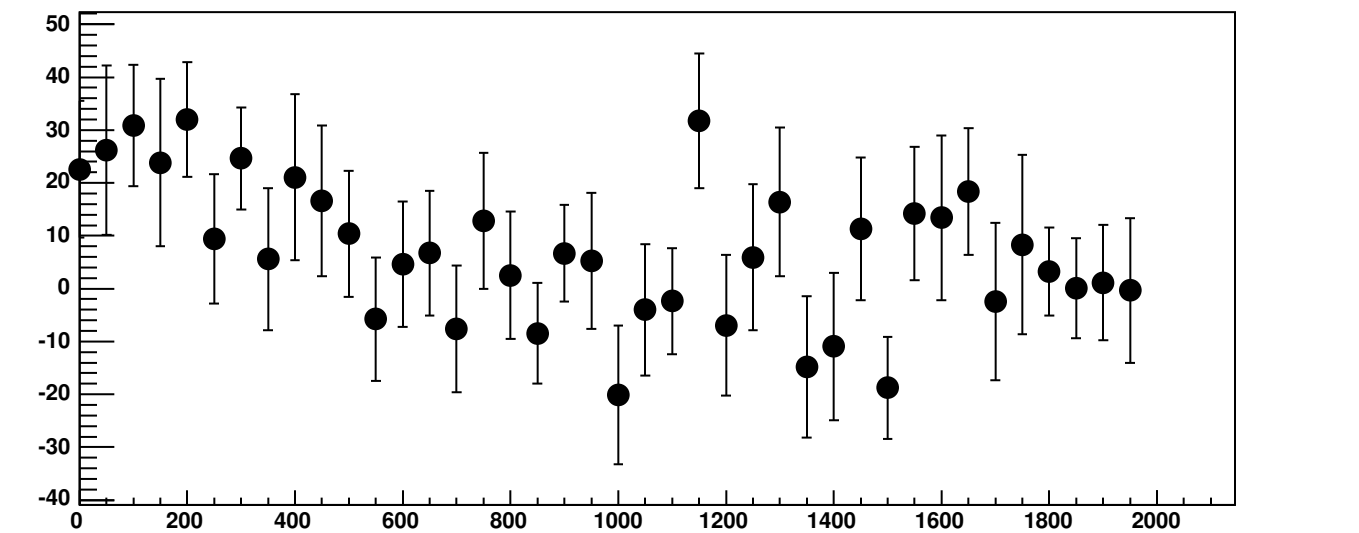
Chip 2, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC

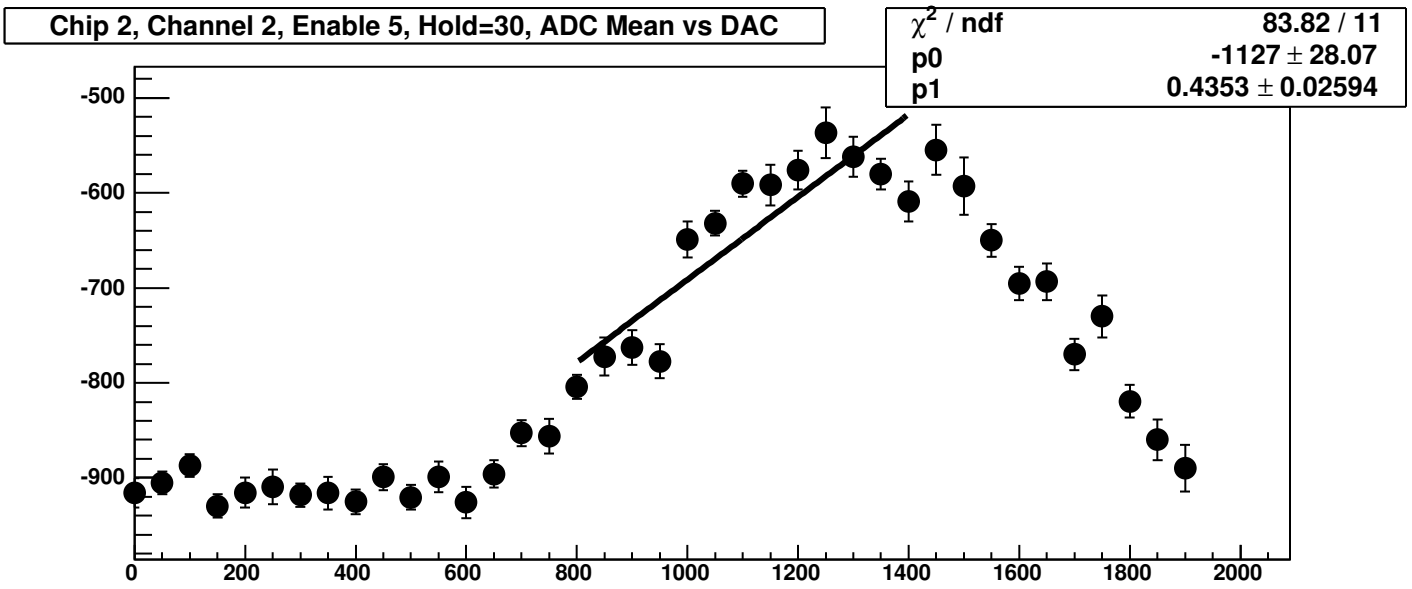


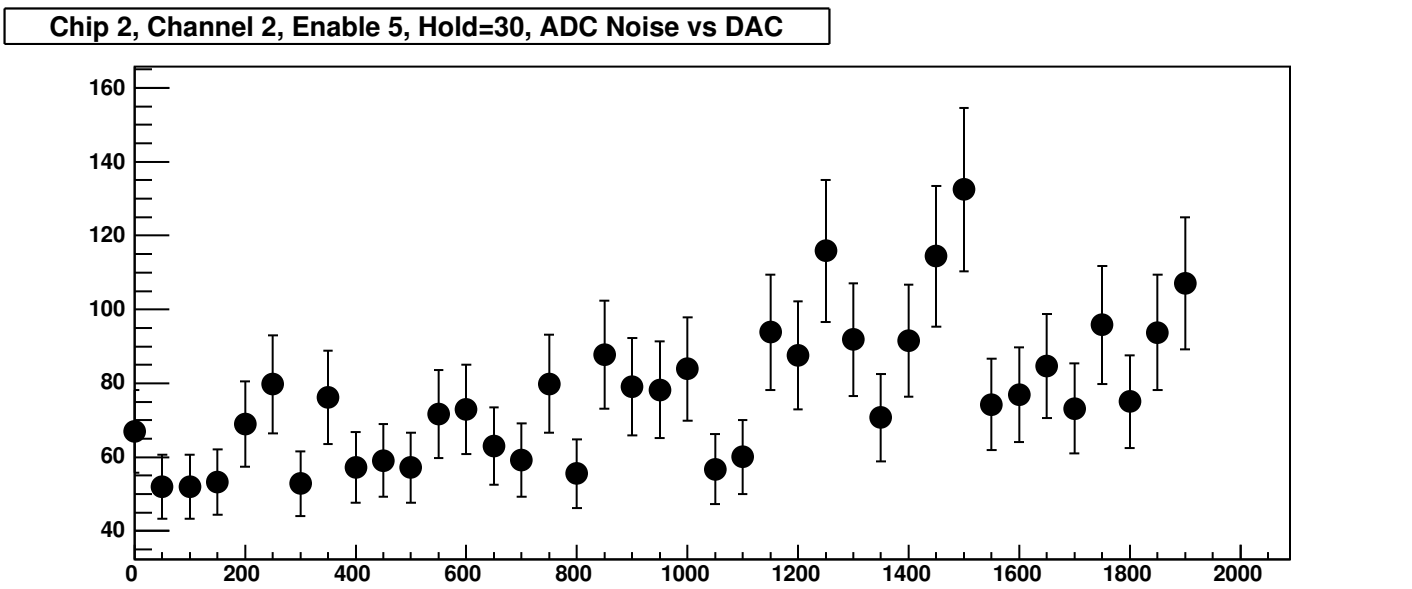
Chip 2, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC

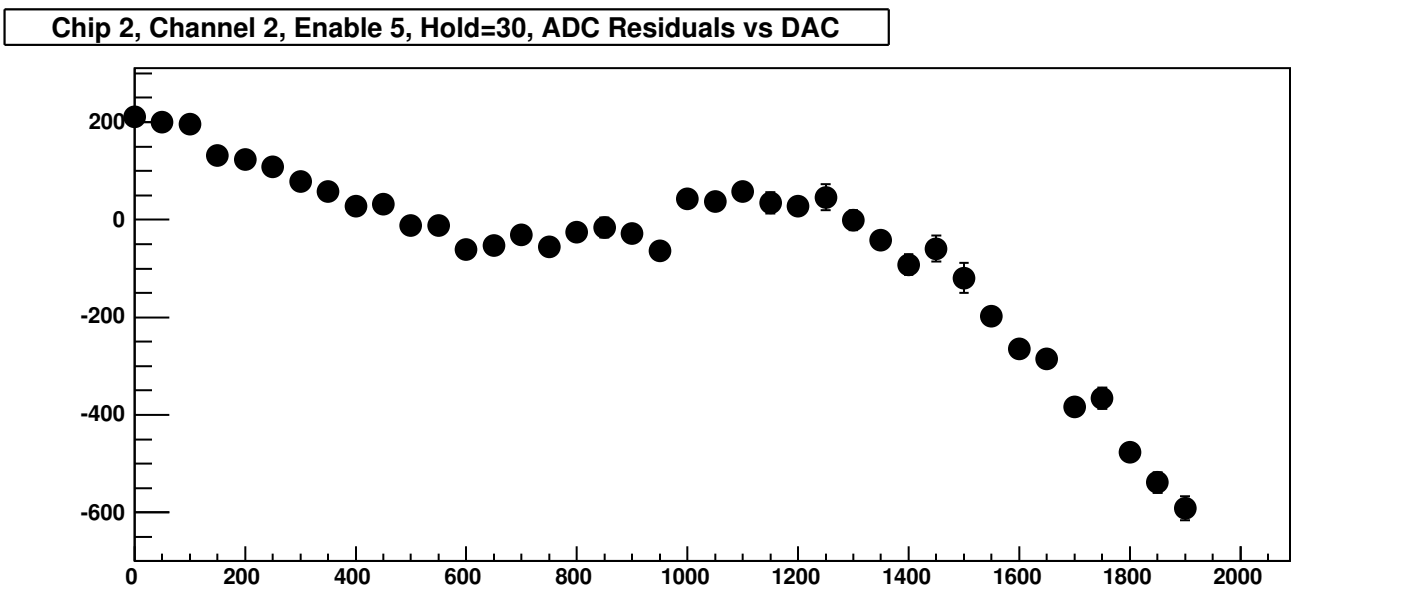


Chip 2, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC

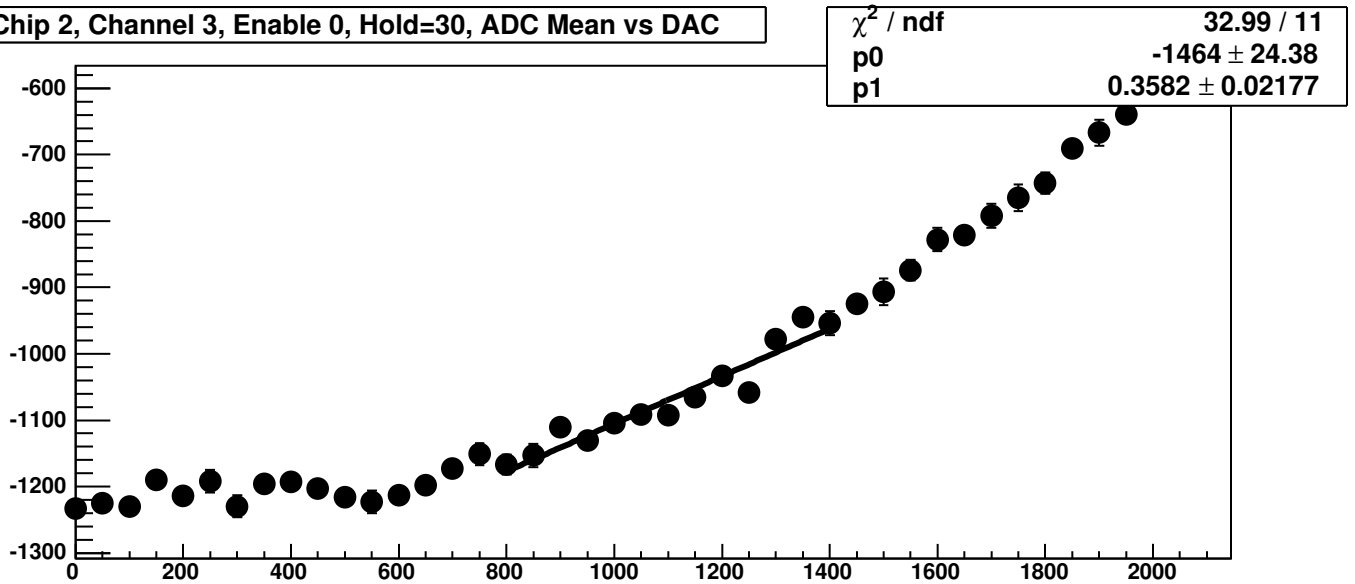



Chip 2, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 83.82 / 11
p0 -1127 ± 28.07
p1 0.4353 ± 0.02594
-500
-600
-700
-800
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

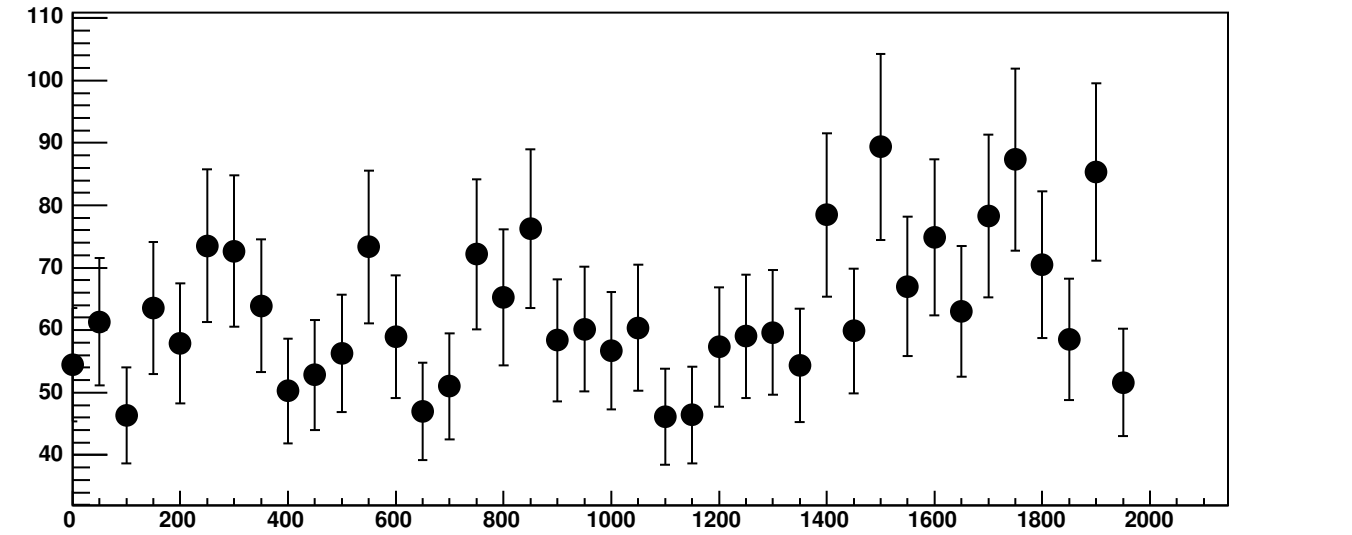

Chip 2, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

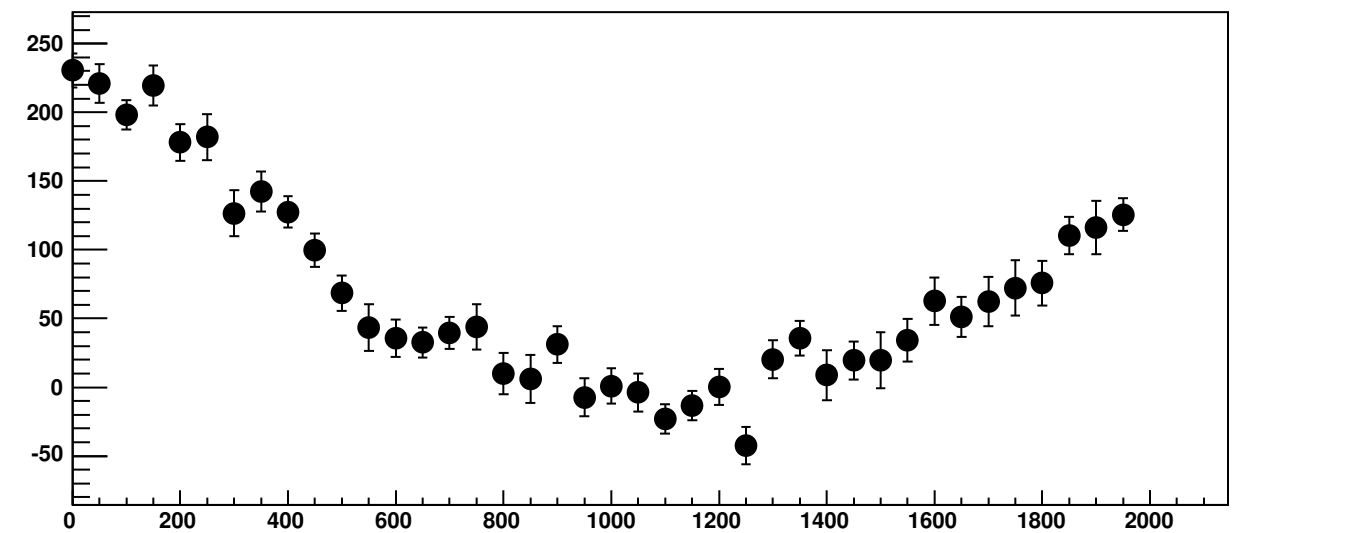
Chip 2, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC

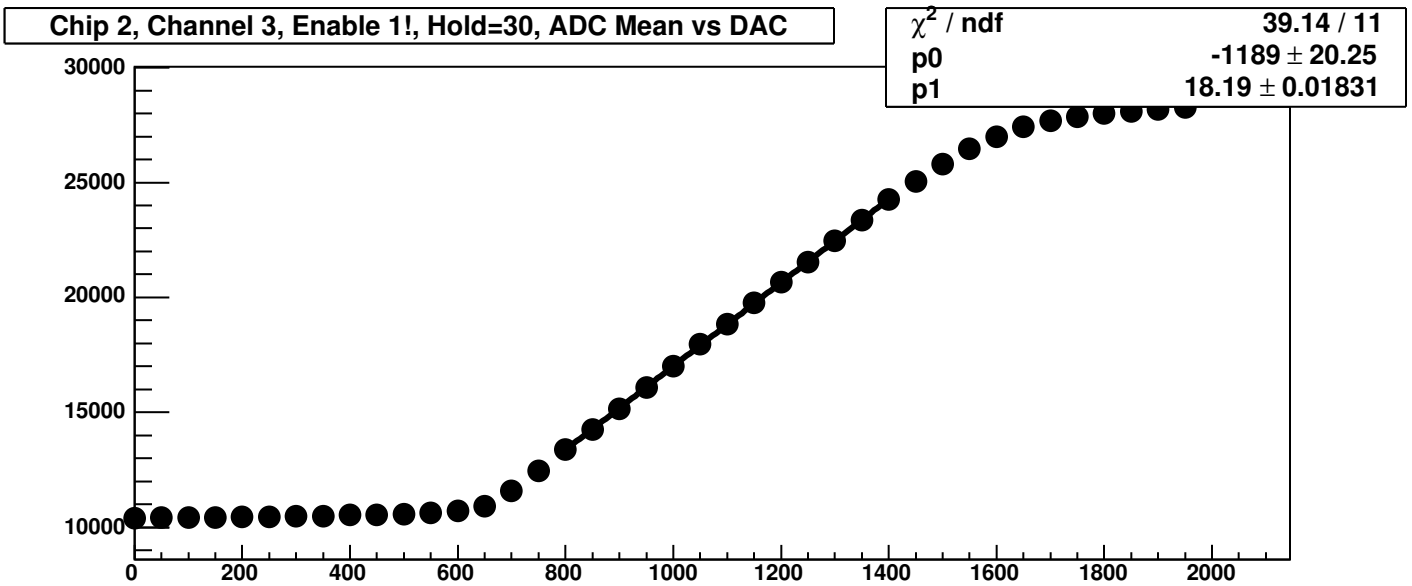


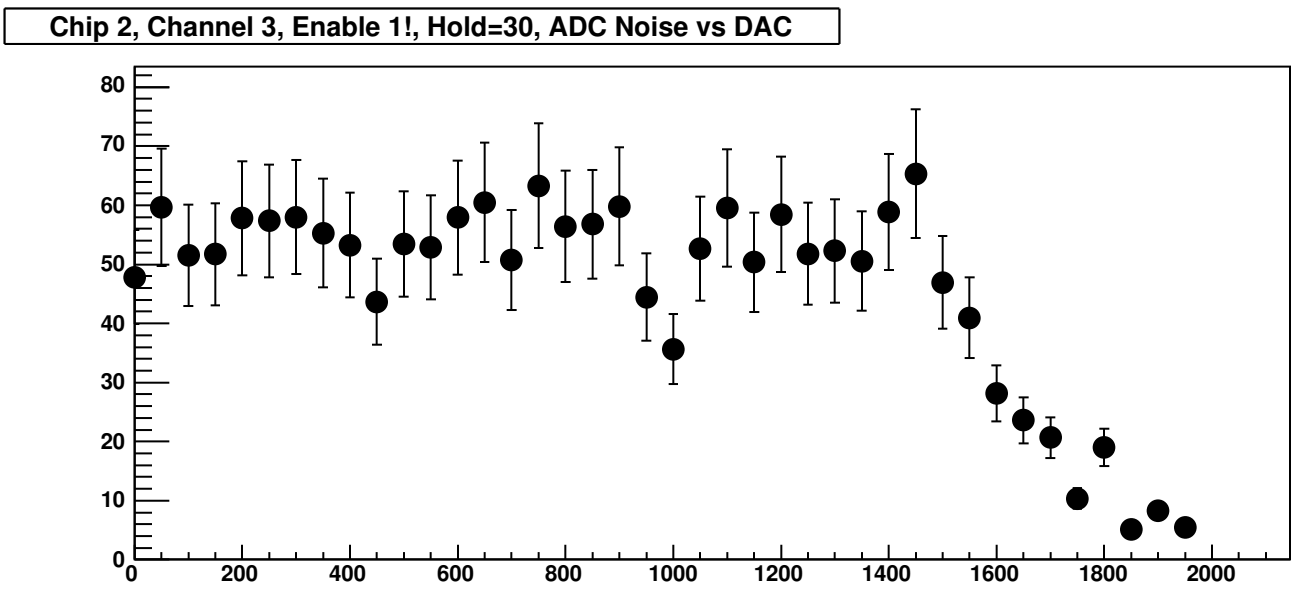
Chip 2, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC

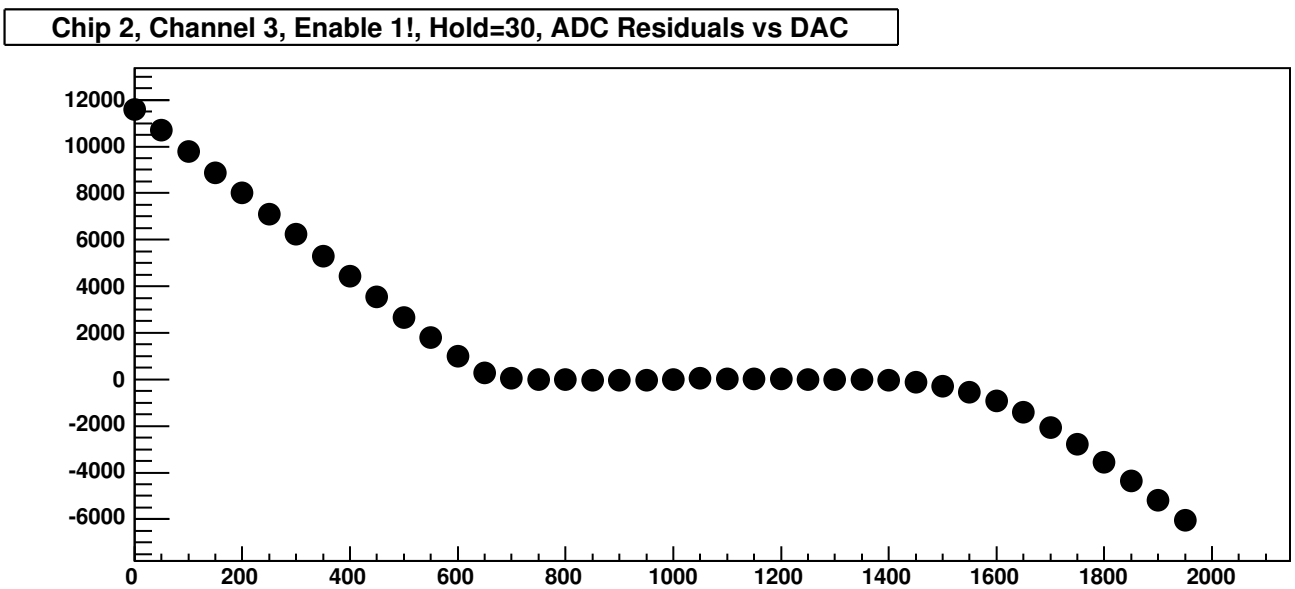


Chip 2, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC

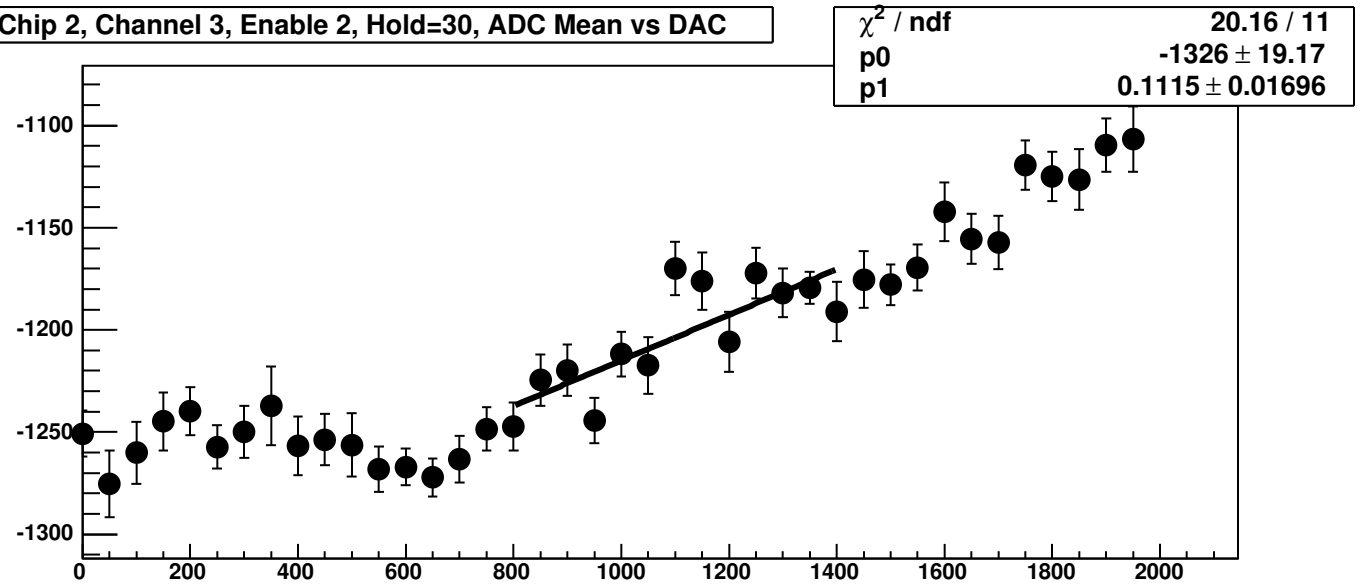



Chip 2, Channel 3, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 39.14 / 11
p0 -1189 ± 20.25
p1 18.19 ± 0.01831
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

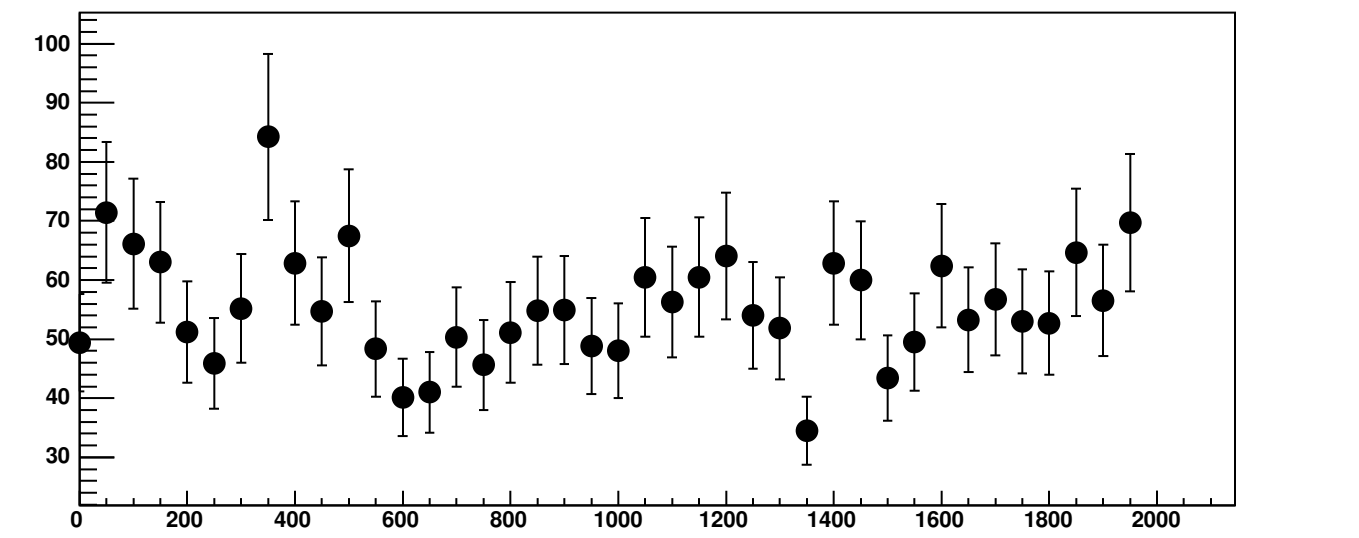

Chip 2, Channel 3, Enable 1!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 3, Enable 1!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

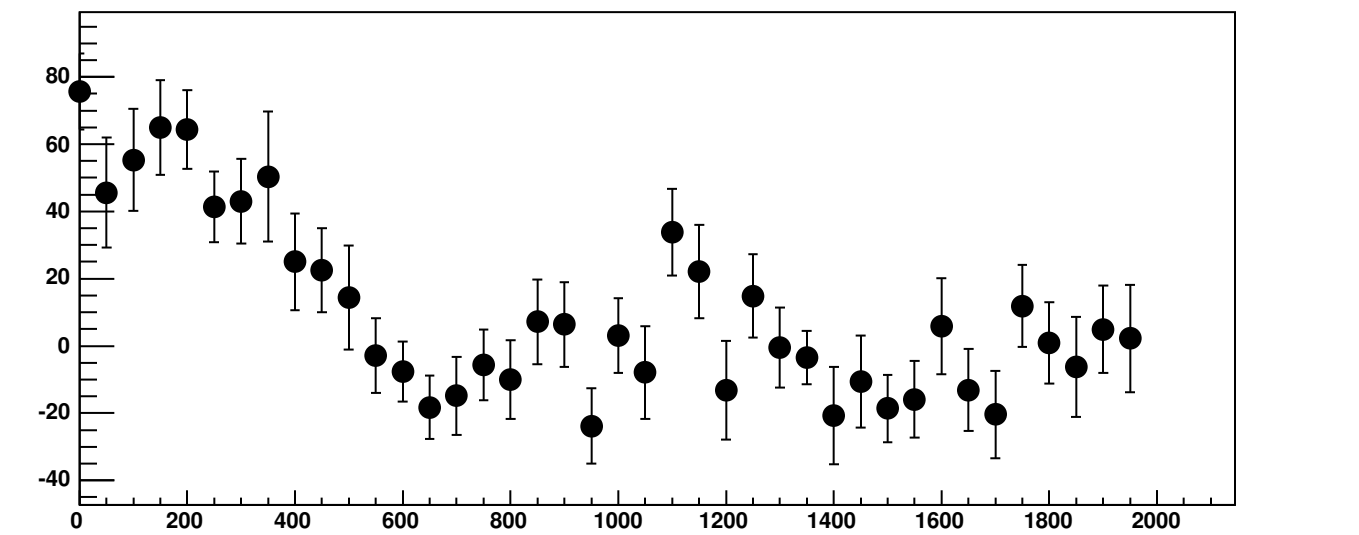
Chip 2, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC



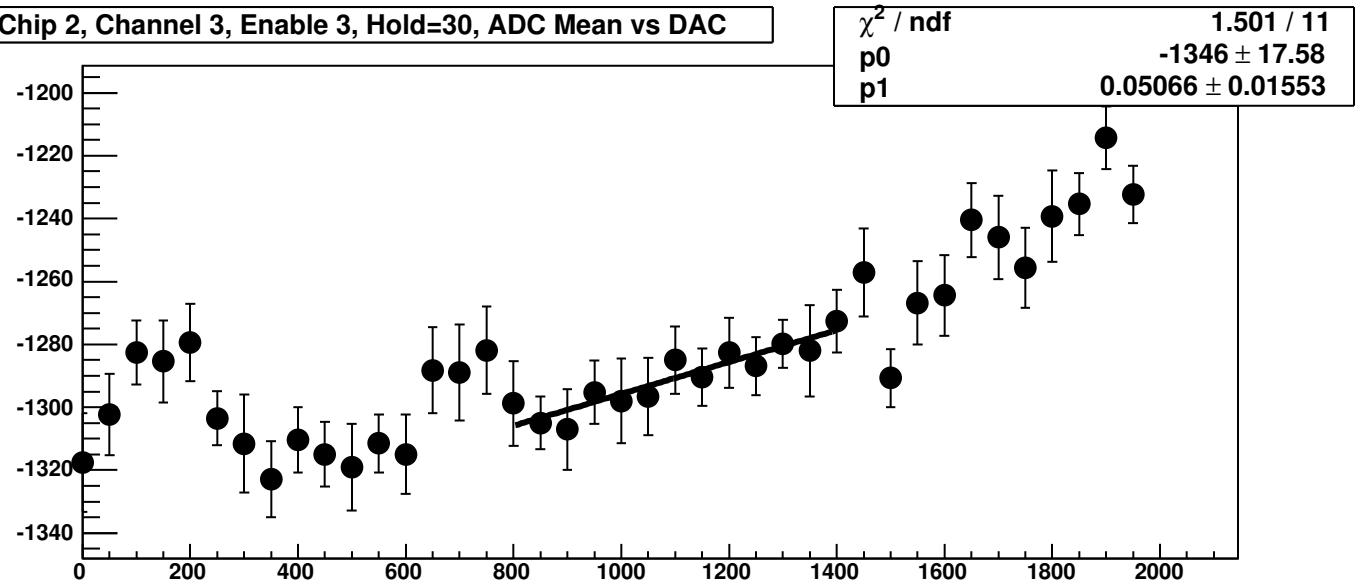
Chip 2, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC



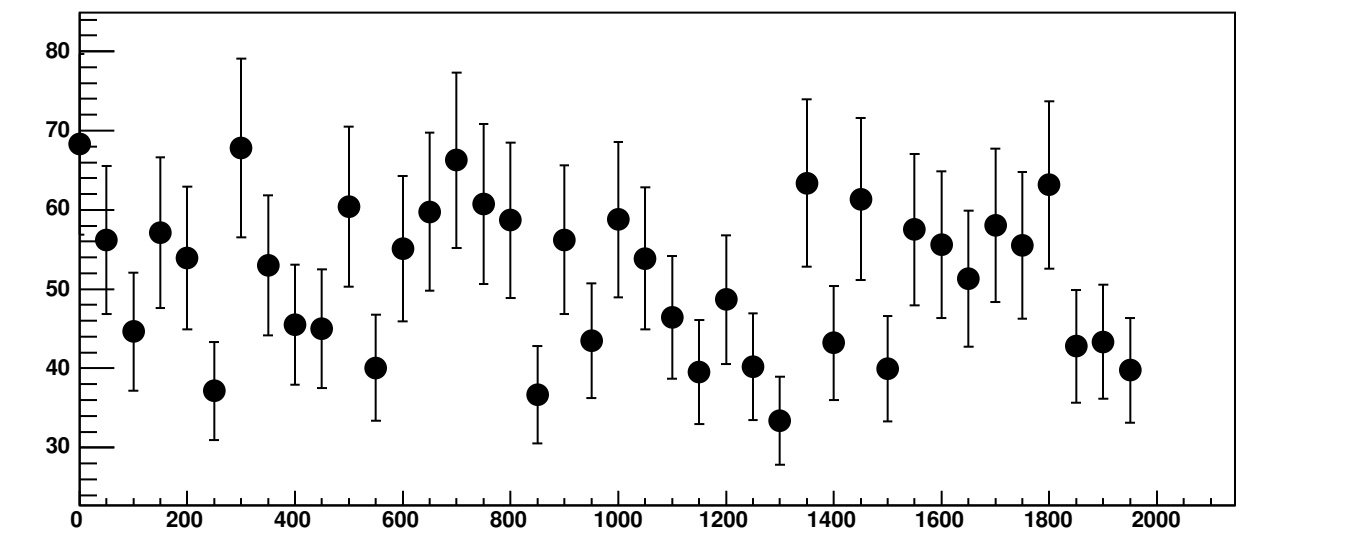
Chip 2, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC



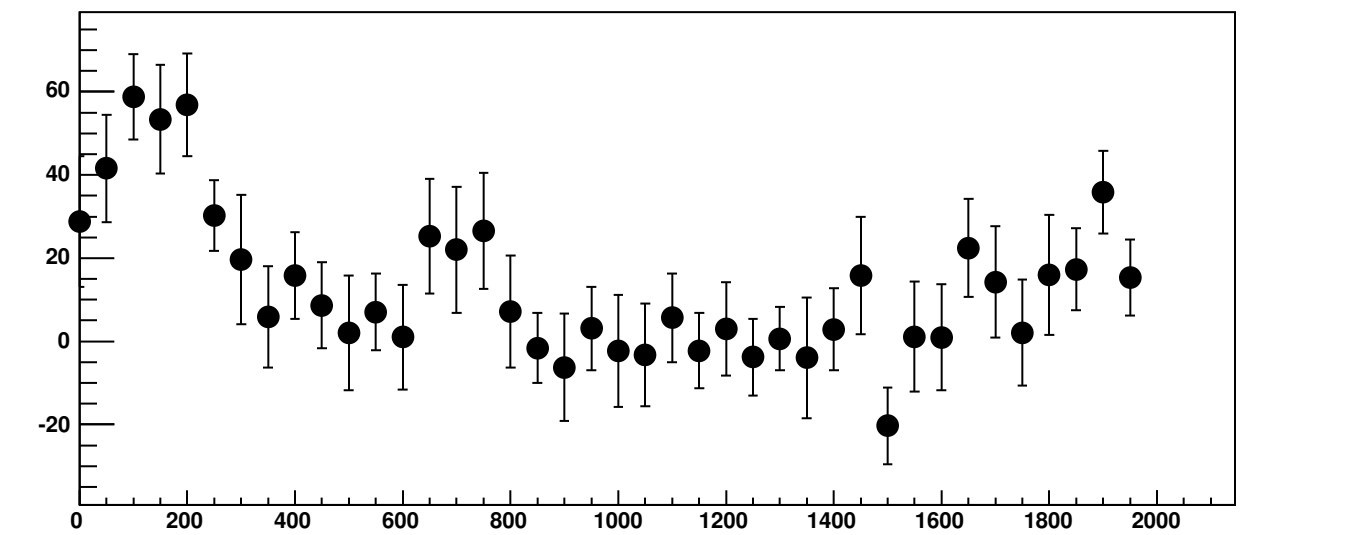
Chip 2, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC

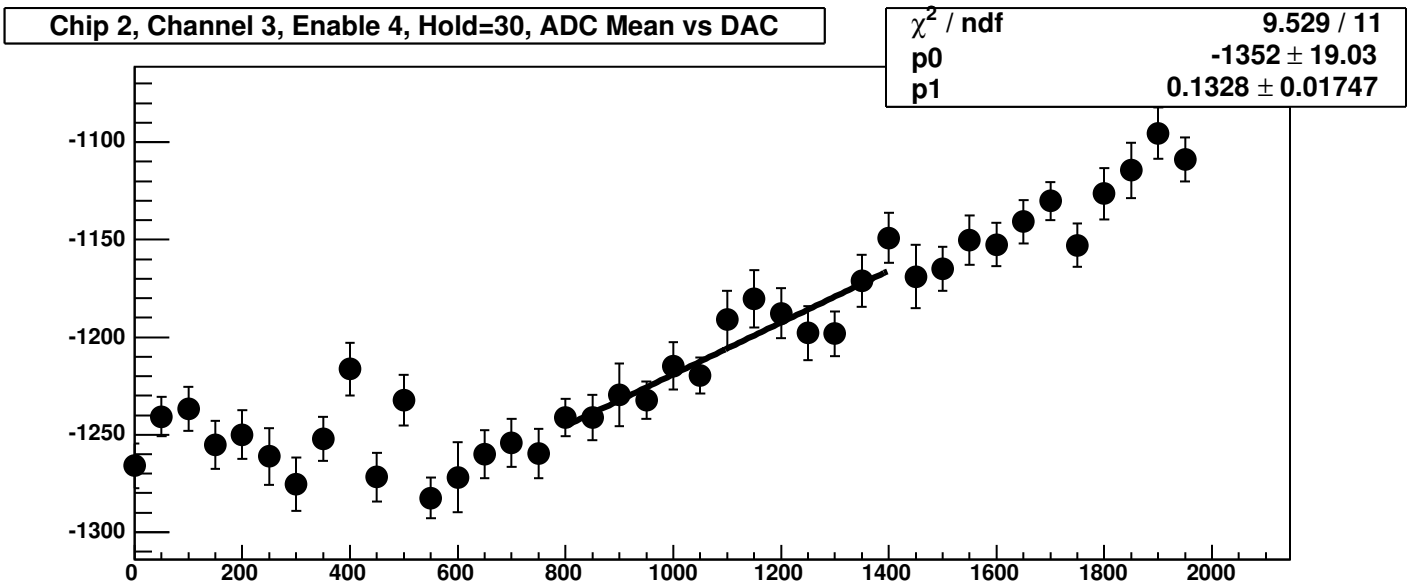


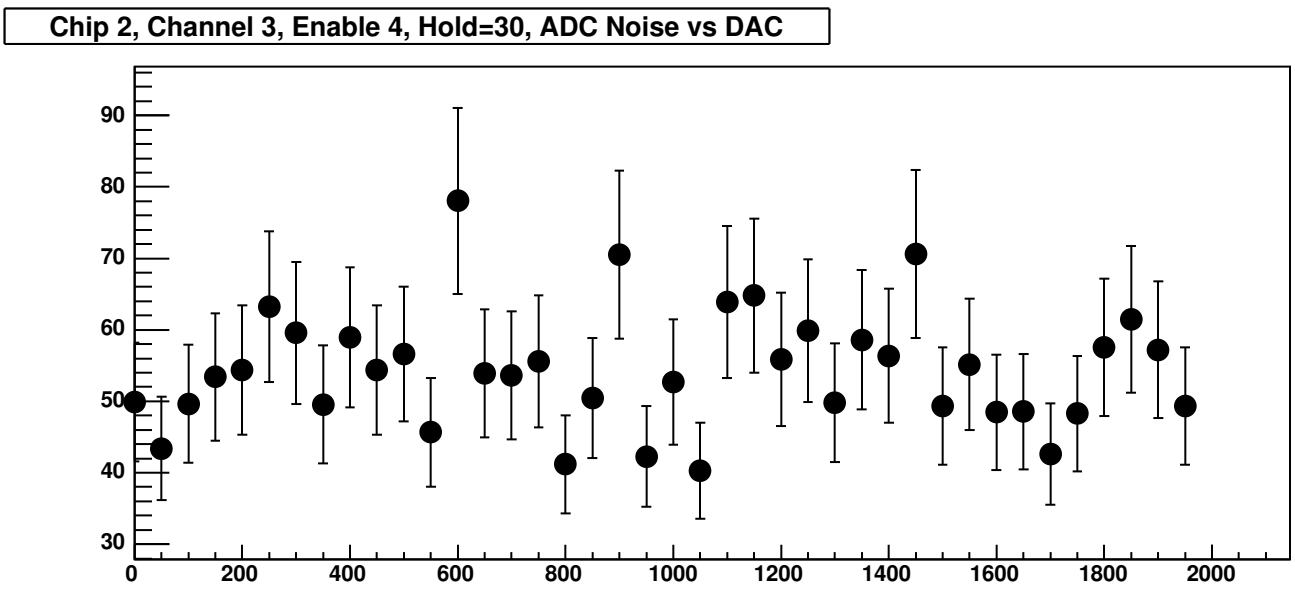
Chip 2, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC

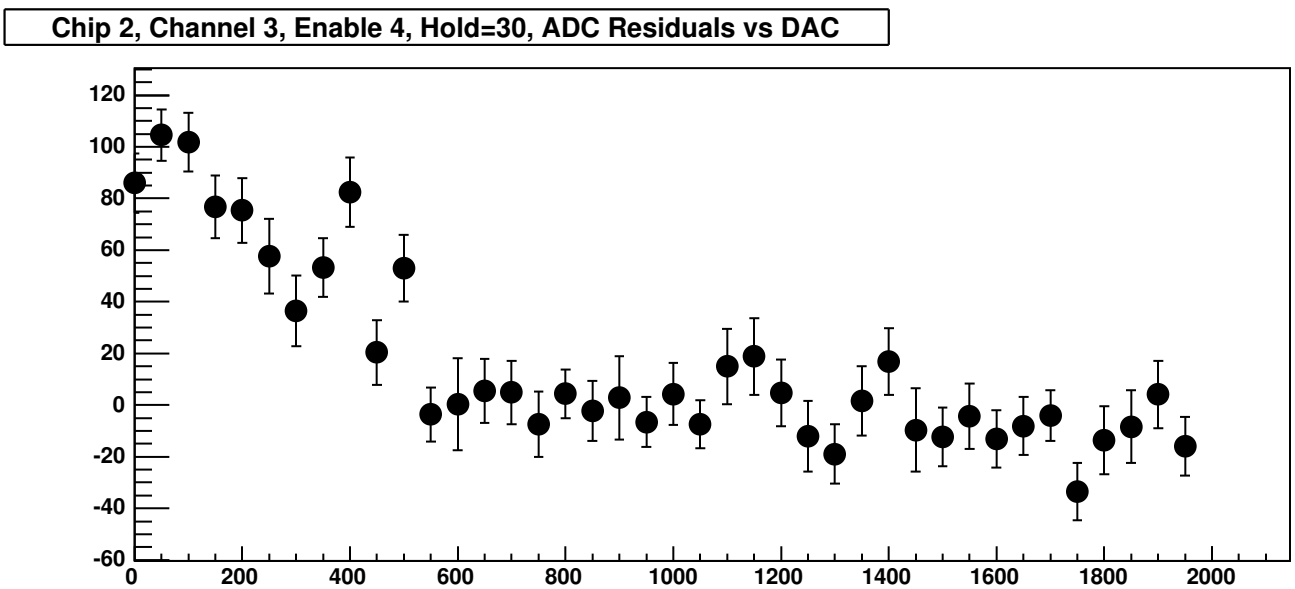


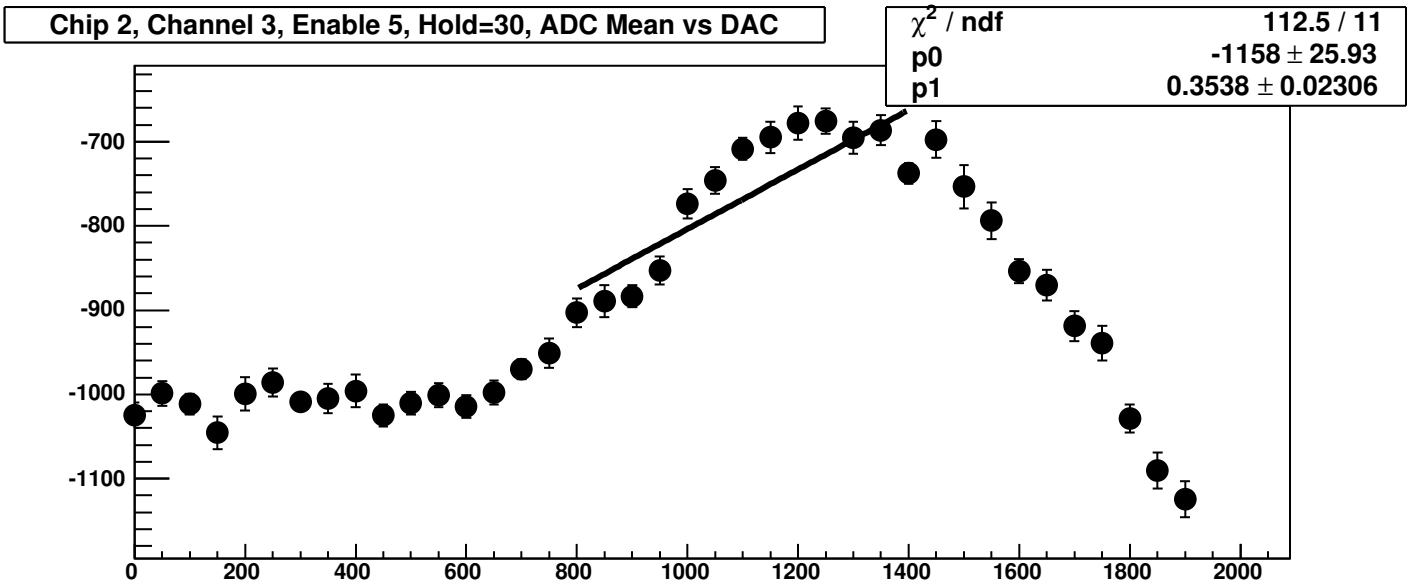
Chip 2, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC

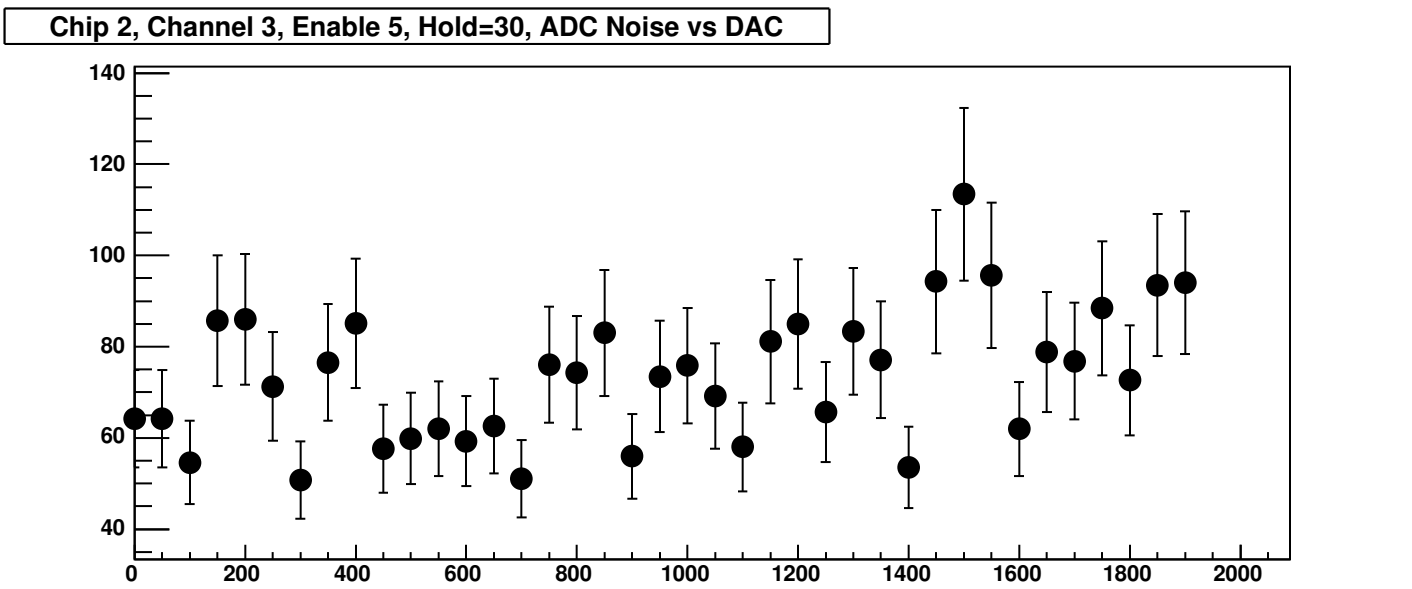


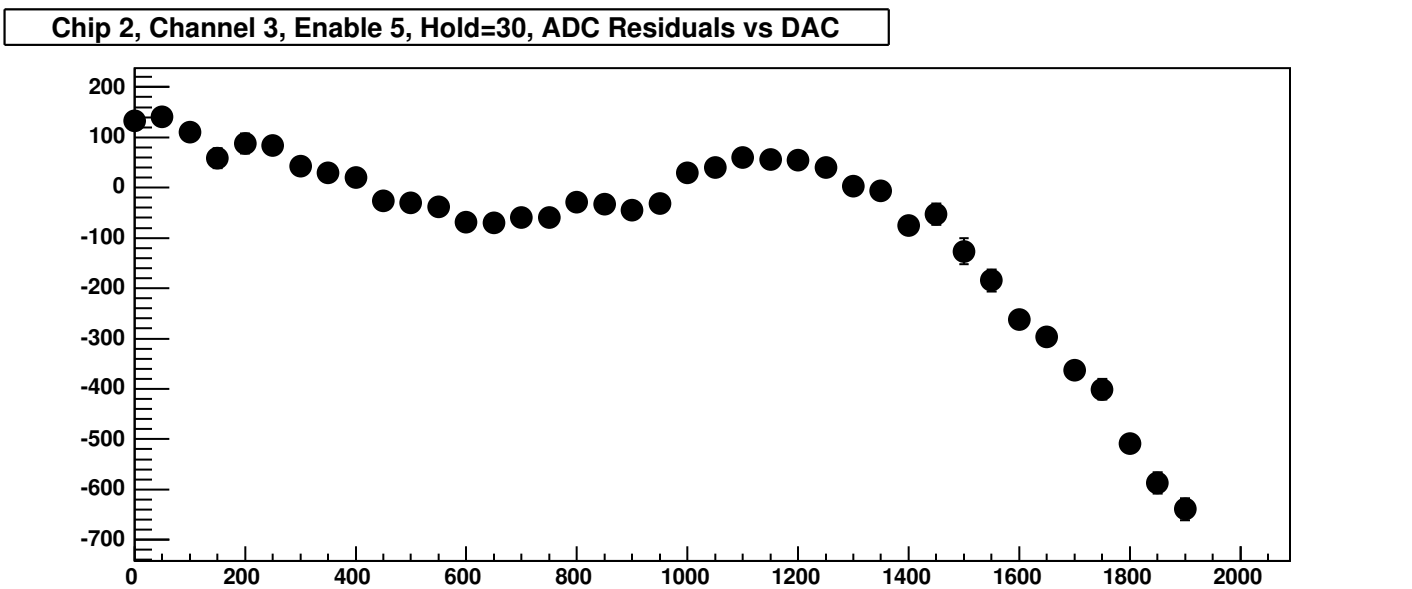

Chip 2, Channel 3, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 9.529 / 11
p0 -1352 ± 19.03
p1 0.1328 ± 0.01747
-1100
-1150
-1200
-1250
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 3, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

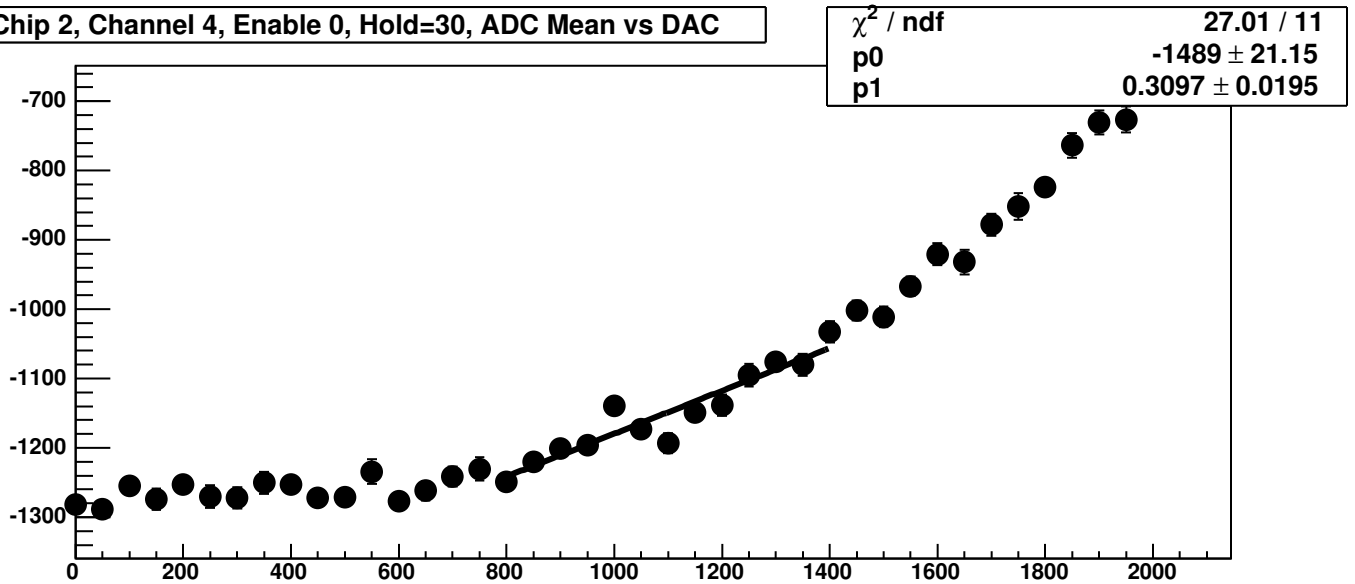

Chip 2, Channel 3, Enable 4, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 112.5 / 11
p0 -1158 ± 25.93
p1 0.3538 ± 0.02306
-700
-800
-900
-1000
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

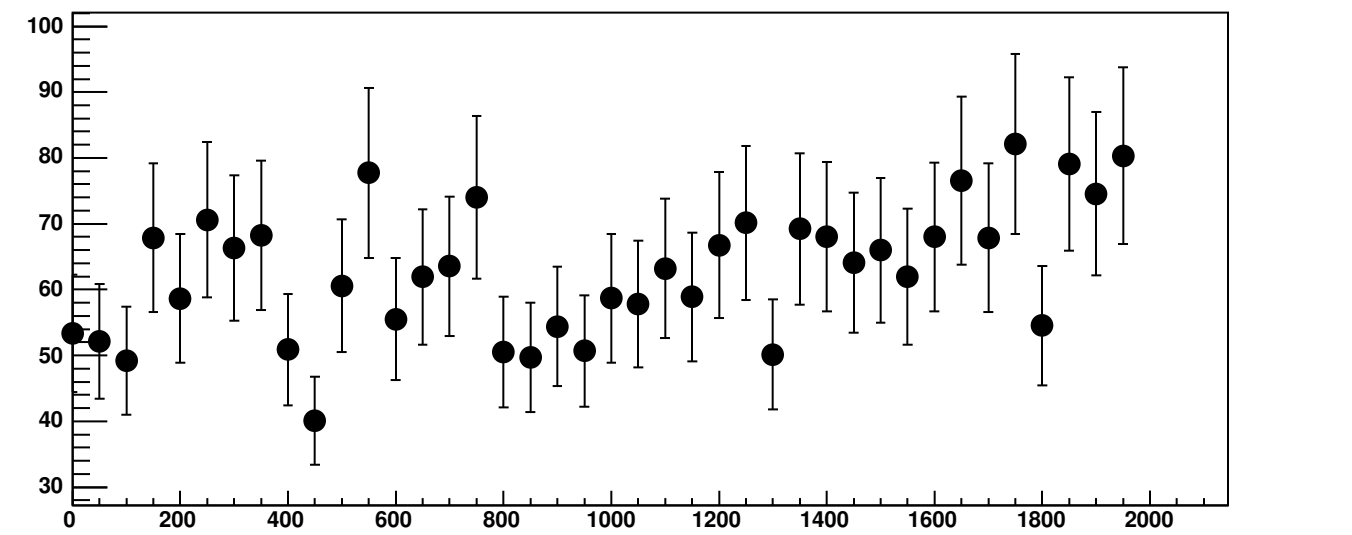

Chip 2, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
200
100
0
-100
-200
-300
-400
-500
-600
-700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

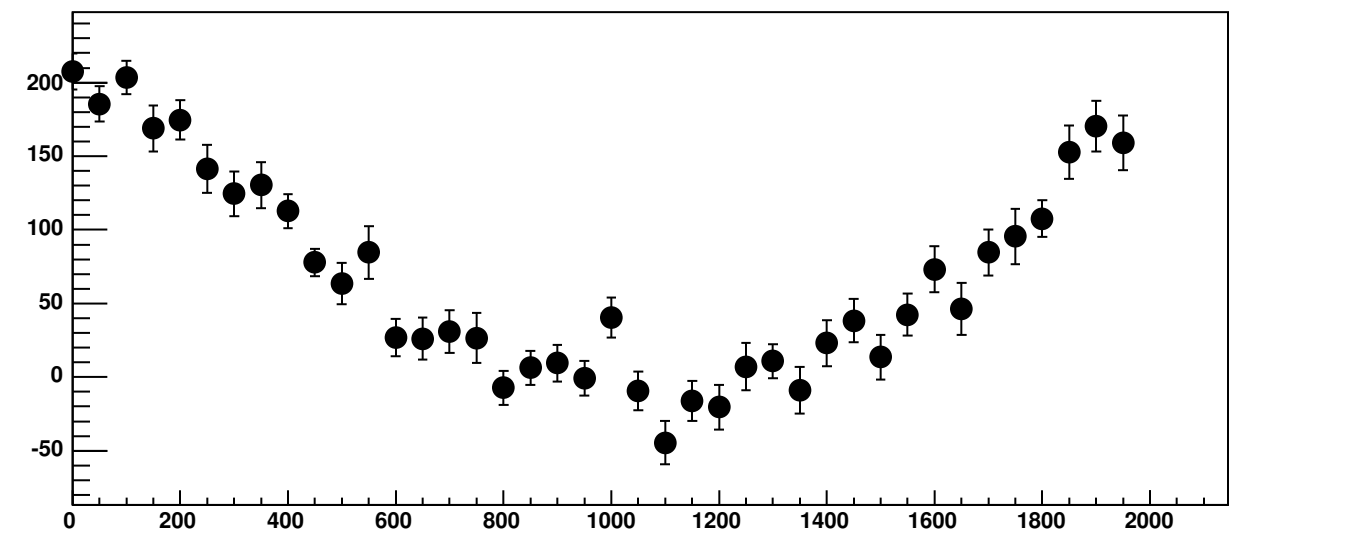
Chip 2, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC



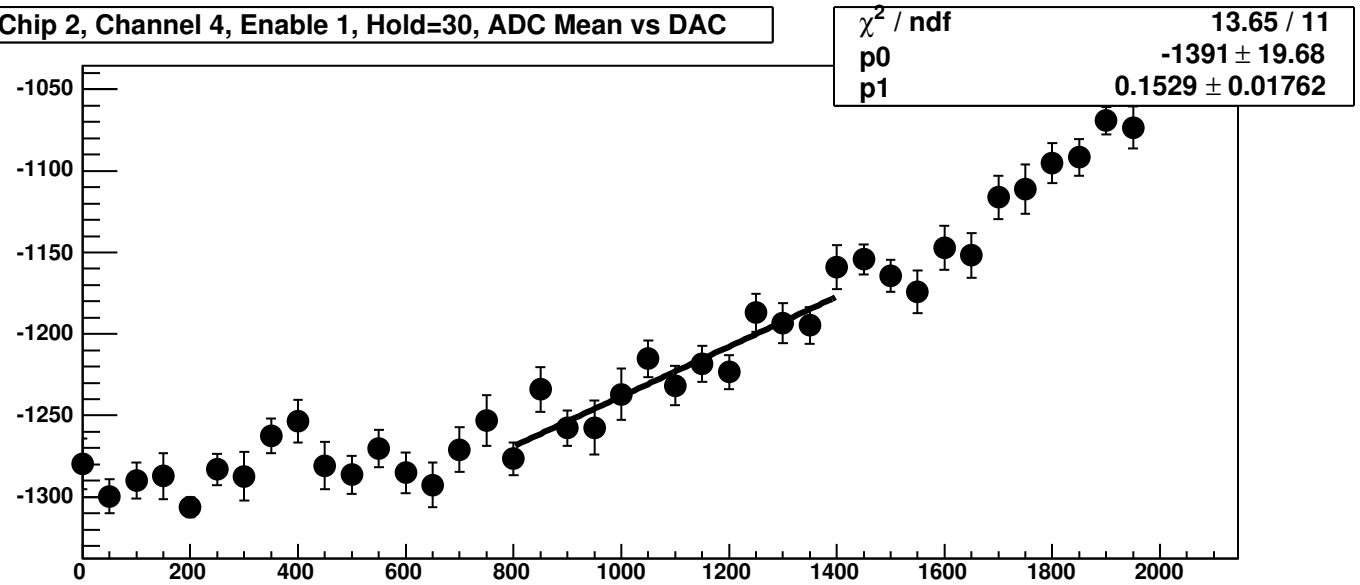
Chip 2, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC



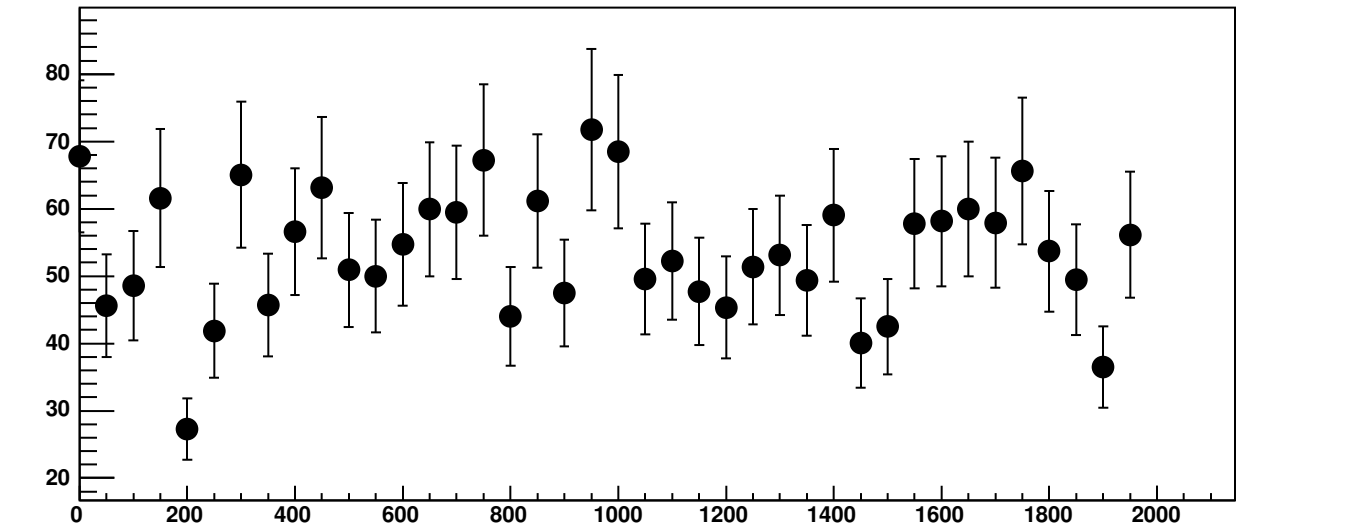
Chip 2, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC



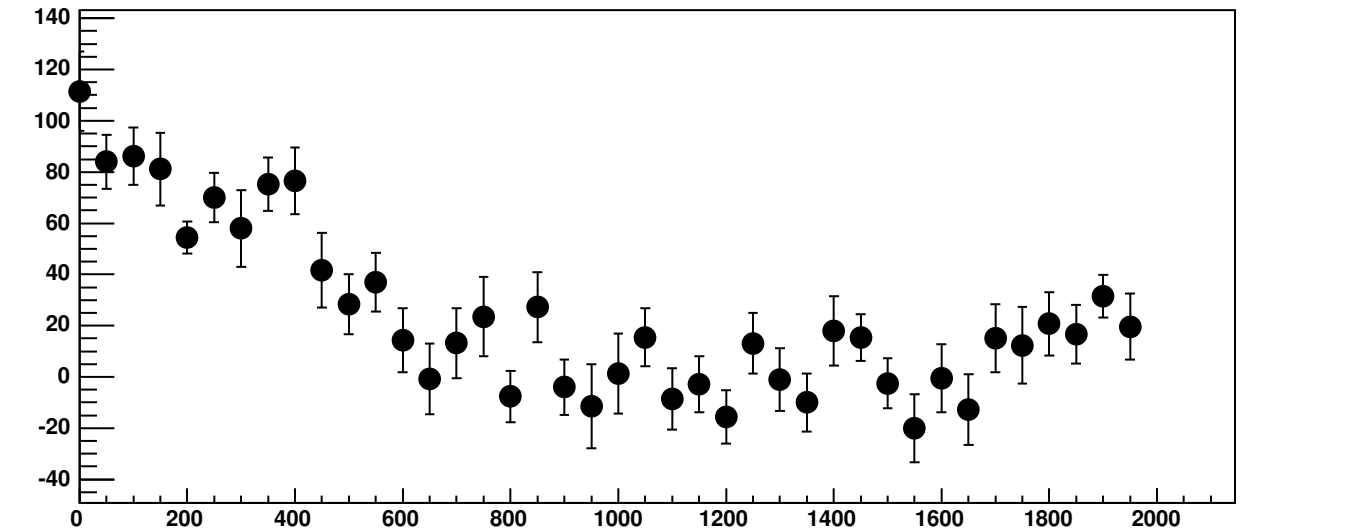
Chip 2, Channel 4, Enable 1, Hold=30, ADC Mean vs DAC

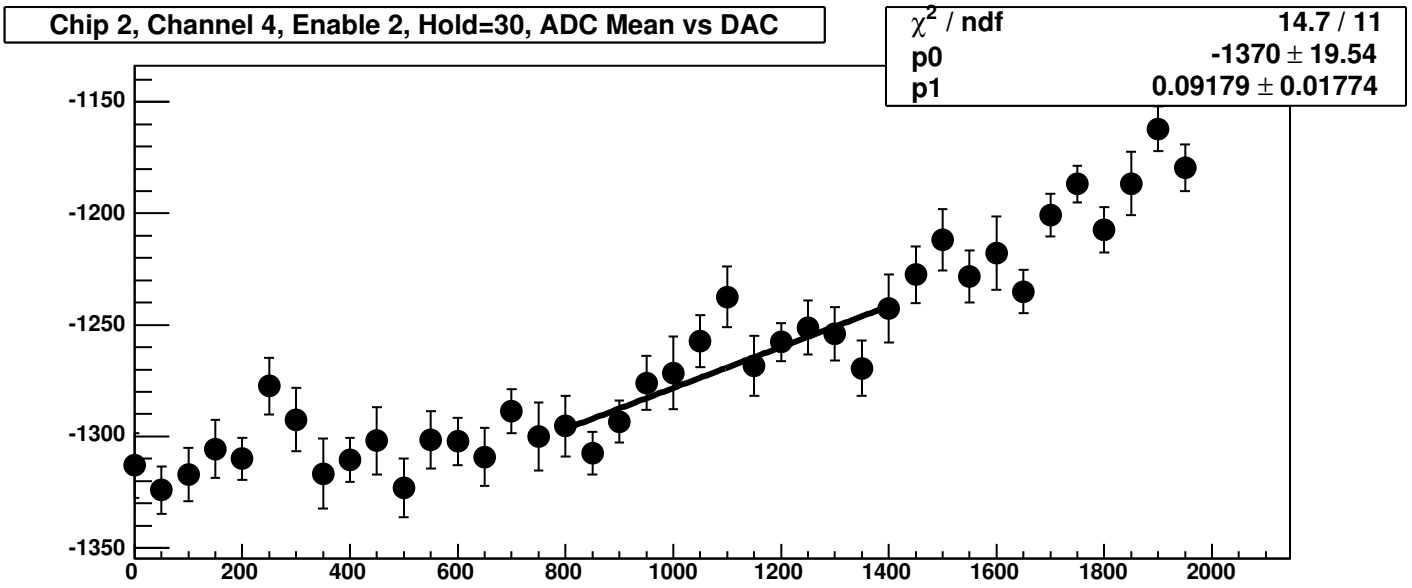


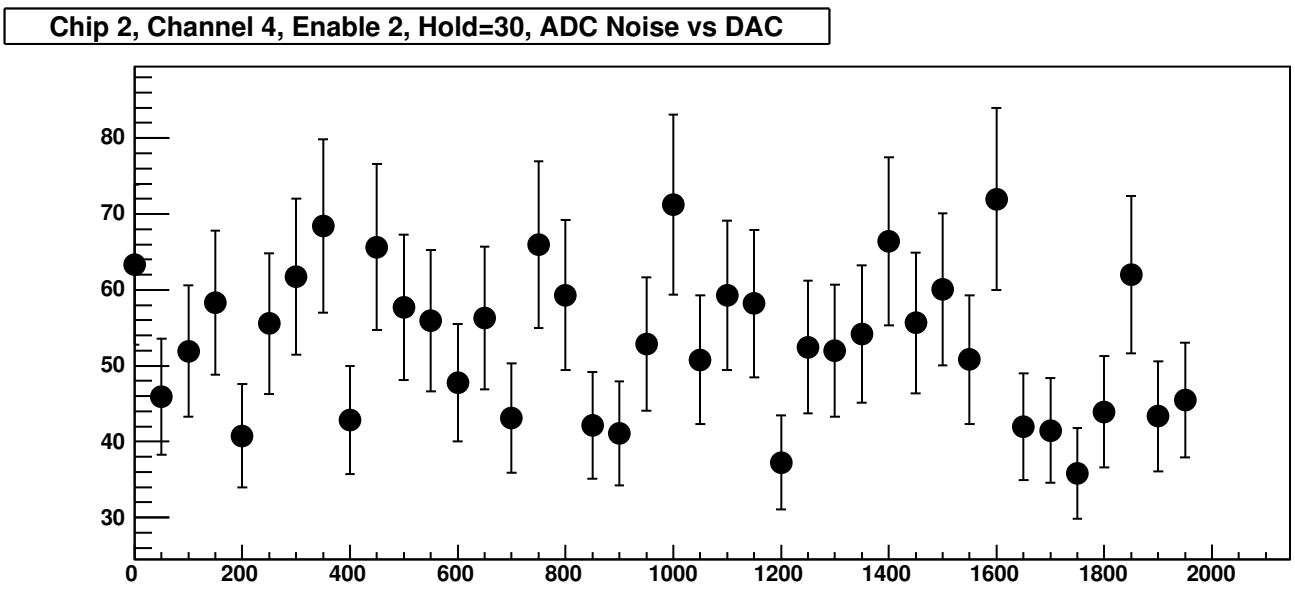
Chip 2, Channel 4, Enable 1, Hold=30, ADC Noise vs DAC

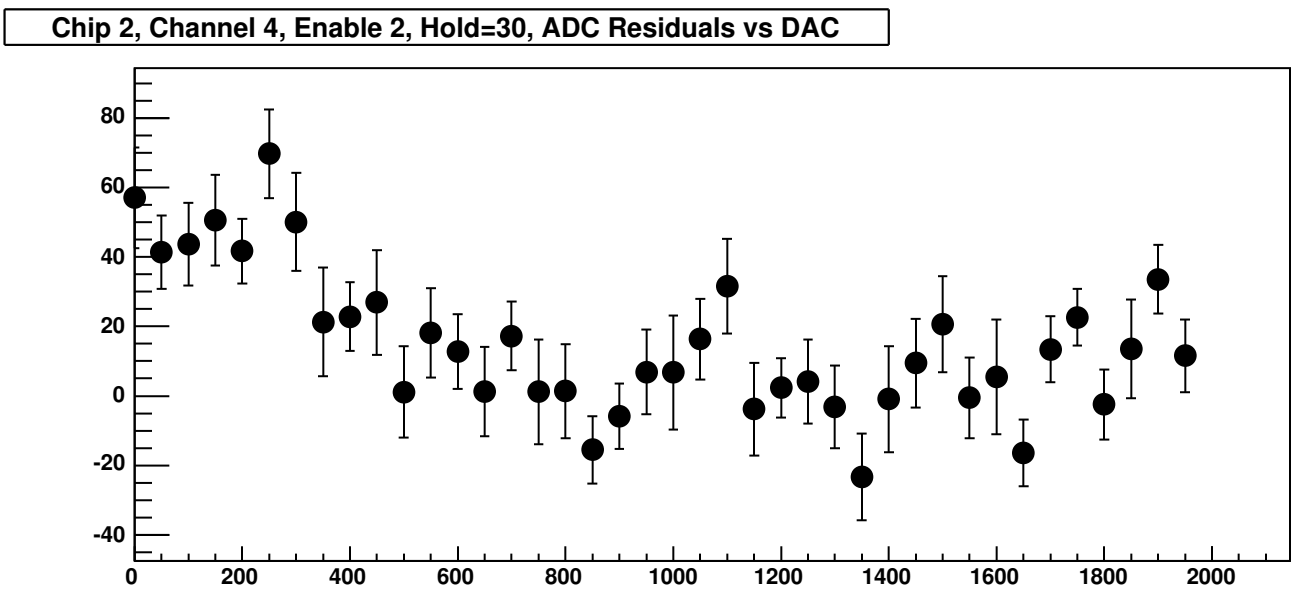


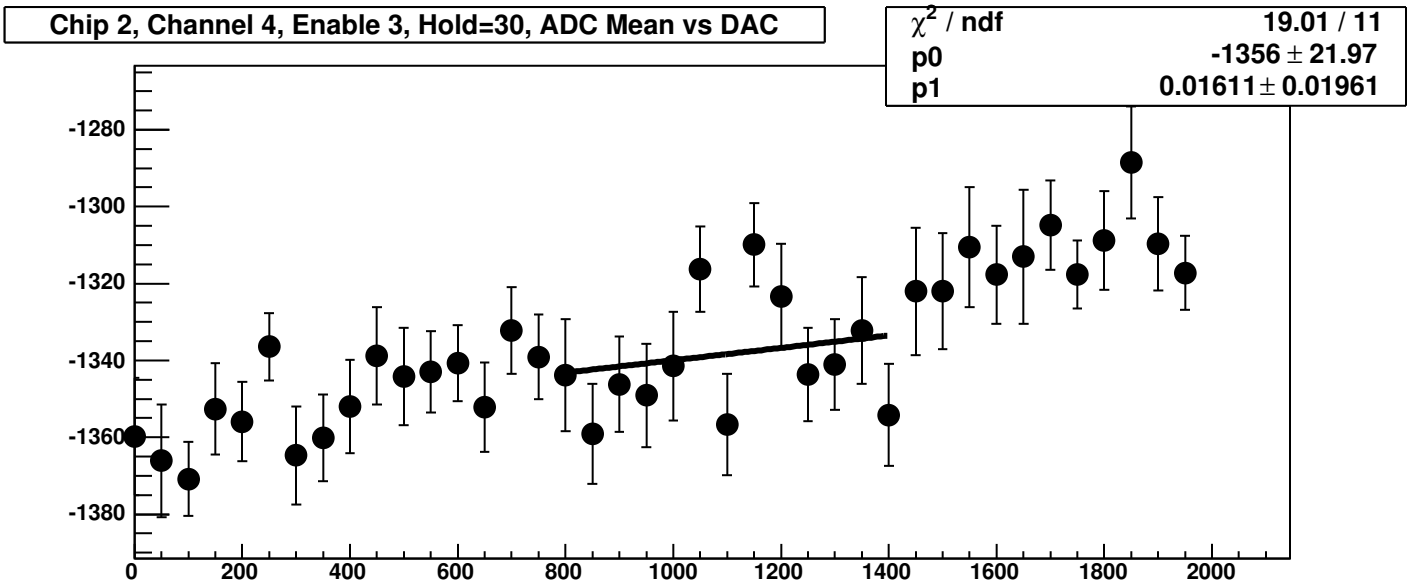
Chip 2, Channel 4, Enable 1, Hold=30, ADC Residuals vs DAC

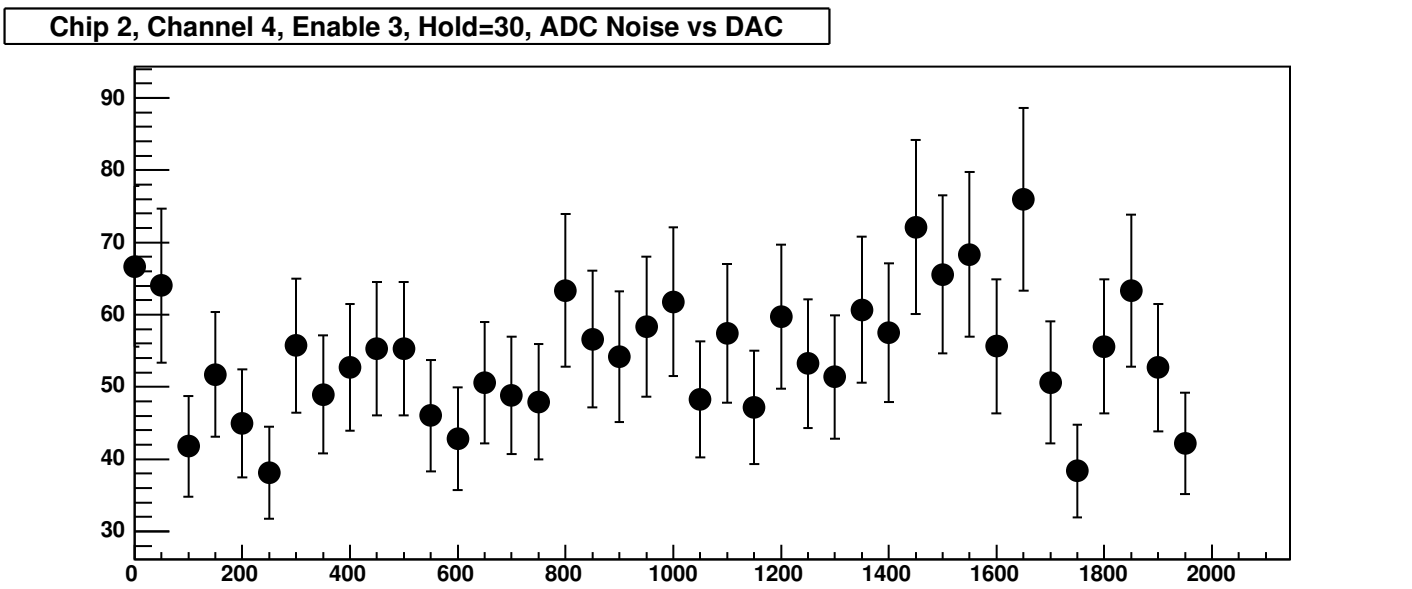


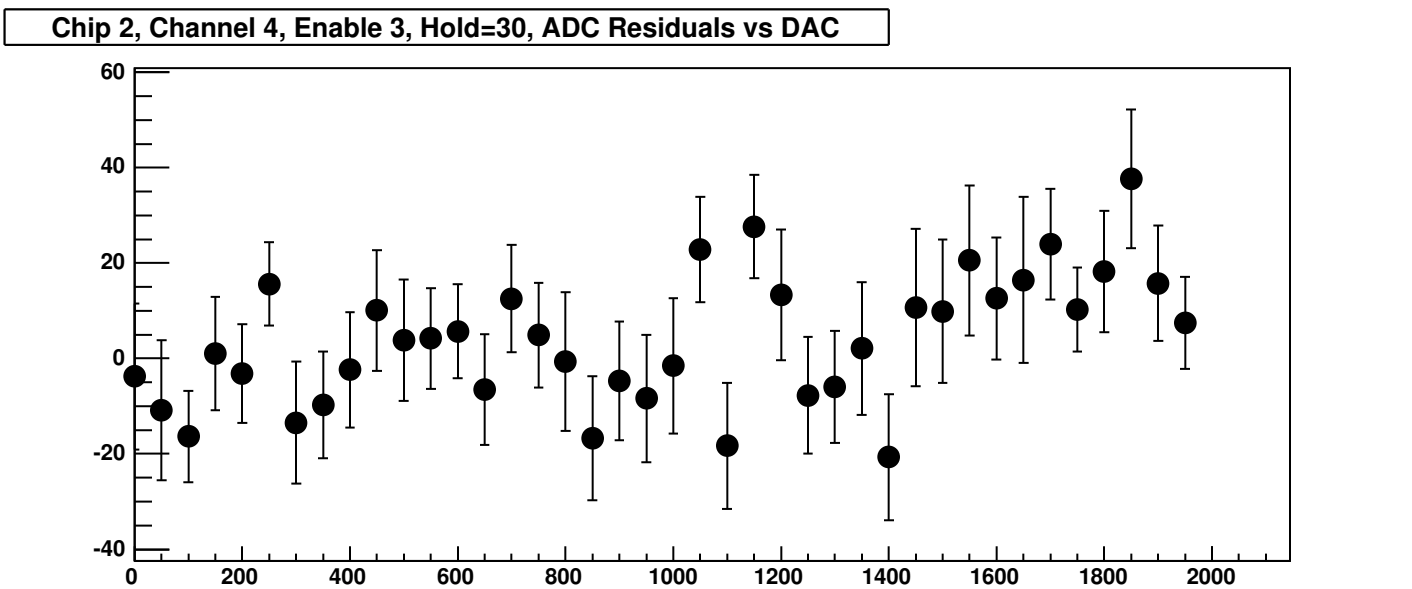

Chip 2, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 14.7 / 11
p0 -1370 ± 19.54
p1 0.09179 ± 0.01774
-1150
-1200
-1250
-1300
-1350
0
200
400
600
800
1000
1200
1400
1600
1800
2000

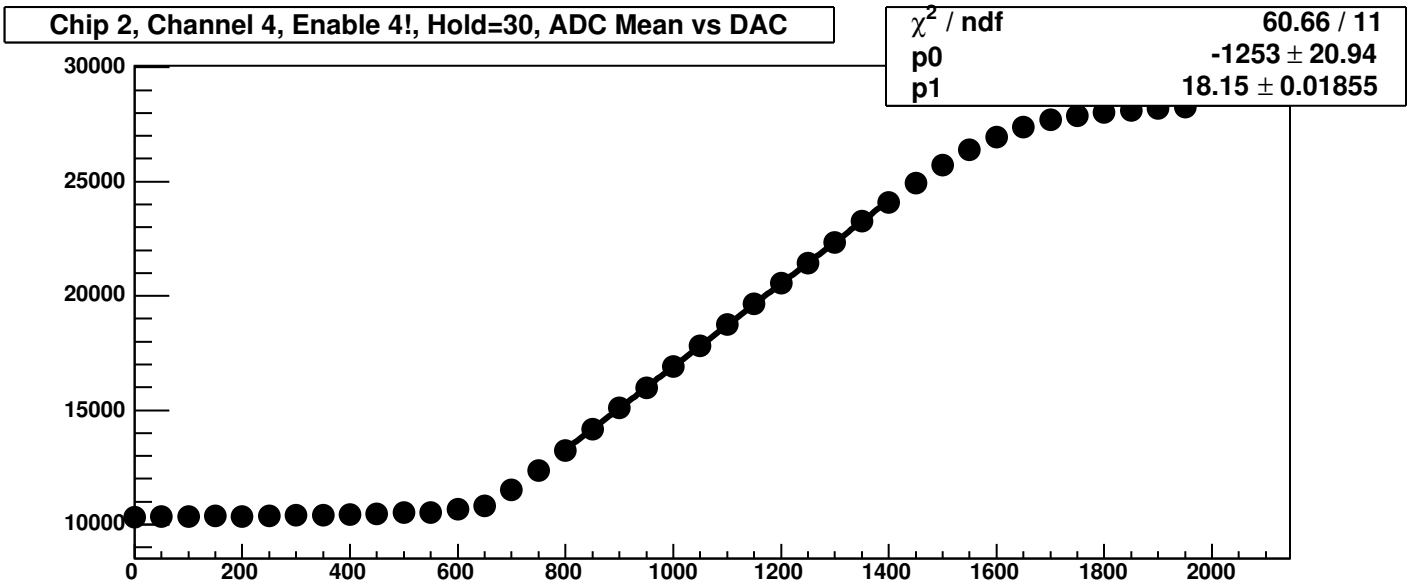

Chip 2, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

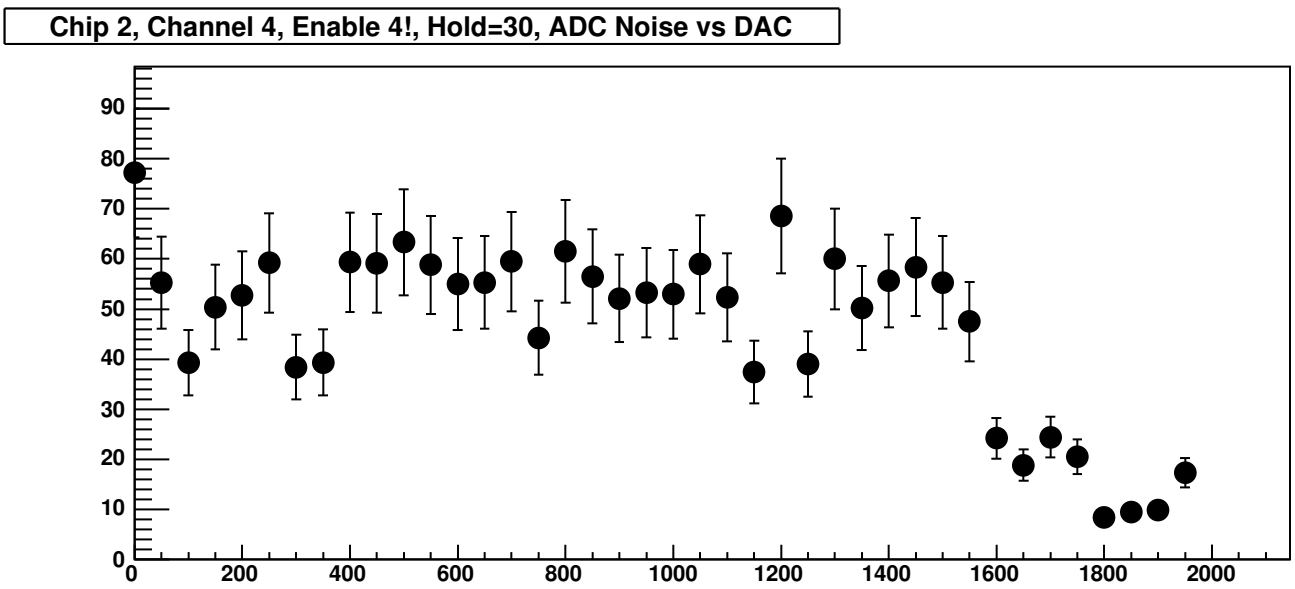

Chip 2, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

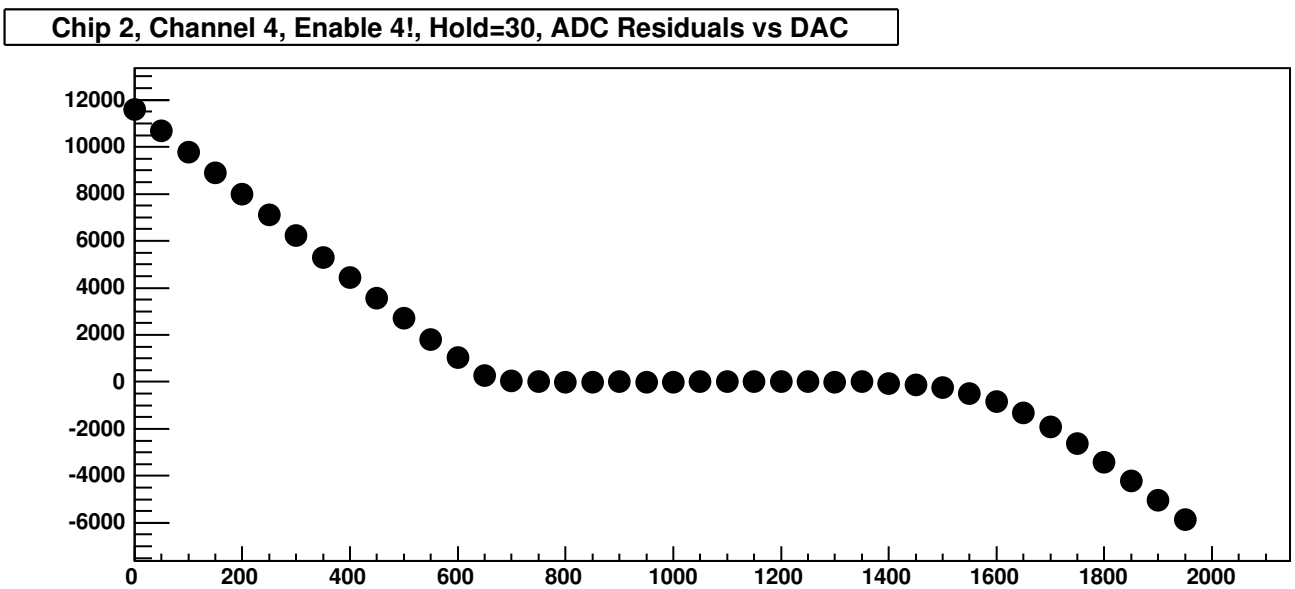

Chip 2, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 19.01 / 11
p0 -1356 ± 21.97
p1 0.01611 ± 0.01961
-1280
-1300
-1320
-1340
-1360
-1380
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

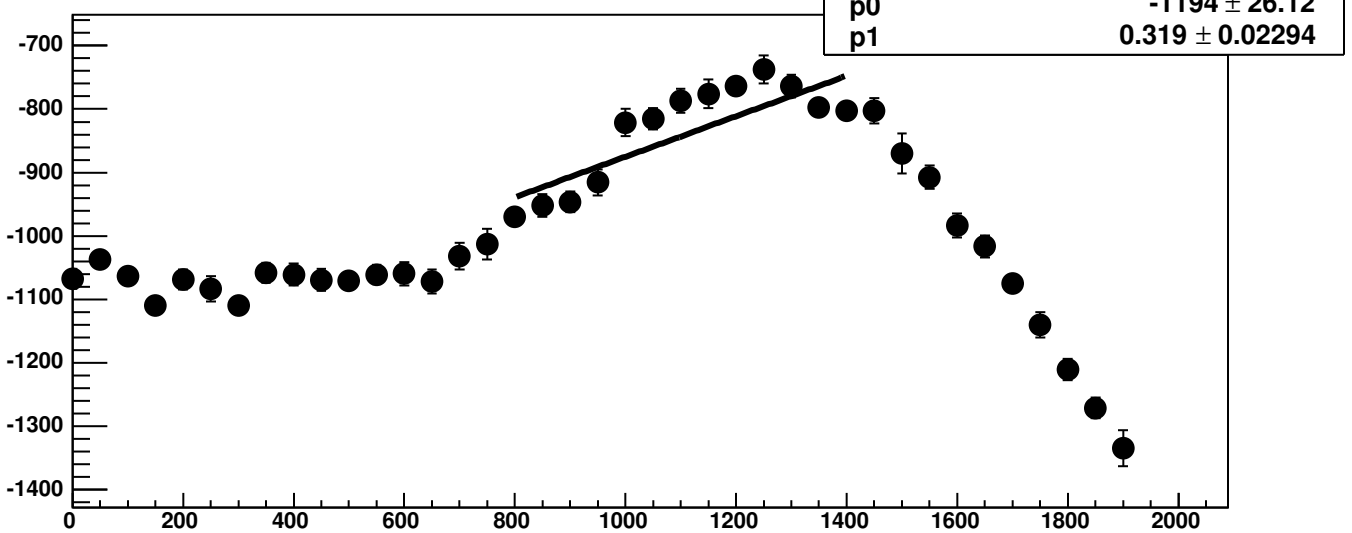

Chip 2, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 4, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 60.66 / 11
p0 -1253 ± 20.94
p1 18.15 ± 0.01855
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

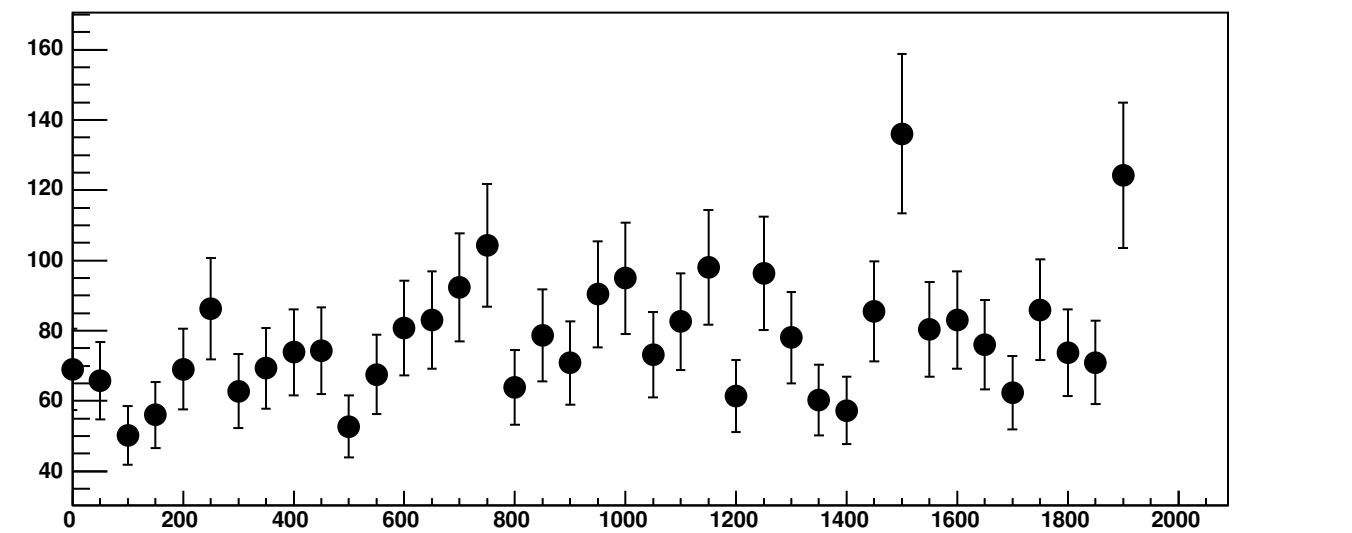

Chip 2, Channel 4, Enable 4!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 4, Enable 4!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

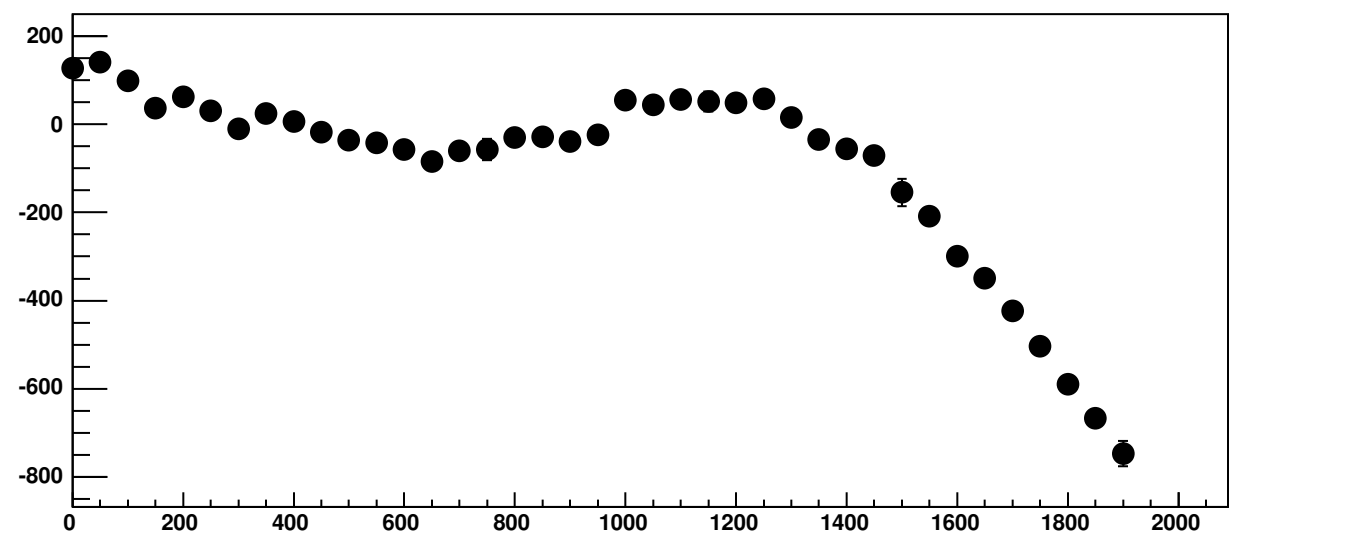
Chip 2, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC

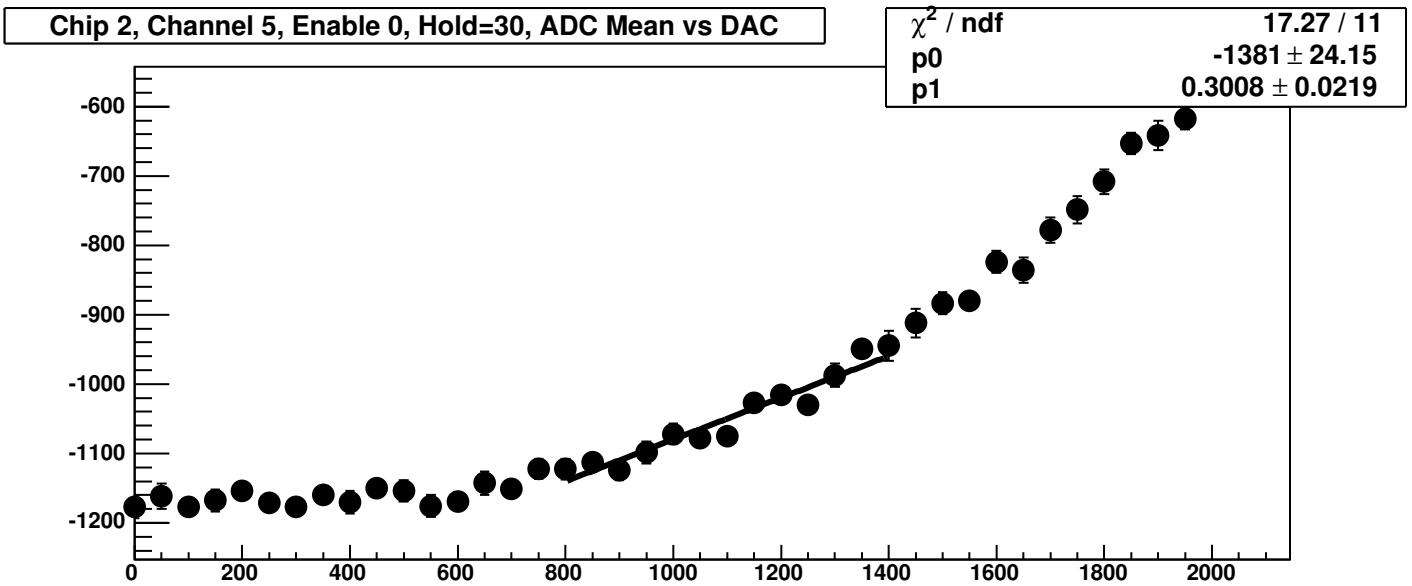


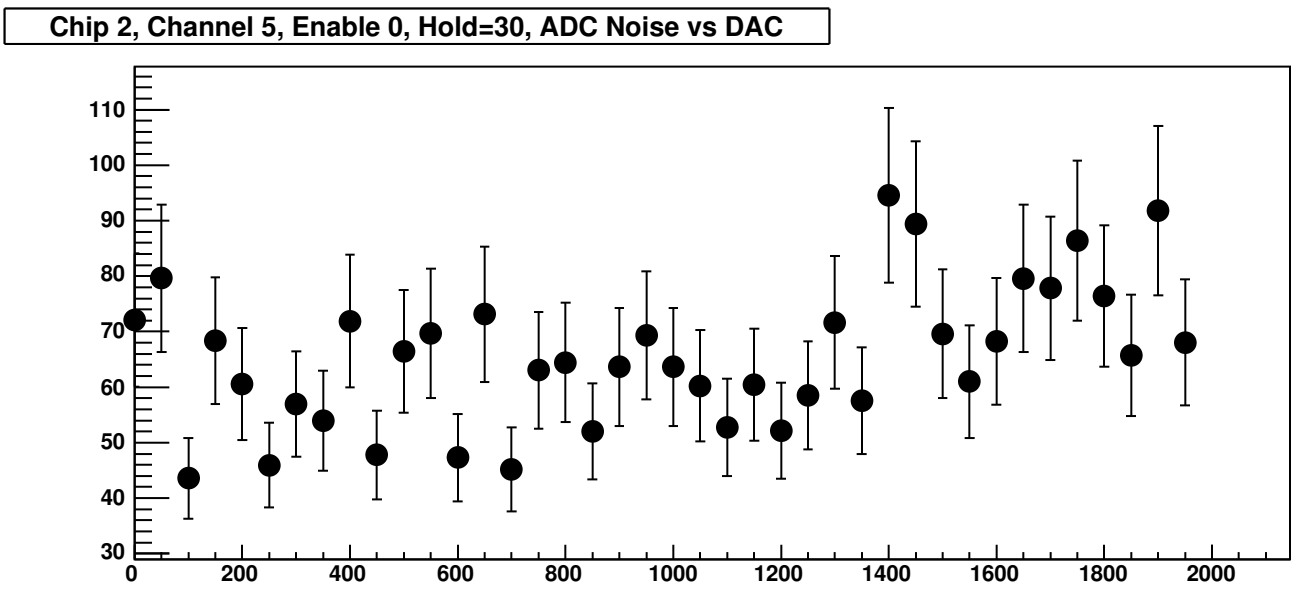
Chip 2, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC

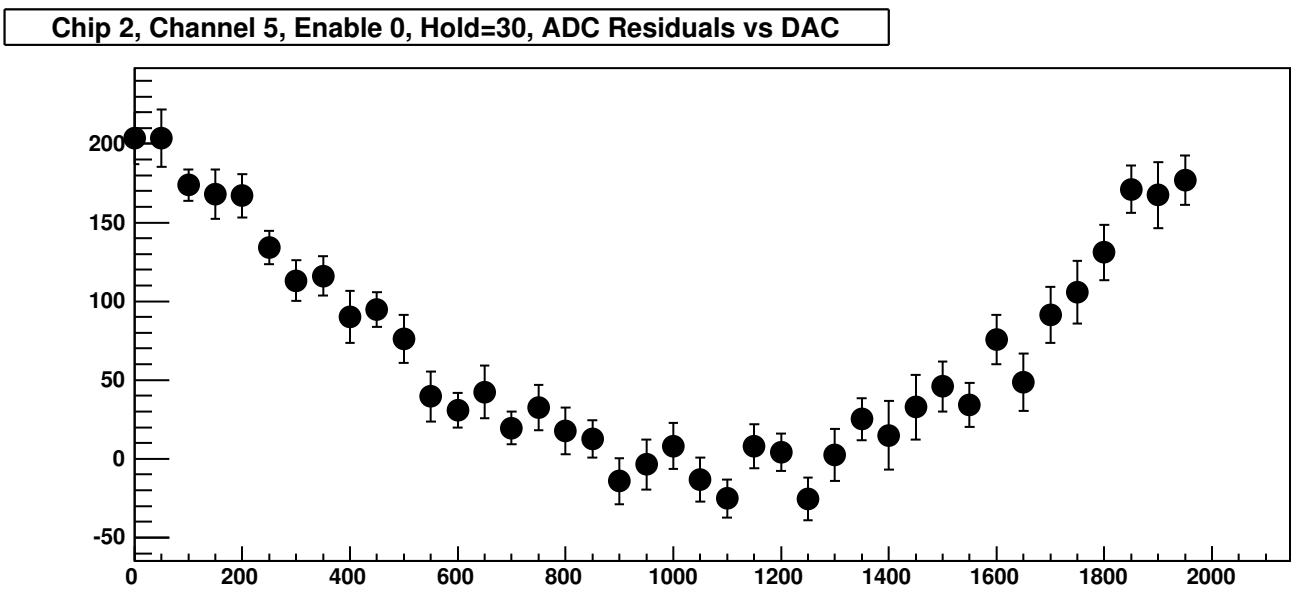


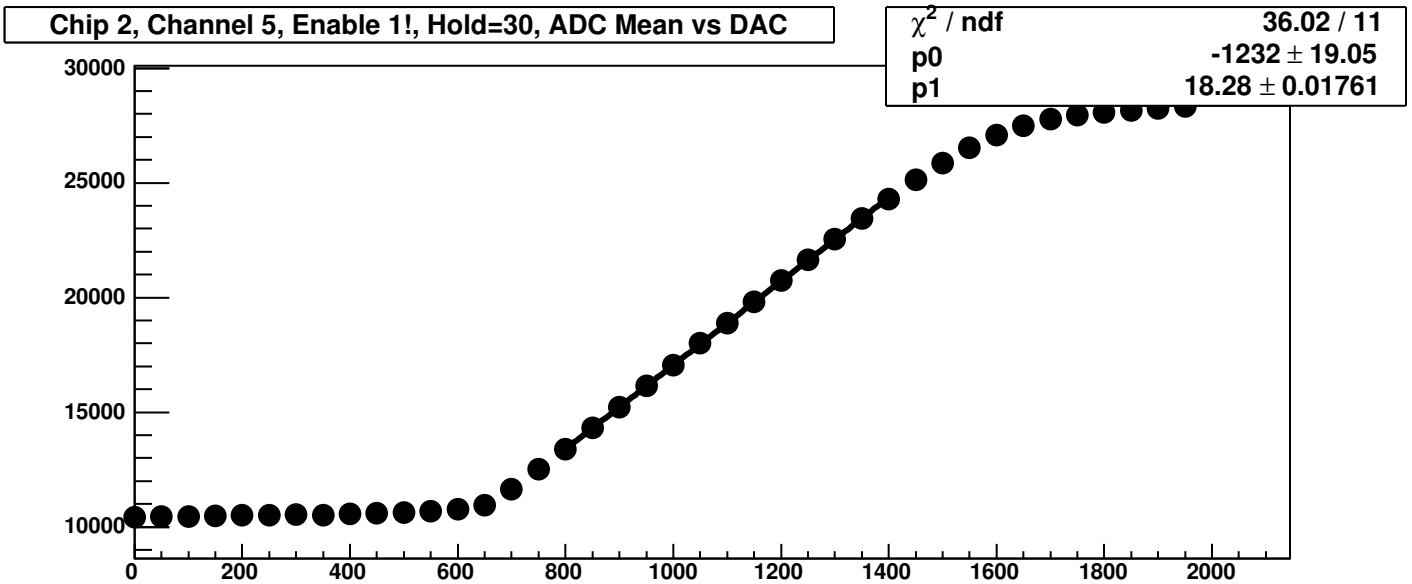
Chip 2, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC

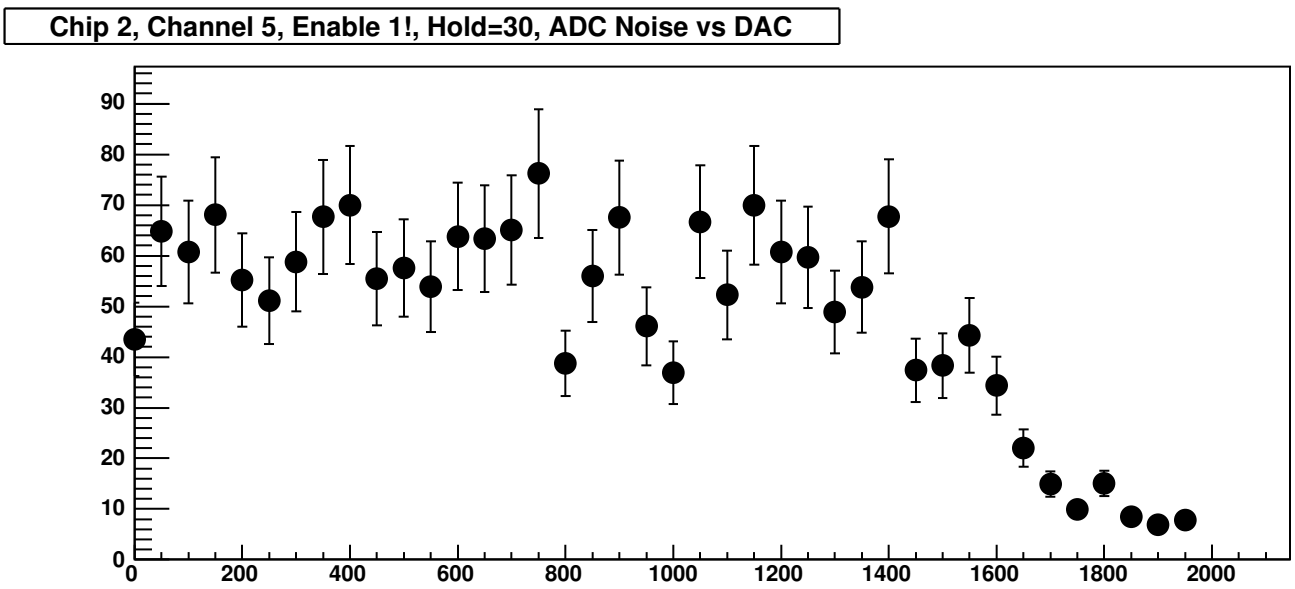


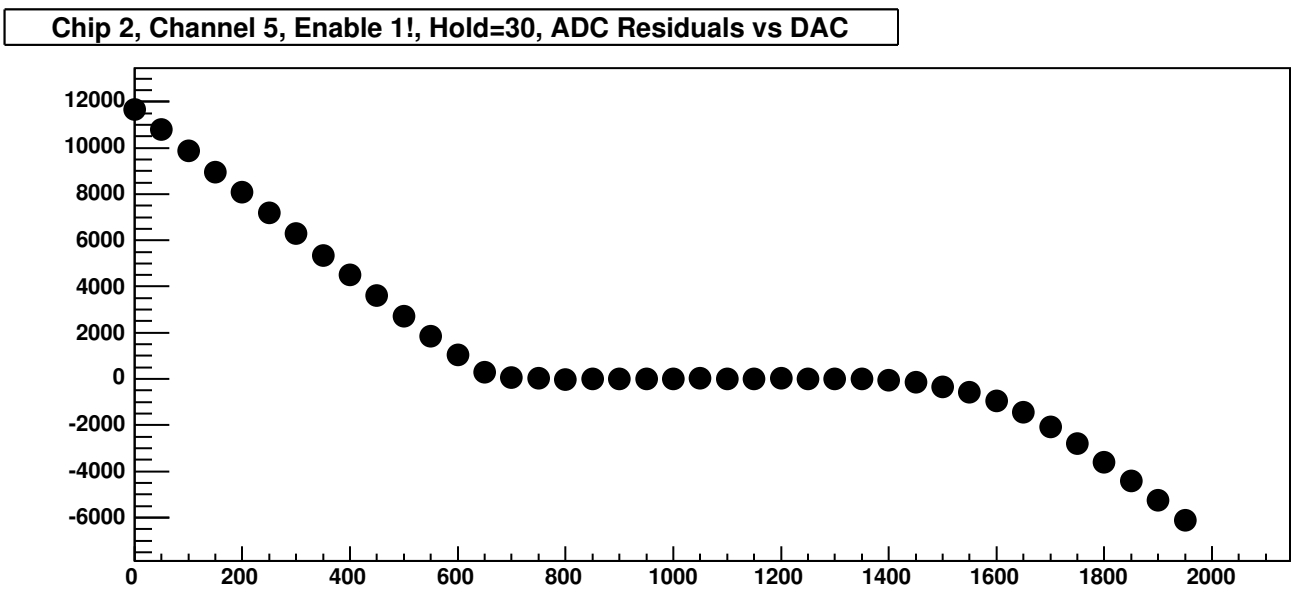

Chip 2, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 17.27 / 11
p0 -1381 ± 24.15
p1 0.3008 ± 0.0219
-600
-700
-800
-900
-1000
-1100
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

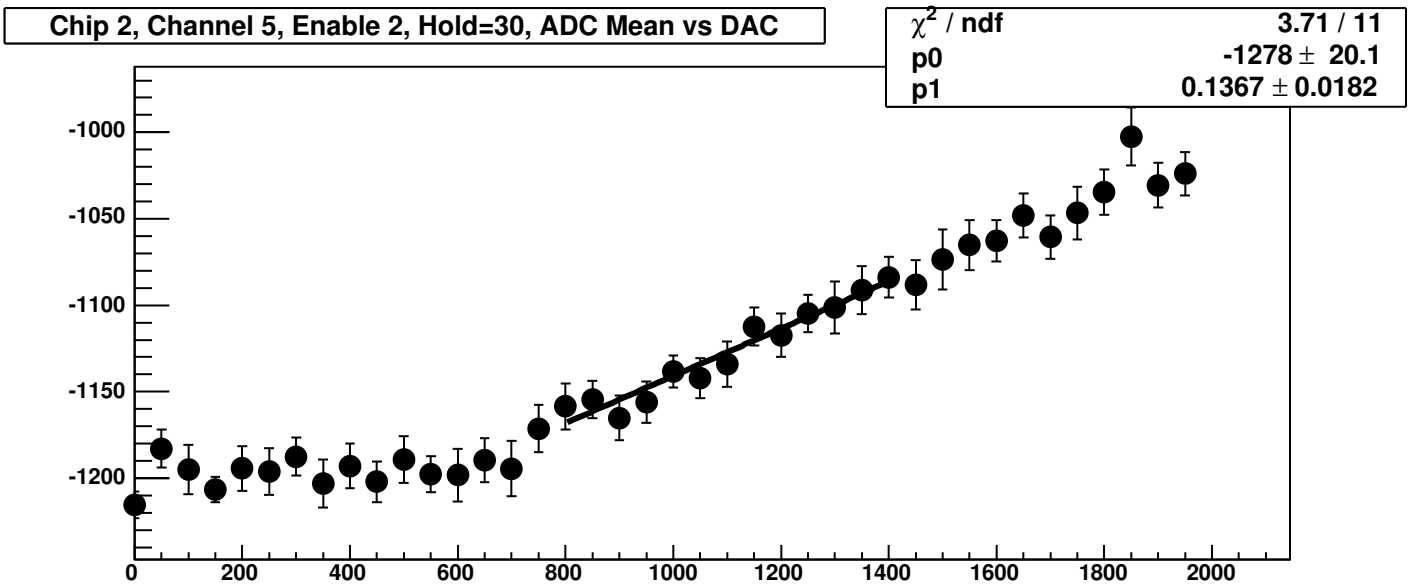

Chip 2, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

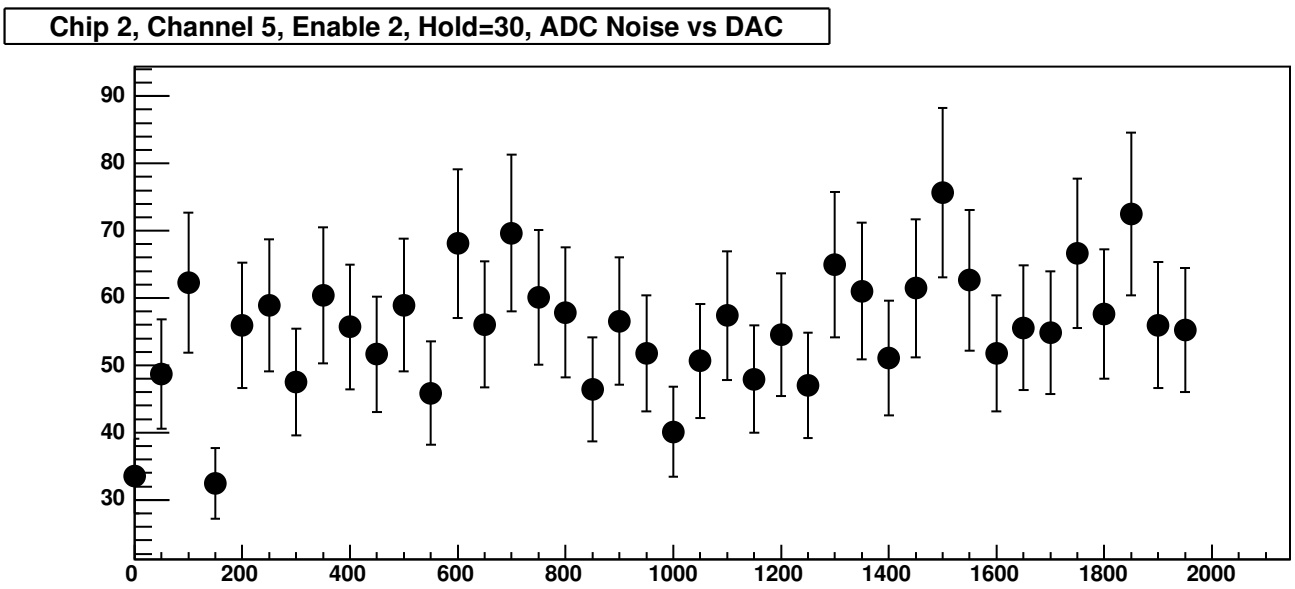

Chip 2, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

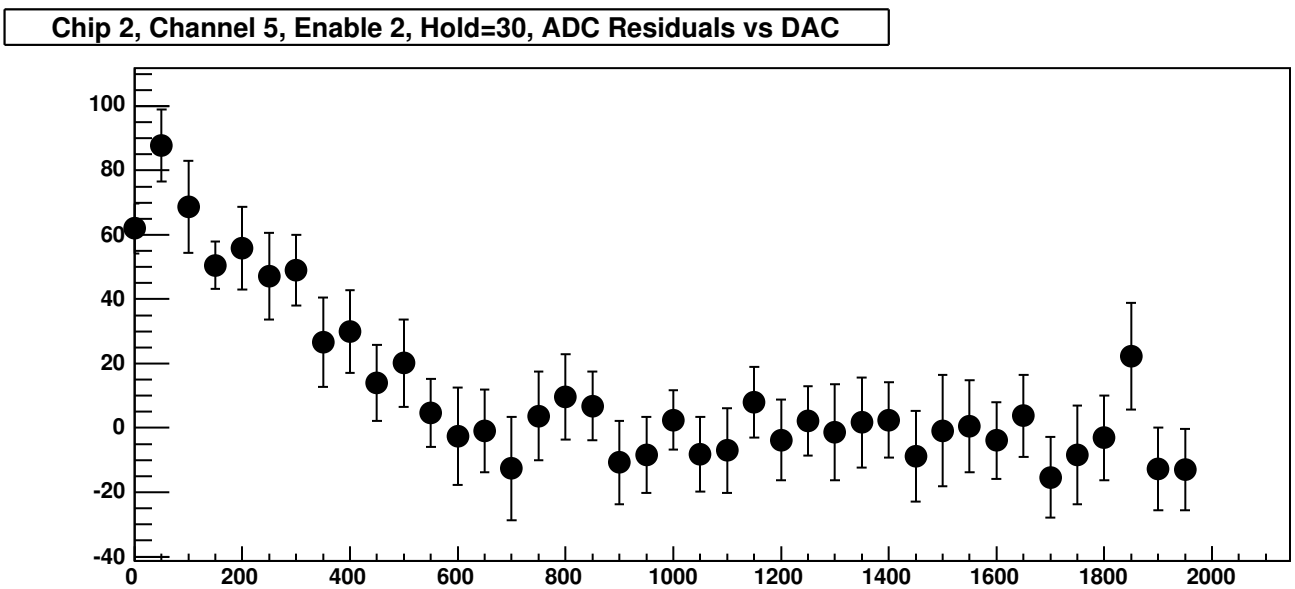

Chip 2, Channel 5, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 36.02 / 11
p0 -1232 ± 19.05
p1 18.28 ± 0.01761
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 5, Enable 1!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

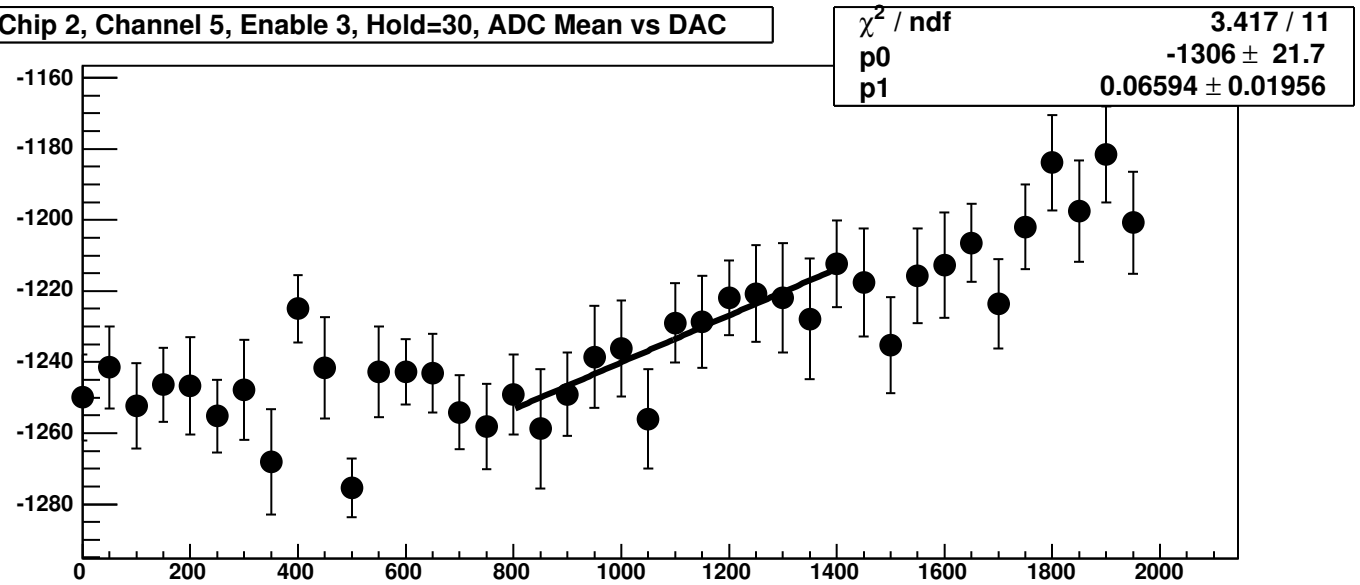

Chip 2, Channel 5, Enable 1!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3.71 / 11
p0 -1278 ± 20.1
p1 0.1367 ± 0.0182
-1000
-1050
-1100
-1150
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

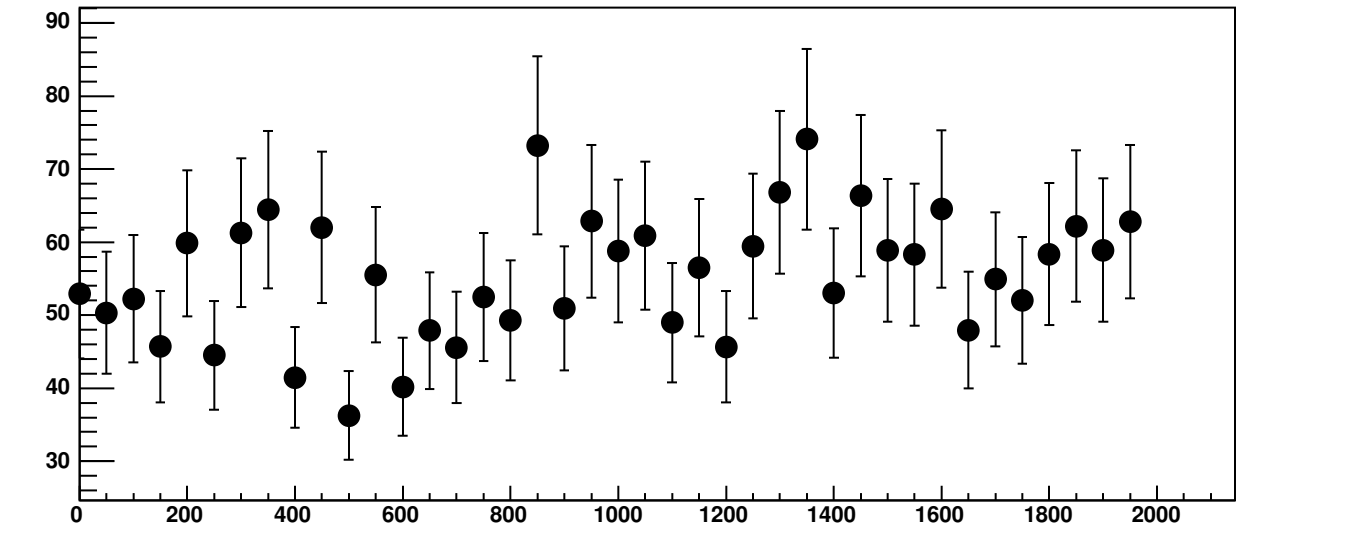

Chip 2, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

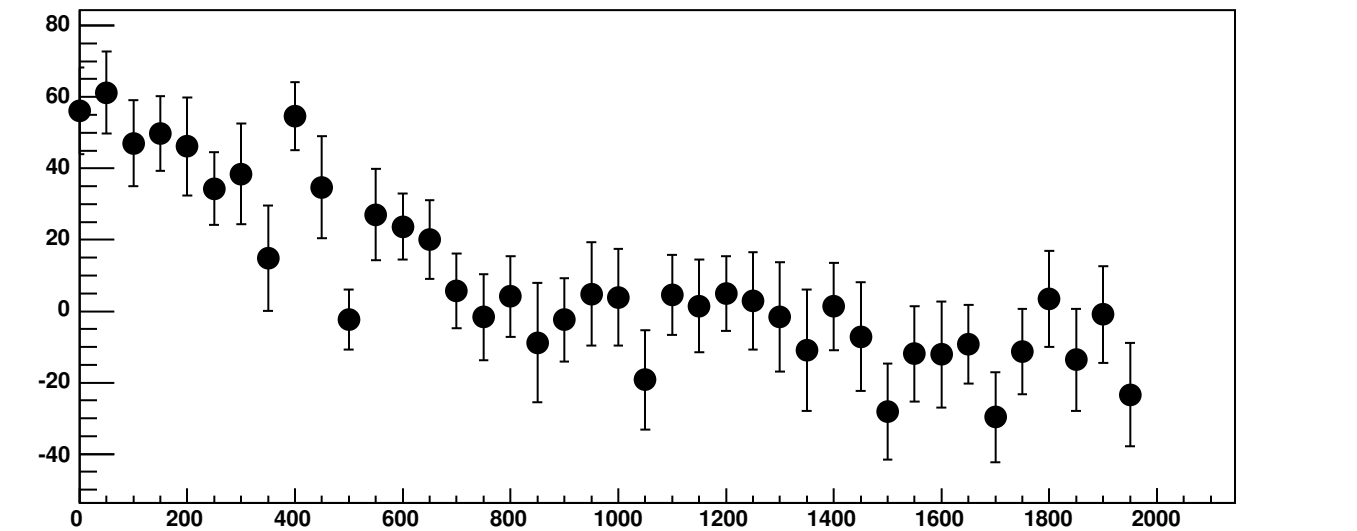
Chip 2, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC

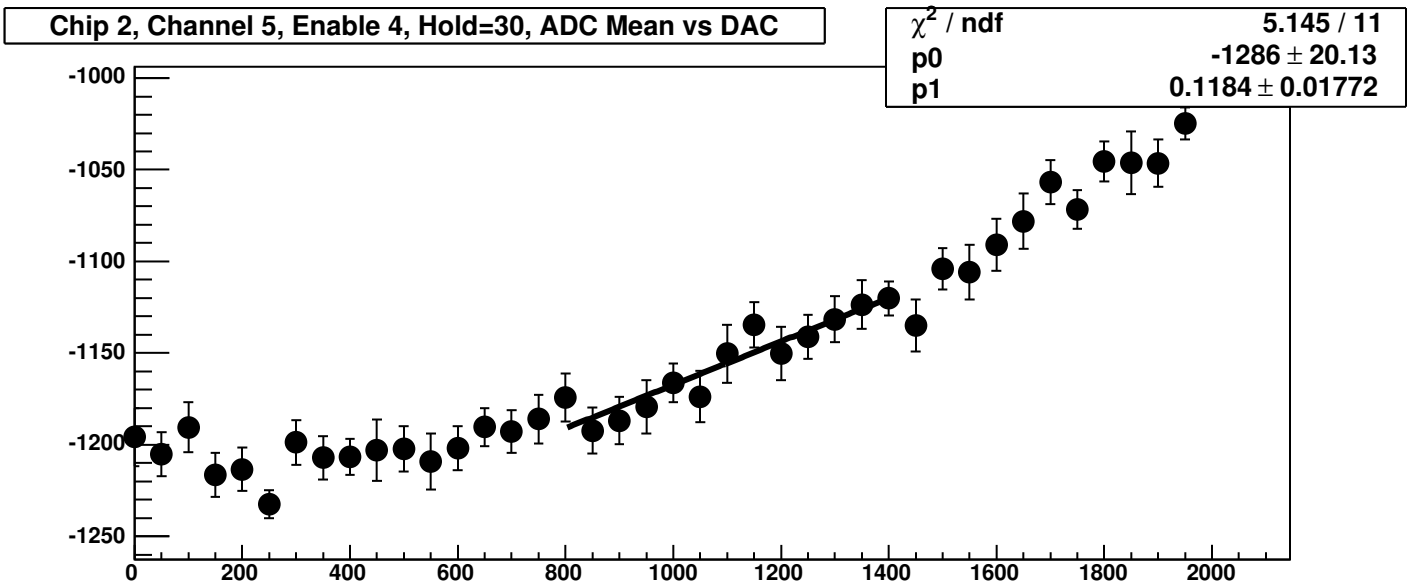


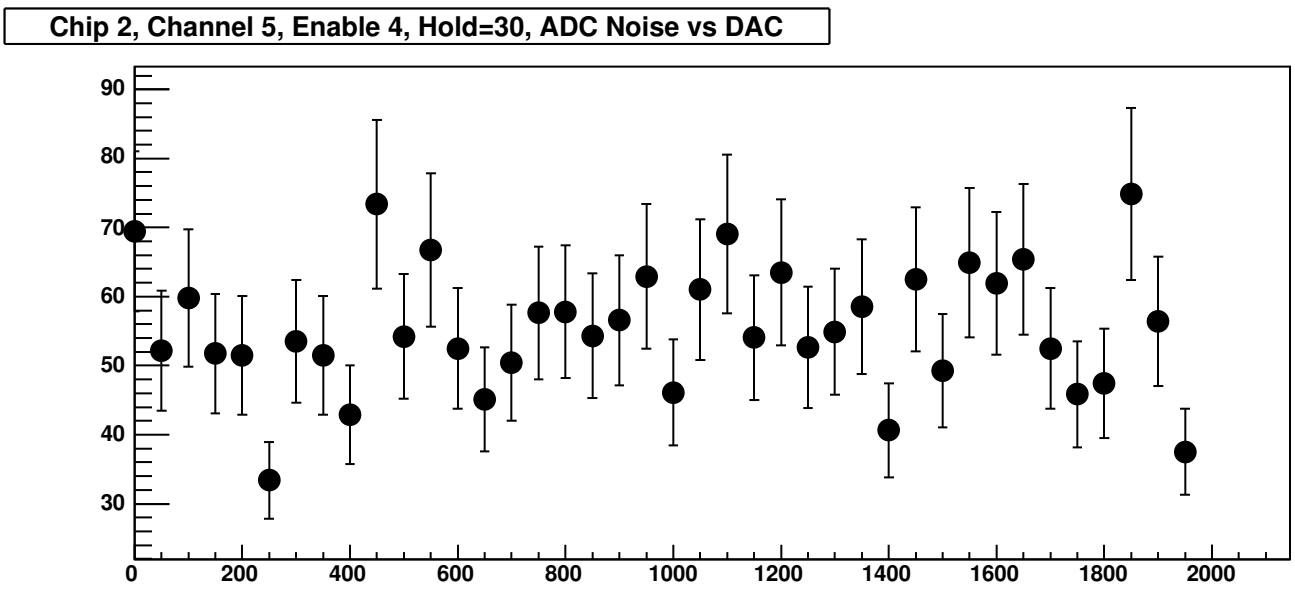
Chip 2, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC

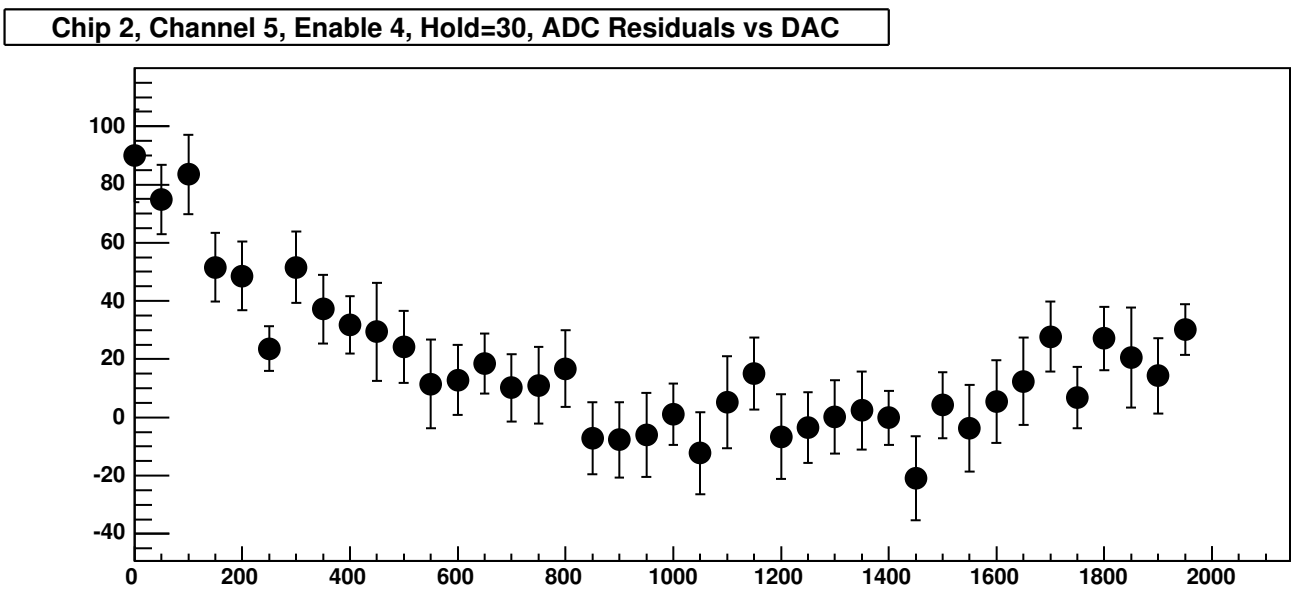


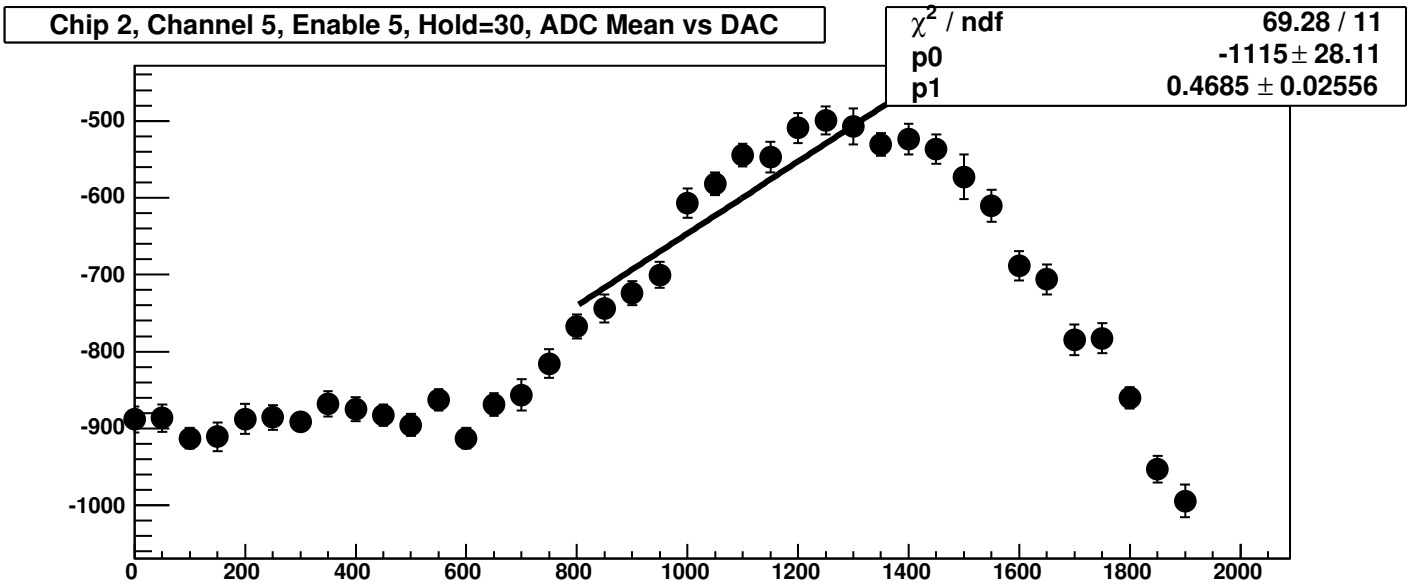
Chip 2, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC

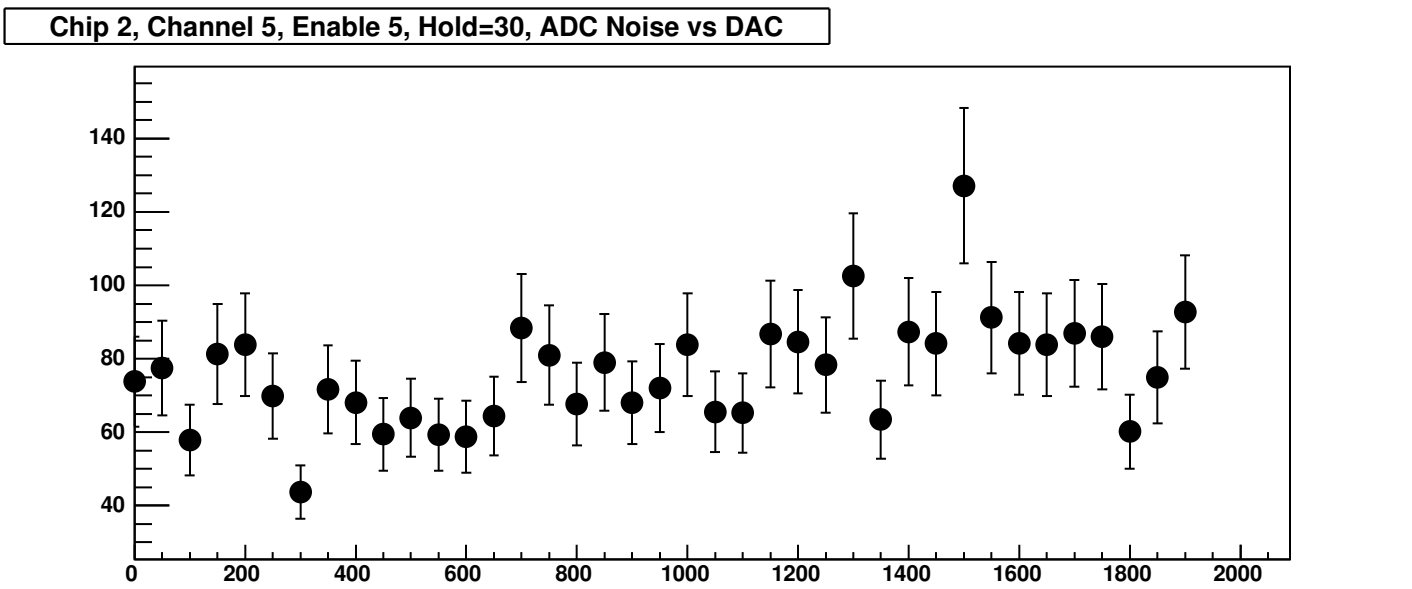


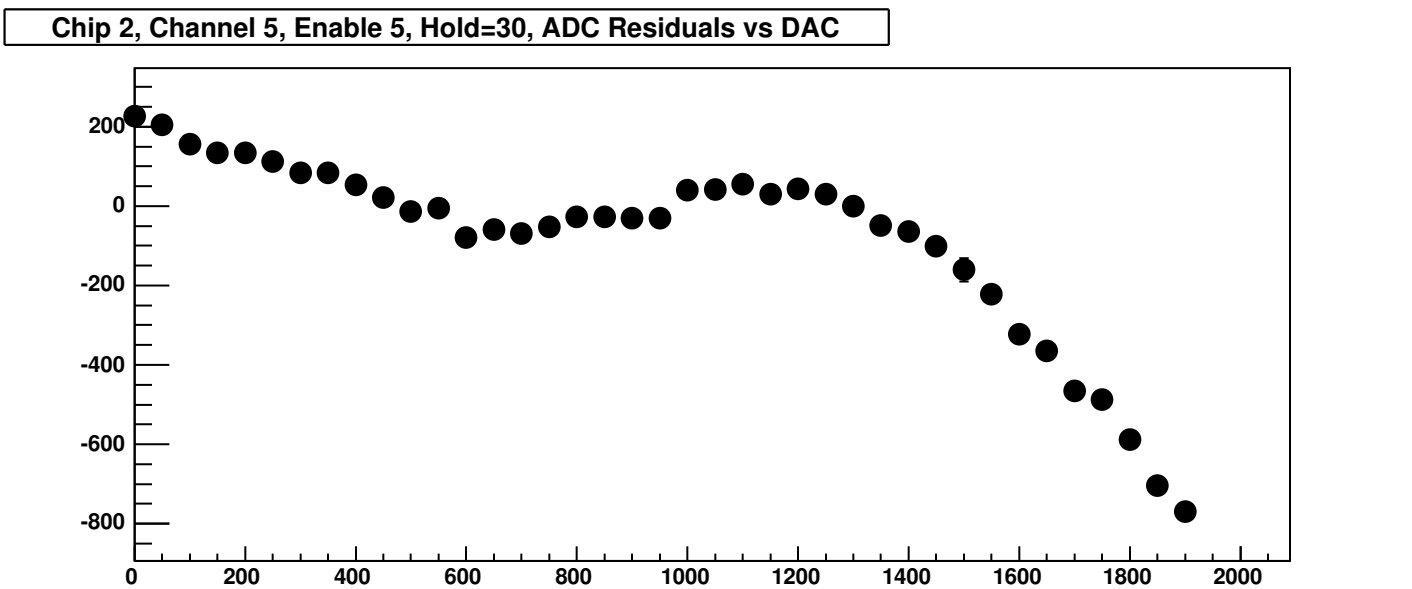

Chip 2, Channel 5, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5.145 / 11
p0 -1286 ± 20.13
p1 0.1184 ± 0.01772
-1000
-1050
-1100
-1150
-1200
-1250
0
200
400
600
800
1000
1200
1400
1600
1800
2000

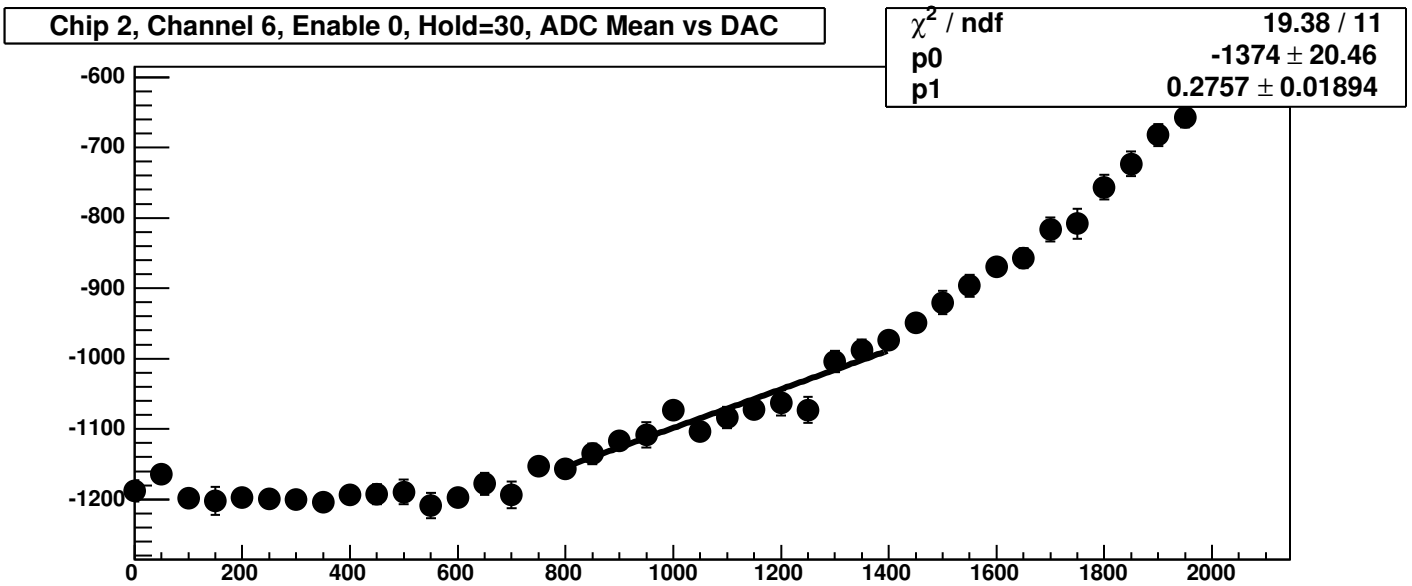

Chip 2, Channel 5, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

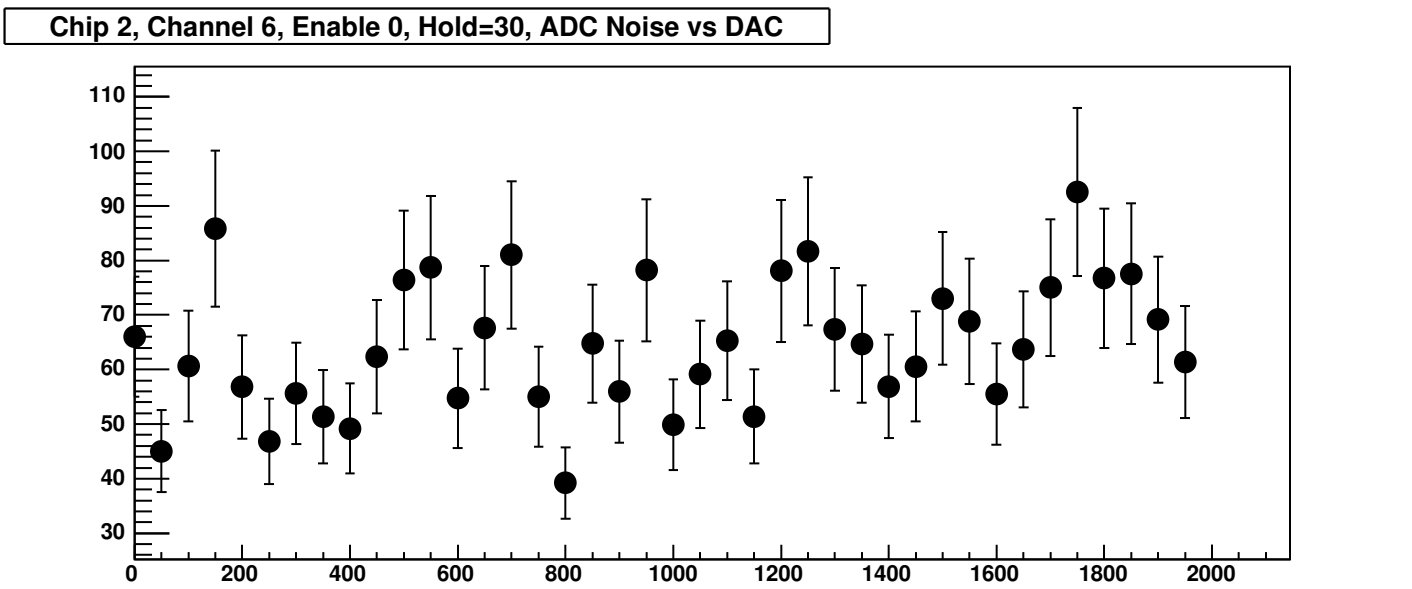

Chip 2, Channel 5, Enable 4, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

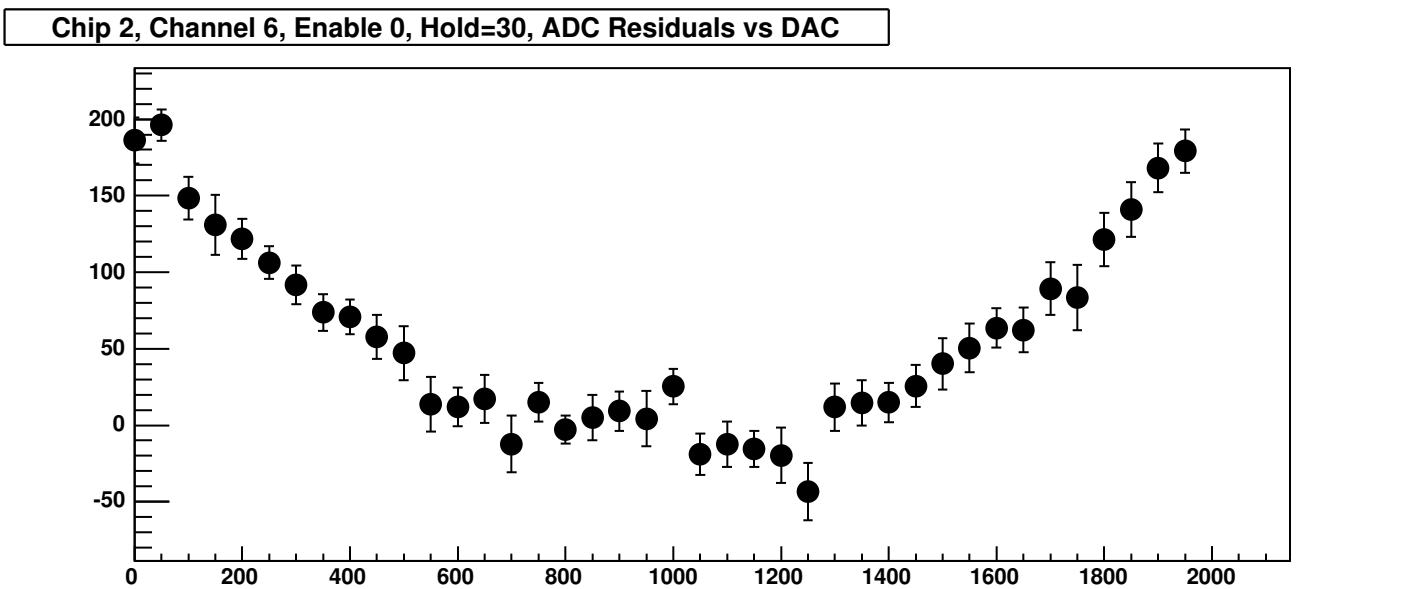

Chip 2, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 69.28 / 11
p0 -1115 ± 28.11
p1 0.4685 ± 0.02556
-500
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

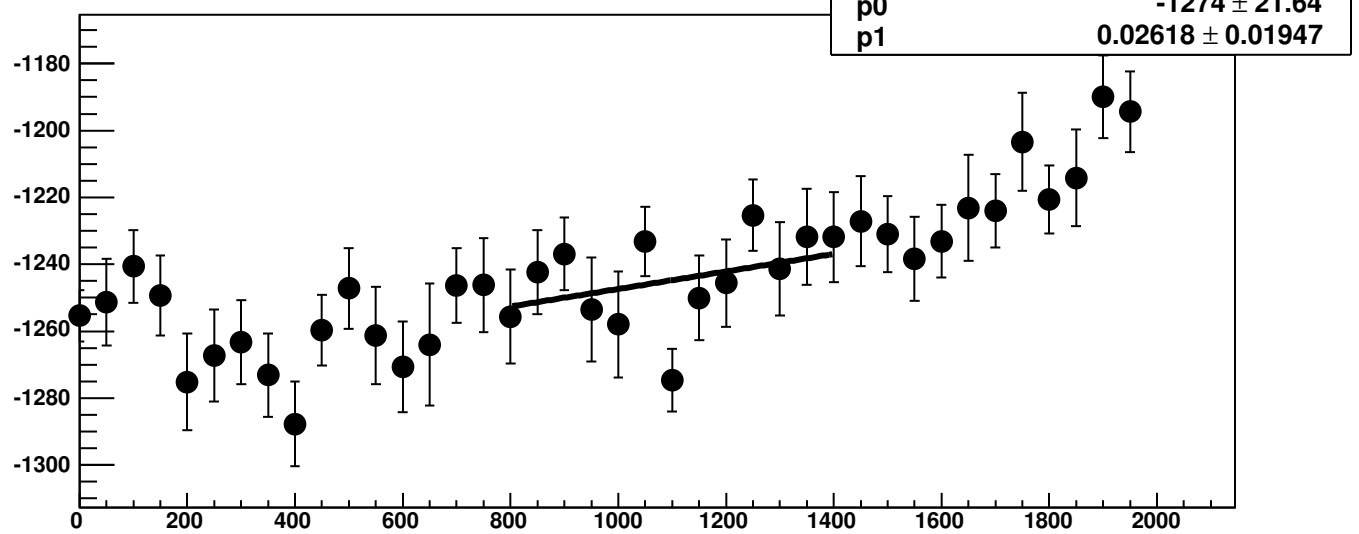

Chip 2, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 19.38 / 11
p0 -1374 ± 20.46
p1 0.2757 ± 0.01894
-600
-700
-800
-900
-1000
-1100
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

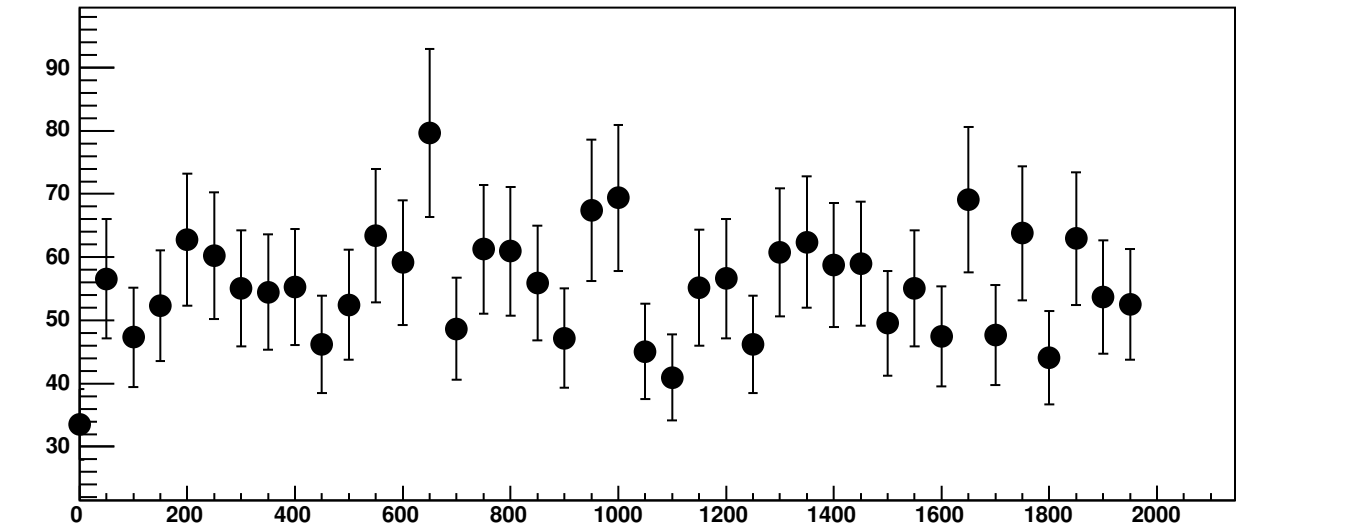

Chip 2, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

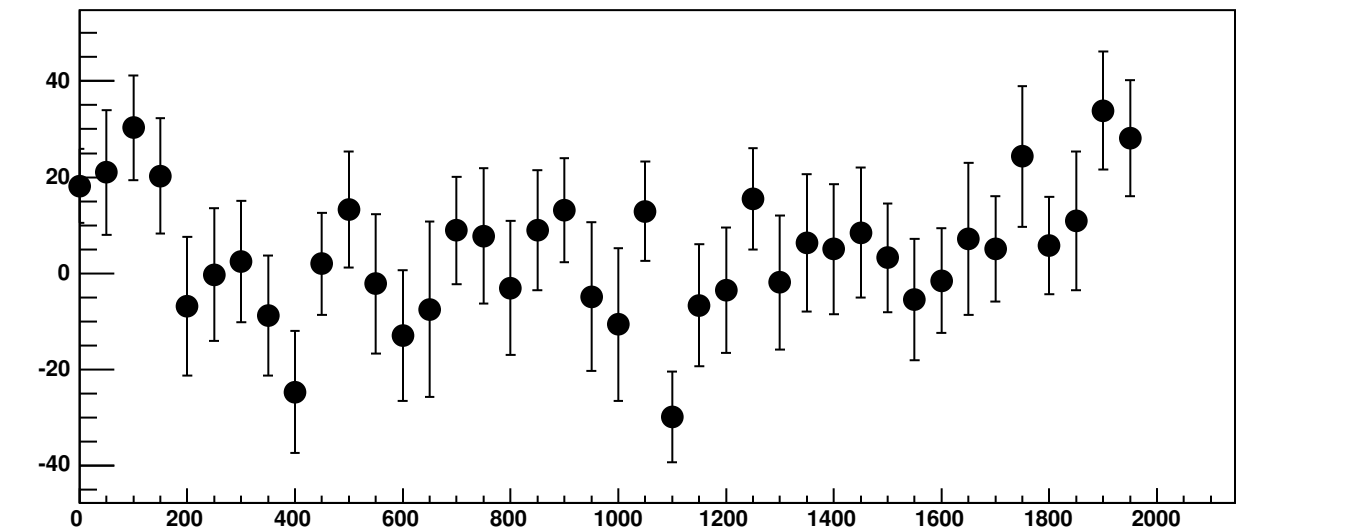
Chip 2, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC

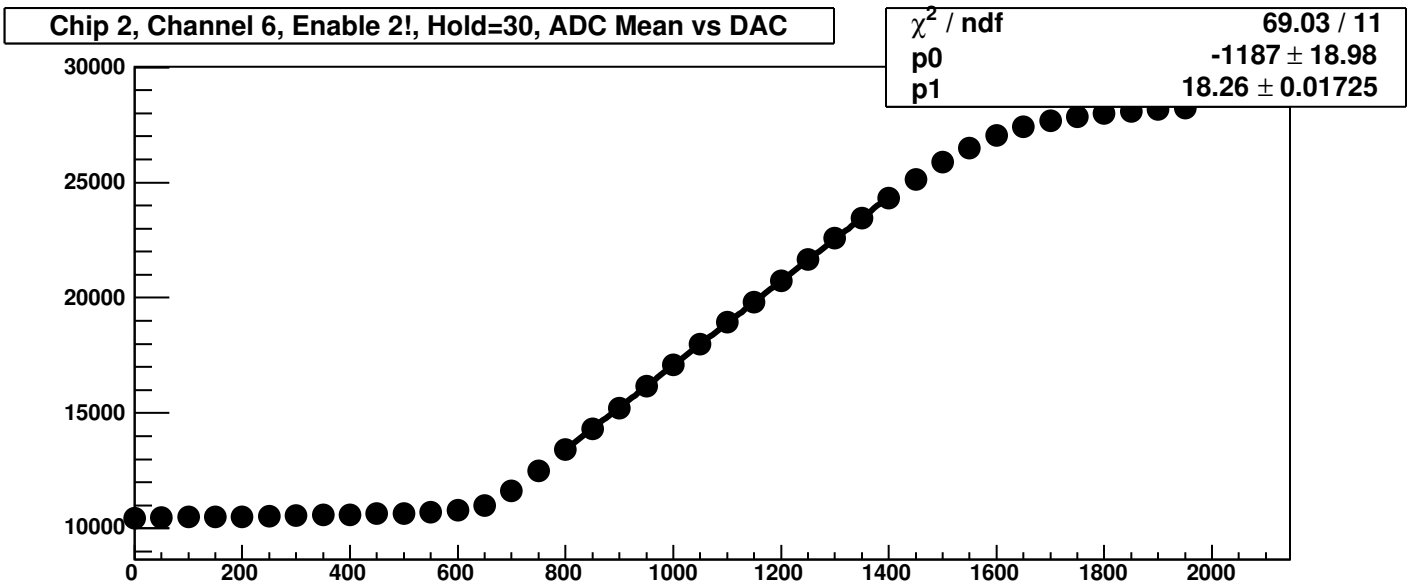


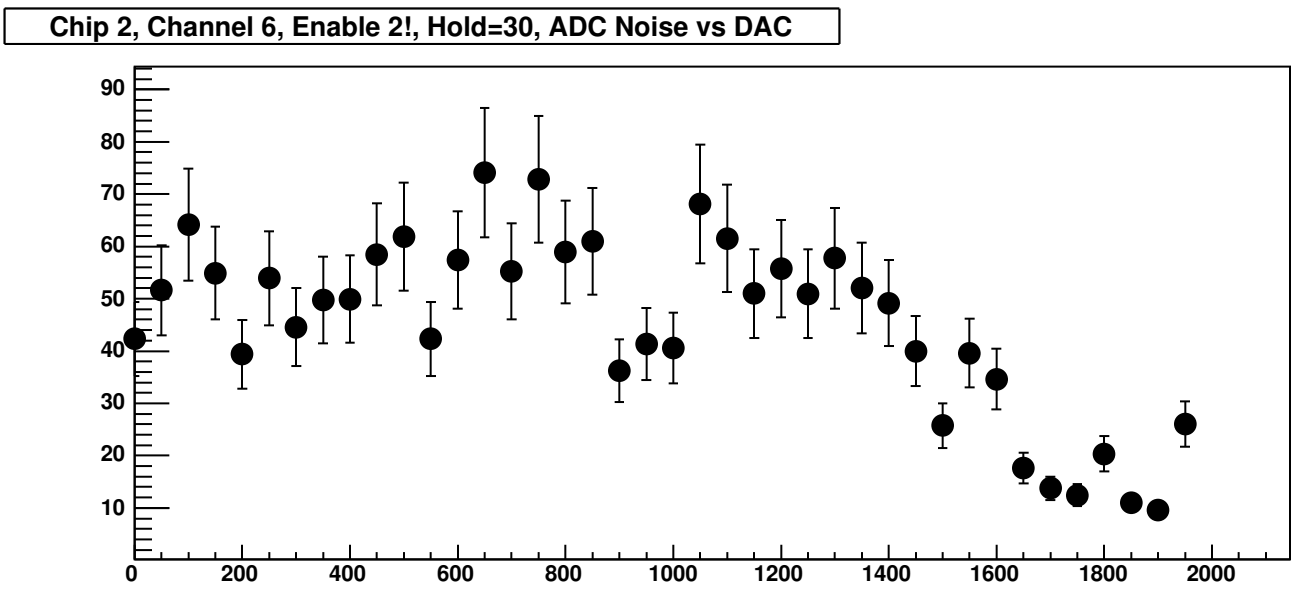
Chip 2, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC

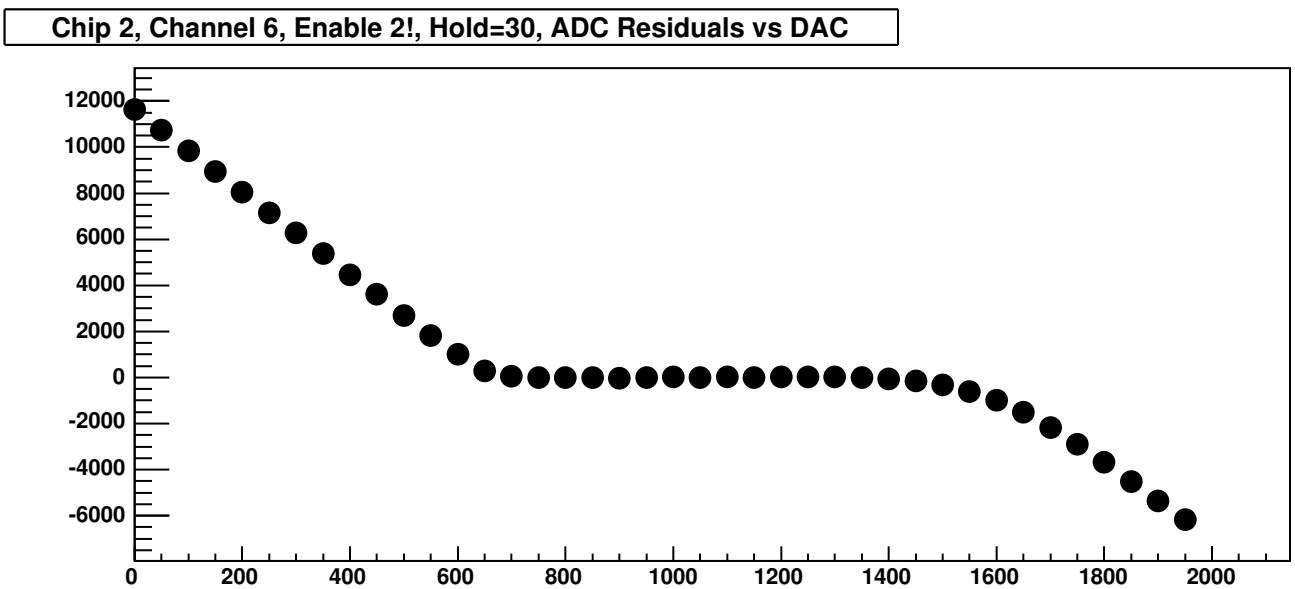


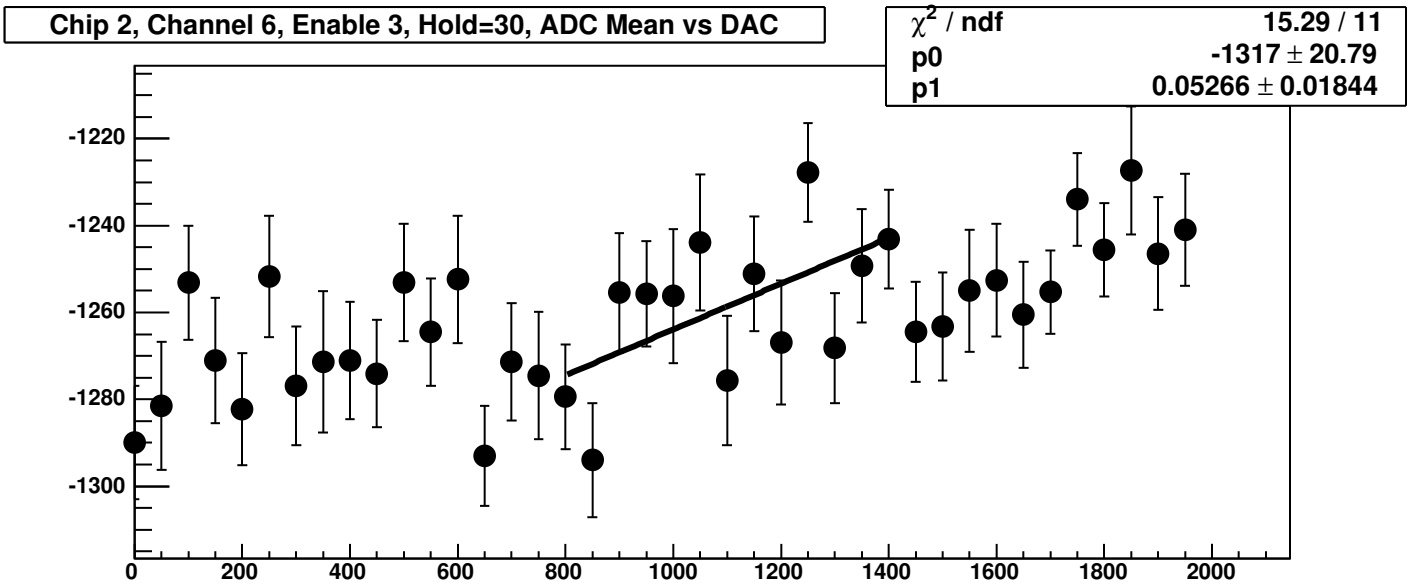
Chip 2, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC

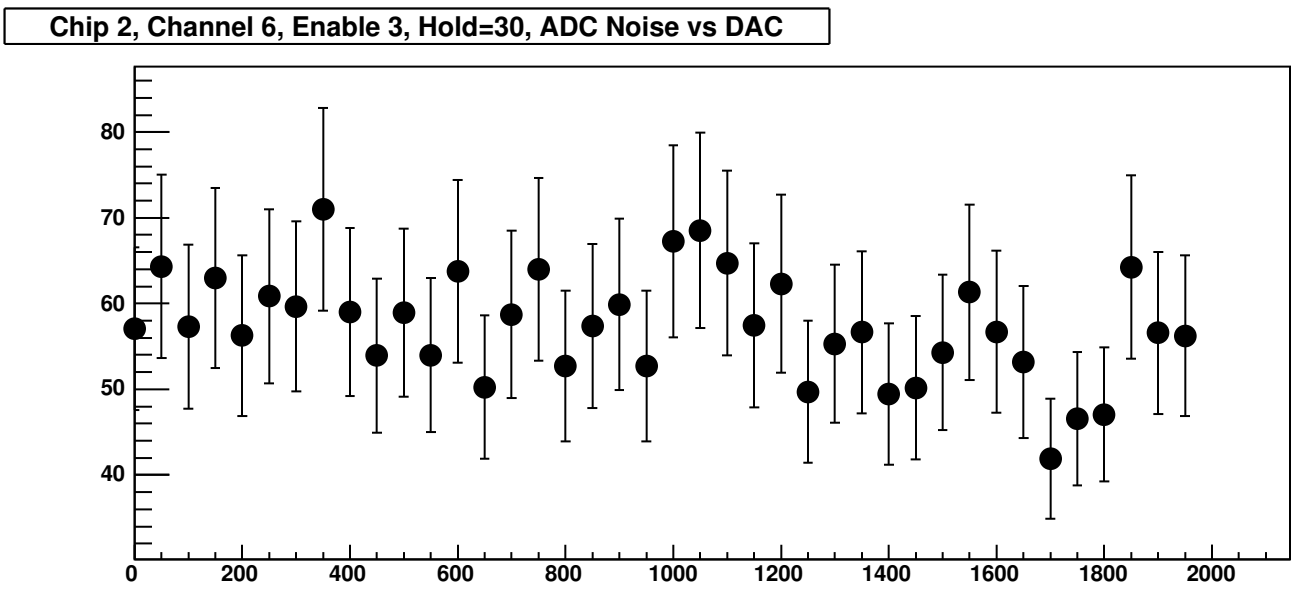


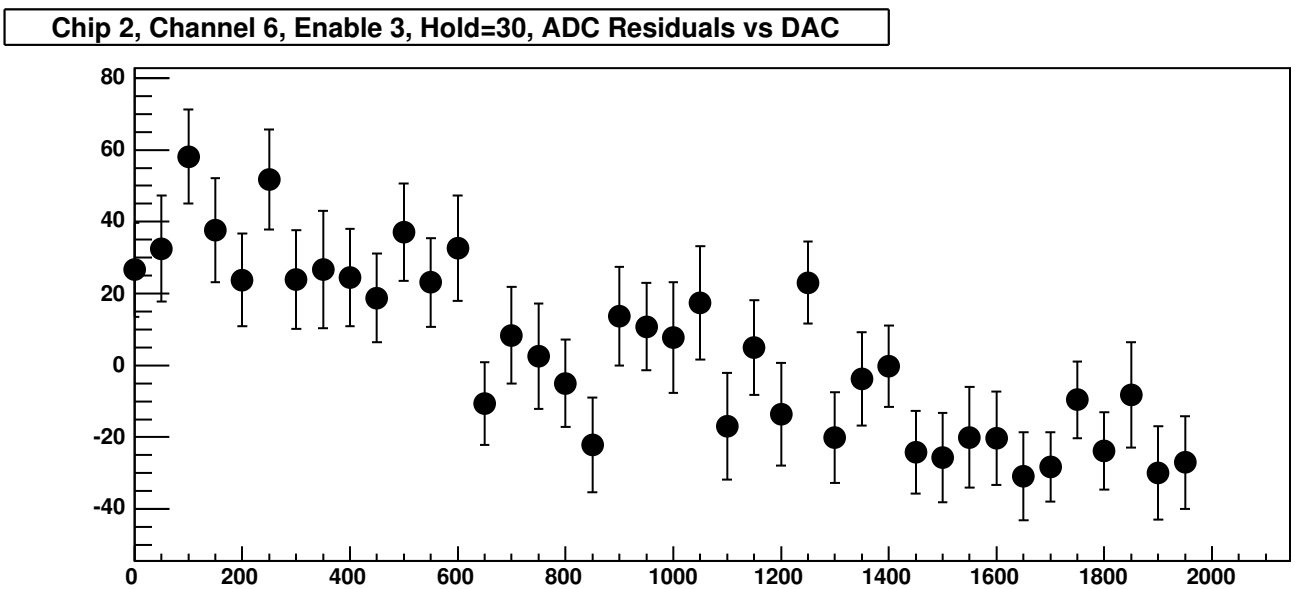

Chip 2, Channel 6, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 69.03 / 11
p0 -1187 ± 18.98
p1 18.26 ± 0.01725
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 6, Enable 2!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

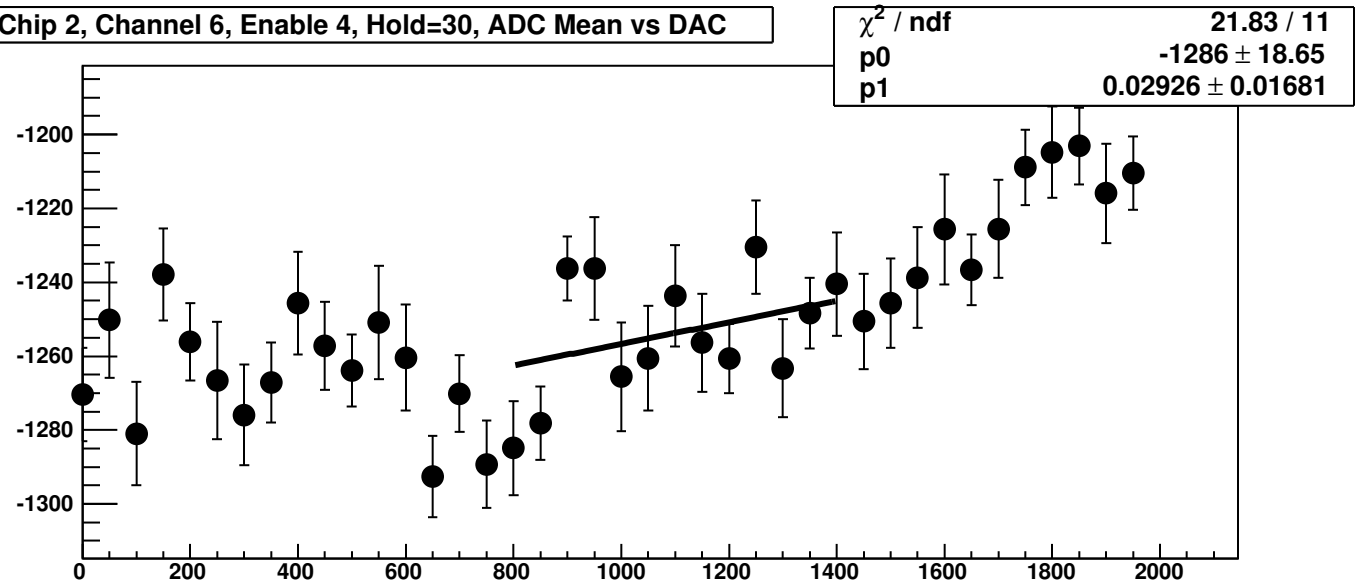

Chip 2, Channel 6, Enable 2!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 15.29 / 11
p0 -1317 ± 20.79
p1 0.05266 ± 0.01844
-1220
-1240
-1260
-1280
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

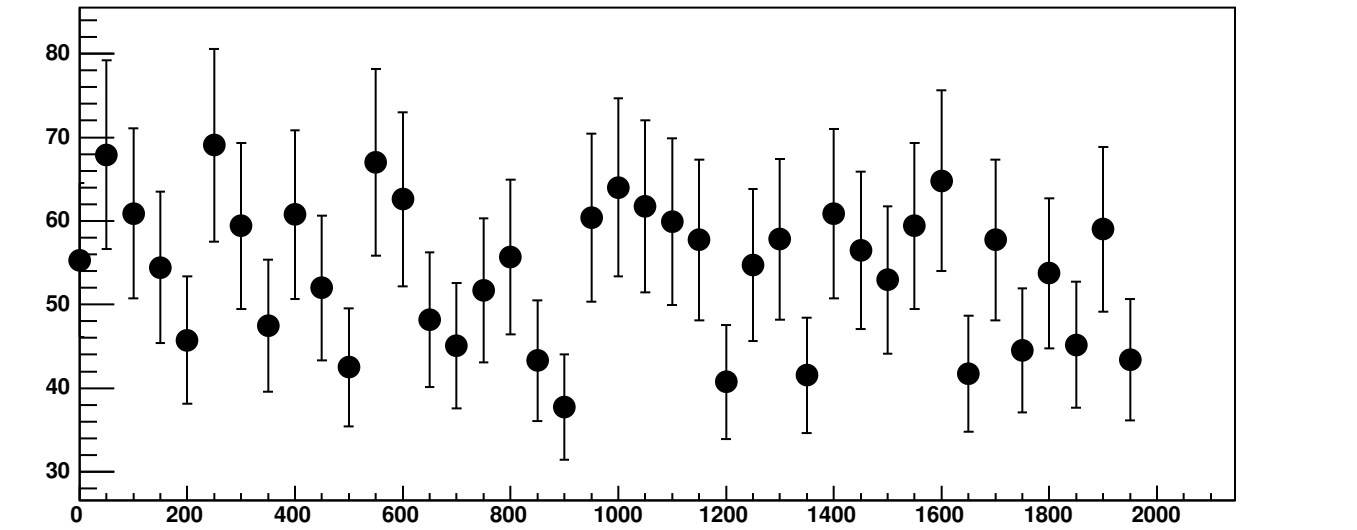

Chip 2, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

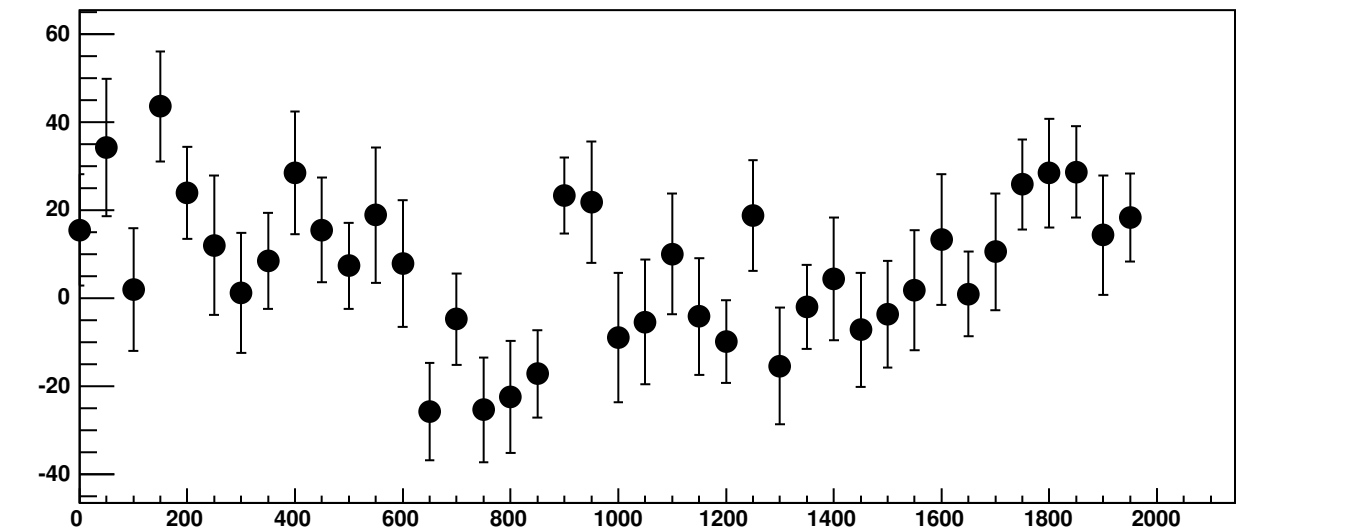
Chip 2, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC



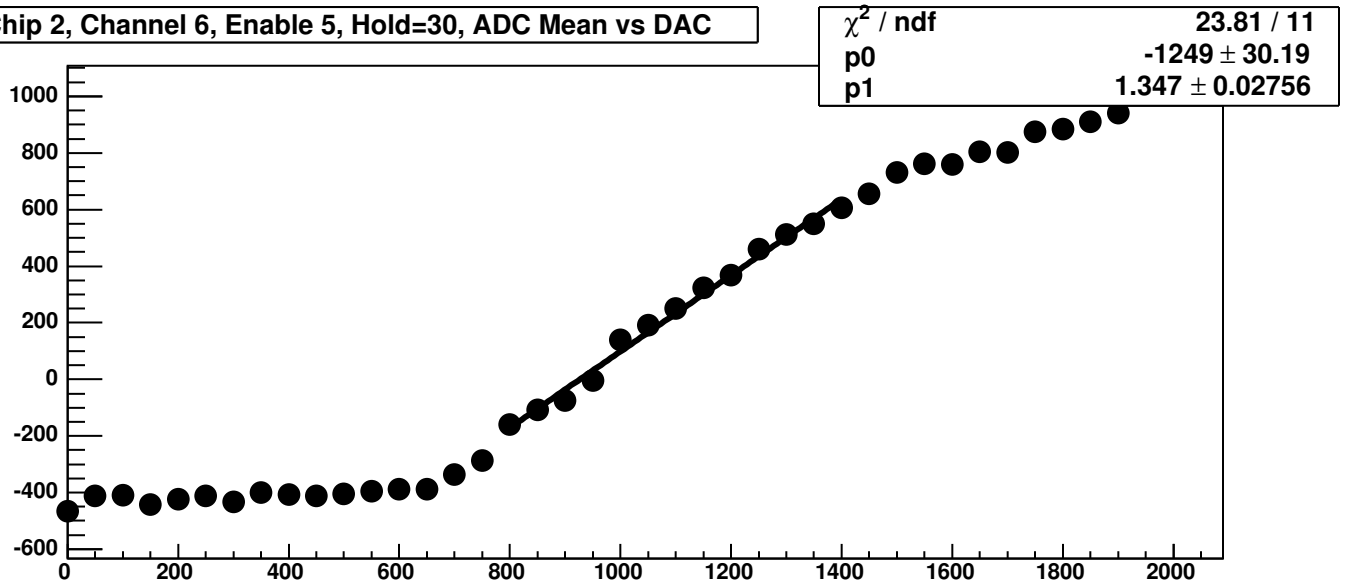
Chip 2, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC



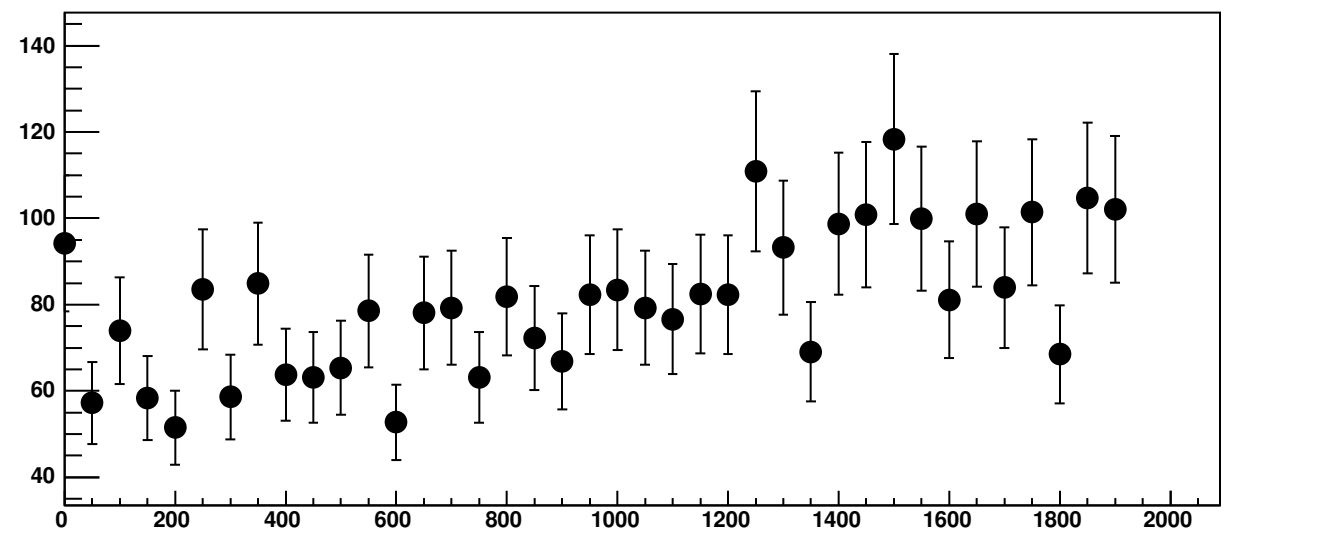
Chip 2, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC



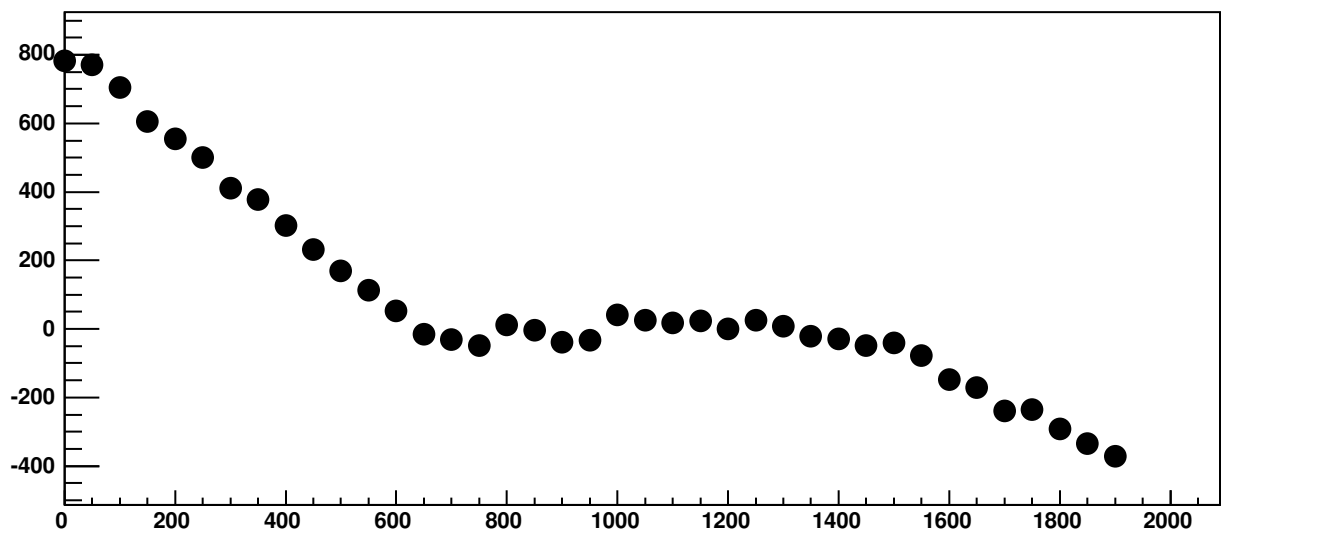
Chip 2, Channel 6, Enable 5, Hold=30, ADC Mean vs DAC

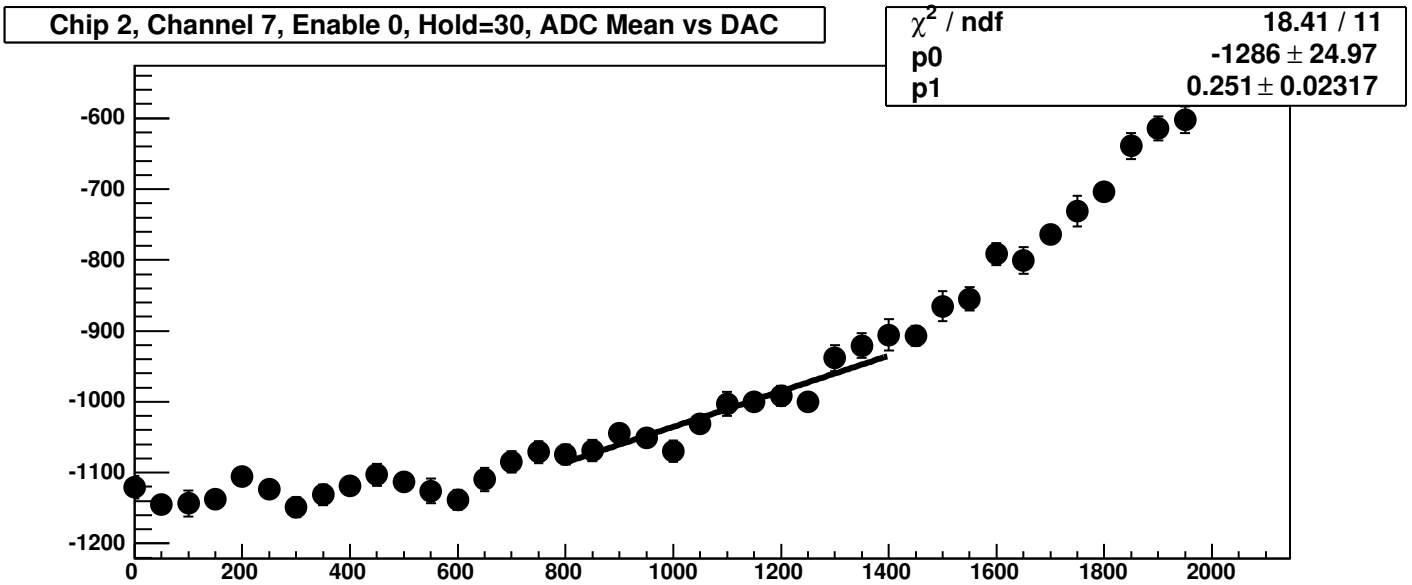


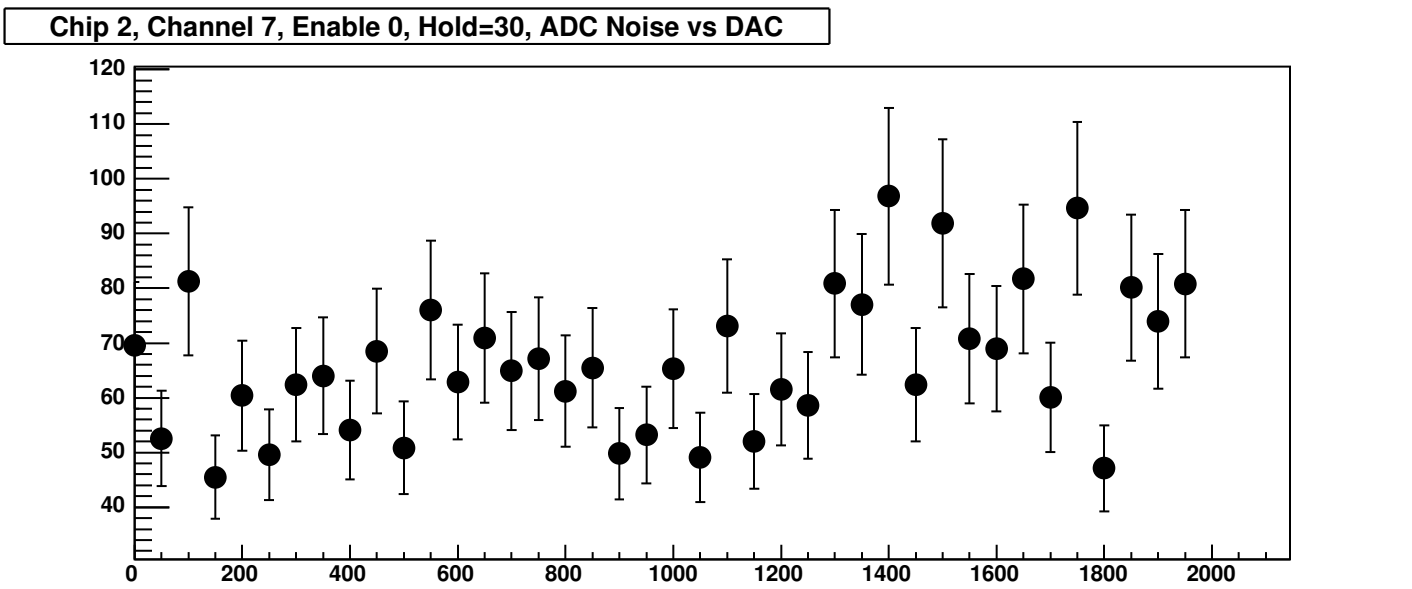
Chip 2, Channel 6, Enable 5, Hold=30, ADC Noise vs DAC

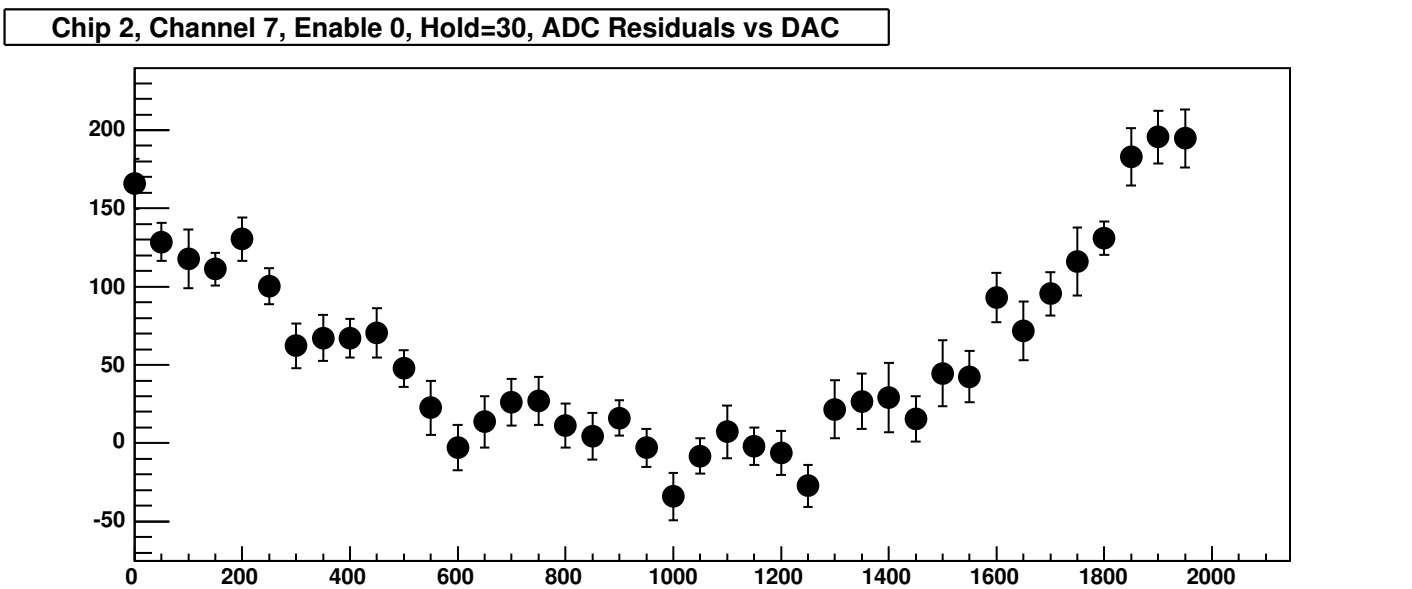


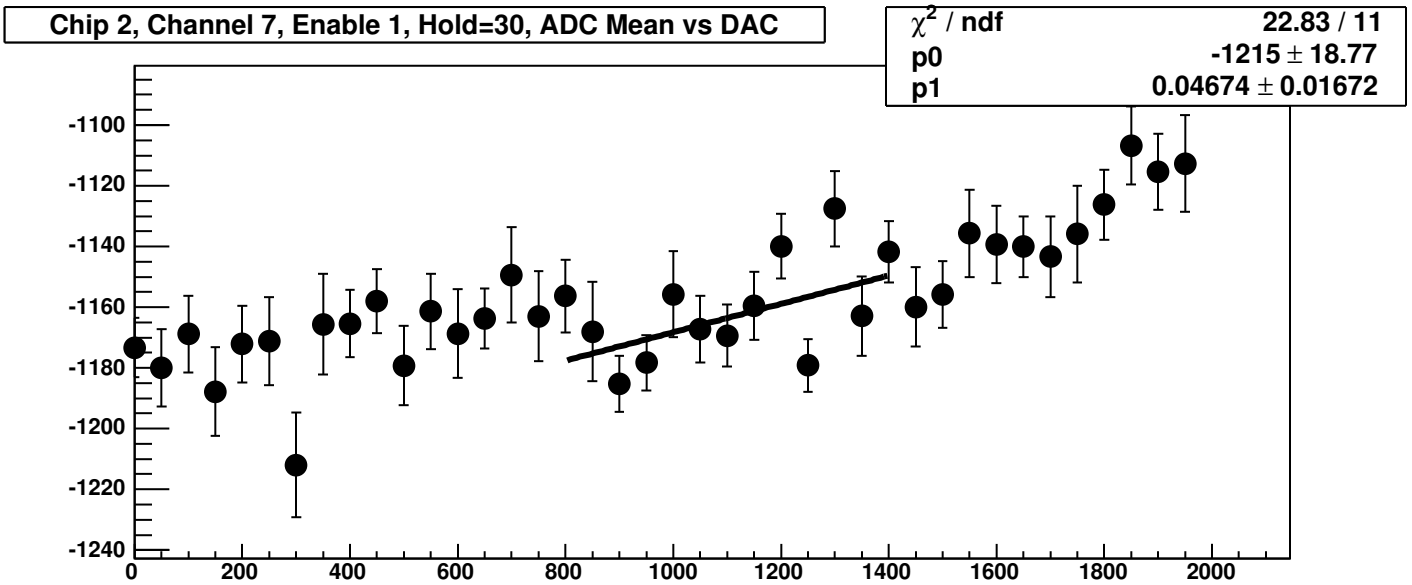
Chip 2, Channel 6, Enable 5, Hold=30, ADC Residuals vs DAC

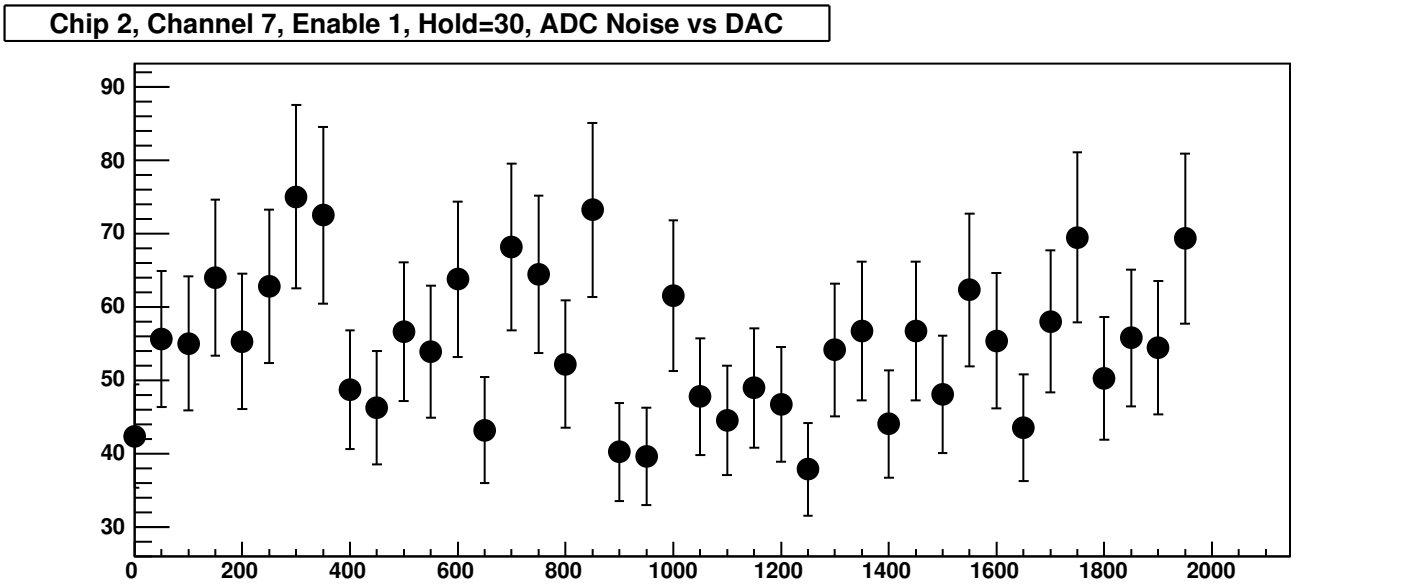


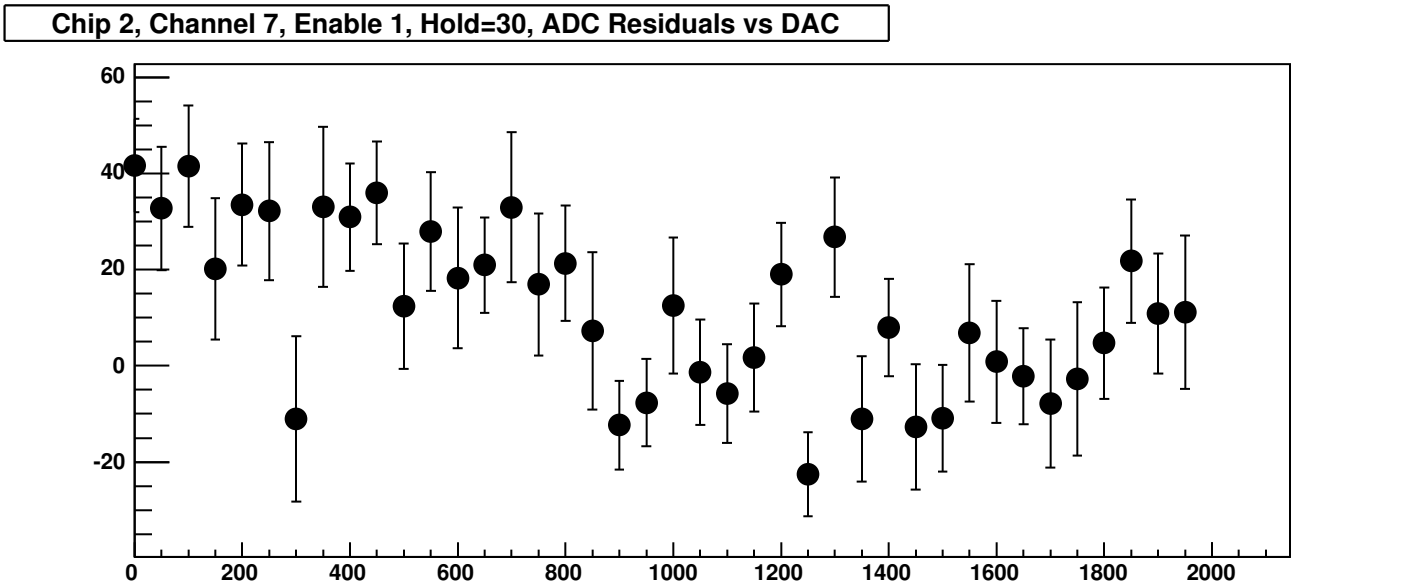

Chip 2, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 18.41 / 11
p0 -1286 ± 24.97
p1 0.251 ± 0.02317
-600
-700
-800
-900
-1000
-1100
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

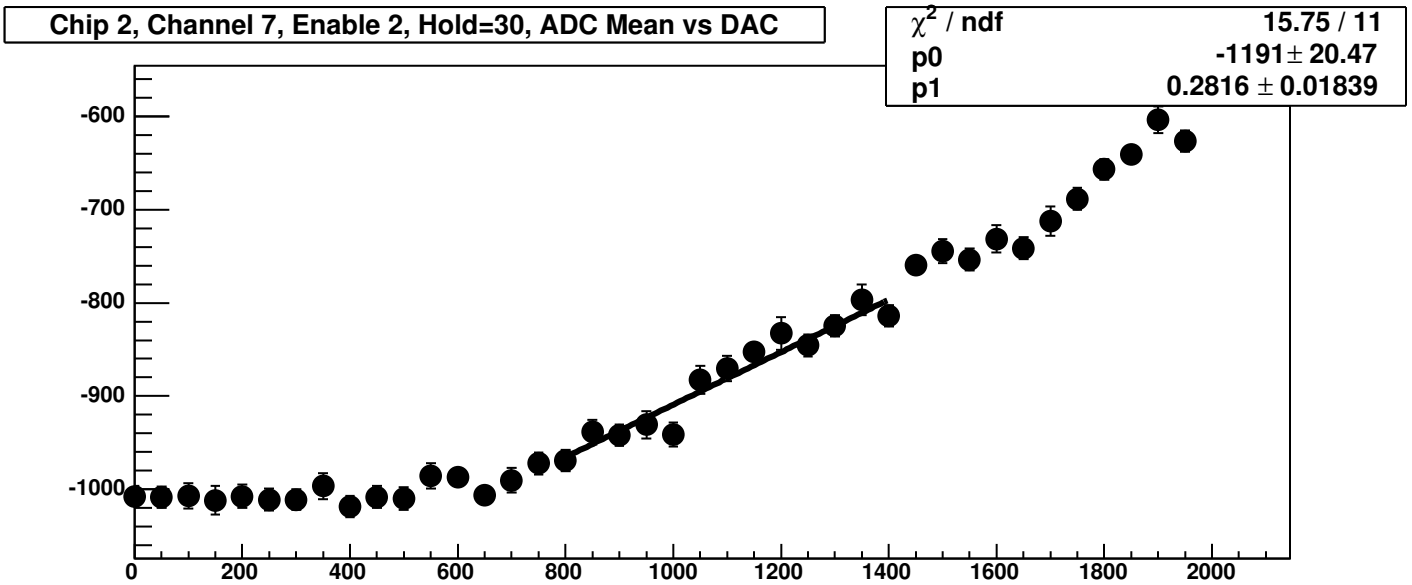

Chip 2, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC
120
110
100
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

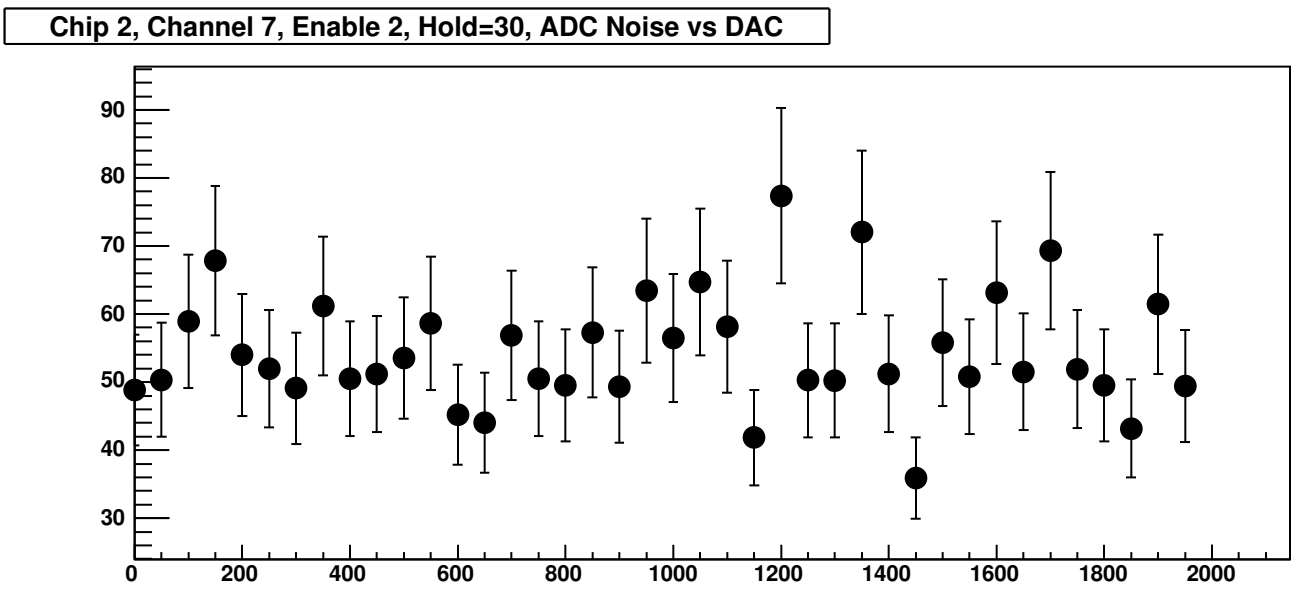

Chip 2, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

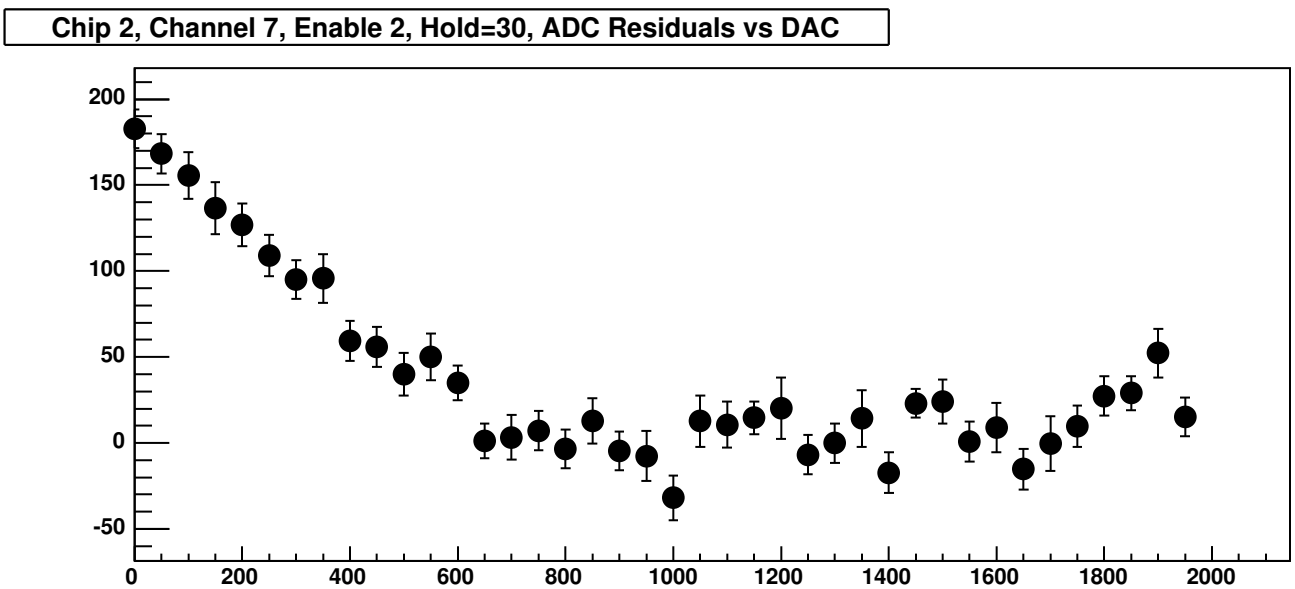

Chip 2, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 22.83 / 11
p0 -1215 ± 18.77
p1 0.04674 ± 0.01672
-1100
-1120
-1140
-1160
-1180
-1200
-1220
-1240
0
200
400
600
800
1000
1200
1400
1600
1800
2000

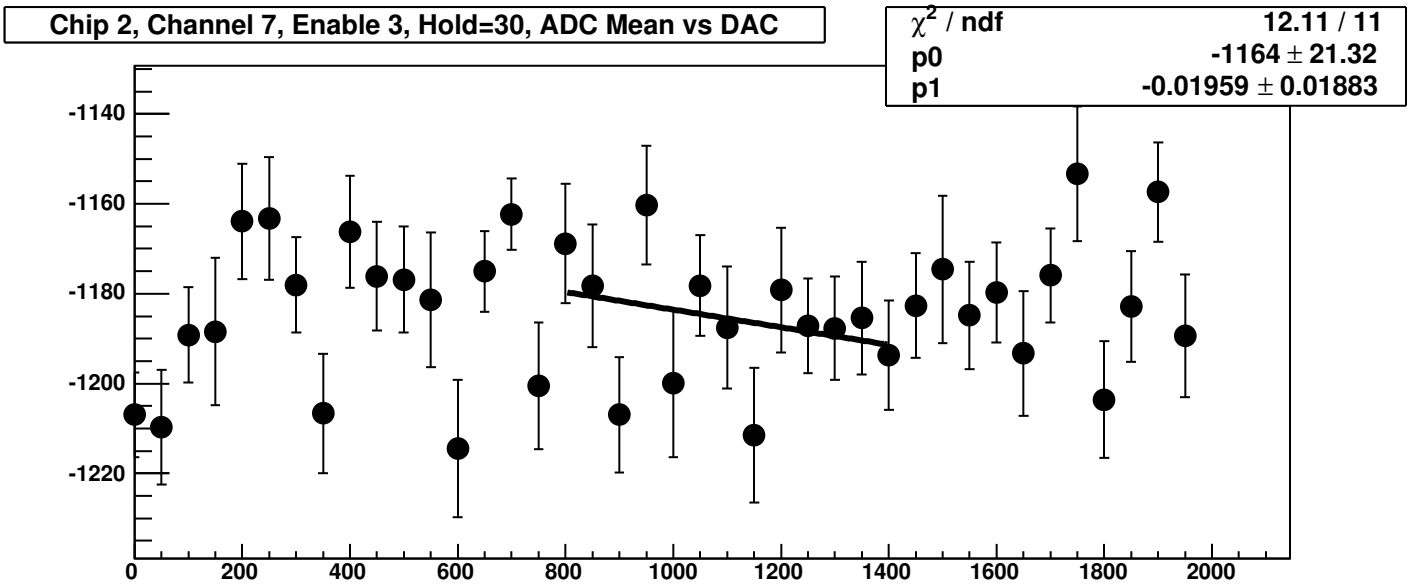

Chip 2, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

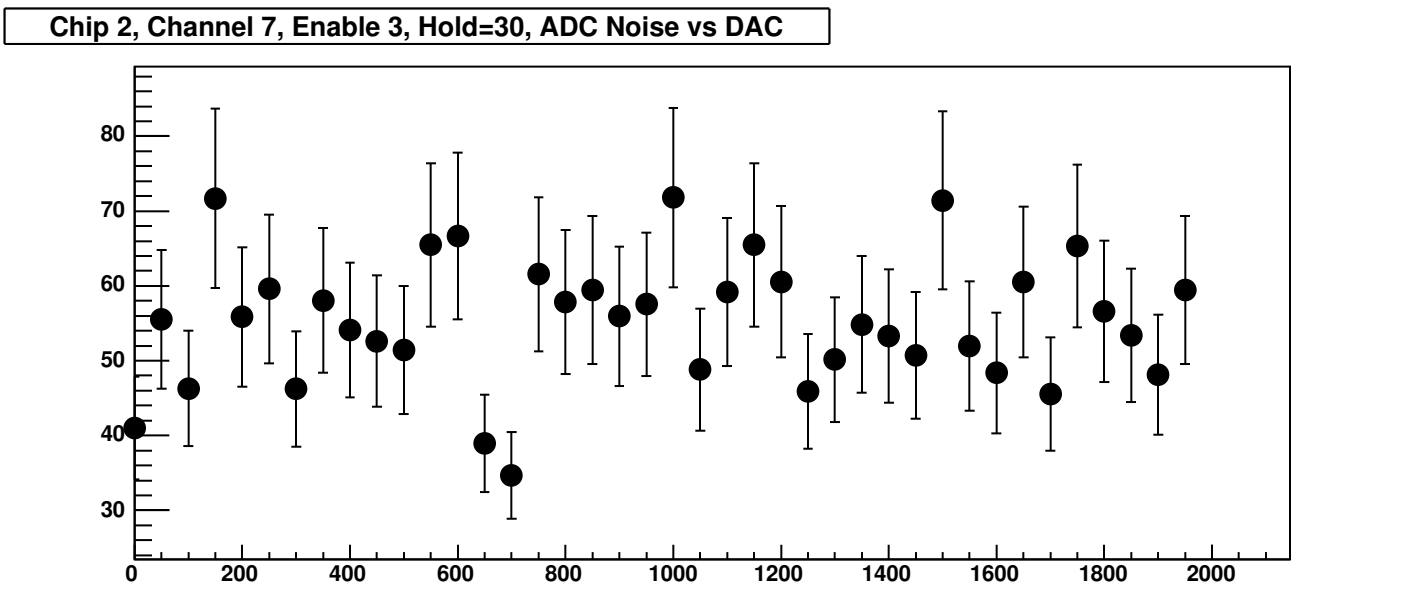

Chip 2, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

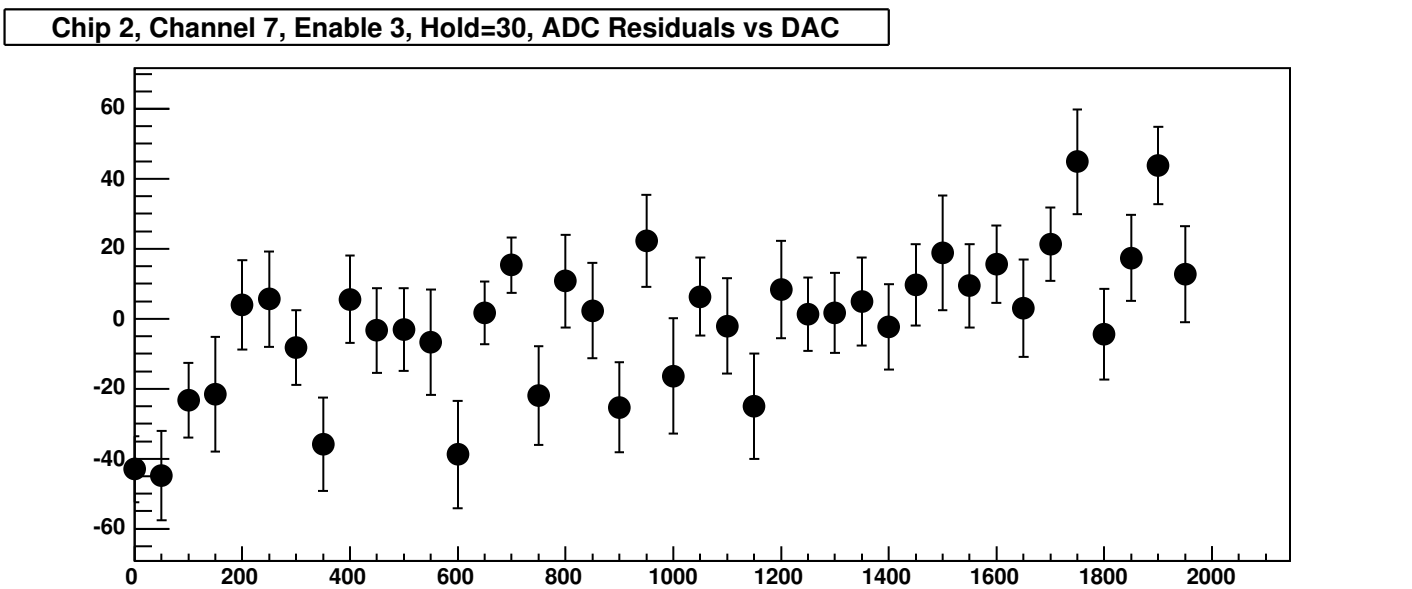

Chip 2, Channel 7, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 15.75 / 11
p0 -1191 ± 20.47
p1 0.2816 ± 0.01839
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

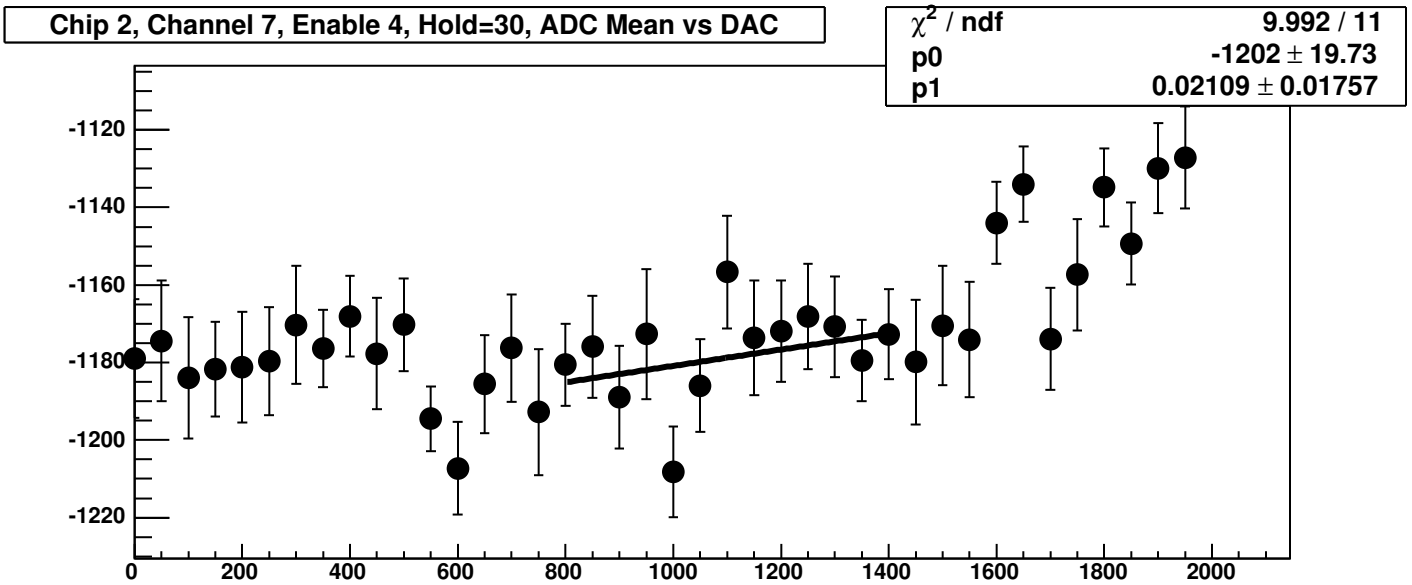

Chip 2, Channel 7, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

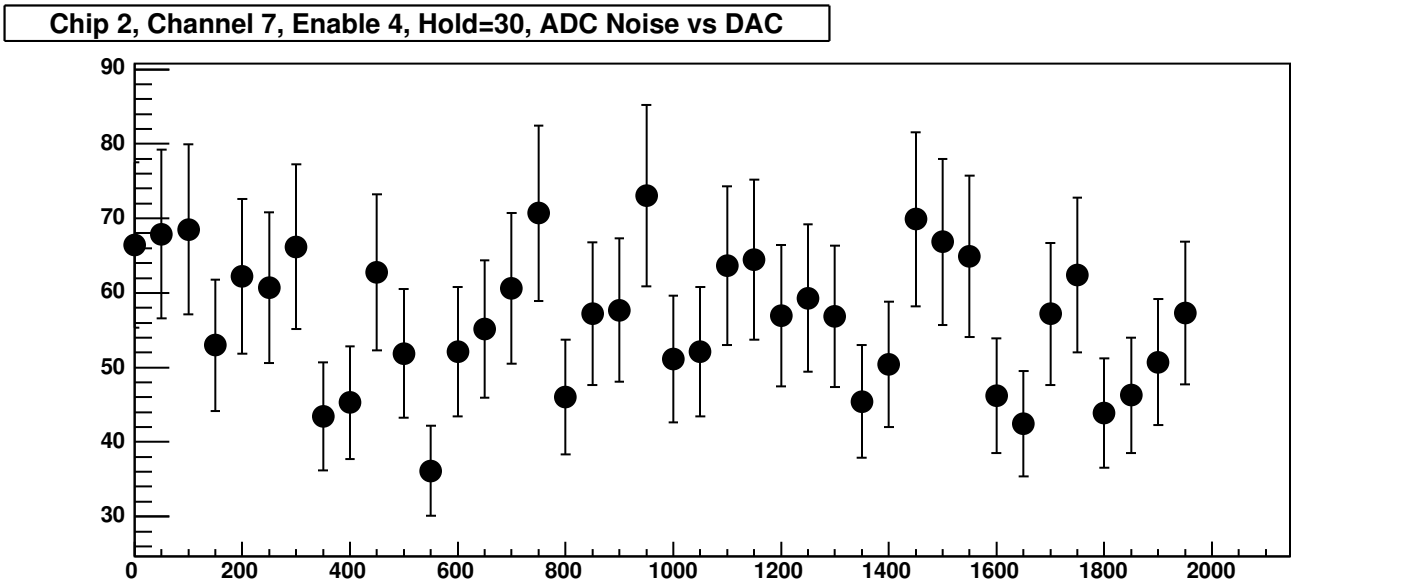

Chip 2, Channel 7, Enable 2, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

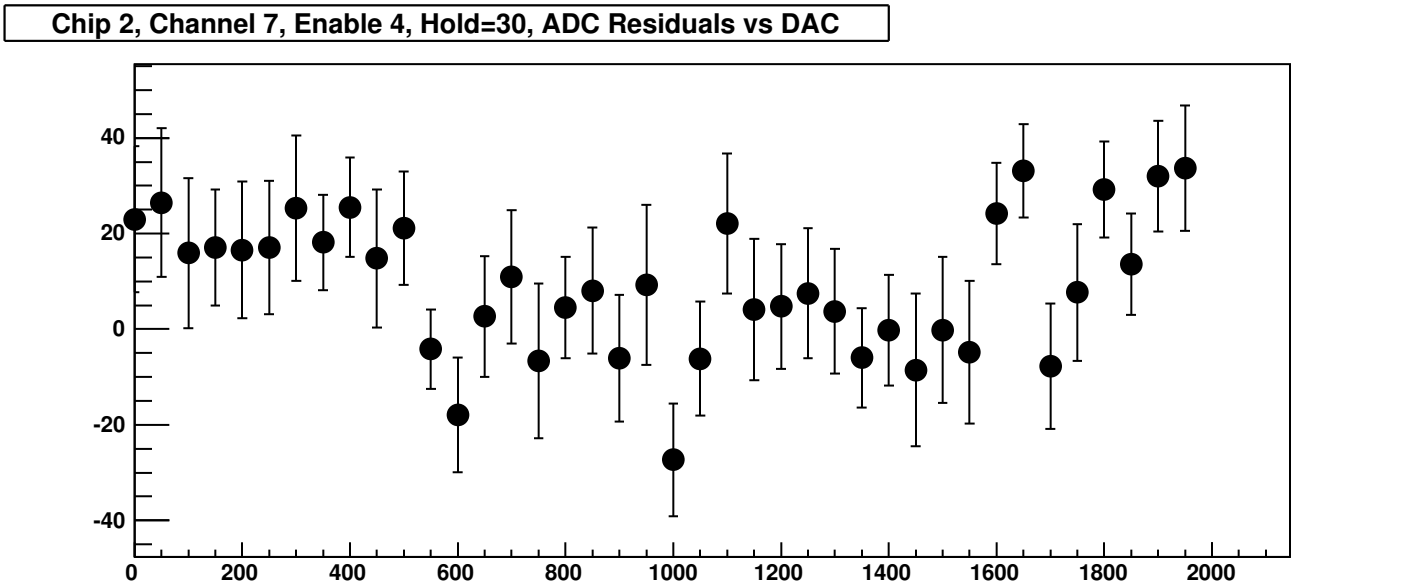

Chip 2, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 12.11 / 11
p0 -1164 ± 21.32
p1 -0.01959 ± 0.01883
-1140
-1160
-1180
-1200
-1220
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

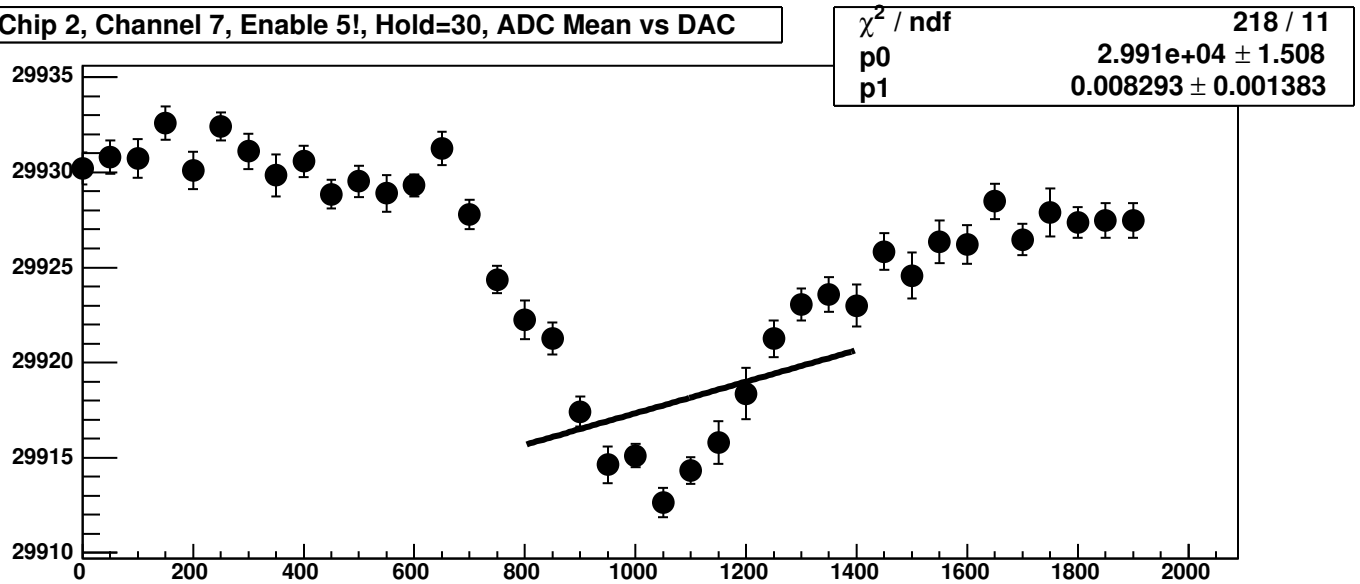

Chip 2, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 9.992 / 11
p0 -1202 ± 19.73
p1 0.02109 ± 0.01757
-1120
-1140
-1160
-1180
-1200
-1220
0
200
400
600
800
1000
1200
1400
1600
1800
2000

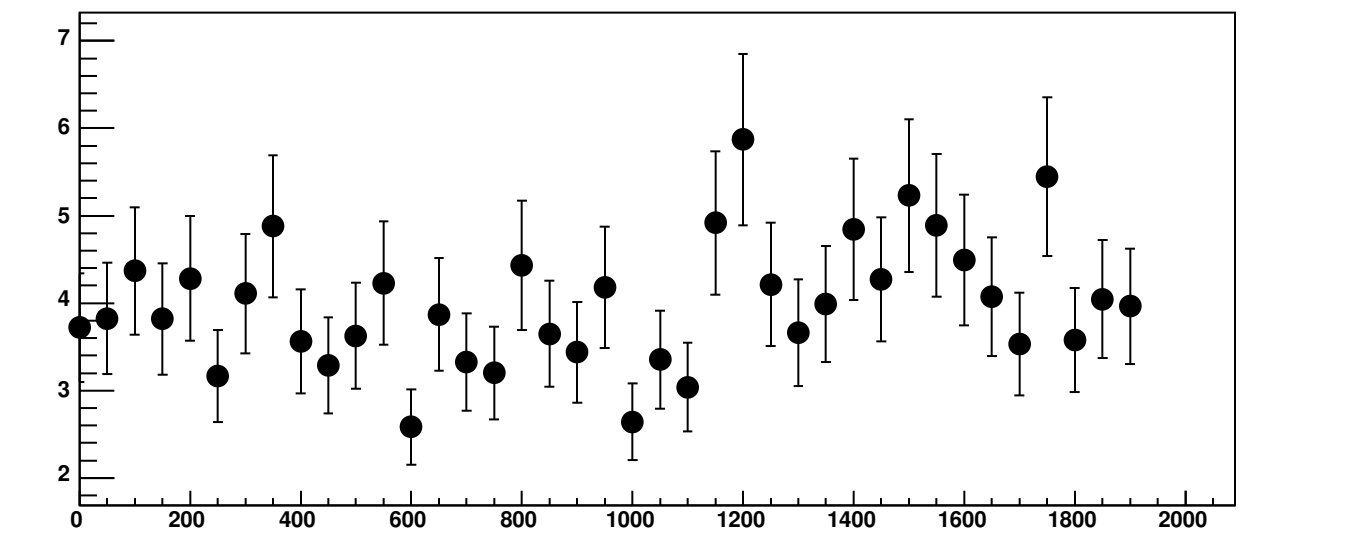

Chip 2, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

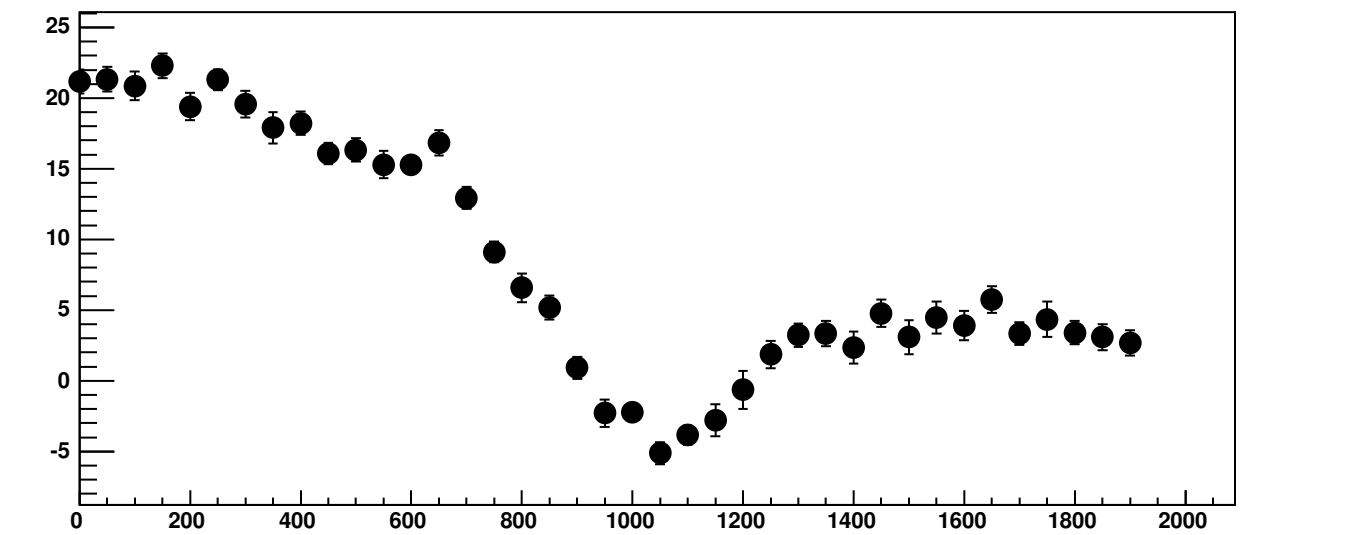
Chip 2, Channel 7, Enable 5!, Hold=30, ADC Mean vs DAC



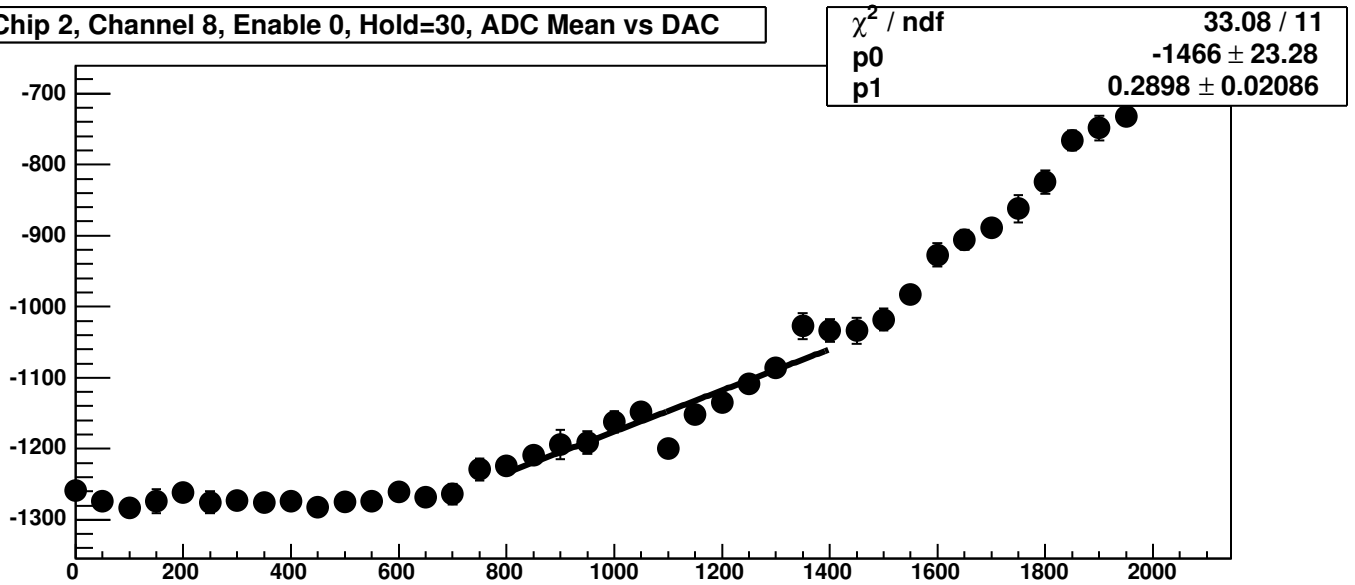
Chip 2, Channel 7, Enable 5!, Hold=30, ADC Noise vs DAC



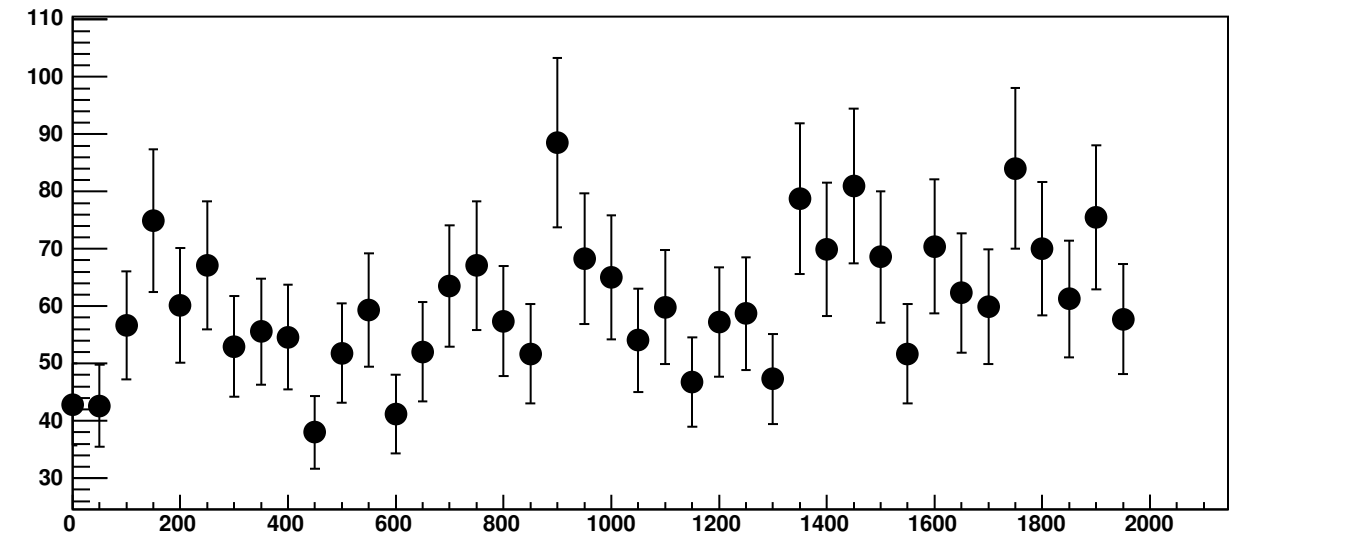
Chip 2, Channel 7, Enable 5!, Hold=30, ADC Residuals vs DAC



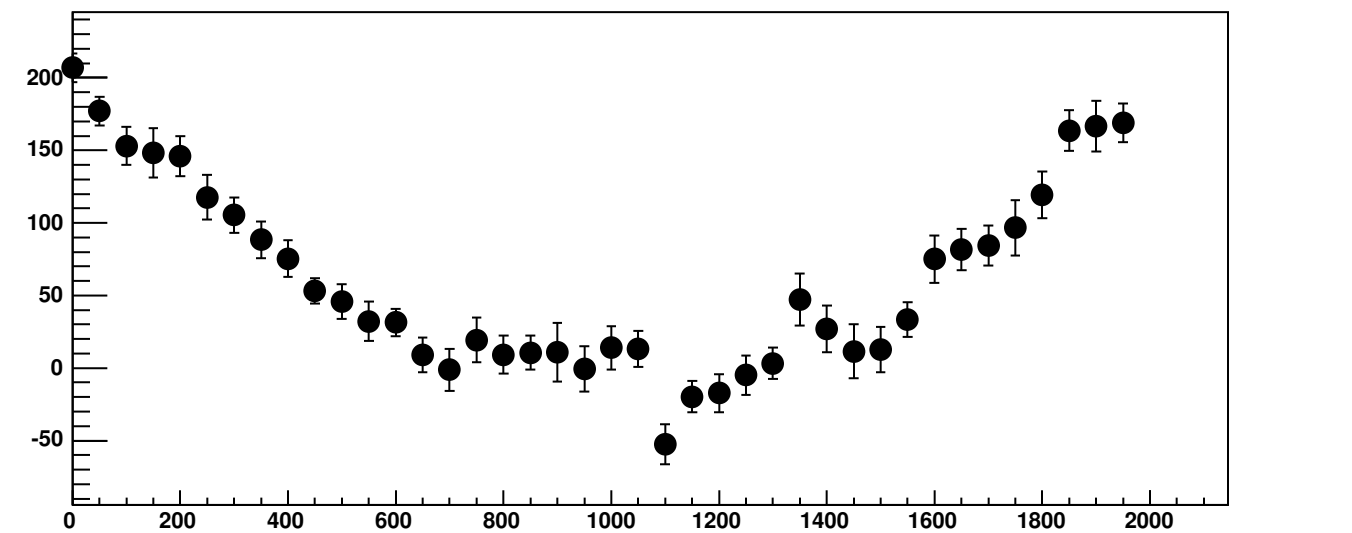
Chip 2, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC

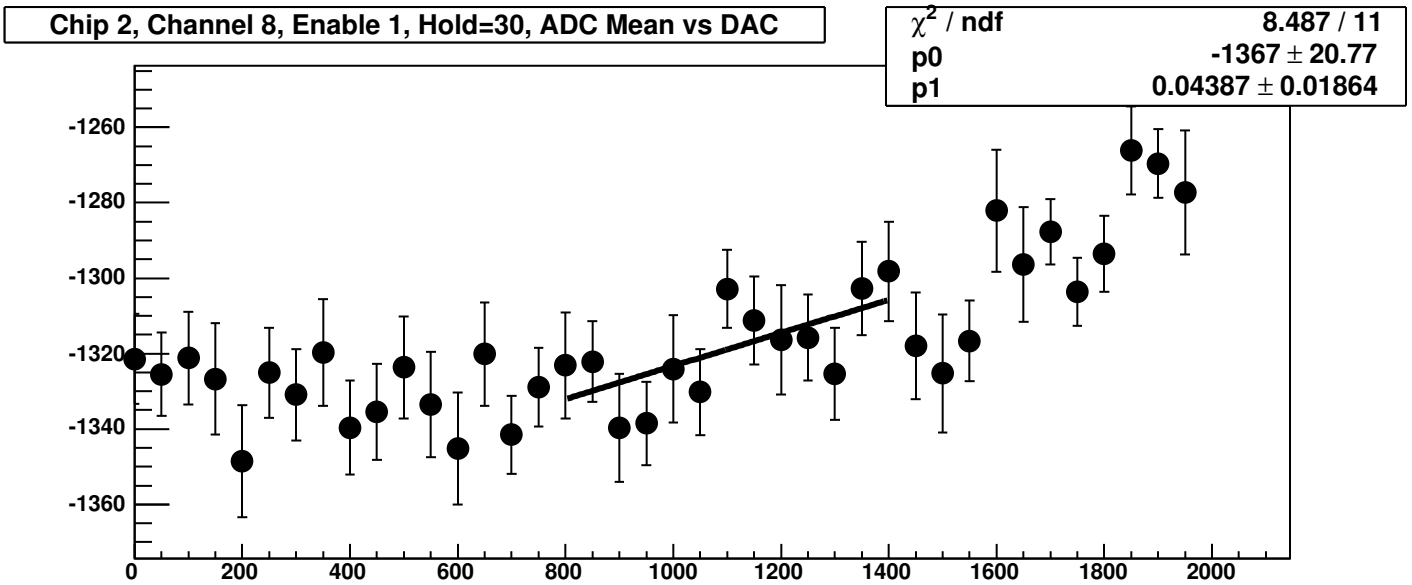


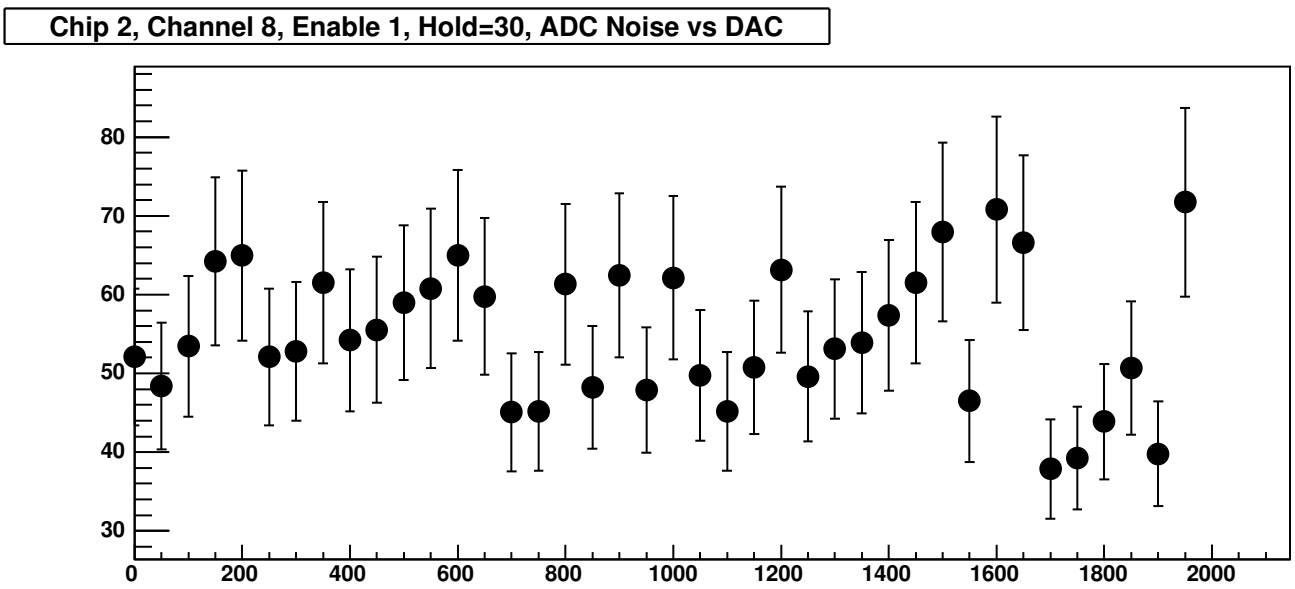
Chip 2, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC

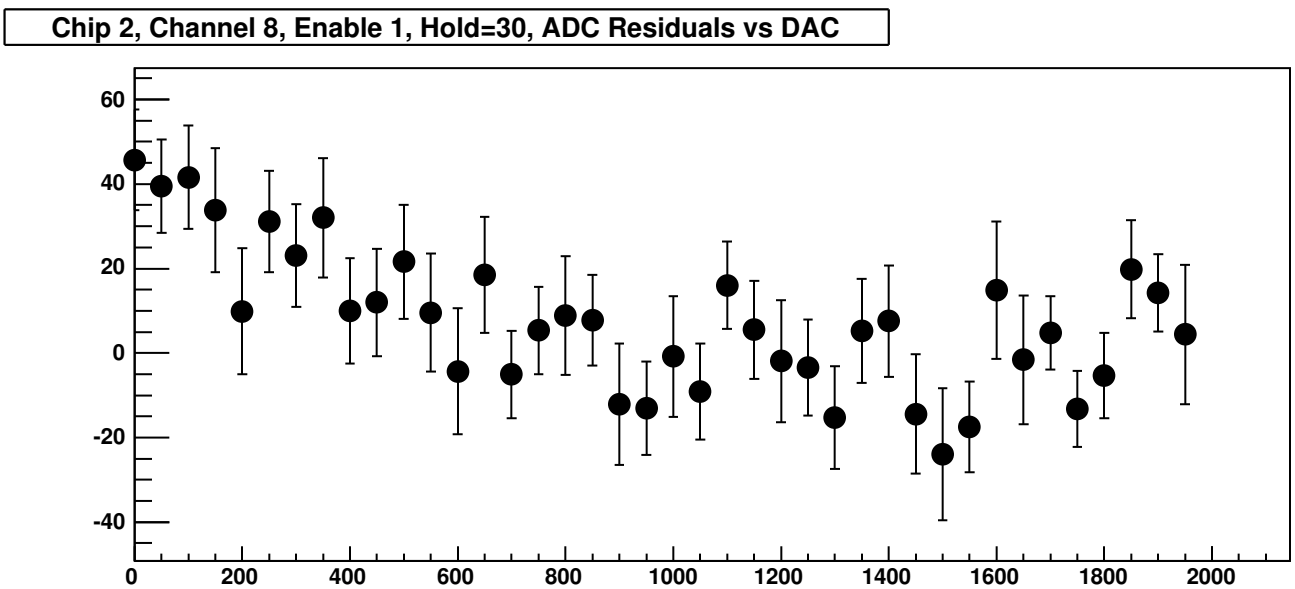


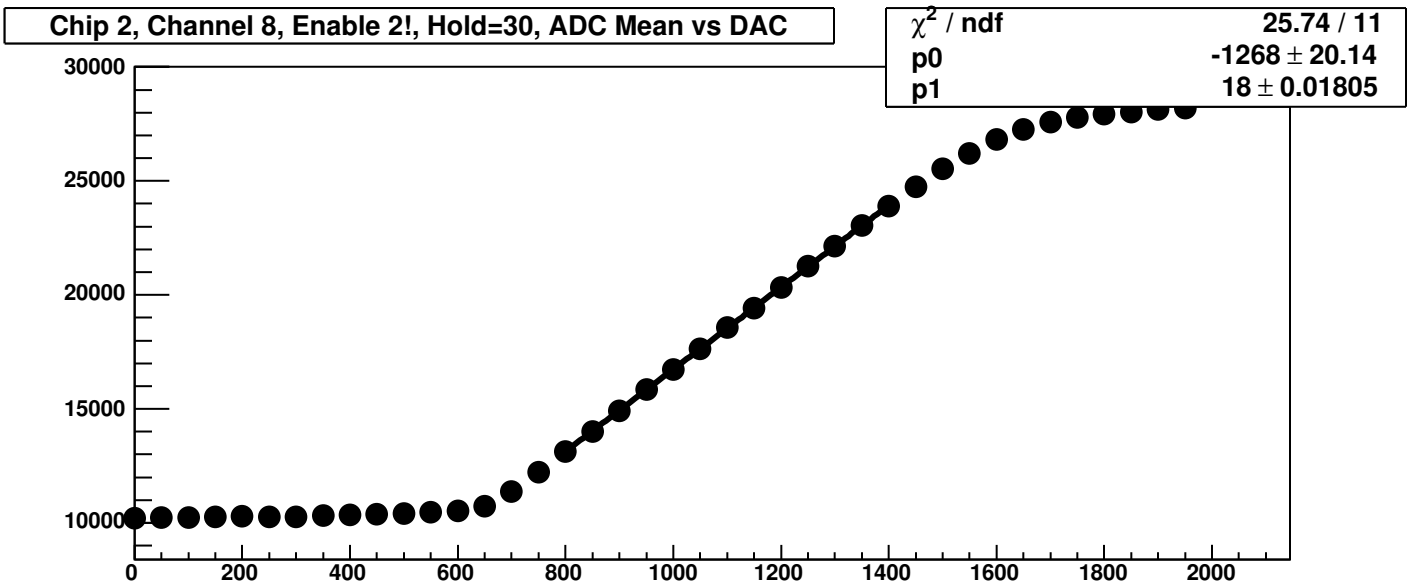
Chip 2, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC

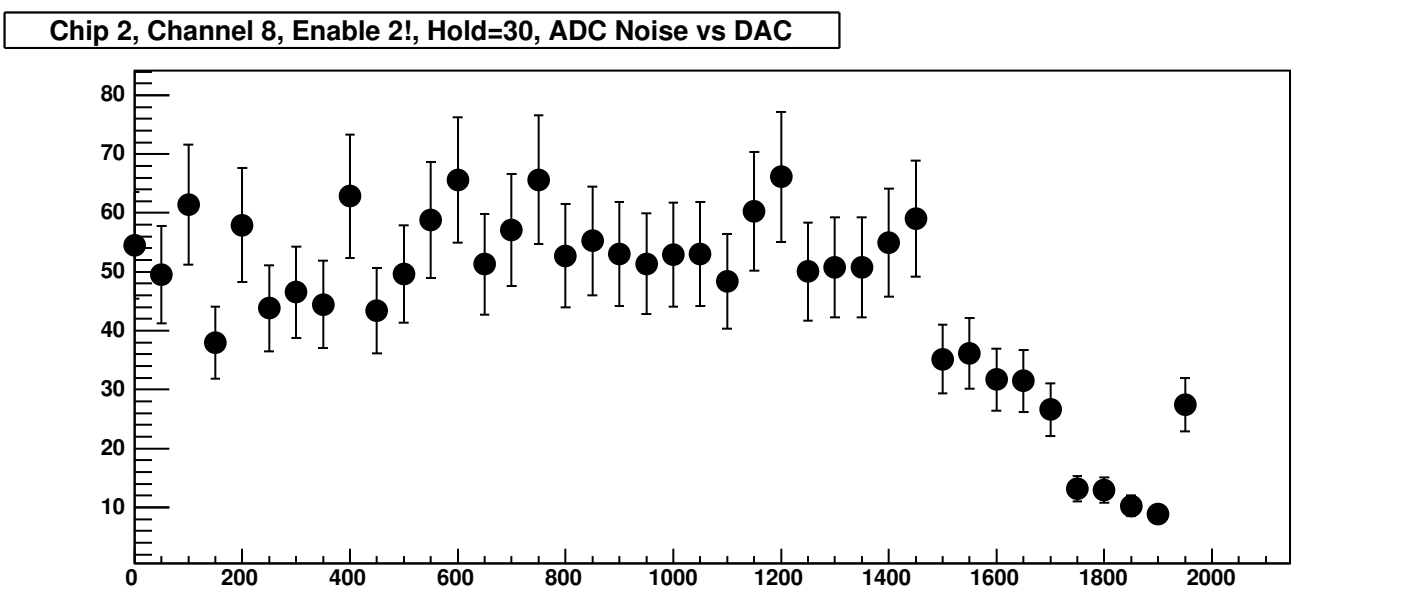


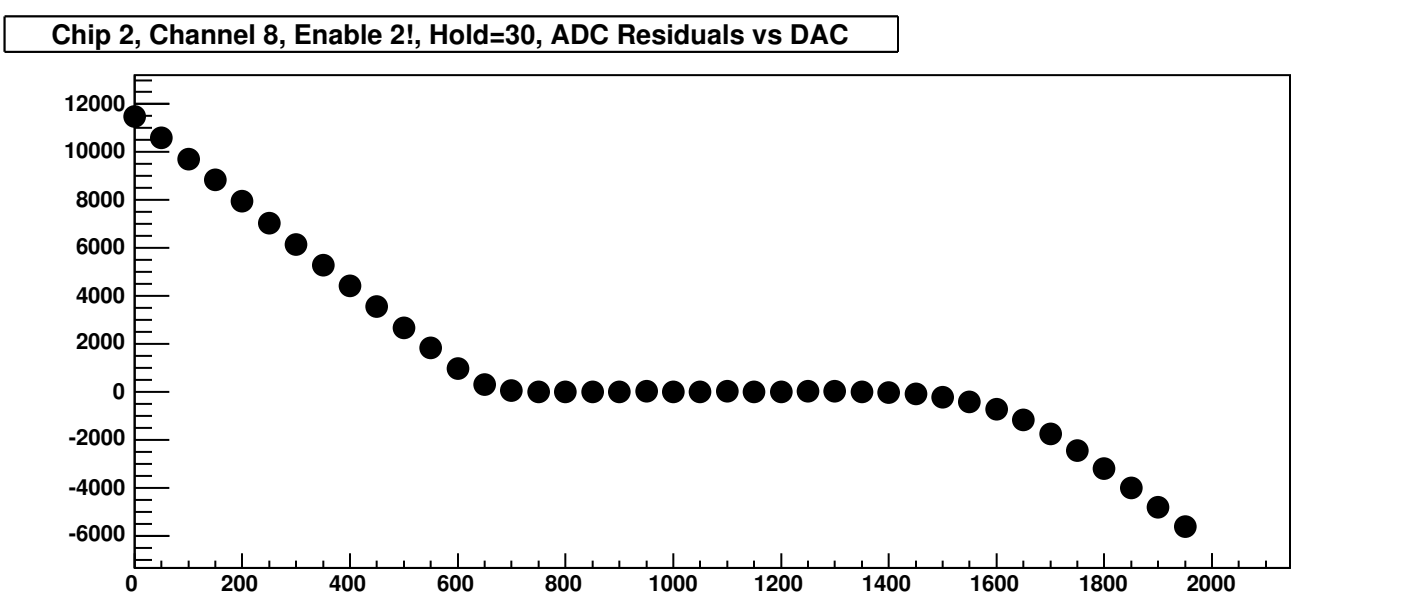

Chip 2, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 8.487 / 11
p0 -1367 ± 20.77
p1 0.04387 ± 0.01864
-1260
-1280
-1300
-1320
-1340
-1360
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

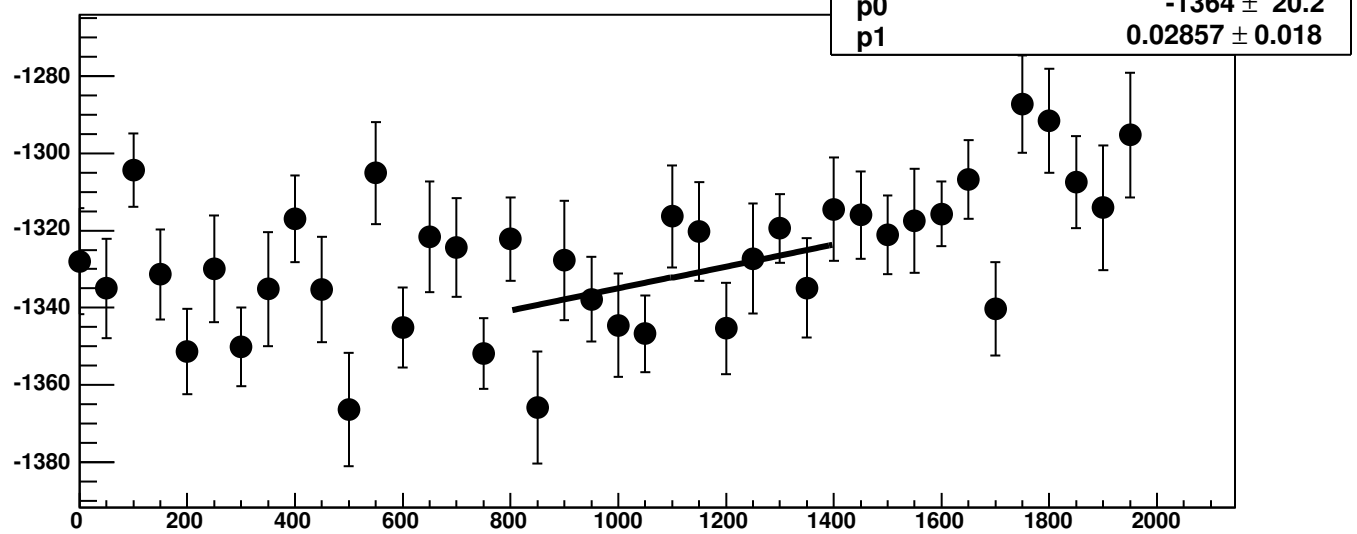

Chip 2, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 8, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 25.74 / 11
p0 -1268 ± 20.14
p1 18 ± 0.01805
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

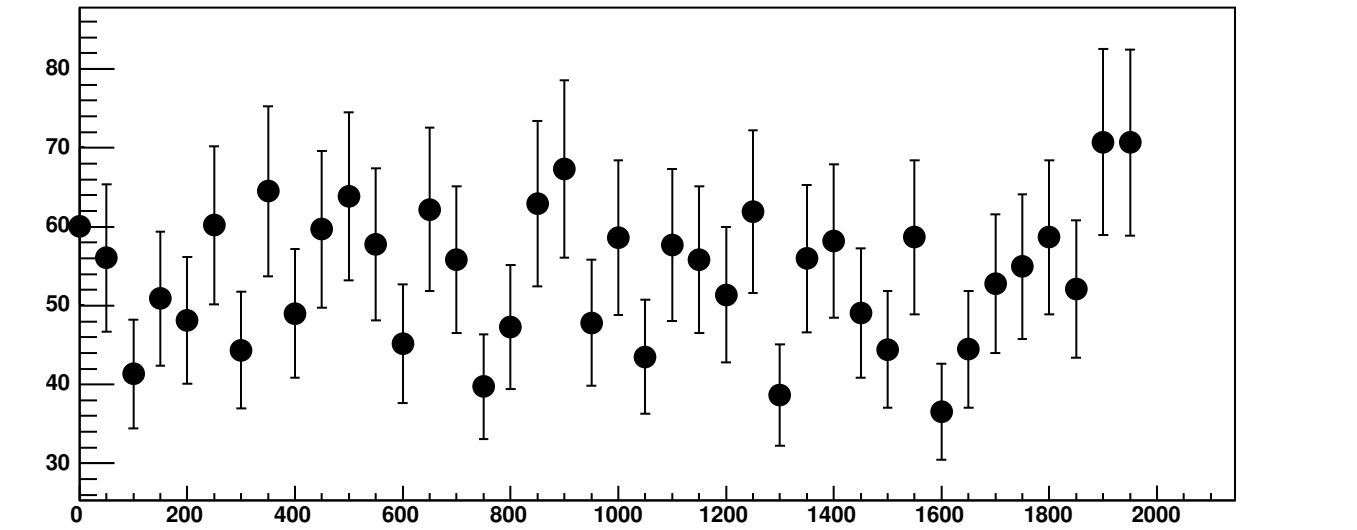

Chip 2, Channel 8, Enable 2!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 8, Enable 2!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

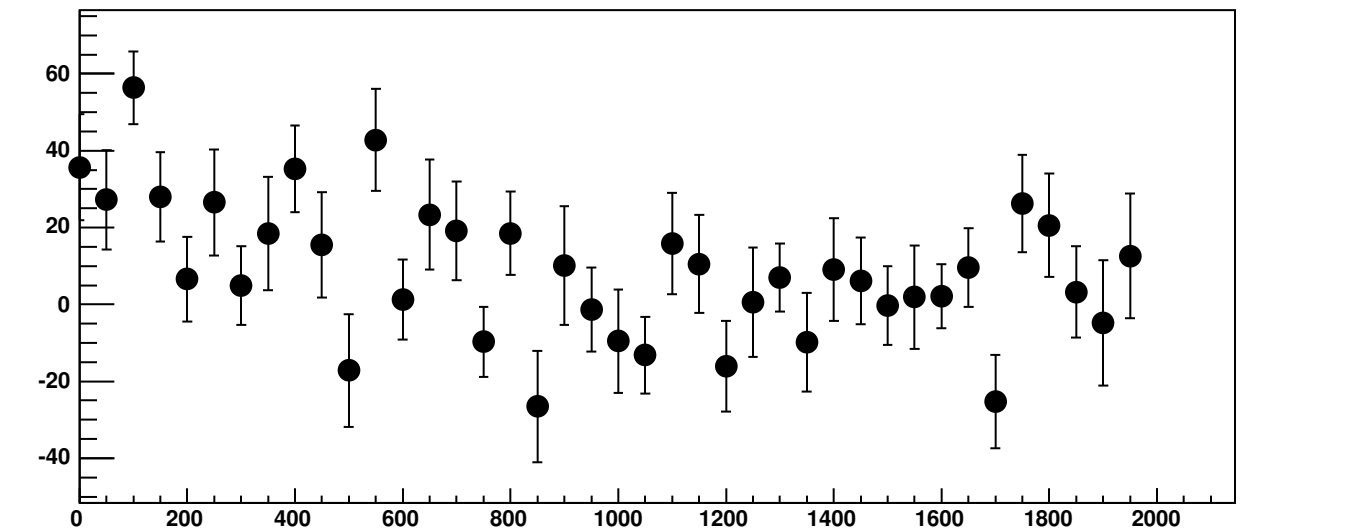
Chip 2, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC

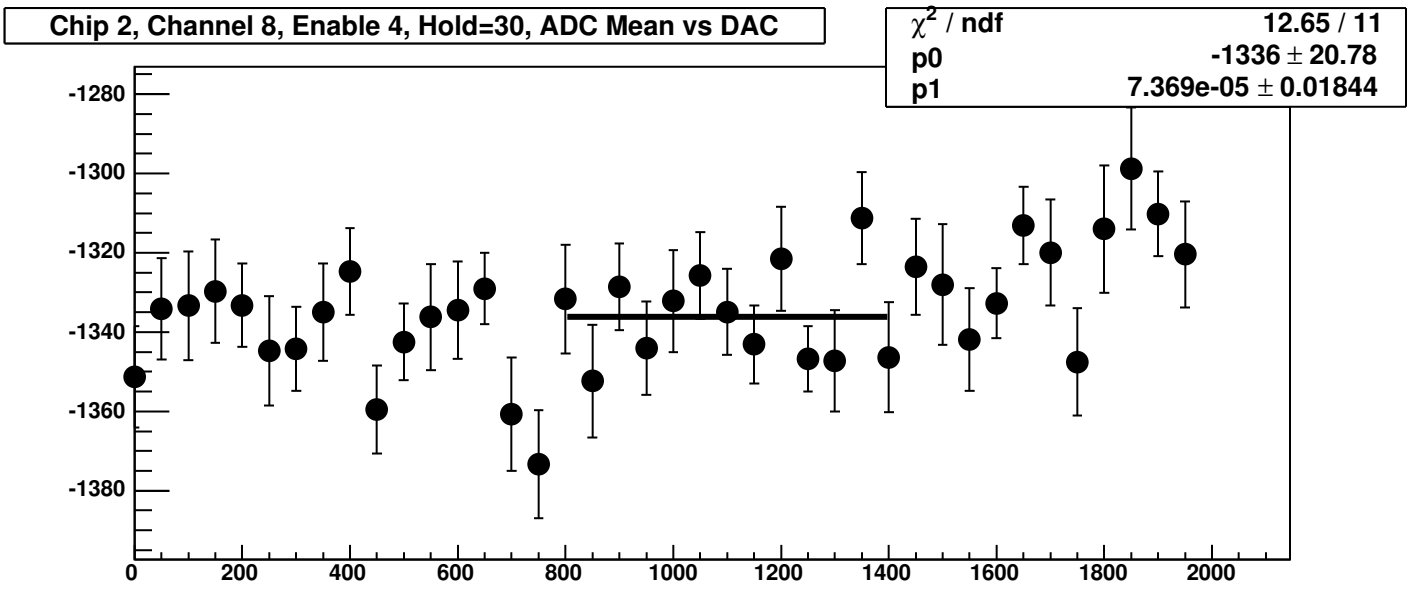


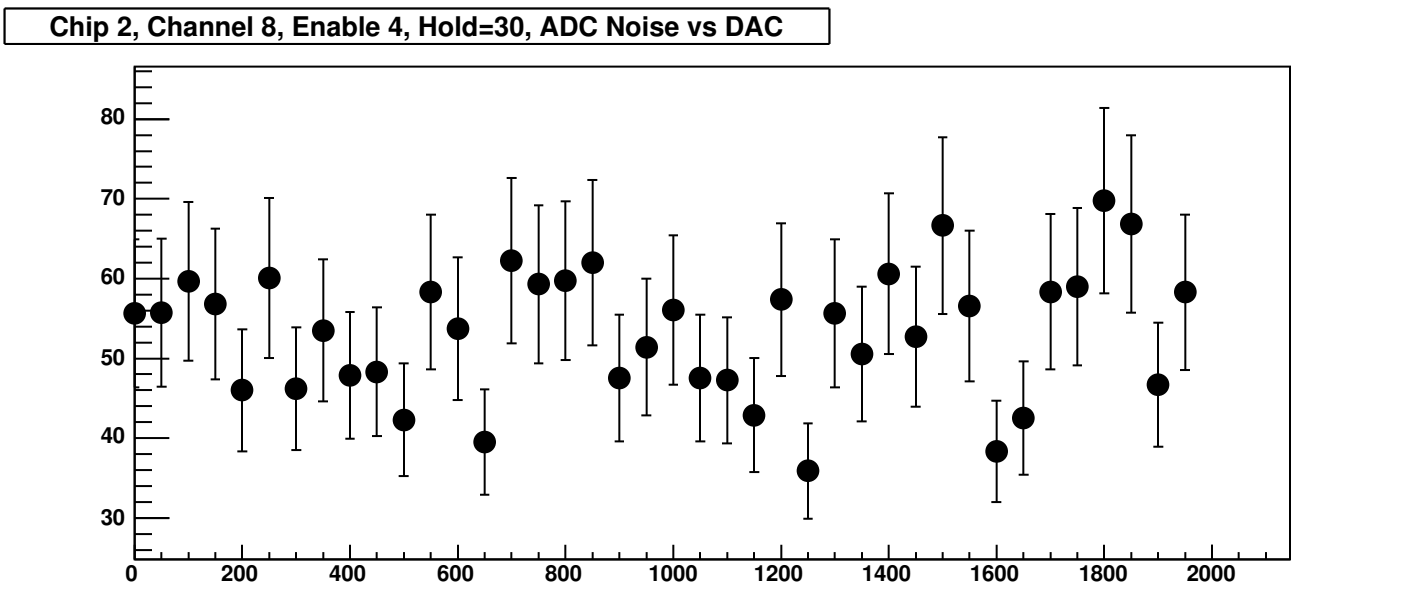
Chip 2, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC

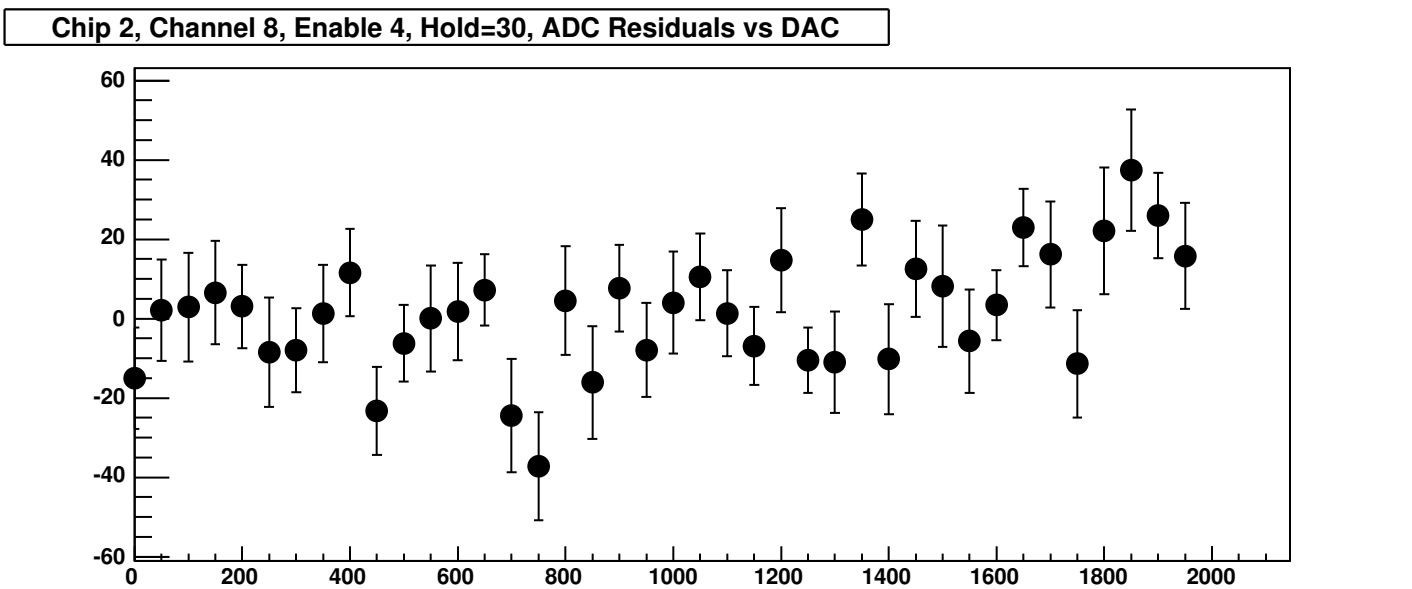


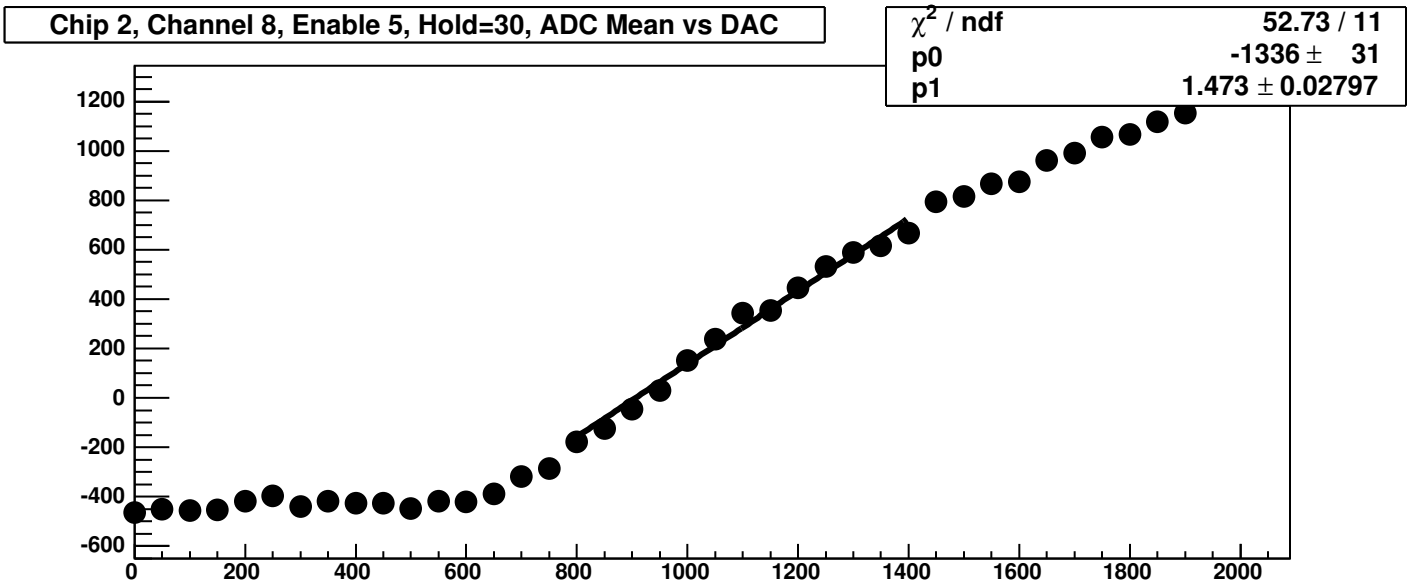
Chip 2, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC

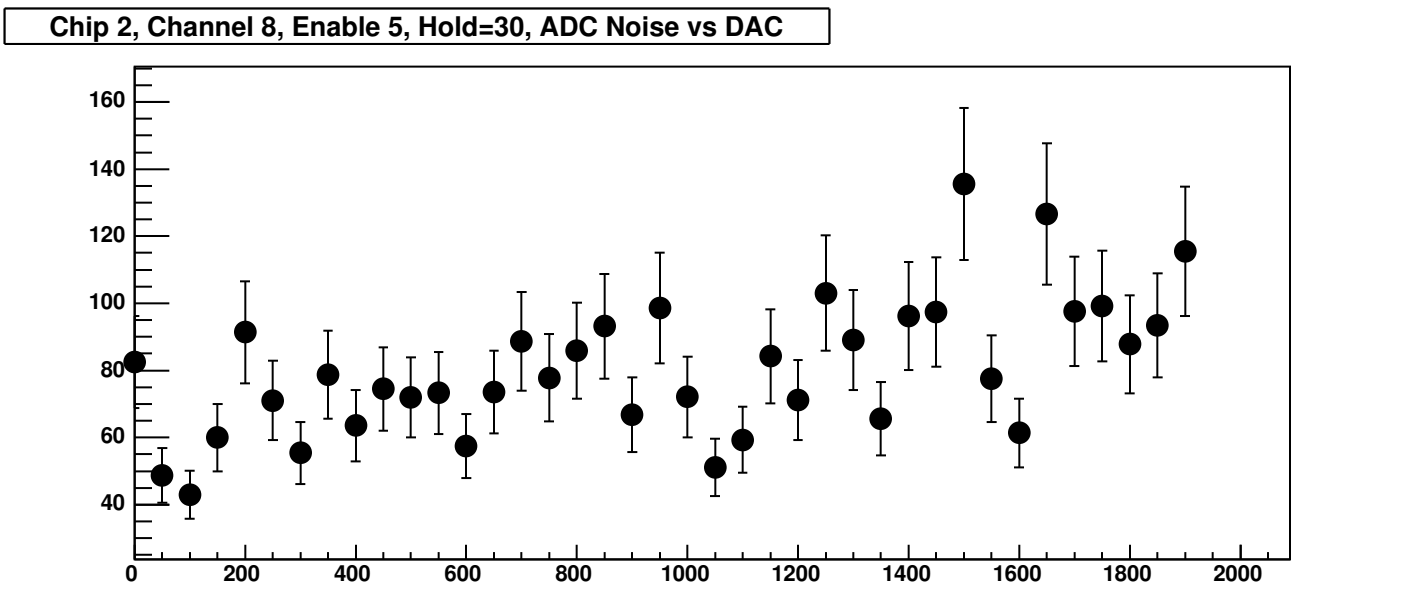


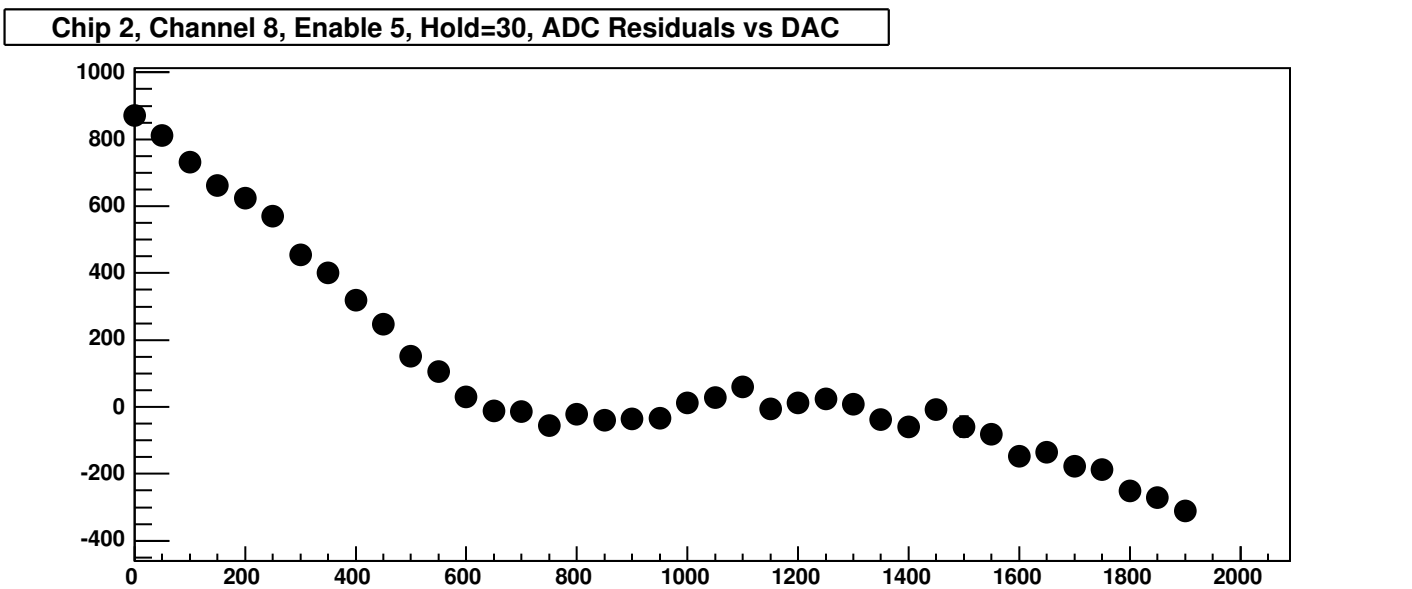

Chip 2, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 12.65 / 11
p0 -1336 ± 20.78
p1 7.369e-05 ± 0.01844
-1280
-1300
-1320
-1340
-1360
-1380
0
200
400
600
800
1000
1200
1400
1600
1800
2000

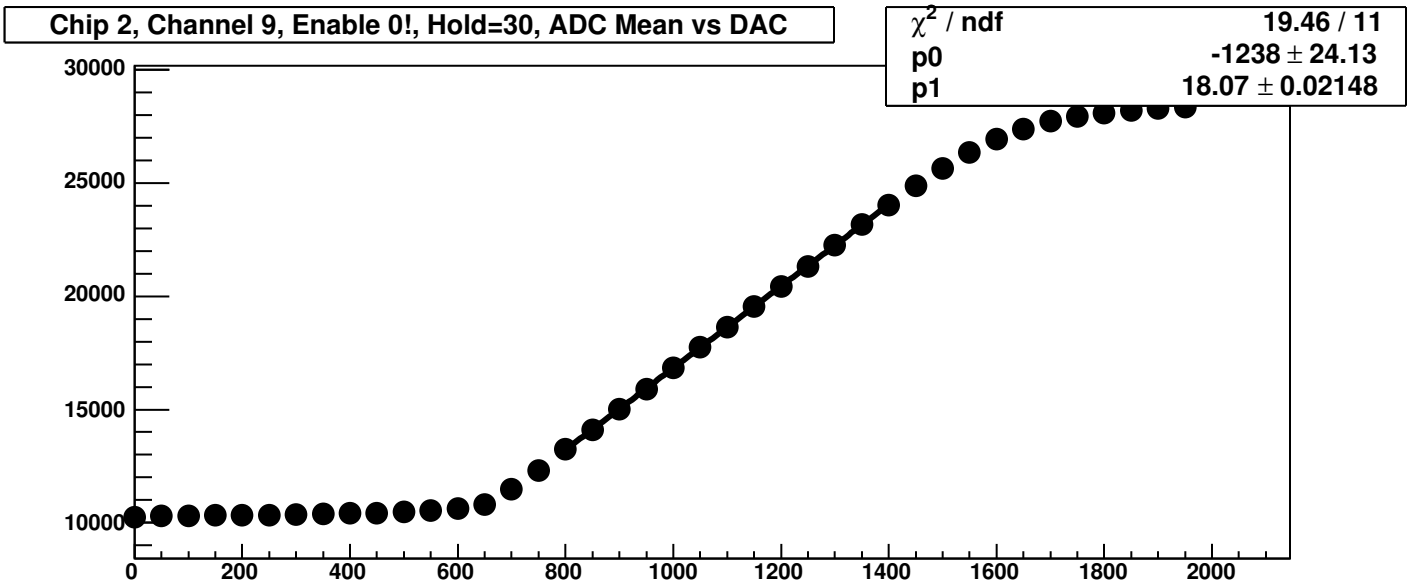

Chip 2, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

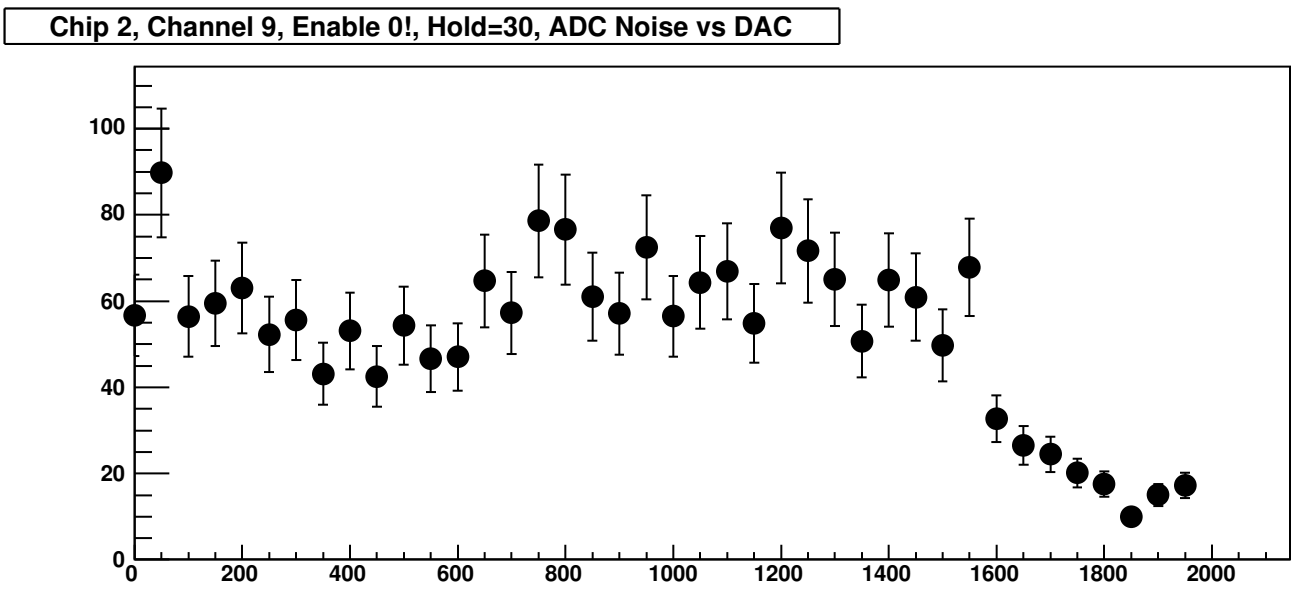

Chip 2, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

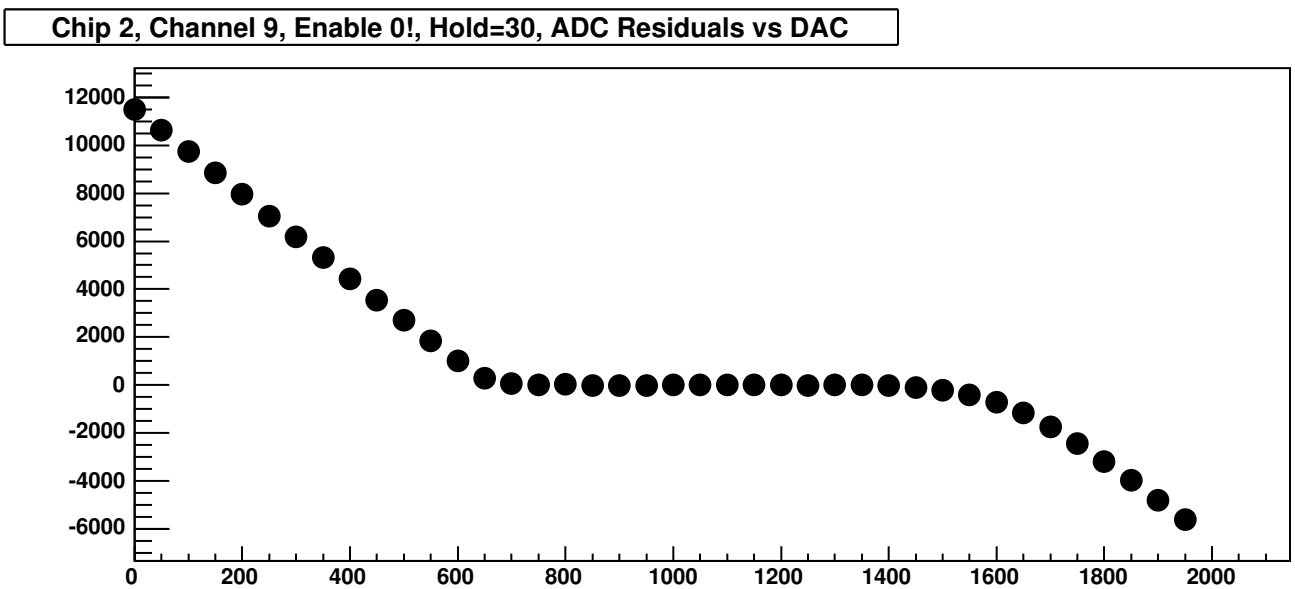

Chip 2, Channel 8, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 52.73 / 11
p0 -1336 ± 31
p1 1.473 ± 0.02797
1200
1000
800
600
400
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

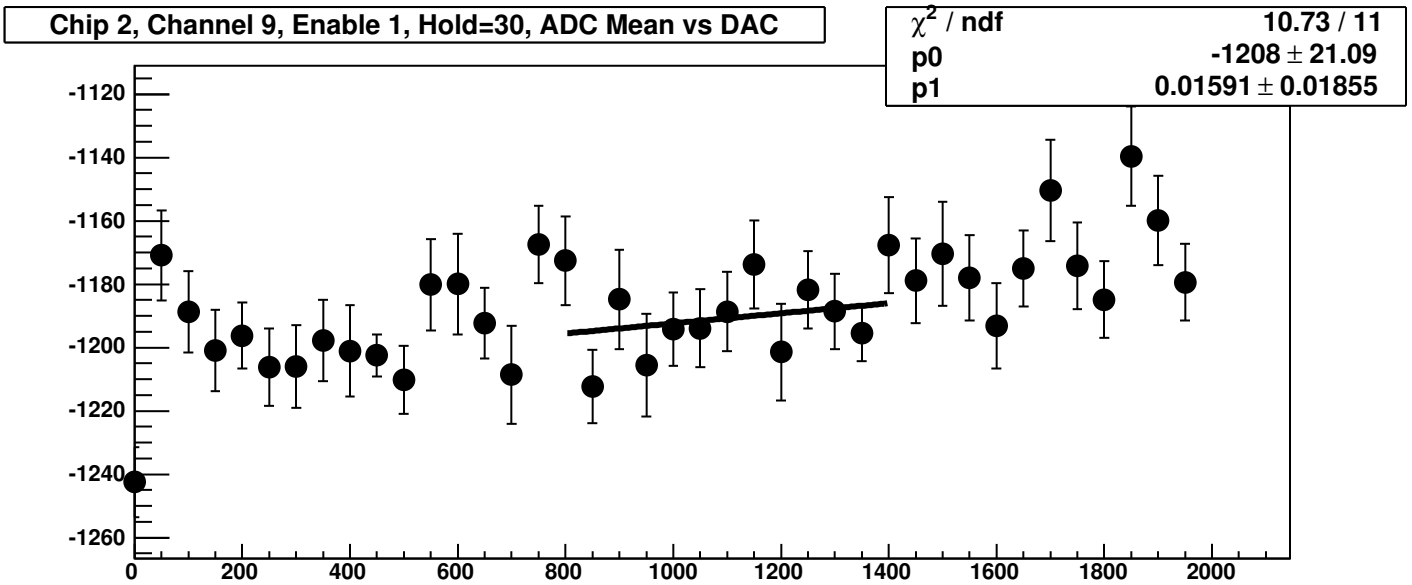

Chip 2, Channel 8, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

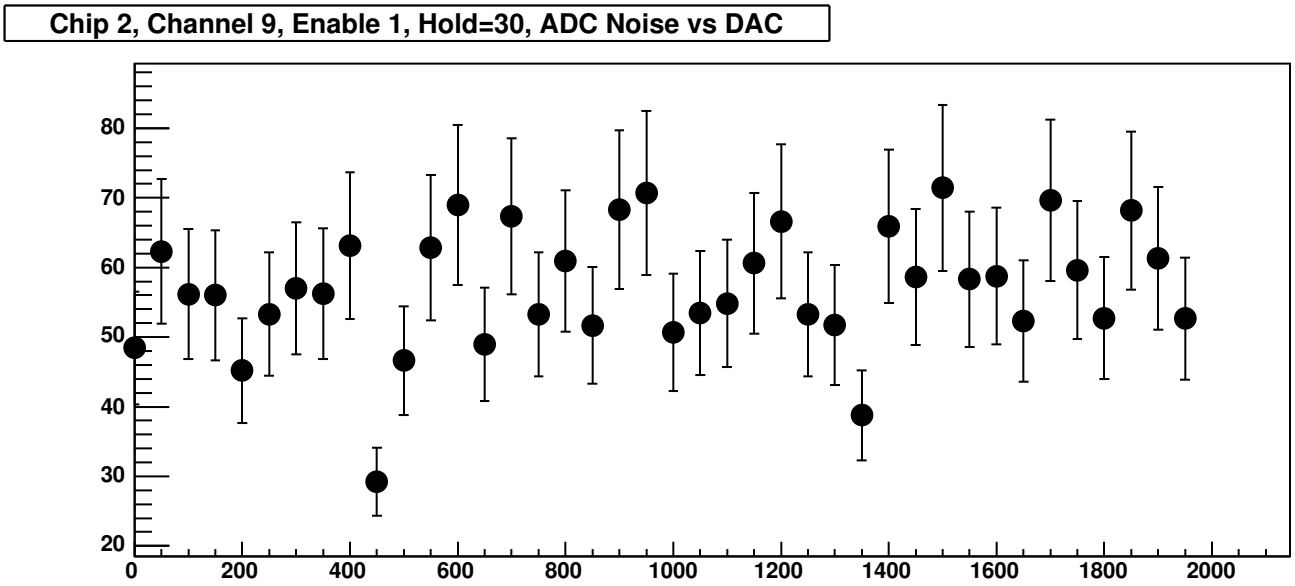

Chip 2, Channel 8, Enable 5, Hold=30, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

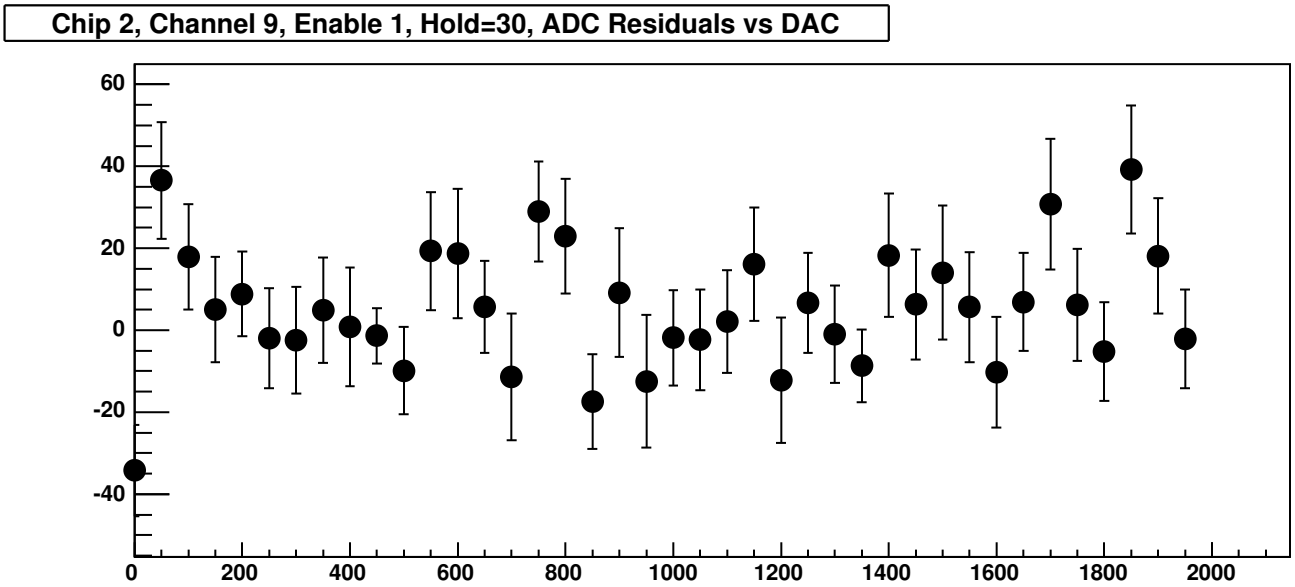

Chip 2, Channel 9, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 19.46 / 11
p0 -1238 ± 24.13
p1 18.07 ± 0.02148
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 9, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

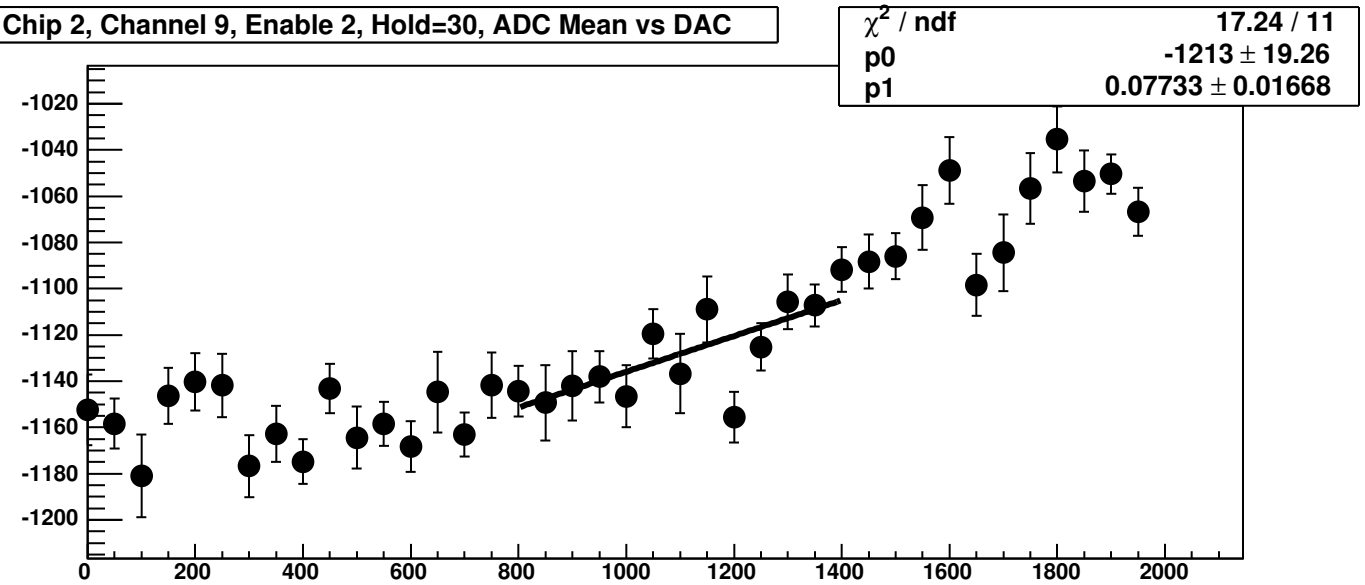

Chip 2, Channel 9, Enable 0!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 10.73 / 11
p0 -1208 ± 21.09
p1 0.01591 ± 0.01855
-1120
-1140
-1160
-1180
-1200
-1220
-1240
-1260
0
200
400
600
800
1000
1200
1400
1600
1800
2000

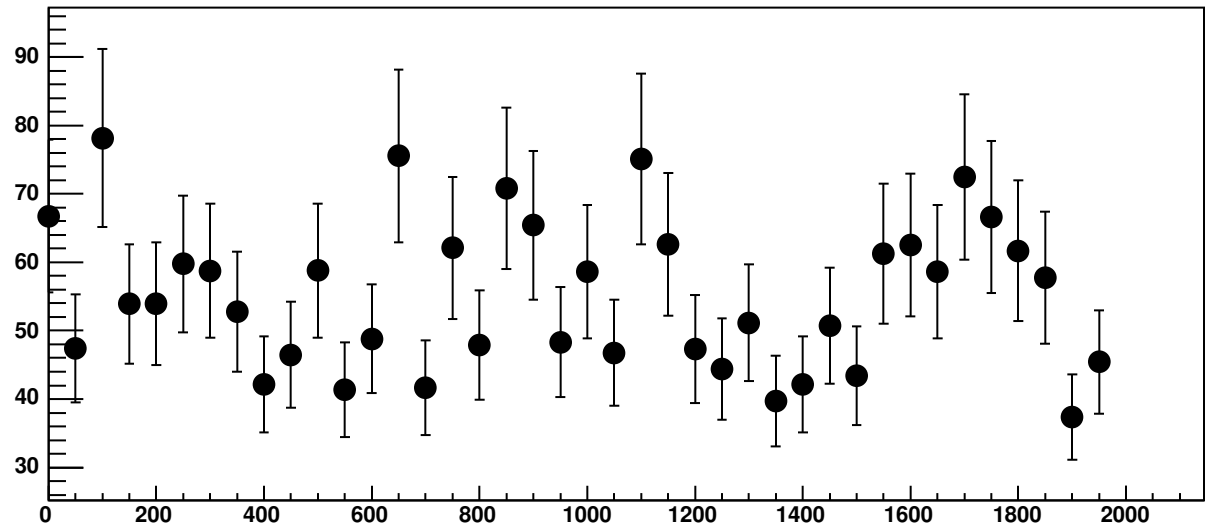

Chip 2, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

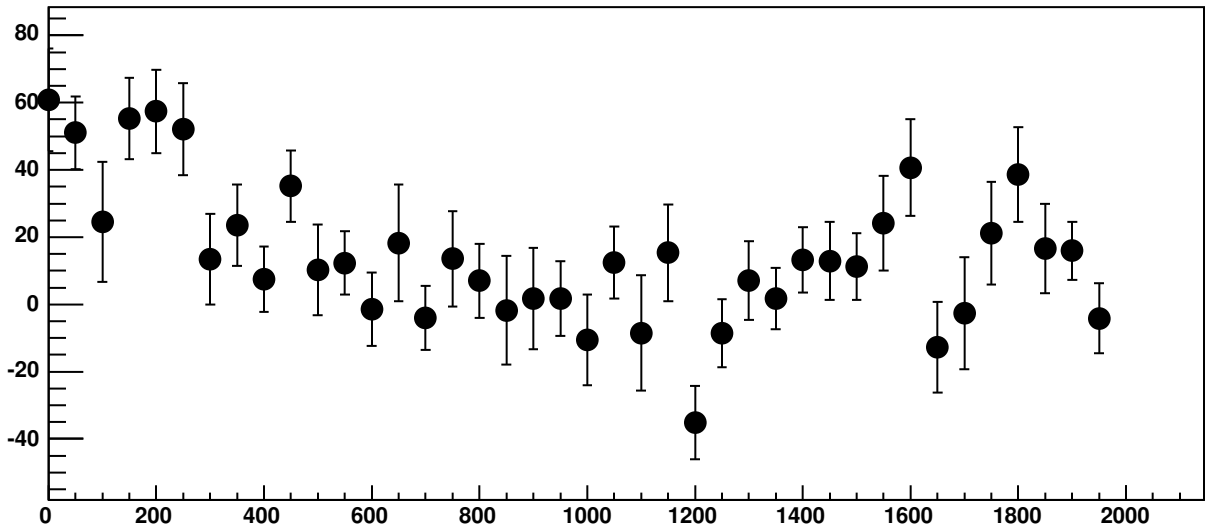
Chip 2, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC

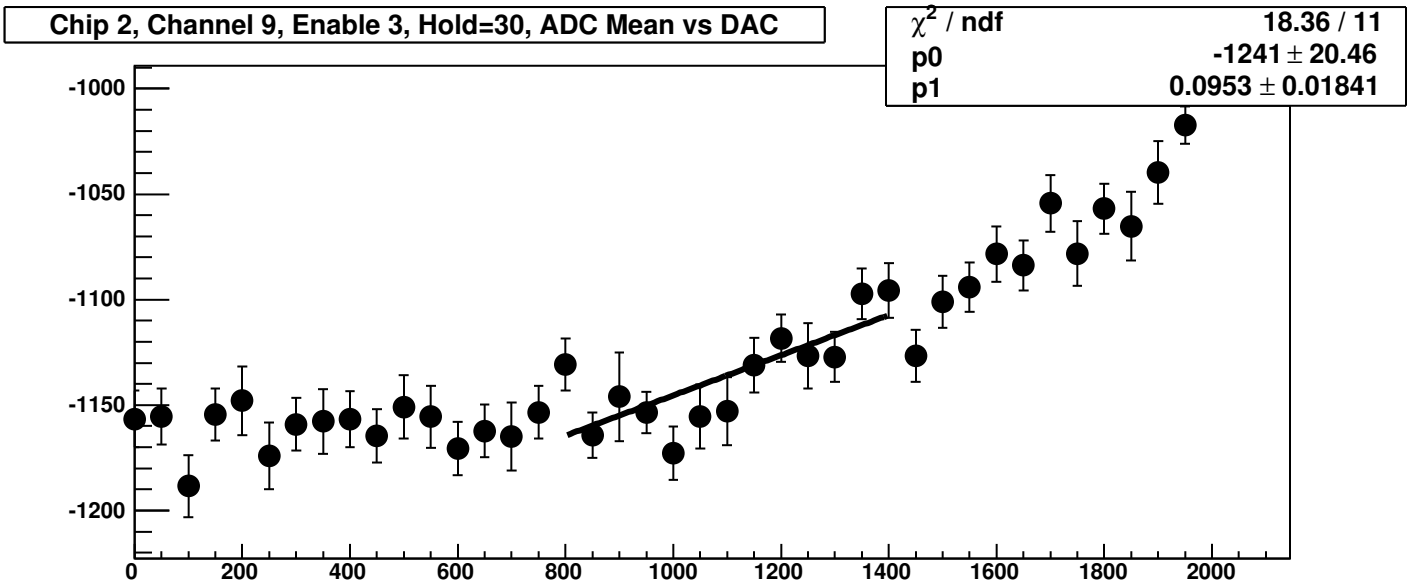


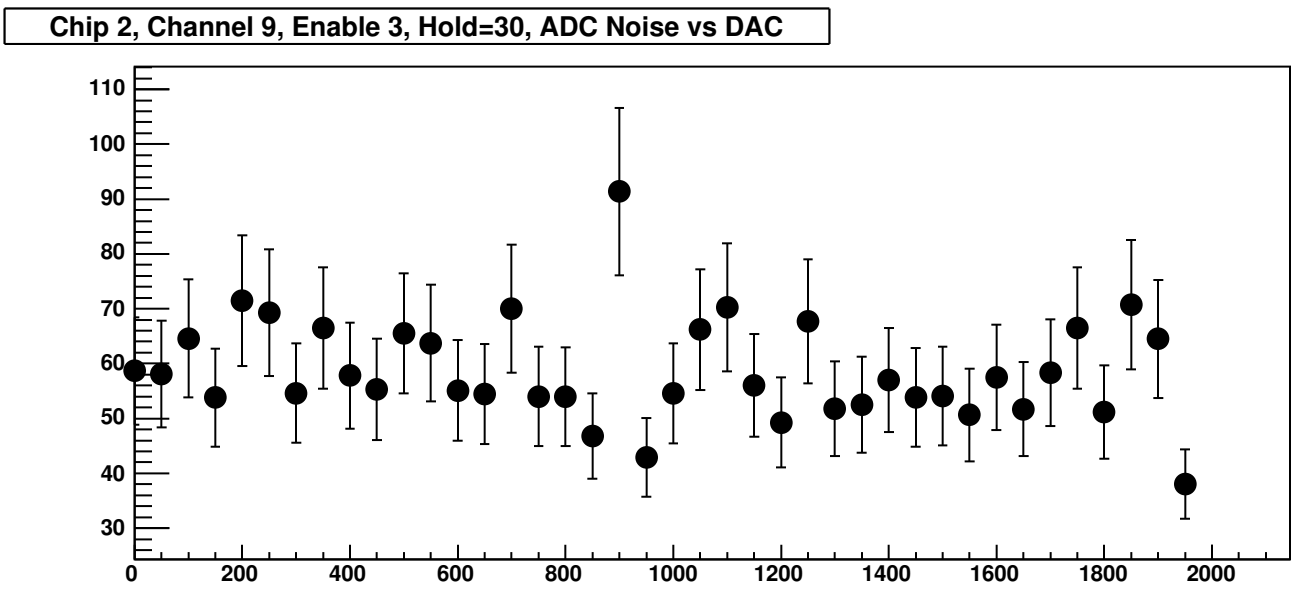
Chip 2, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC

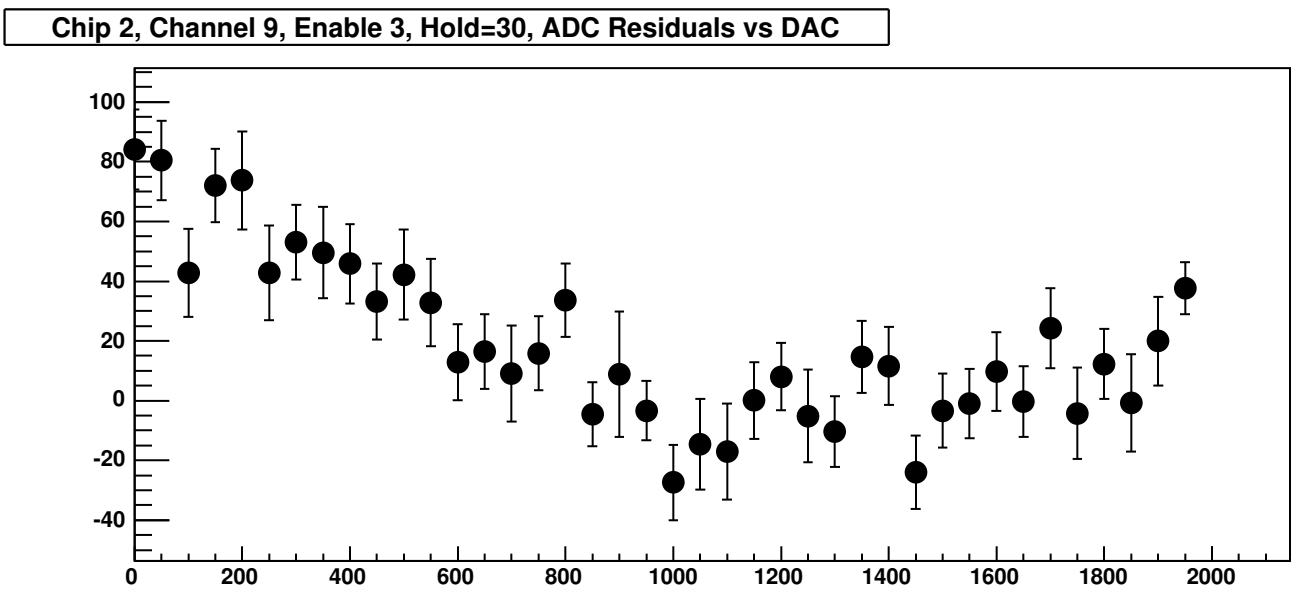


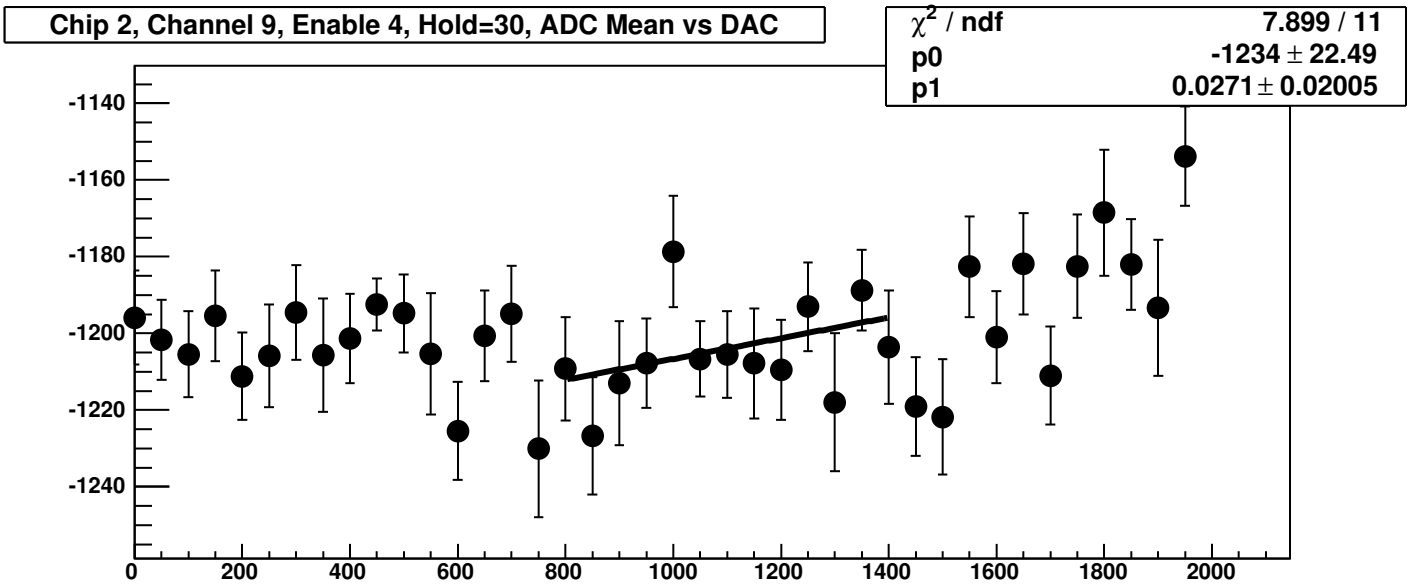
Chip 2, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC

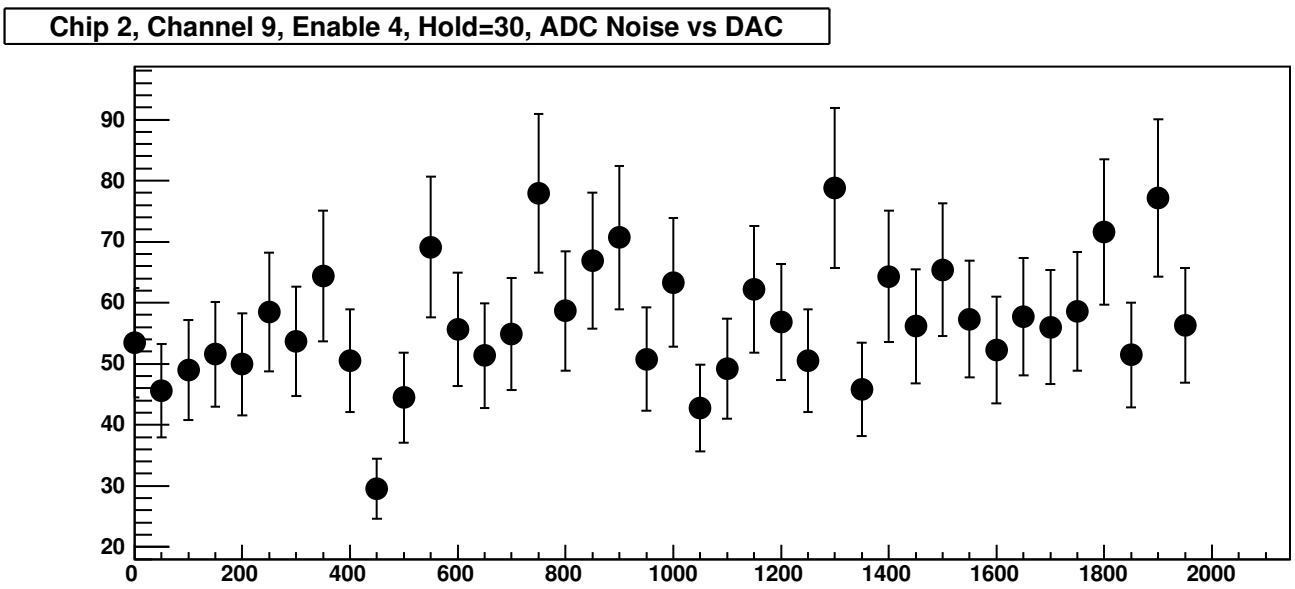


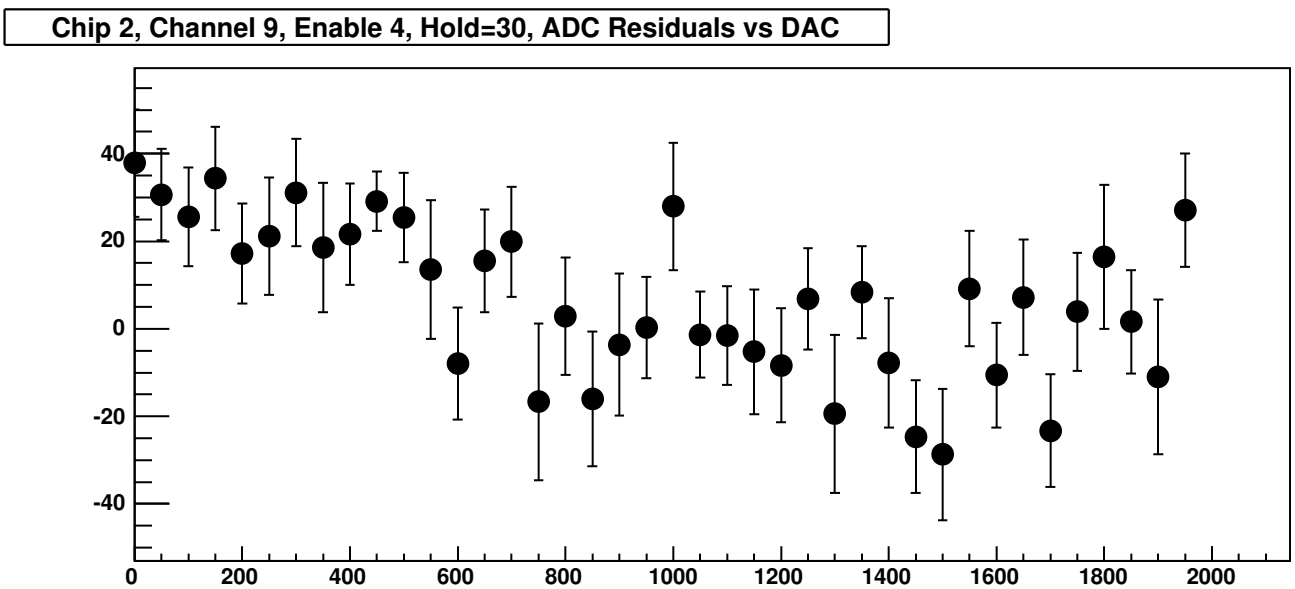

Chip 2, Channel 9, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 18.36 / 11
p0 -1241 ± 20.46
p1 0.0953 ± 0.01841
-1000
-1050
-1100
-1150
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

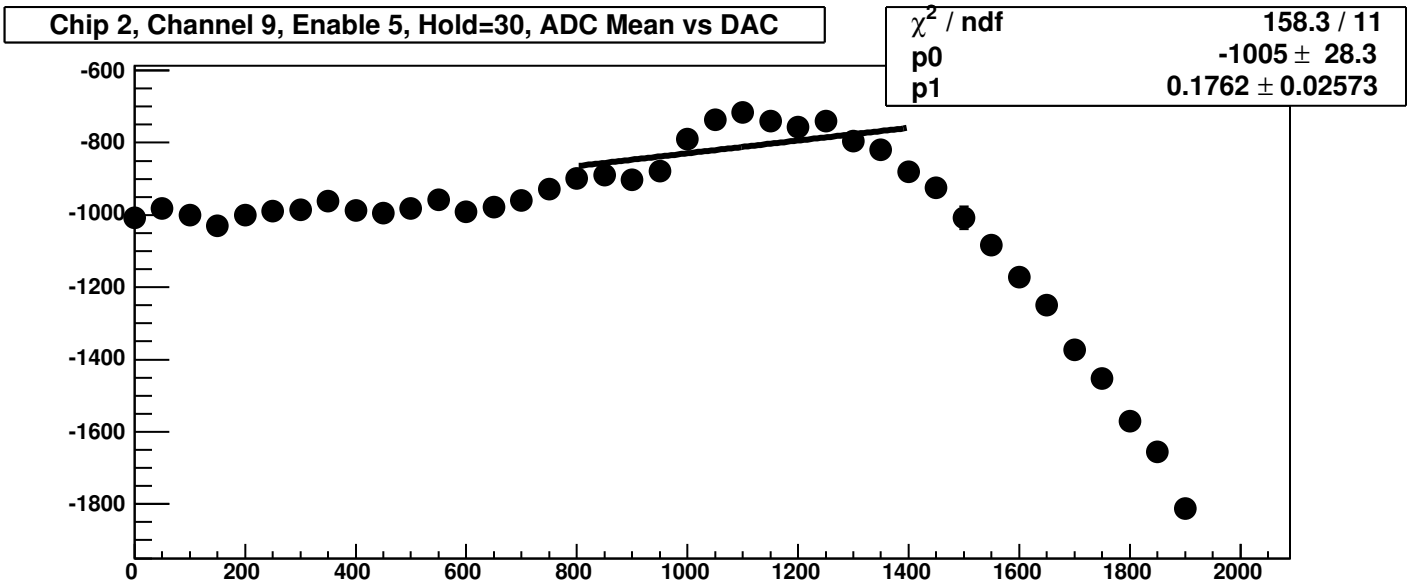

Chip 2, Channel 9, Enable 3, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

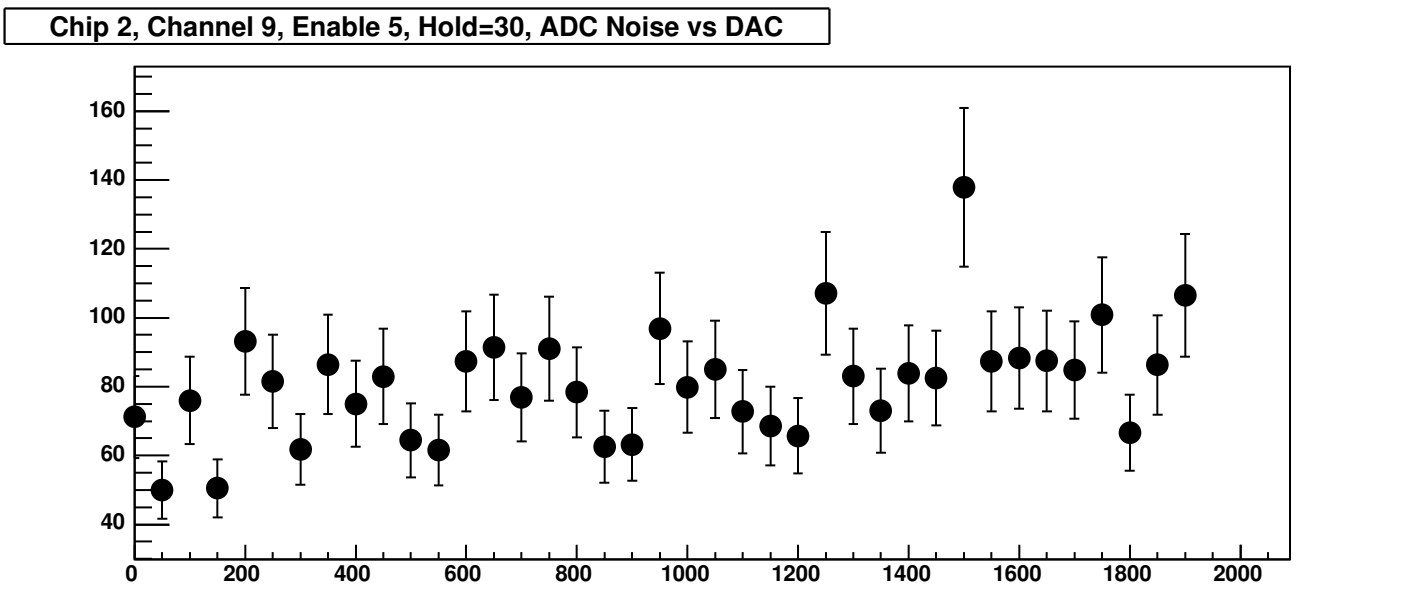

Chip 2, Channel 9, Enable 3, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

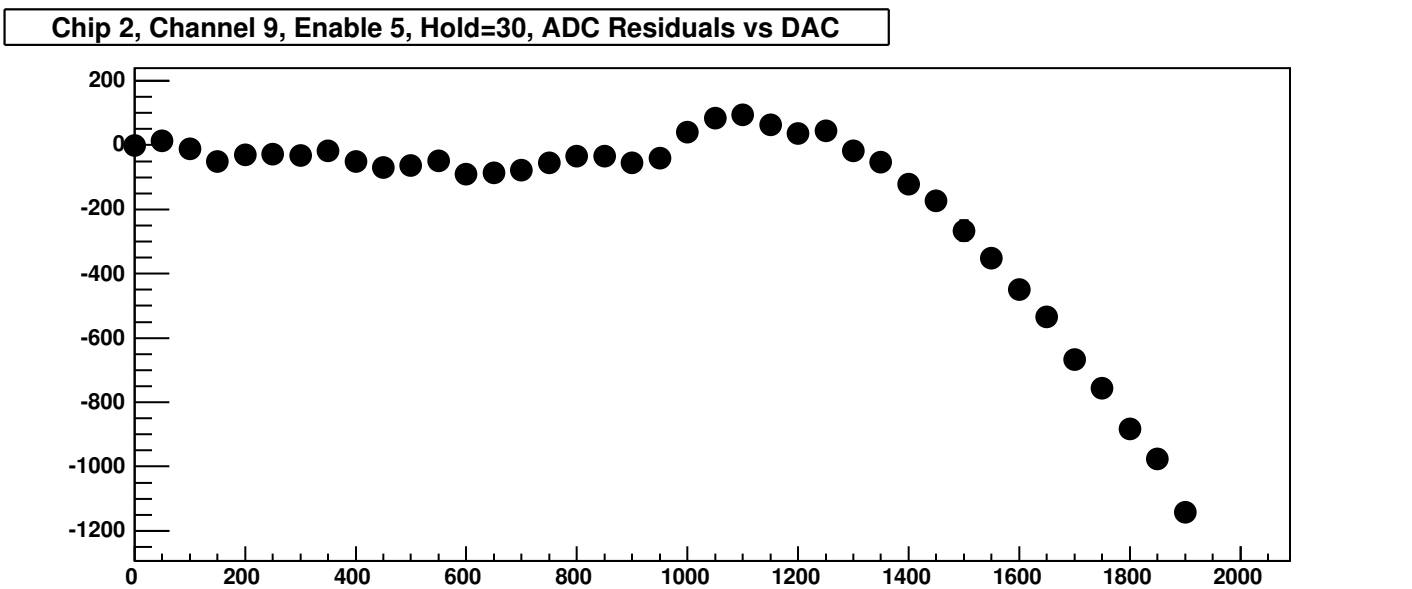

Chip 2, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7.899 / 11
p0 -1234 ± 22.49
p1 0.0271 ± 0.02005
-1140
-1160
-1180
-1200
-1220
-1240
0
200
400
600
800
1000
1200
1400
1600
1800
2000

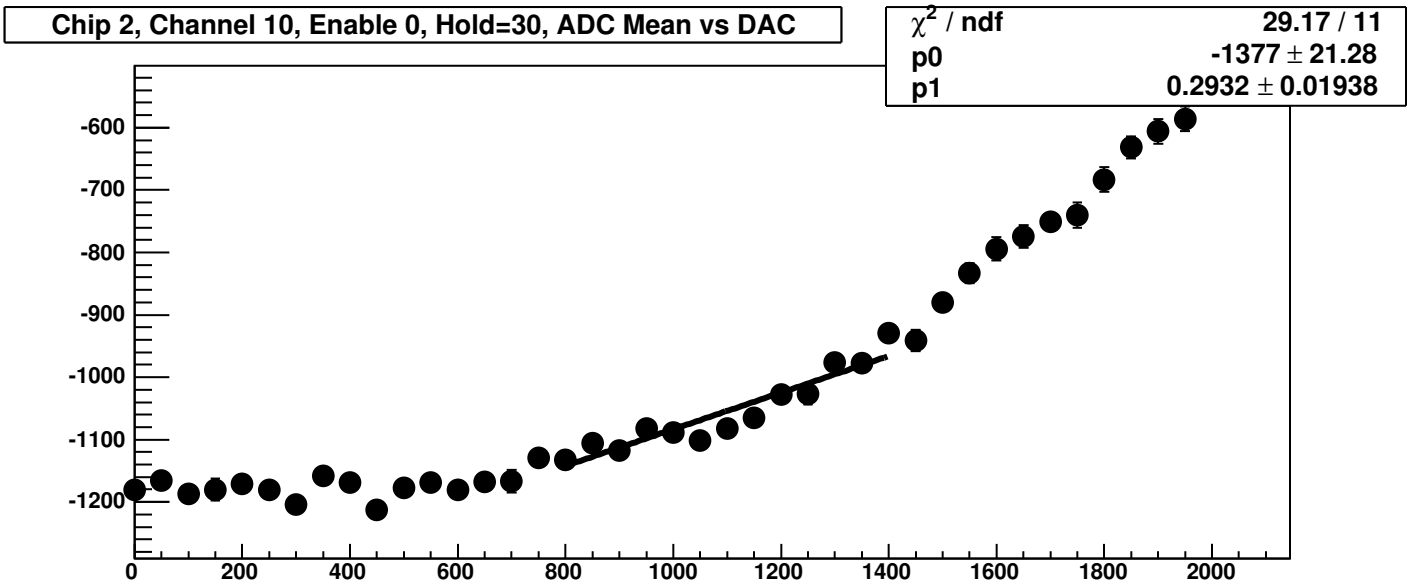

Chip 2, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

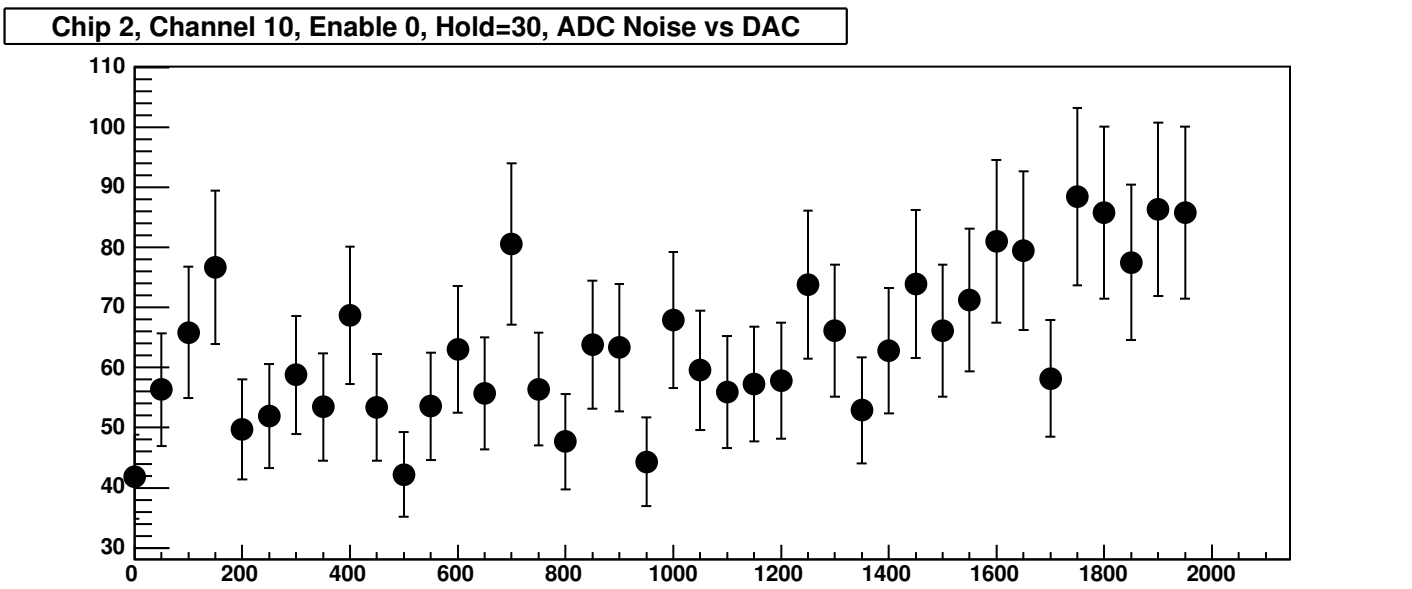

Chip 2, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

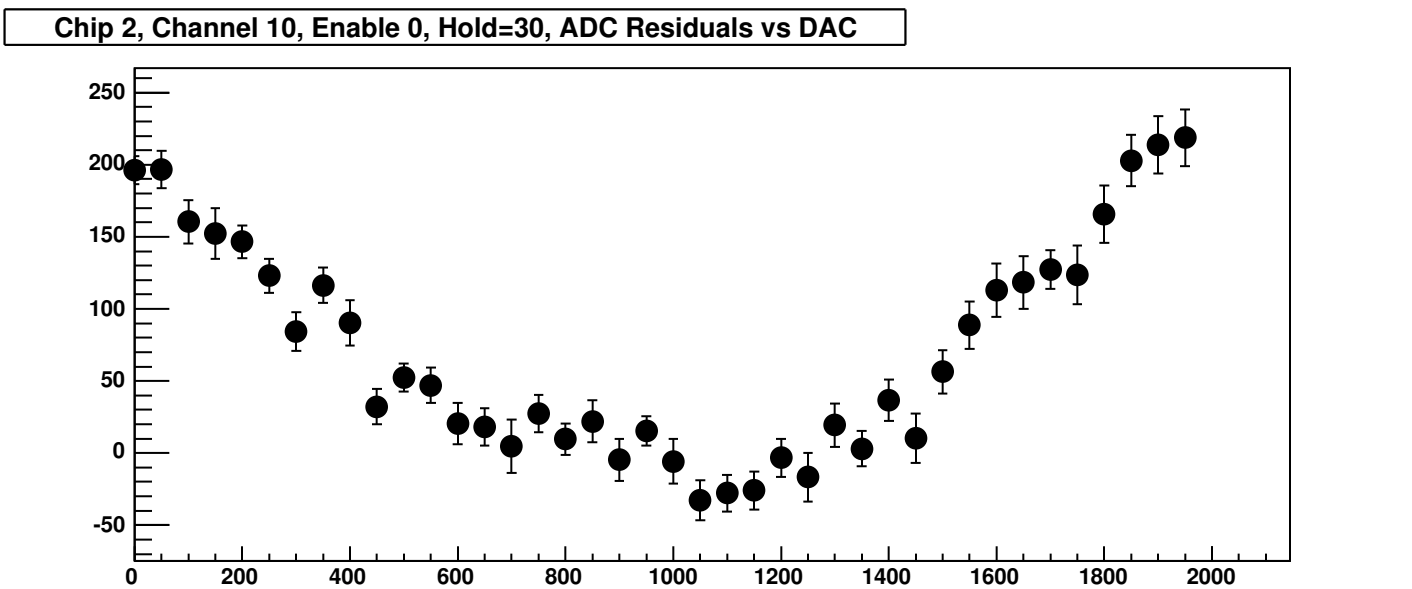

Chip 2, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 158.3 / 11
p0 -1005 ± 28.3
p1 0.1762 ± 0.02573
-600
-800
-1000
-1200
-1400
-1600
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

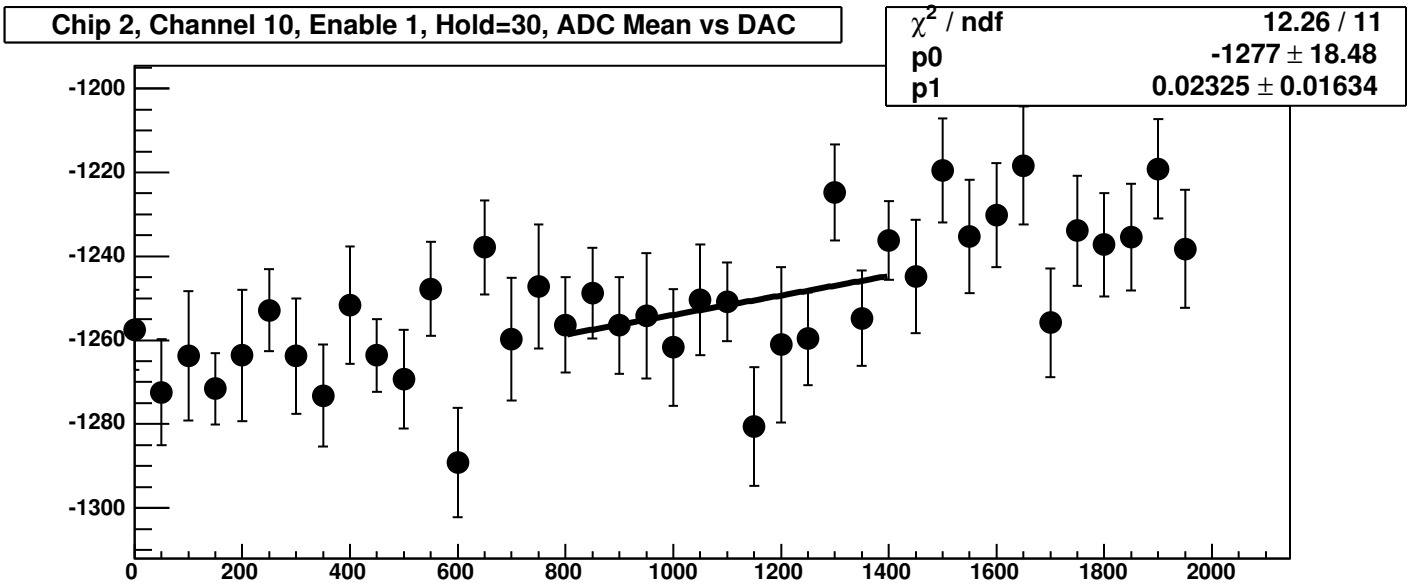

Chip 2, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

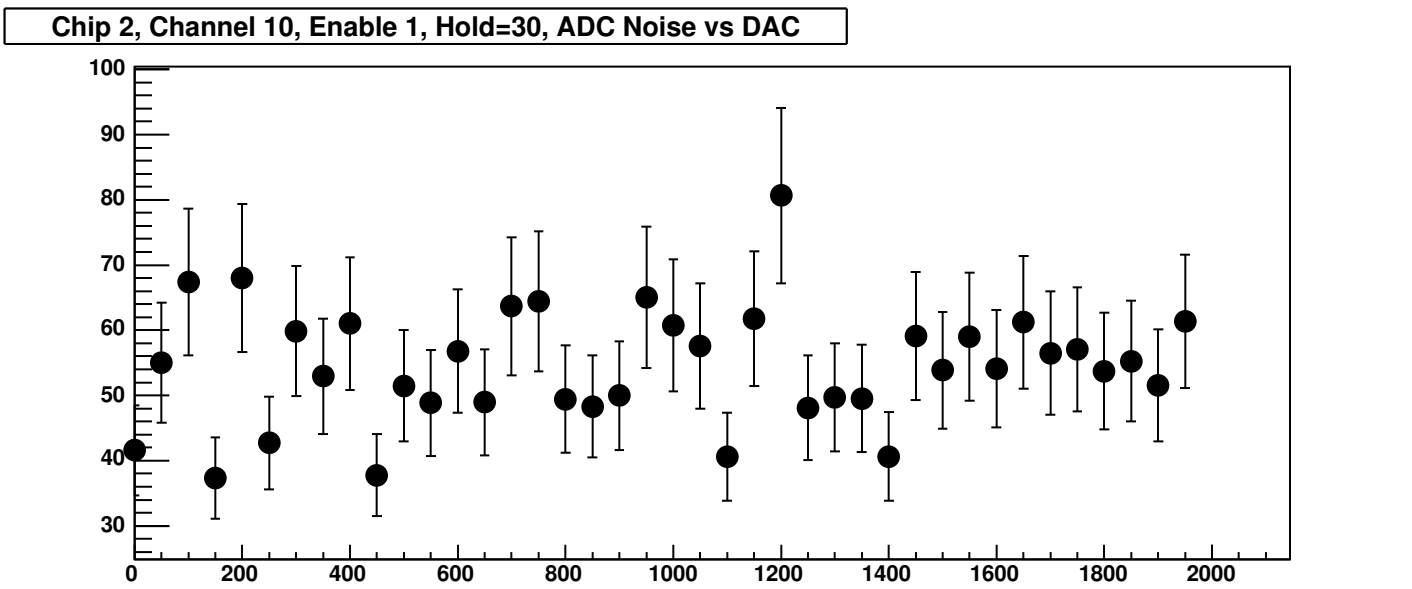

Chip 2, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

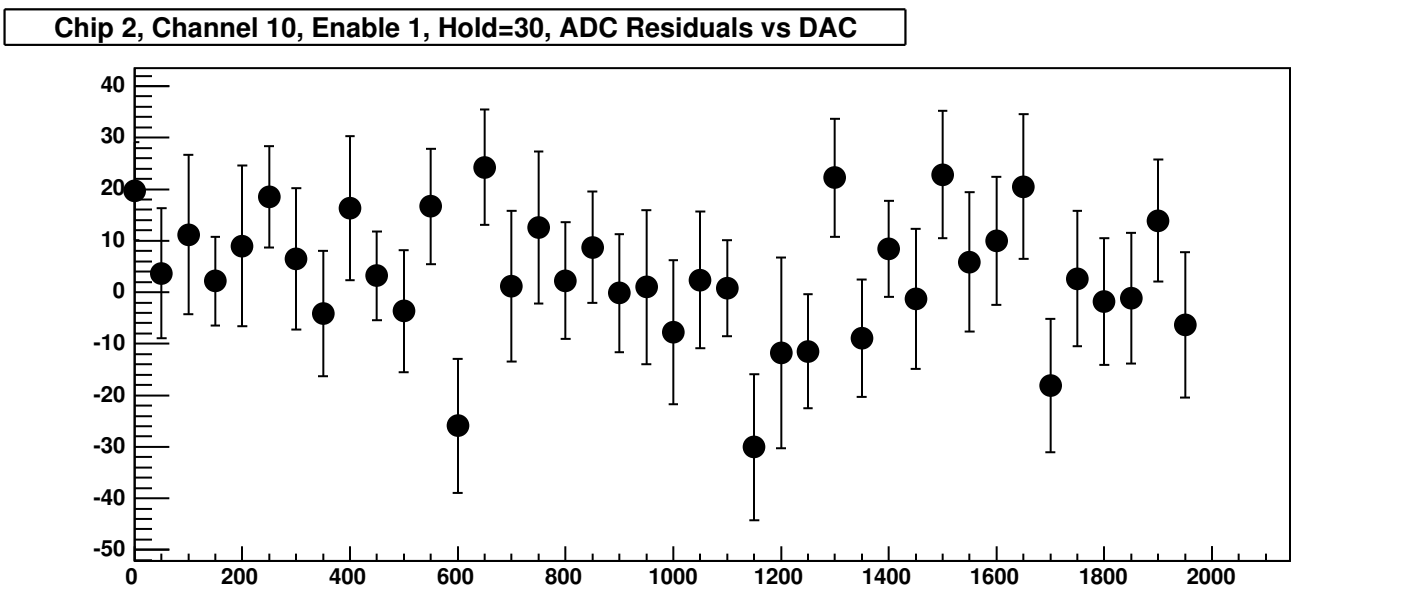

Chip 2, Channel 10, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 29.17 / 11
p0 -1377 ± 21.28
p1 0.2932 ± 0.01938
-600
-700
-800
-900
-1000
-1100
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

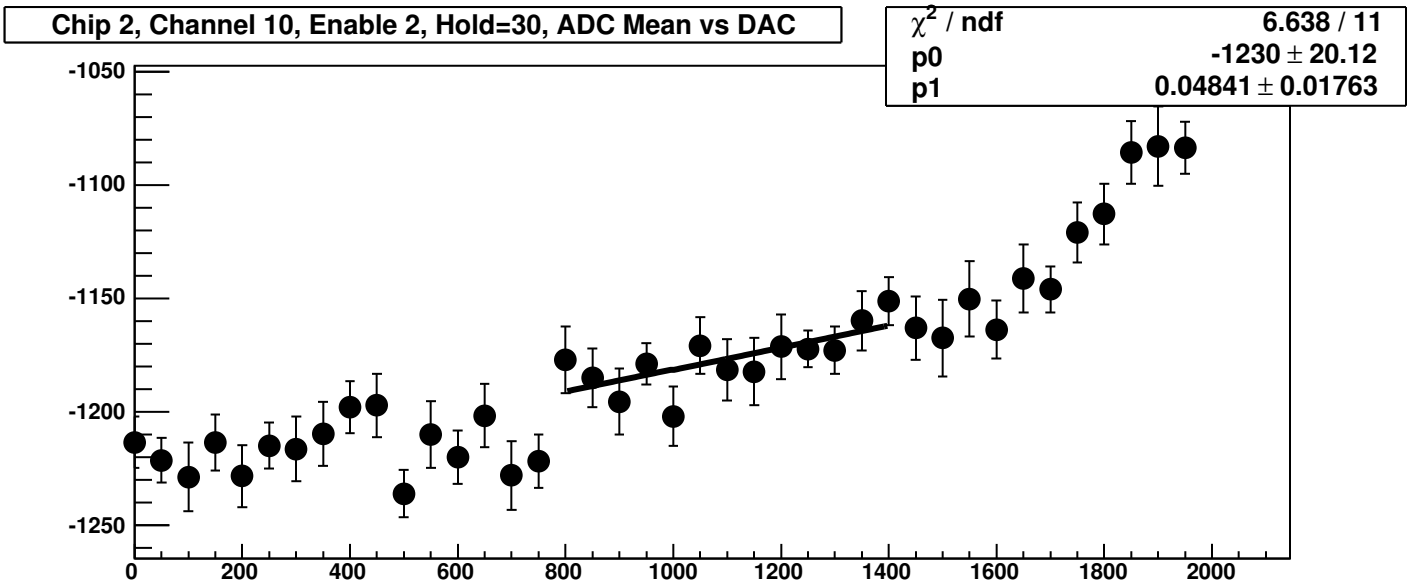

Chip 2, Channel 10, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

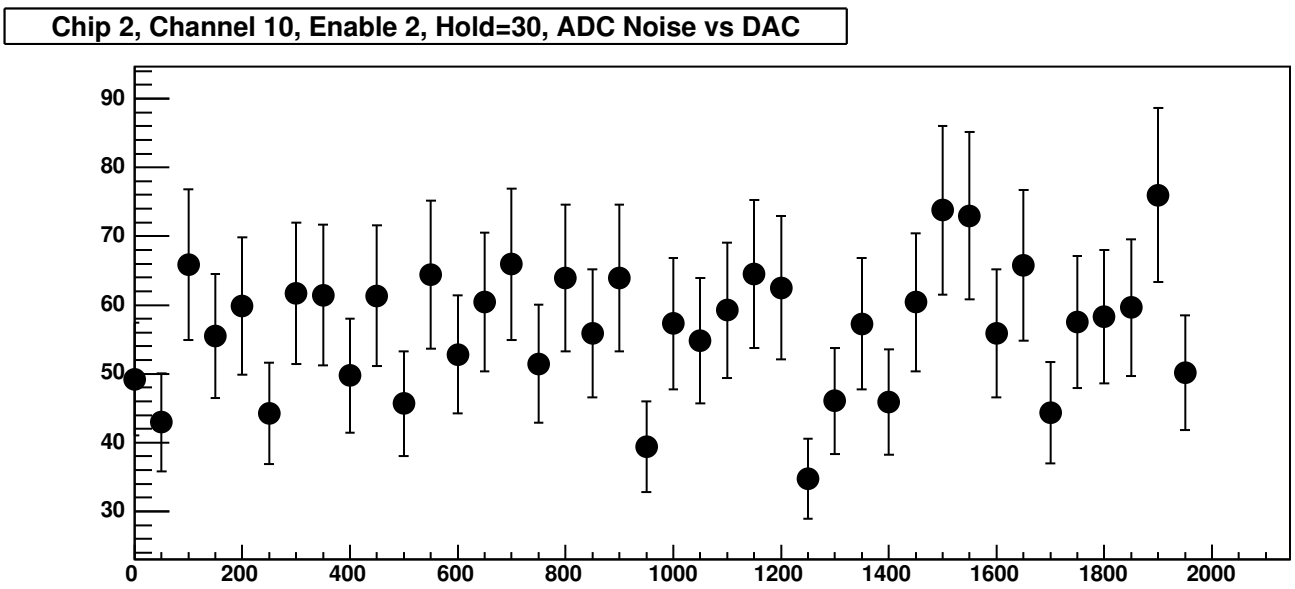

Chip 2, Channel 10, Enable 0, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

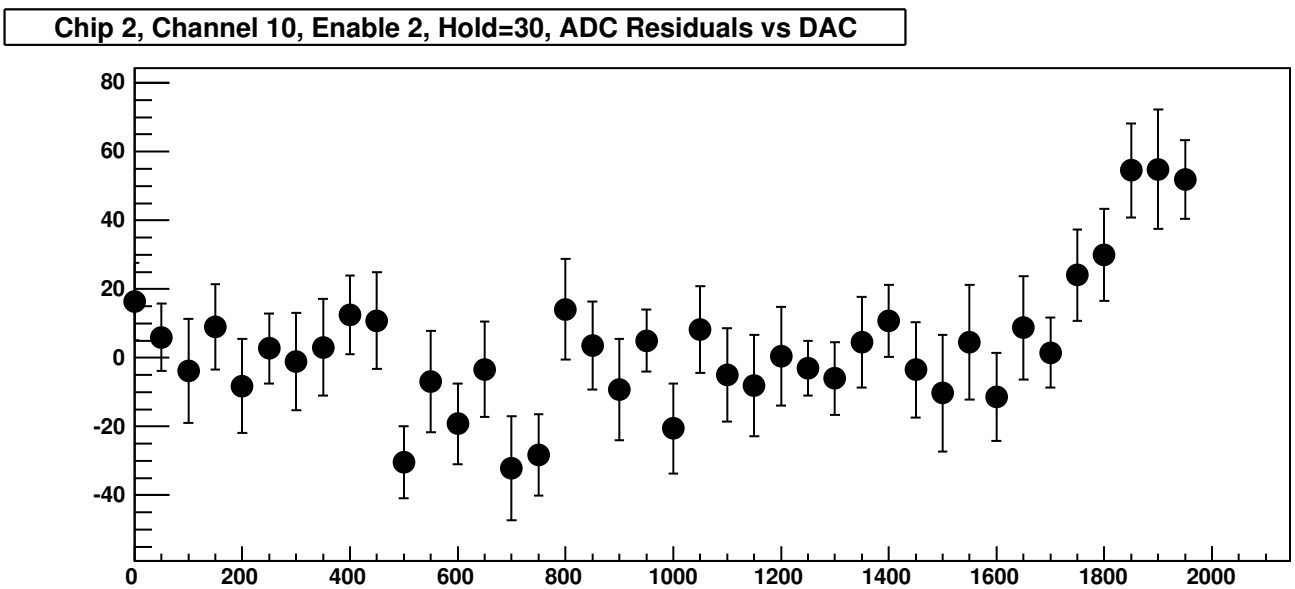

Chip 2, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 12.26 / 11
p0 -1277 ± 18.48
p1 0.02325 ± 0.01634
-1200
-1220
-1240
-1260
-1280
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

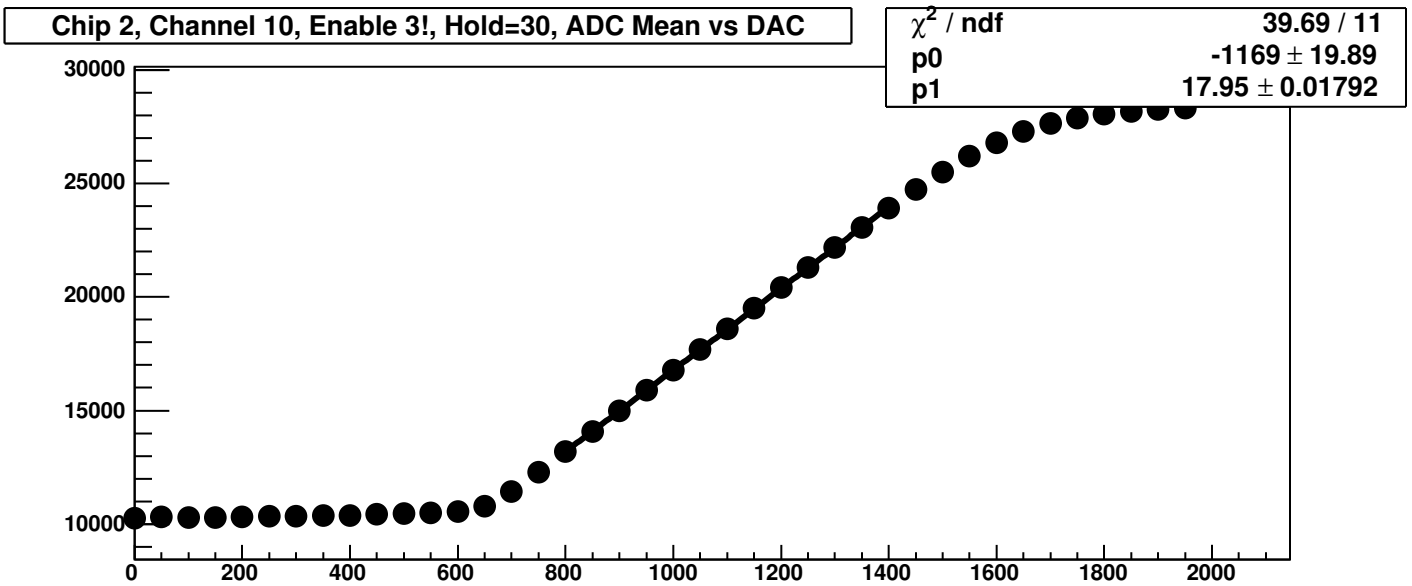

Chip 2, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

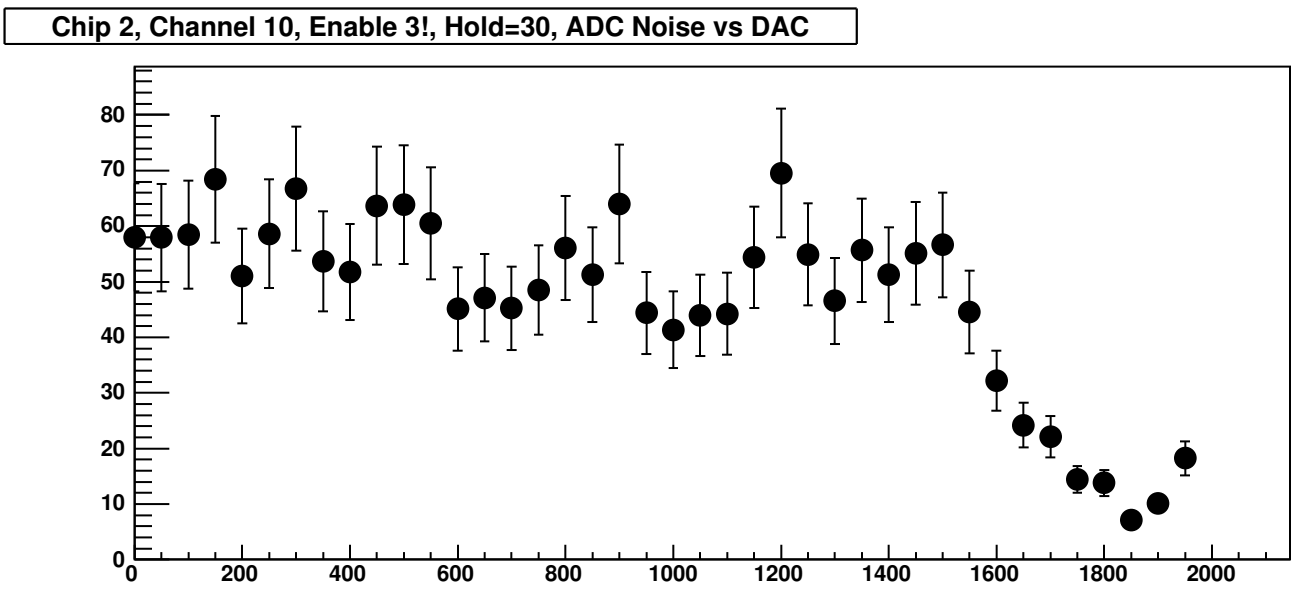

Chip 2, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
40
30
20
10
0
-10
-20
-30
-40
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

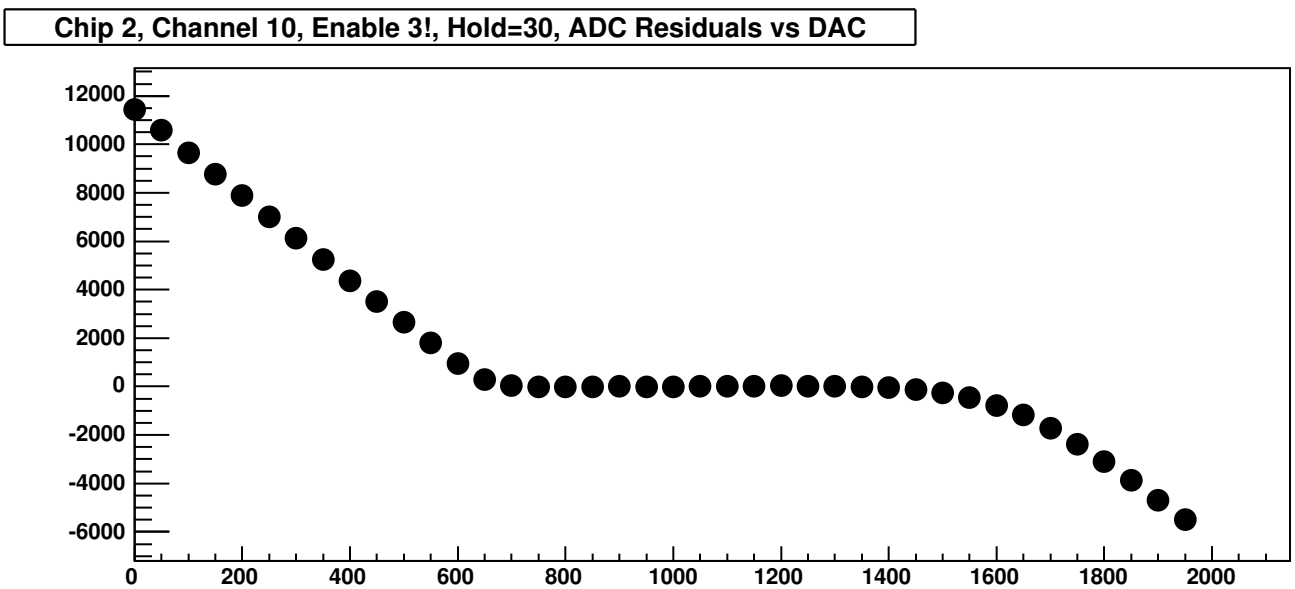

Chip 2, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 6.638 / 11
p0 -1230 ± 20.12
p1 0.04841 ± 0.01763
-1050
-1100
-1150
-1200
-1250
0
200
400
600
800
1000
1200
1400
1600
1800
2000

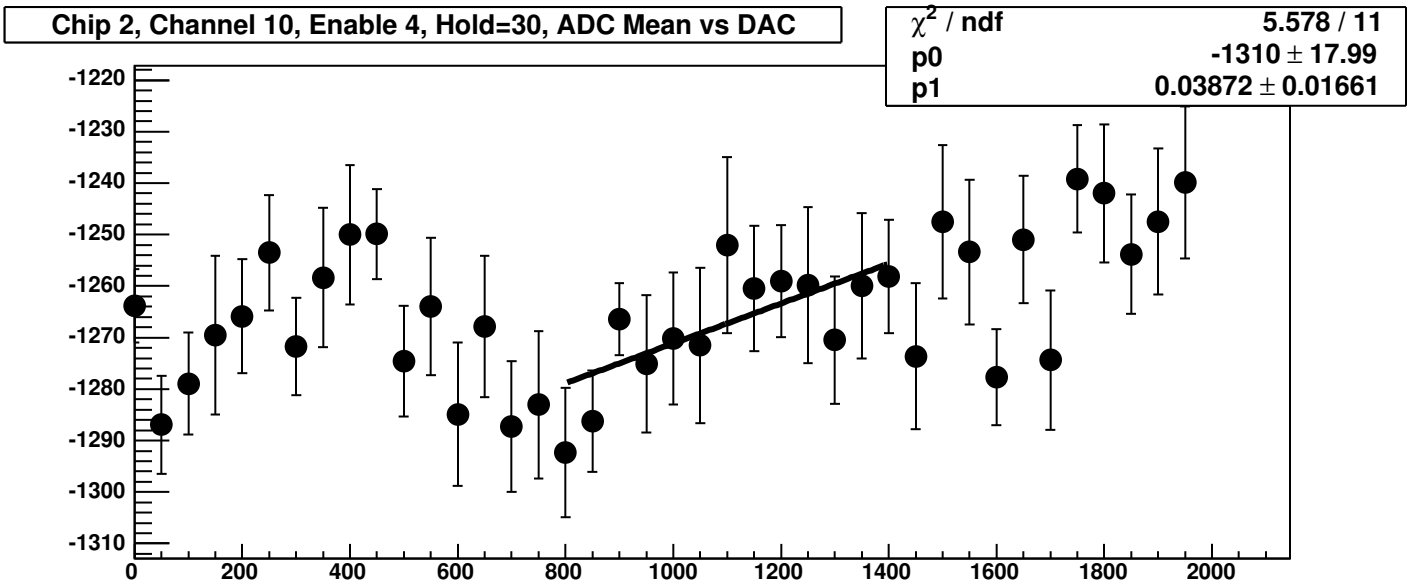

Chip 2, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

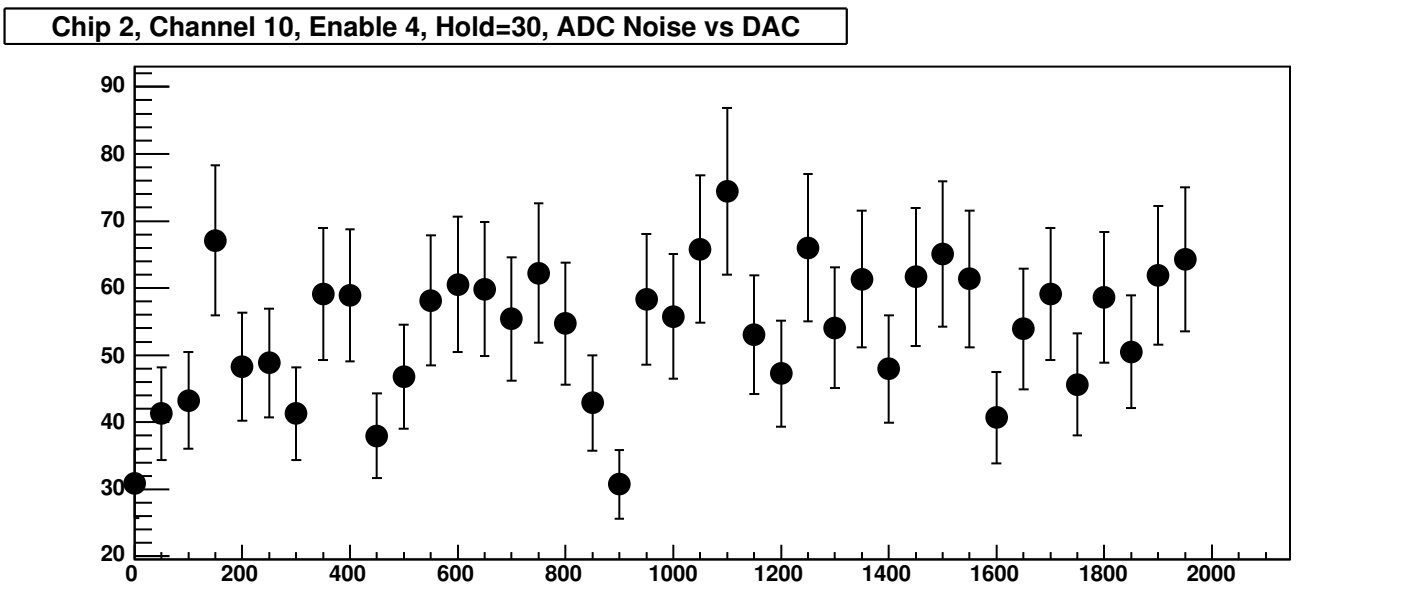

Chip 2, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

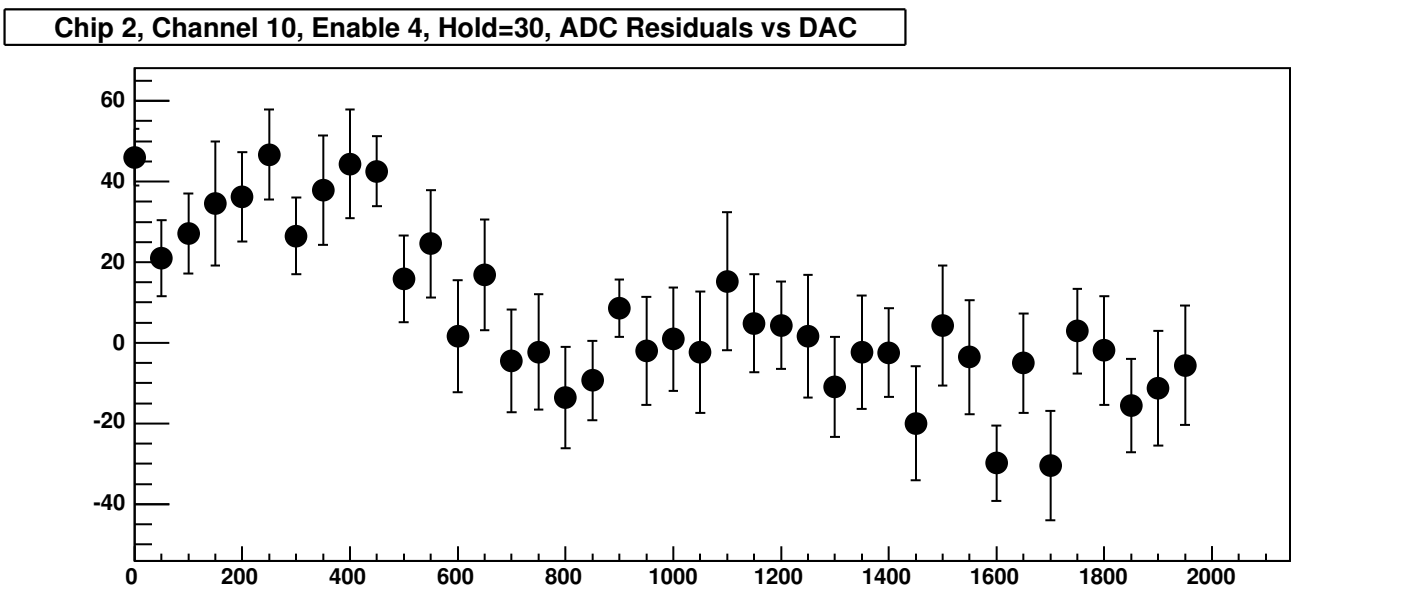

Chip 2, Channel 10, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 39.69 / 11
p0 -1169 ± 19.89
p1 17.95 ± 0.01792
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

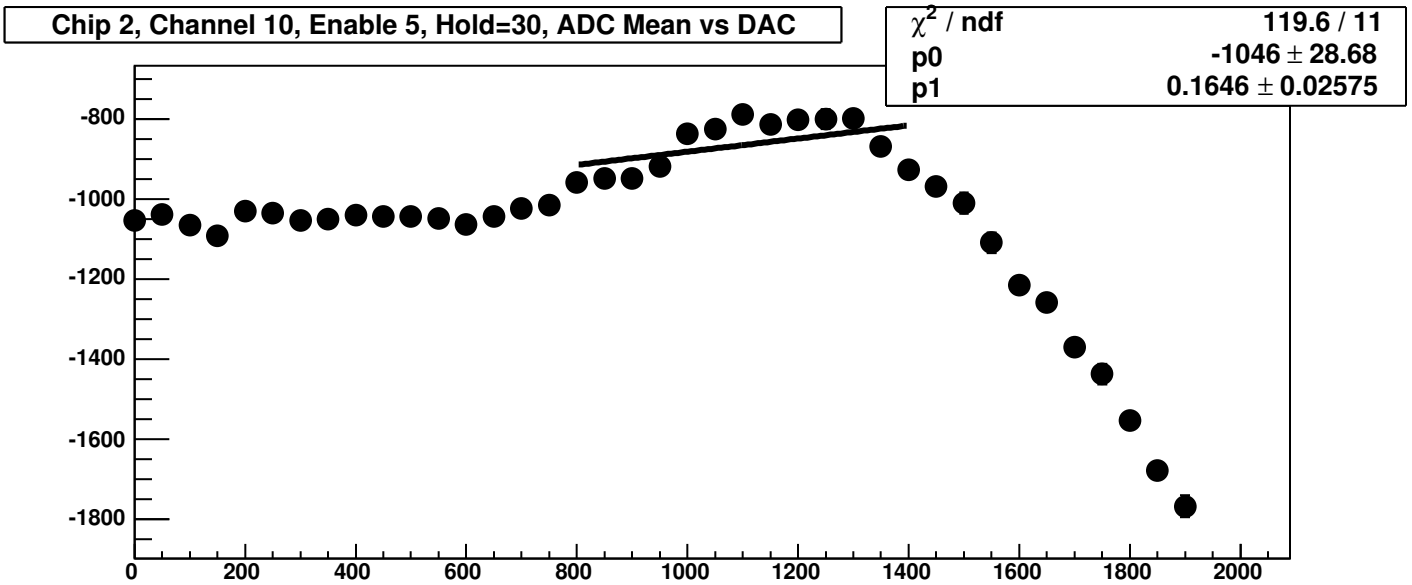

Chip 2, Channel 10, Enable 3!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

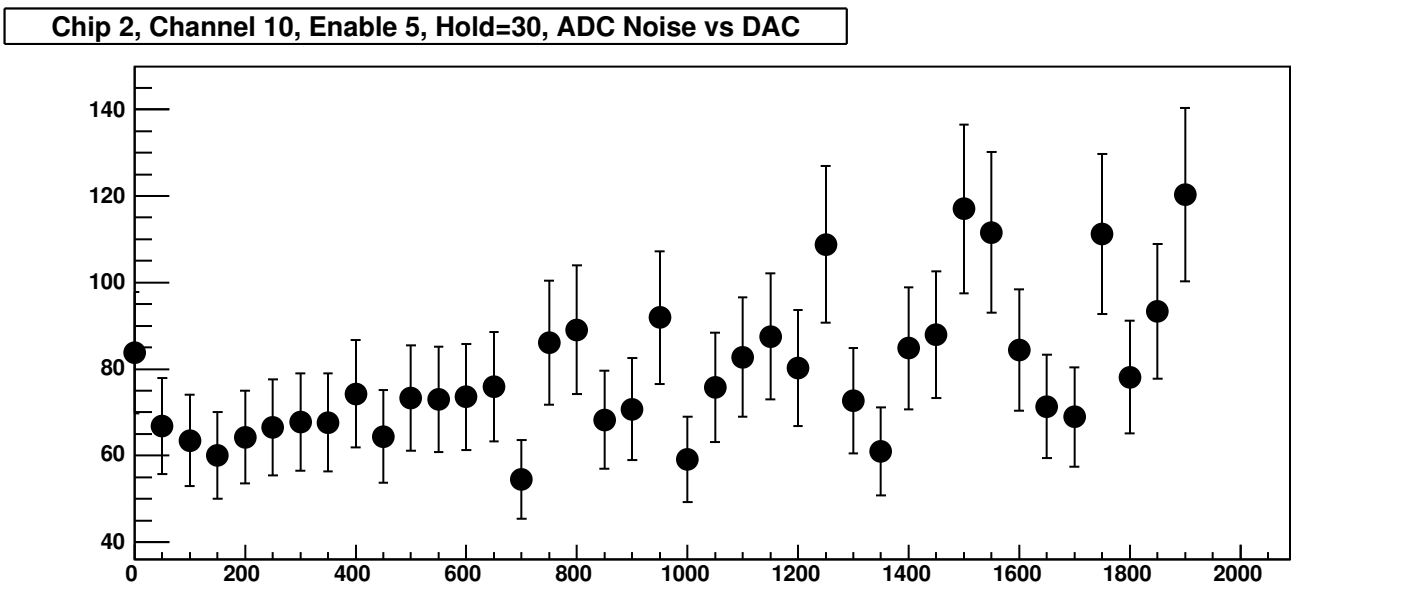

Chip 2, Channel 10, Enable 3!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

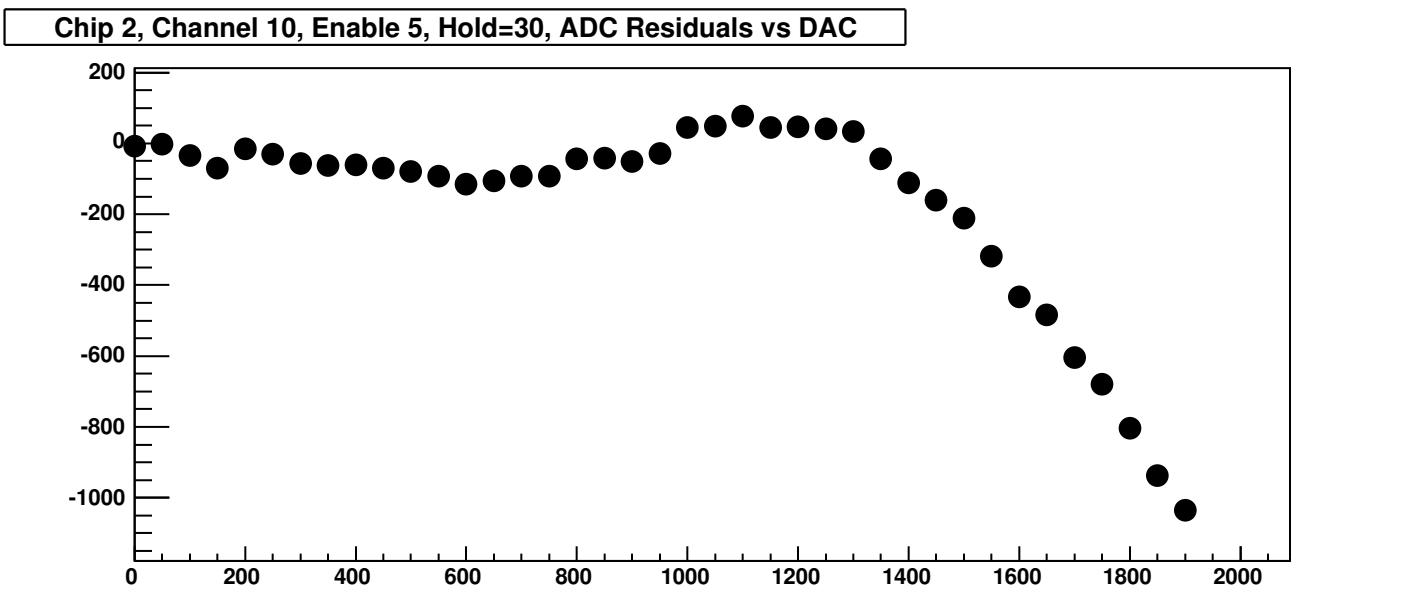

Chip 2, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5.578 / 11
p0 -1310 ± 17.99
p1 0.03872 ± 0.01661
-1220
-1230
-1240
-1250
-1260
-1270
-1280
-1290
-1300
-1310
0
200
400
600
800
1000
1200
1400
1600
1800
2000

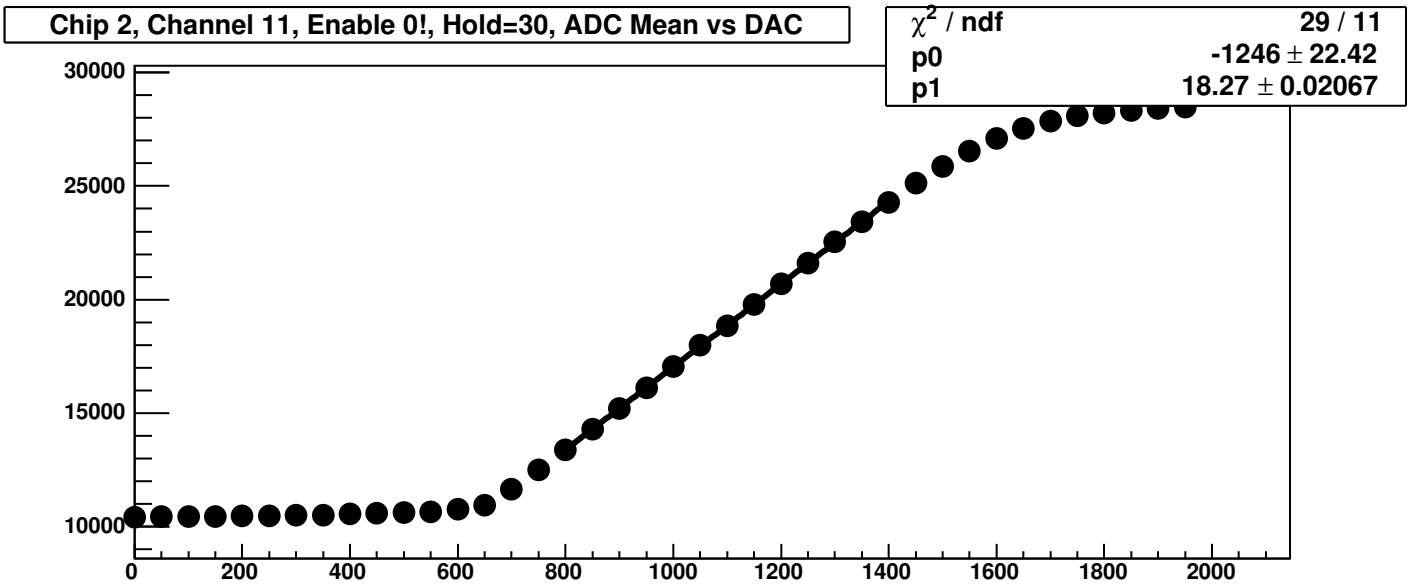

Chip 2, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

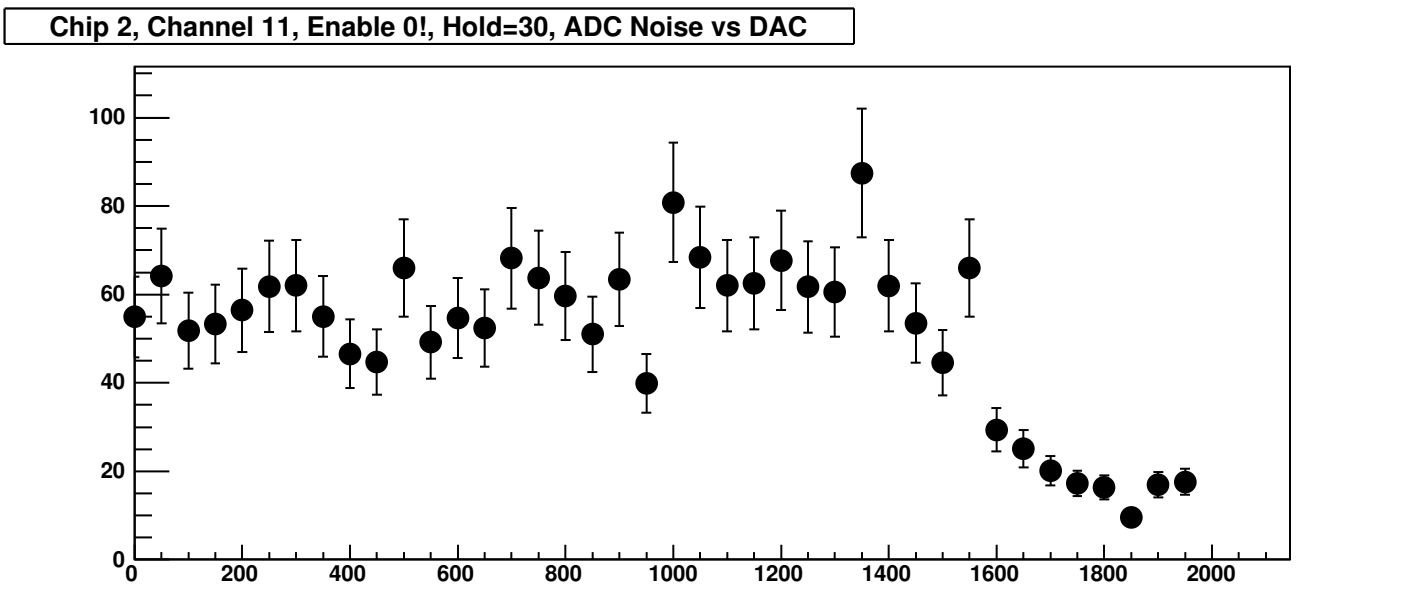

Chip 2, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

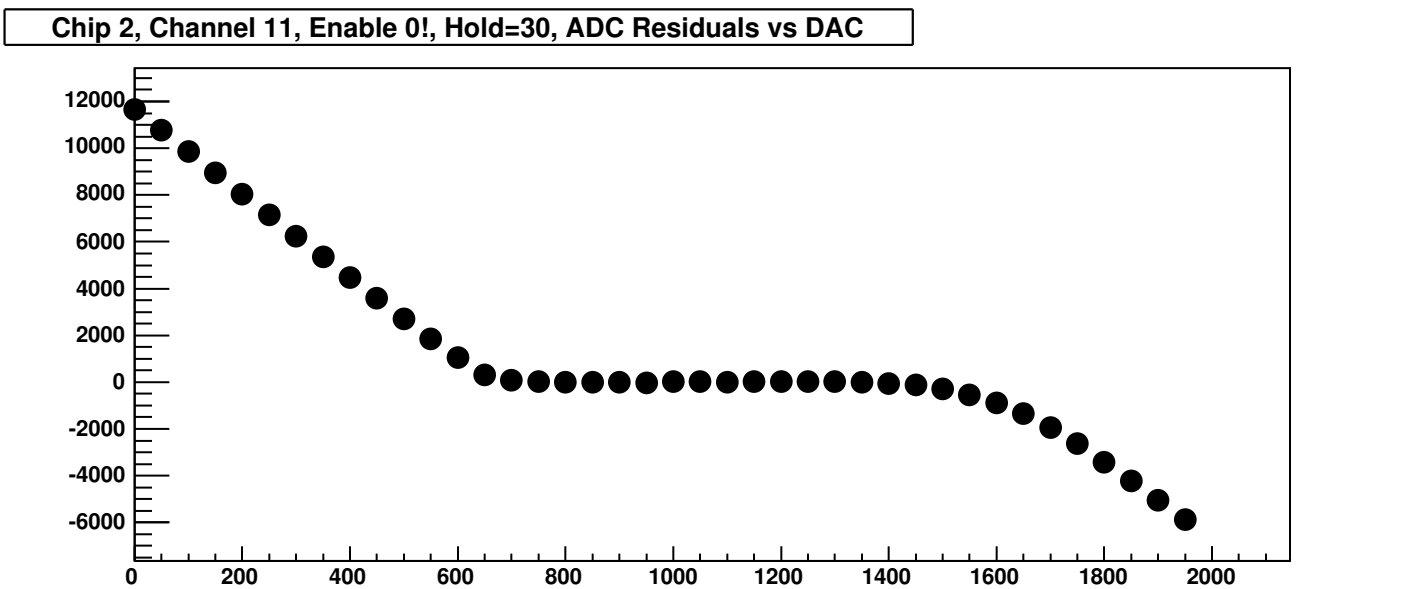

Chip 2, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 119.6 / 11
p0 -1046 ± 28.68
p1 0.1646 ± 0.02575
-800
-1000
-1200
-1400
-1600
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

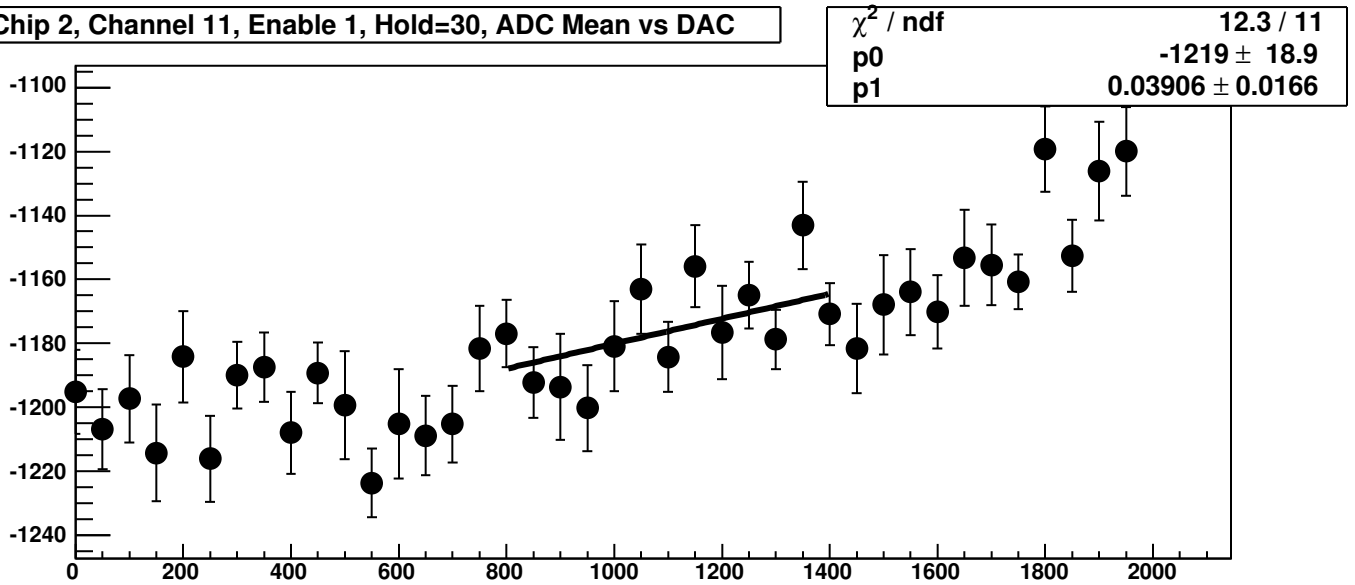

Chip 2, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 11, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 29 / 11
p0 -1246 ± 22.42
p1 18.27 ± 0.02067
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

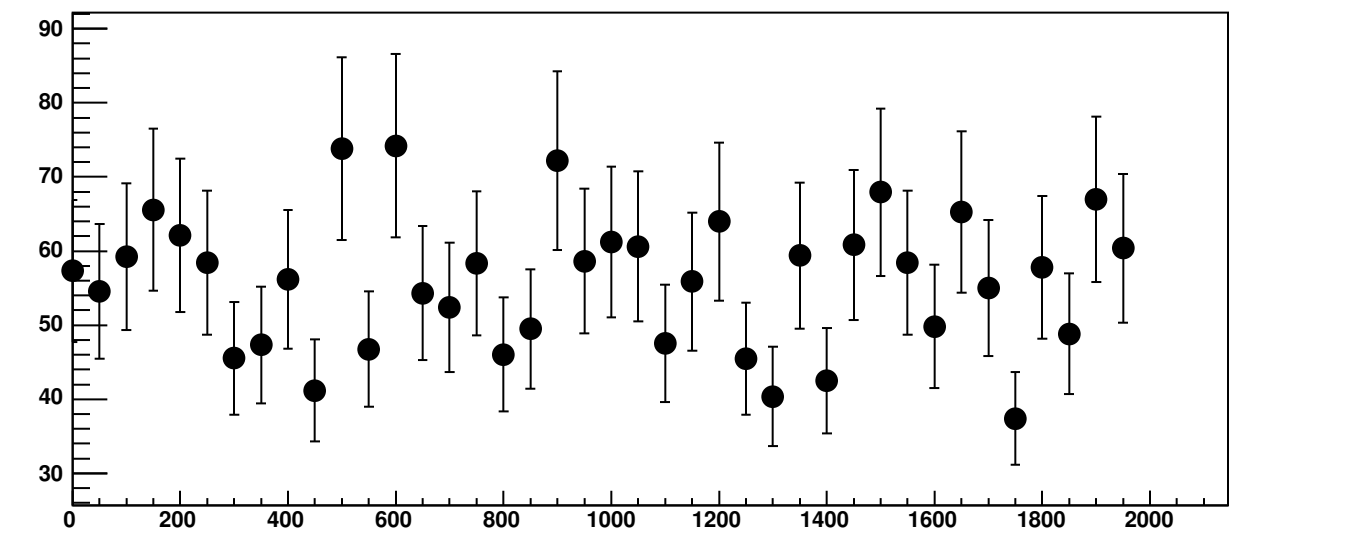

Chip 2, Channel 11, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 11, Enable 0!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

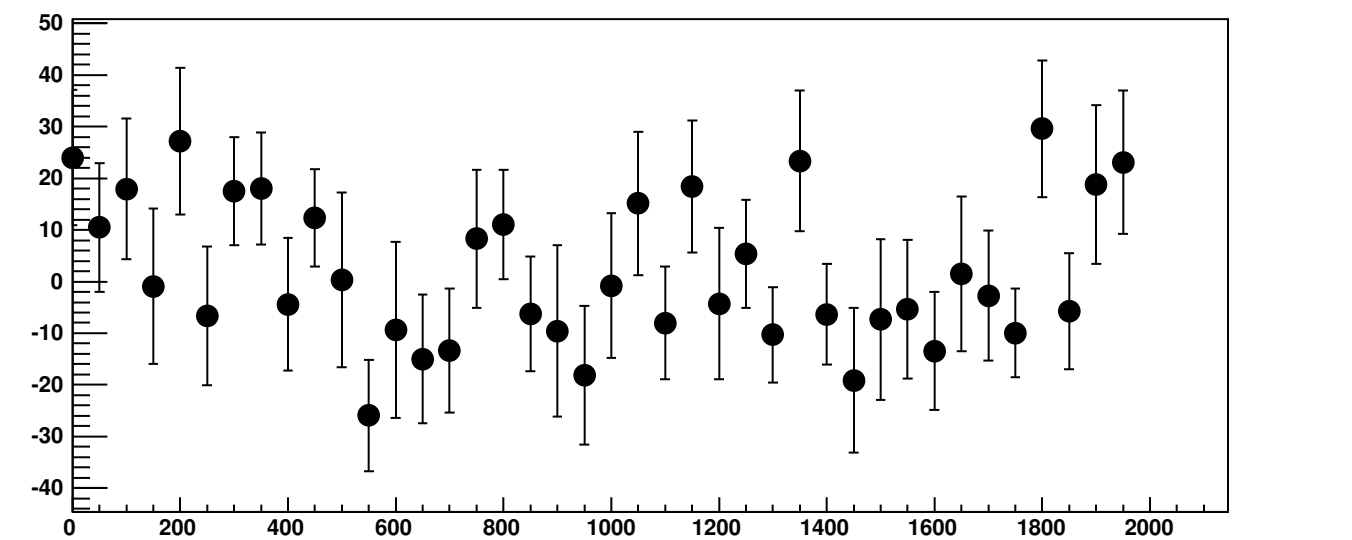
Chip 2, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC

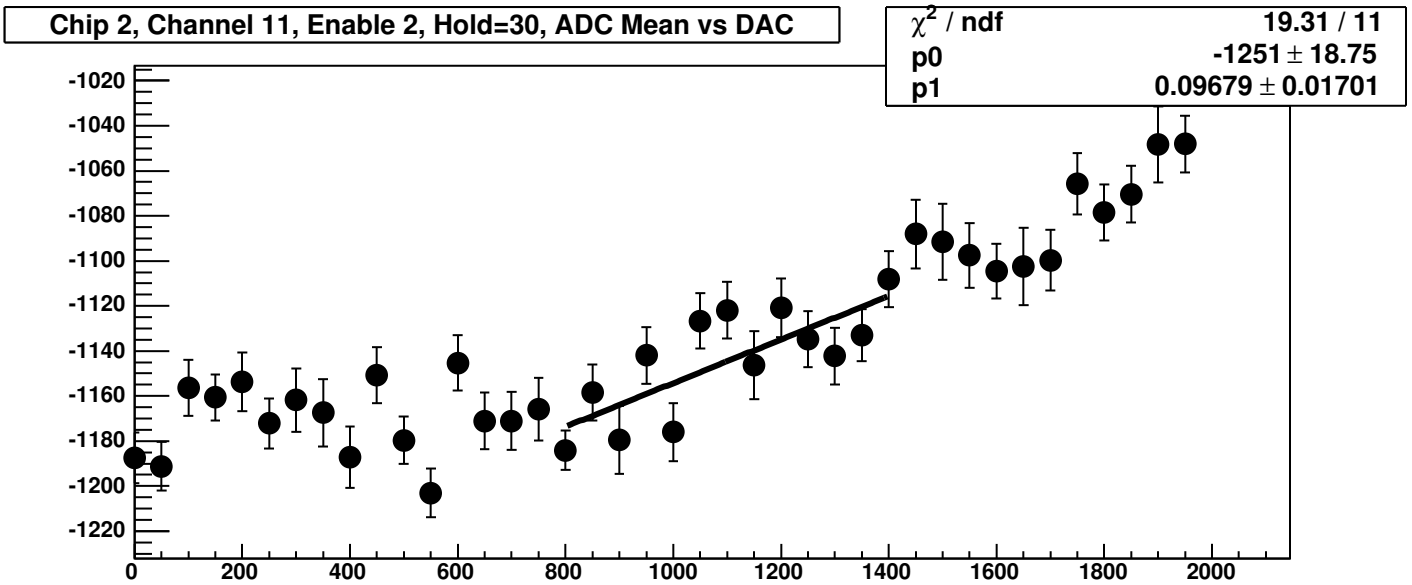


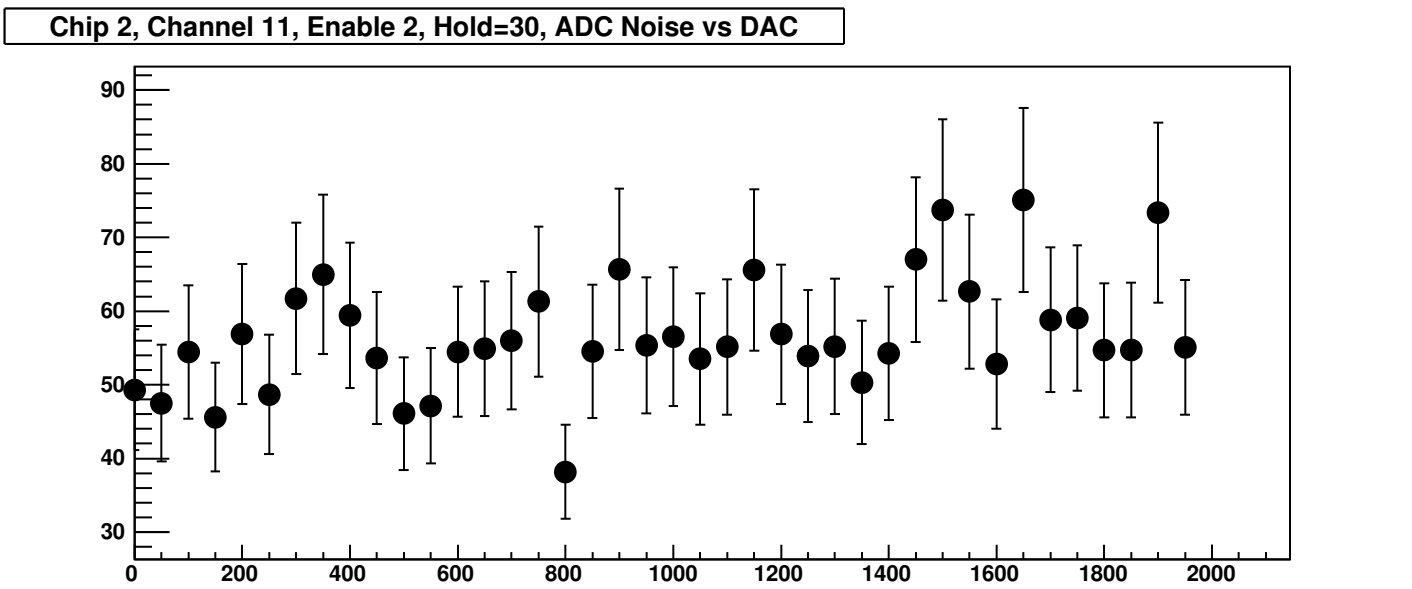
Chip 2, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC

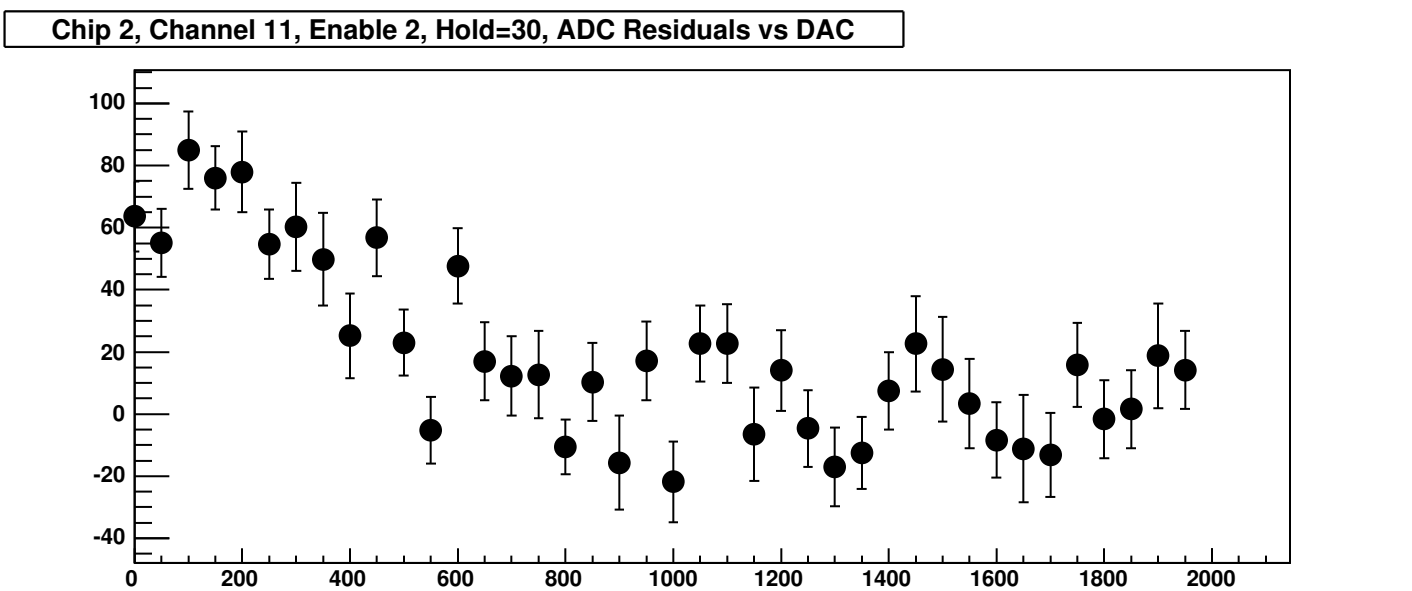


Chip 2, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC

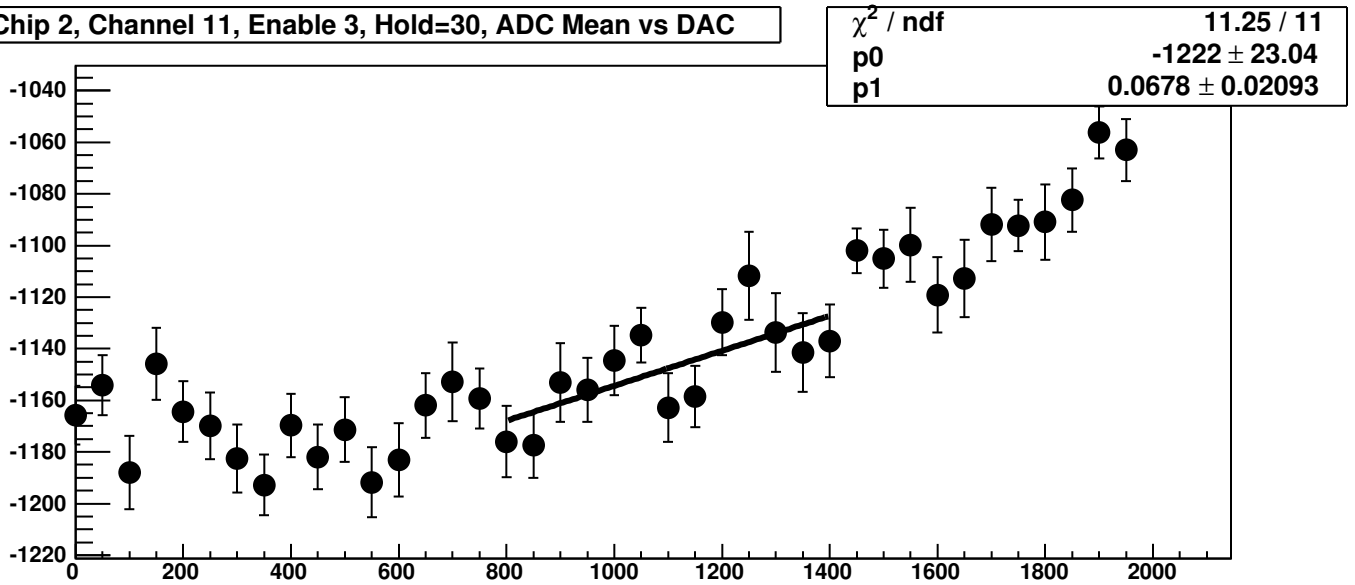



Chip 2, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 19.31 / 11
p0 -1251 ± 18.75
p1 0.09679 ± 0.01701
-1020
-1040
-1060
-1080
-1100
-1120
-1140
-1160
-1180
-1200
-1220
0
200
400
600
800
1000
1200
1400
1600
1800
2000

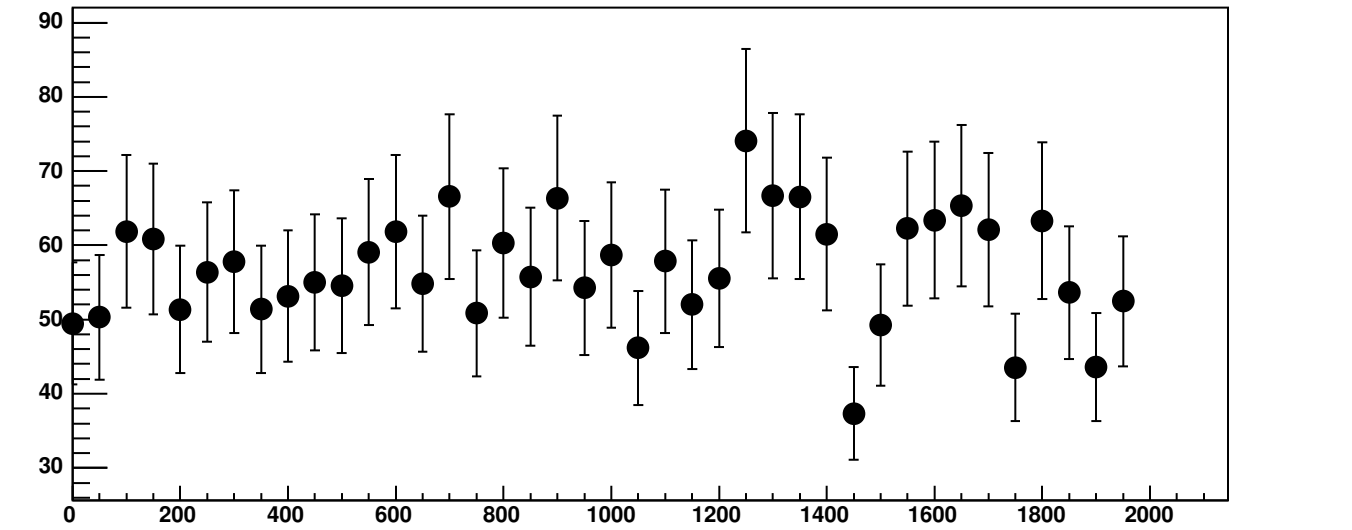

Chip 2, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

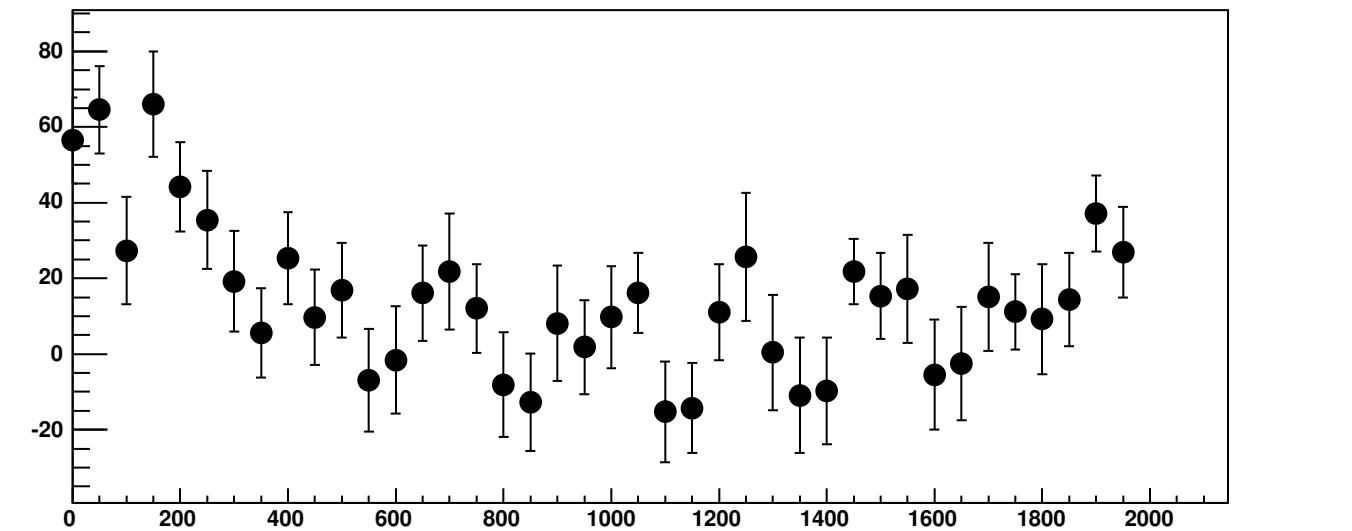
Chip 2, Channel 11, Enable 3, Hold=30, ADC Mean vs DAC

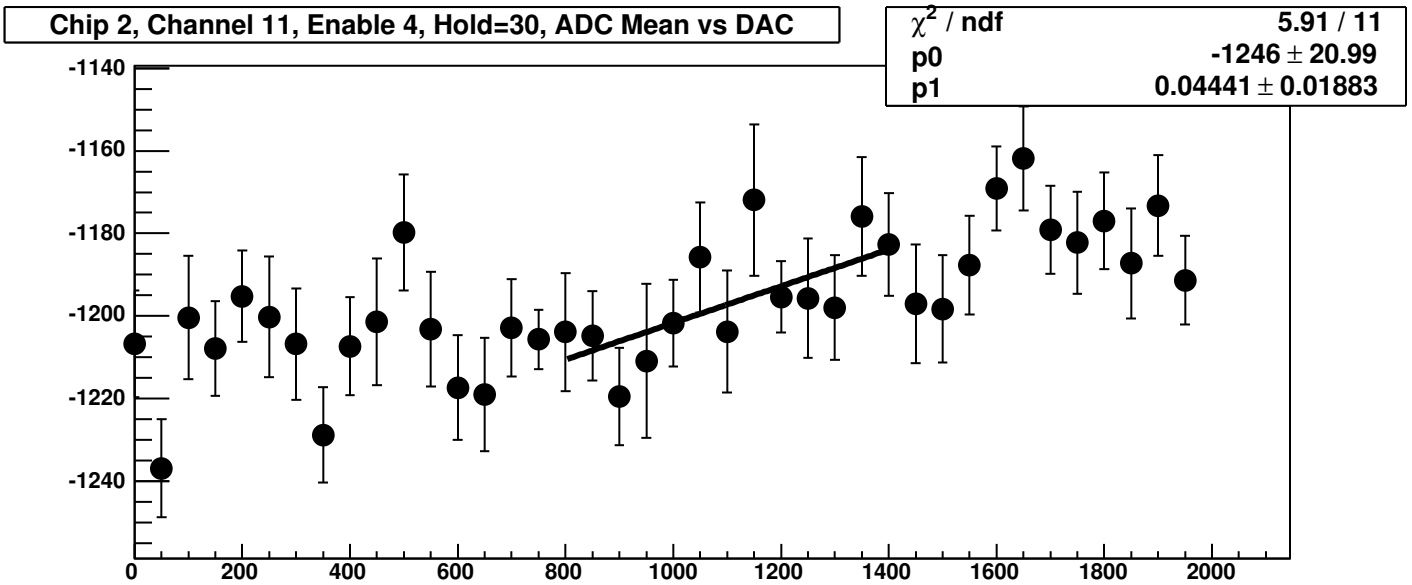


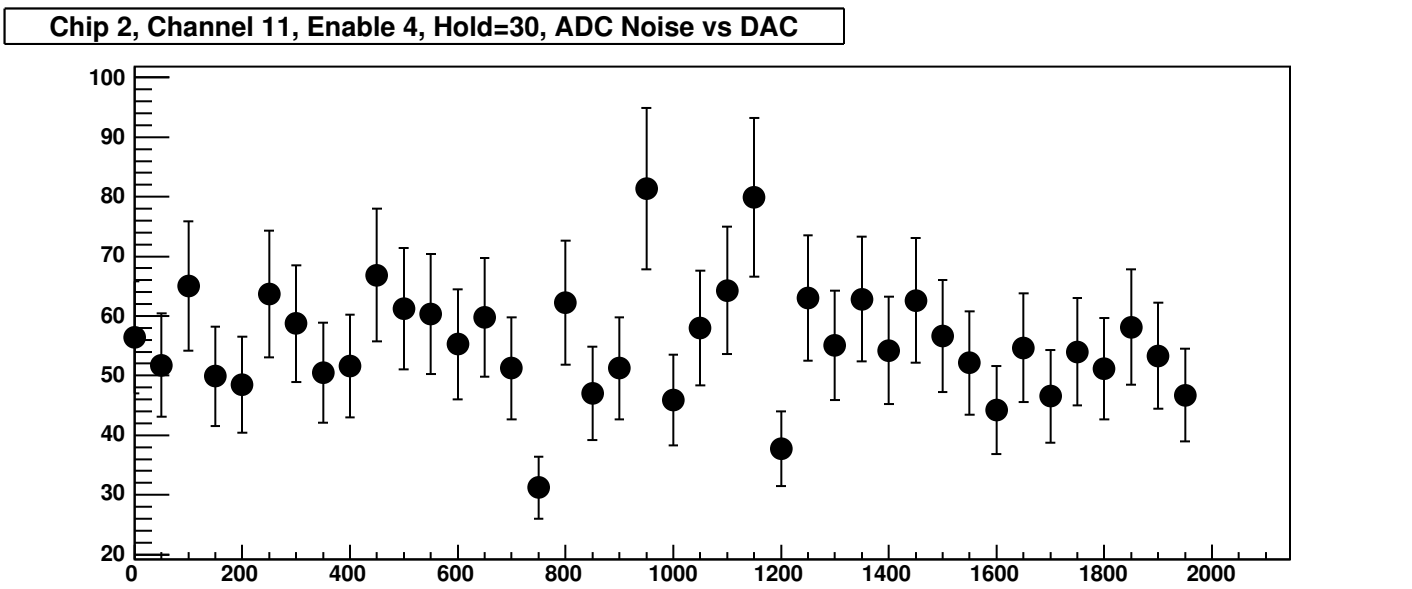
Chip 2, Channel 11, Enable 3, Hold=30, ADC Noise vs DAC

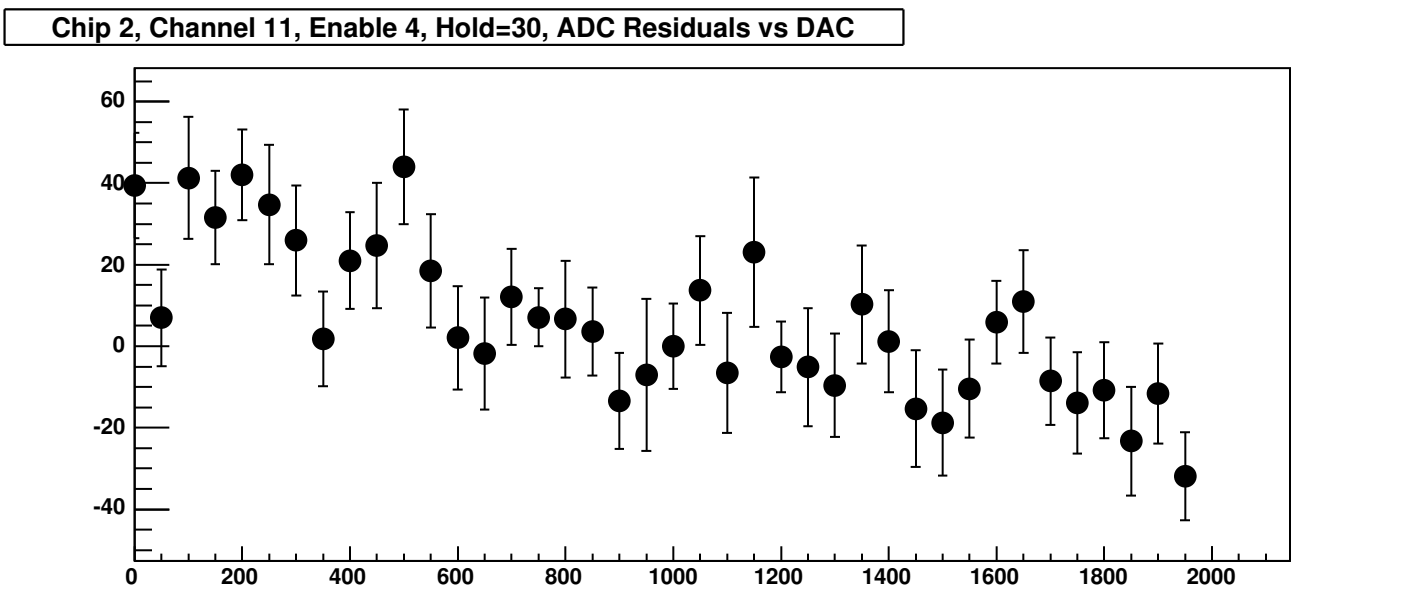


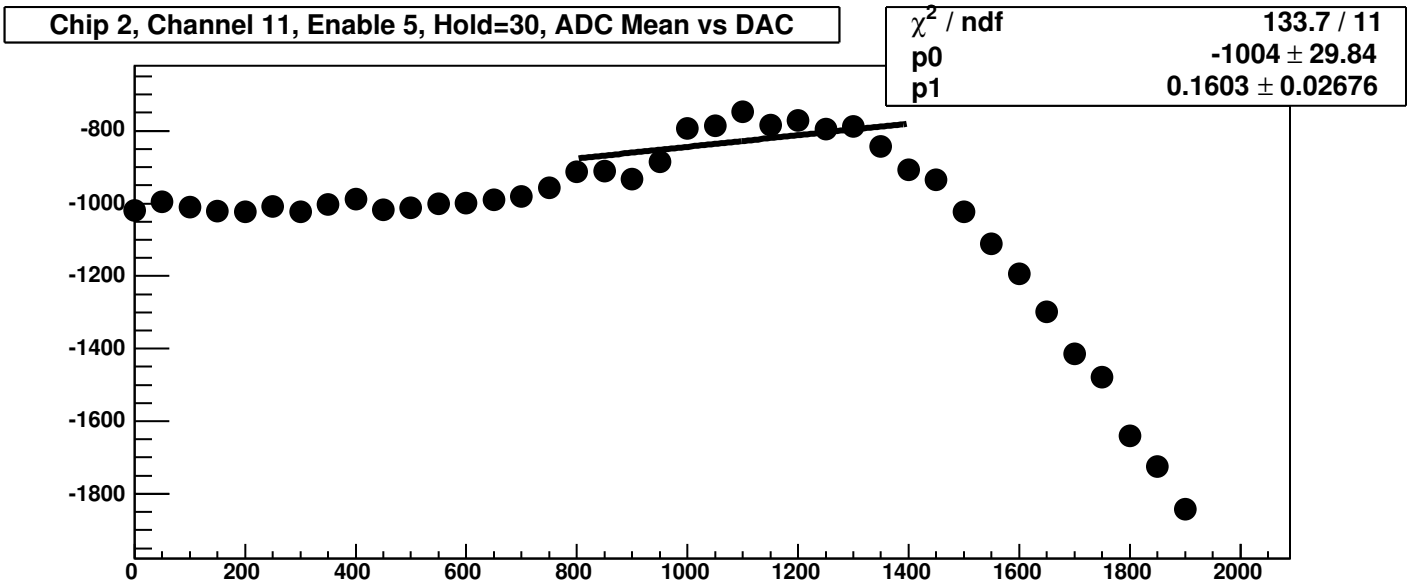
Chip 2, Channel 11, Enable 3, Hold=30, ADC Residuals vs DAC

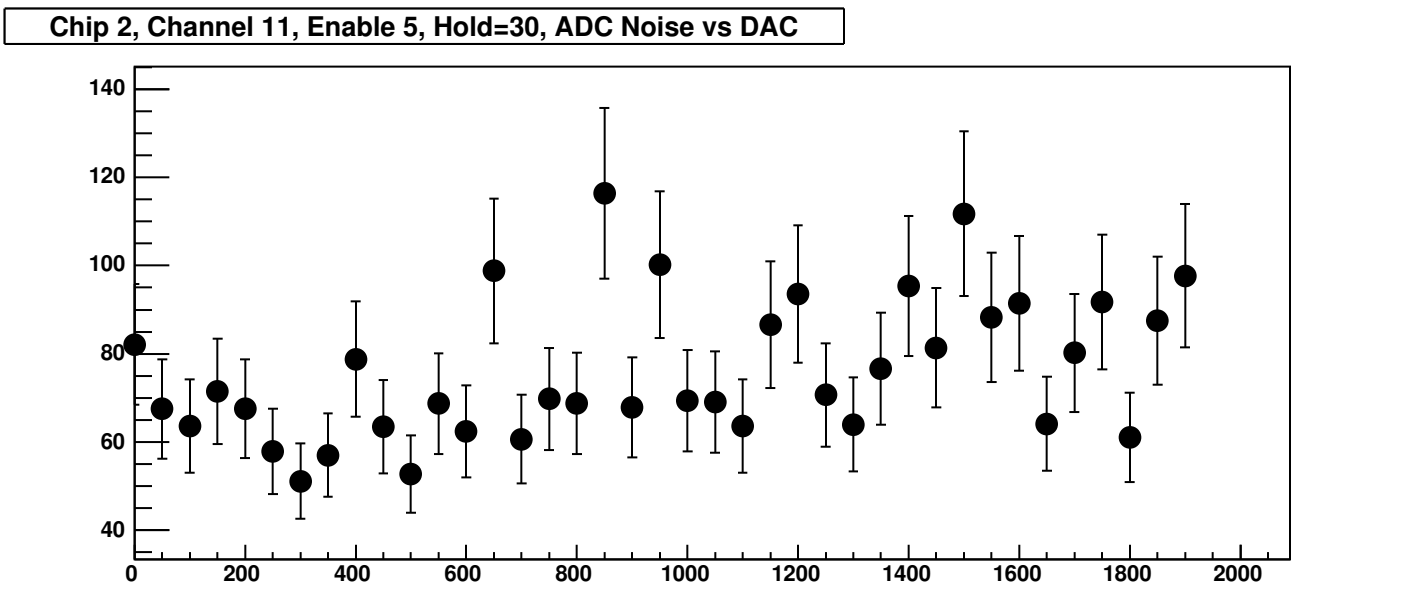


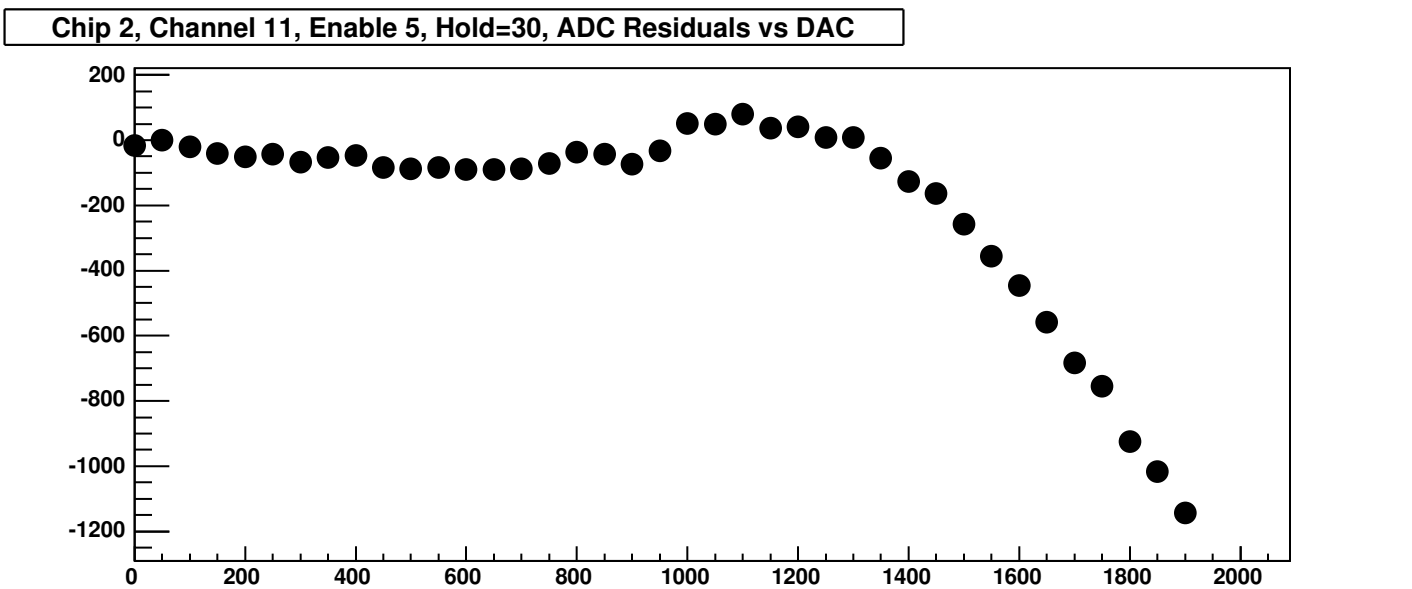

Chip 2, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5.91 / 11
p0 -1246 ± 20.99
p1 0.04441 ± 0.01883
-1140
-1160
-1180
-1200
-1220
-1240
0
200
400
600
800
1000
1200
1400
1600
1800
2000

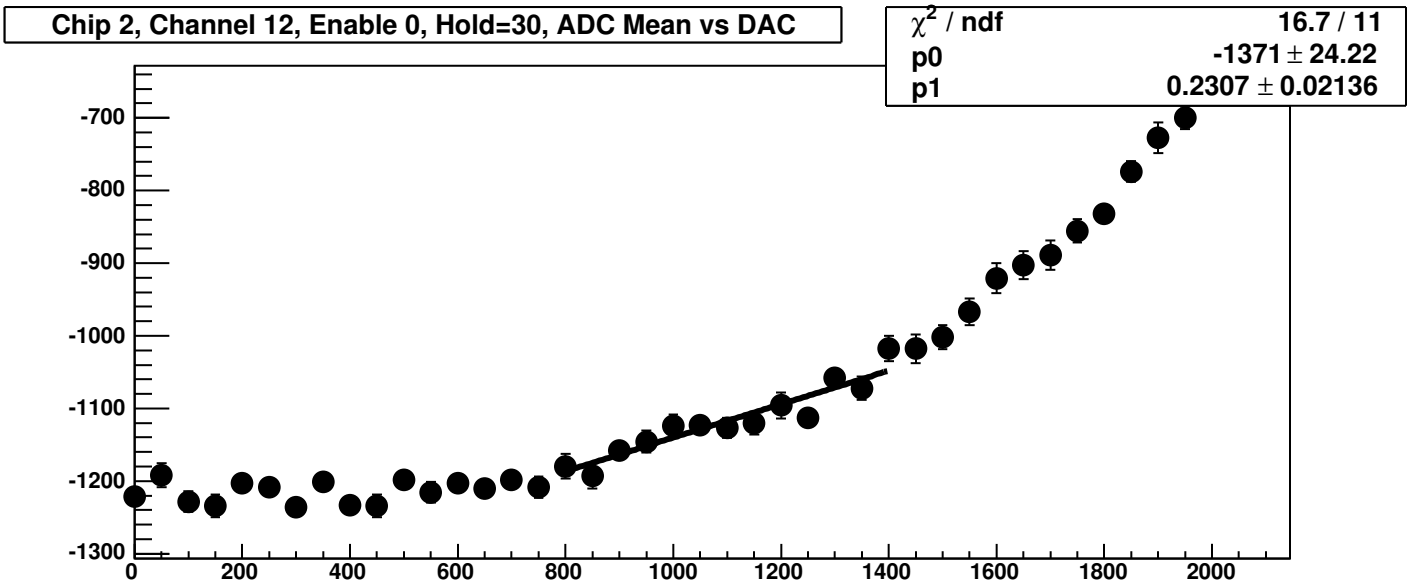

Chip 2, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

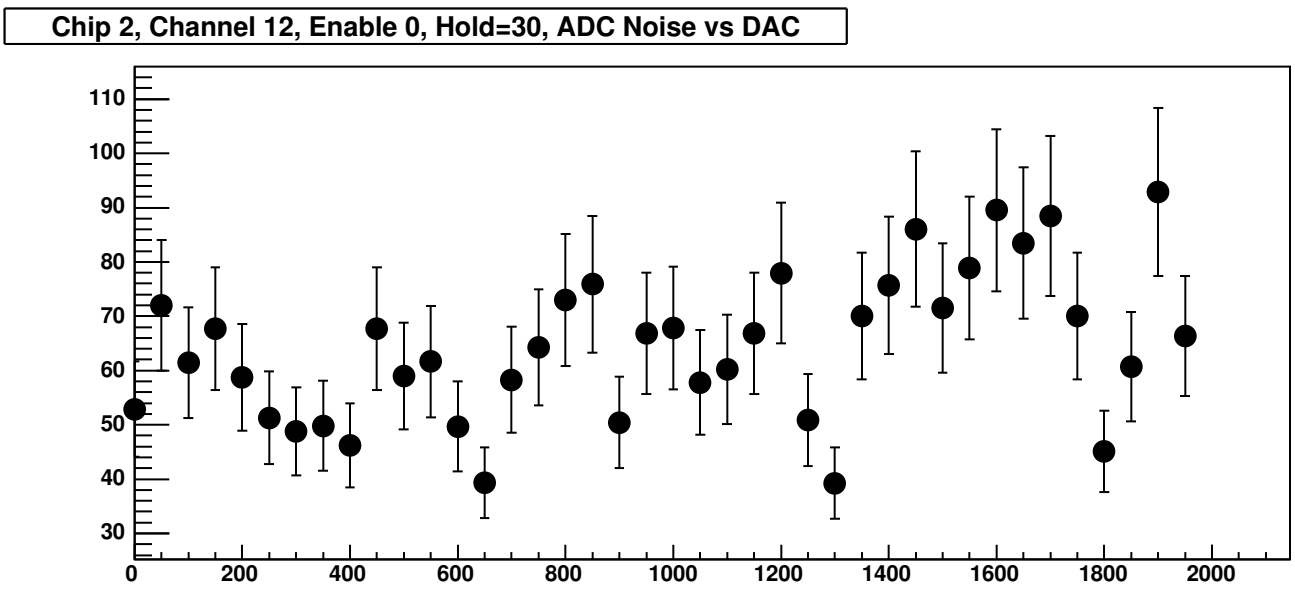

Chip 2, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

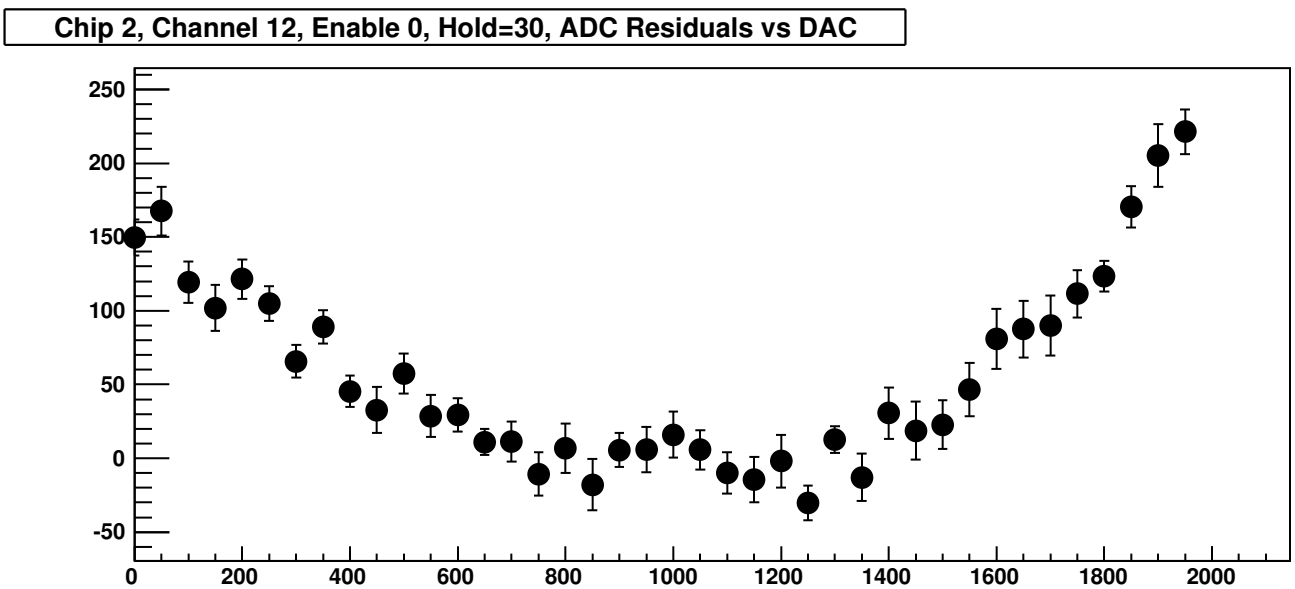

Chip 2, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 133.7 / 11
p0 -1004 ± 29.84
p1 0.1603 ± 0.02676
-800
-1000
-1200
-1400
-1600
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

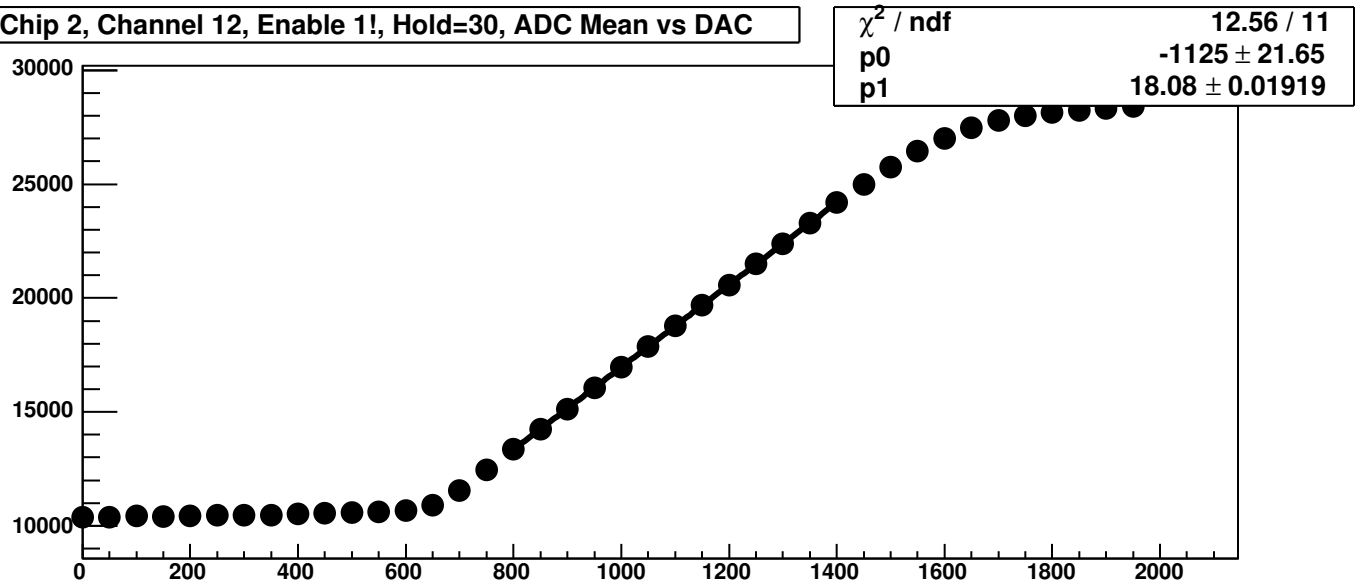

Chip 2, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 16.7 / 11
p0 -1371 ± 24.22
p1 0.2307 ± 0.02136
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

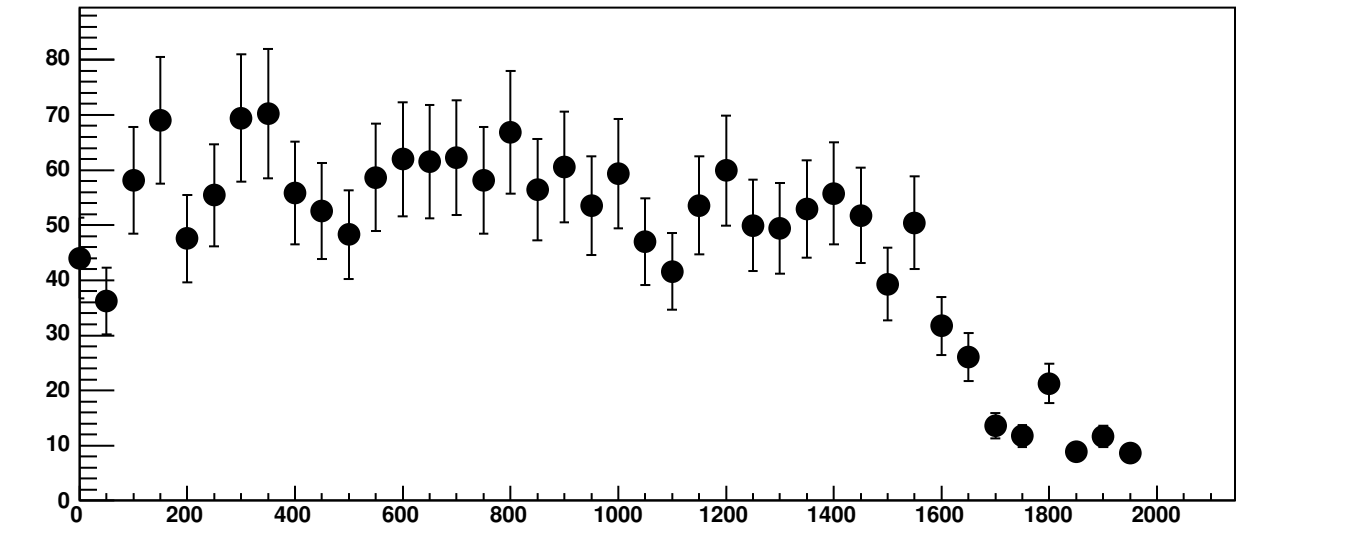

Chip 2, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

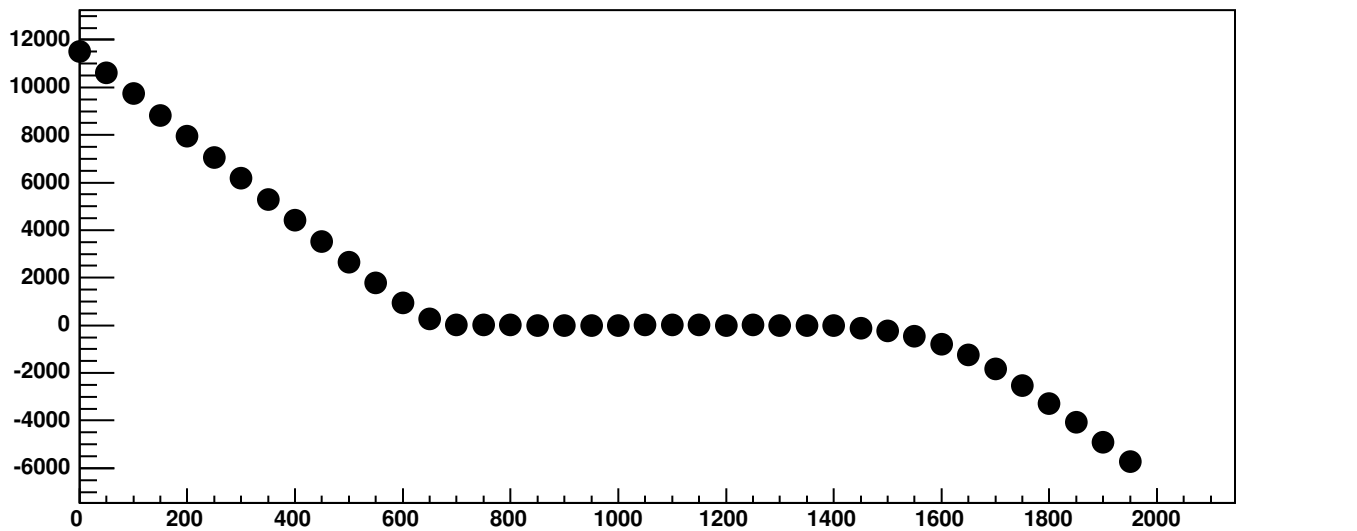
Chip 2, Channel 12, Enable 1!, Hold=30, ADC Mean vs DAC



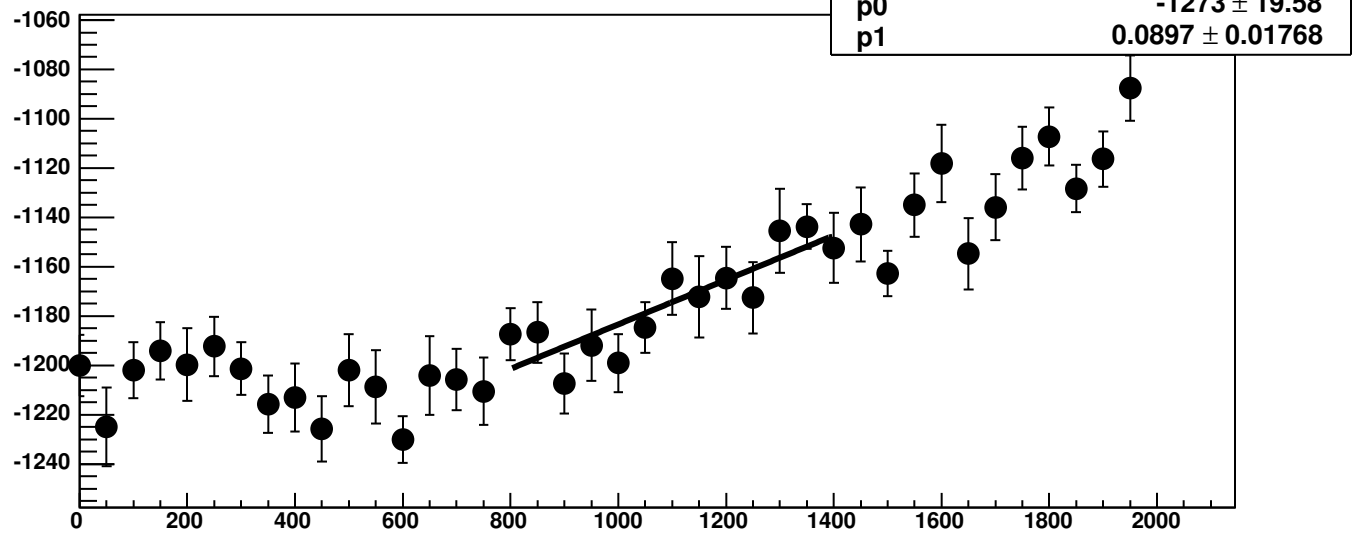
Chip 2, Channel 12, Enable 1!, Hold=30, ADC Noise vs DAC



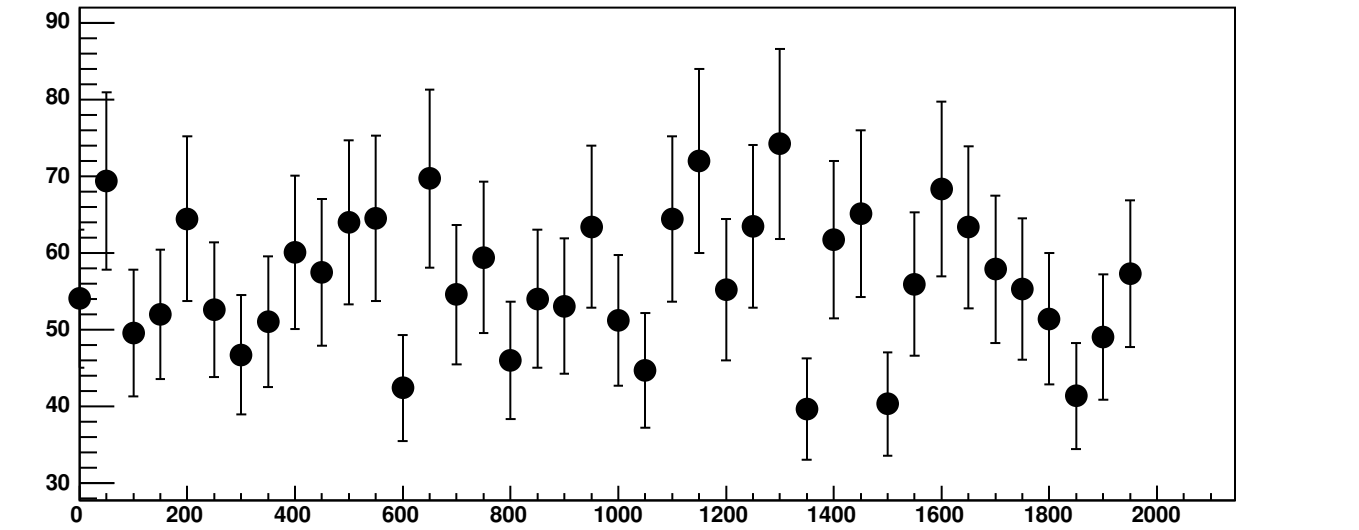
Chip 2, Channel 12, Enable 1!, Hold=30, ADC Residuals vs DAC



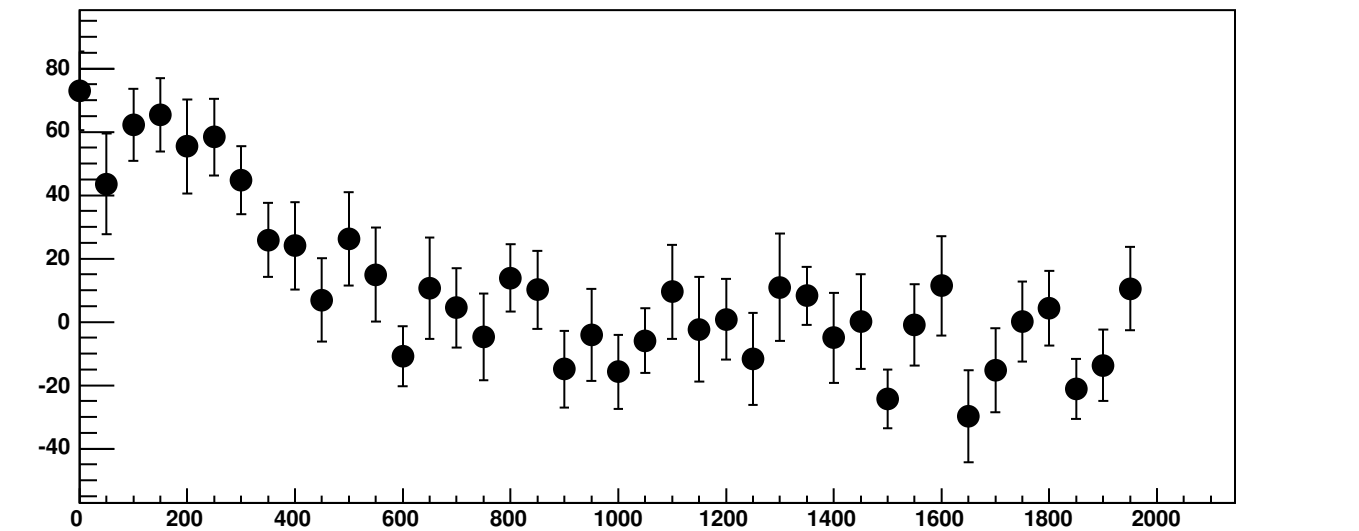
Chip 2, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC

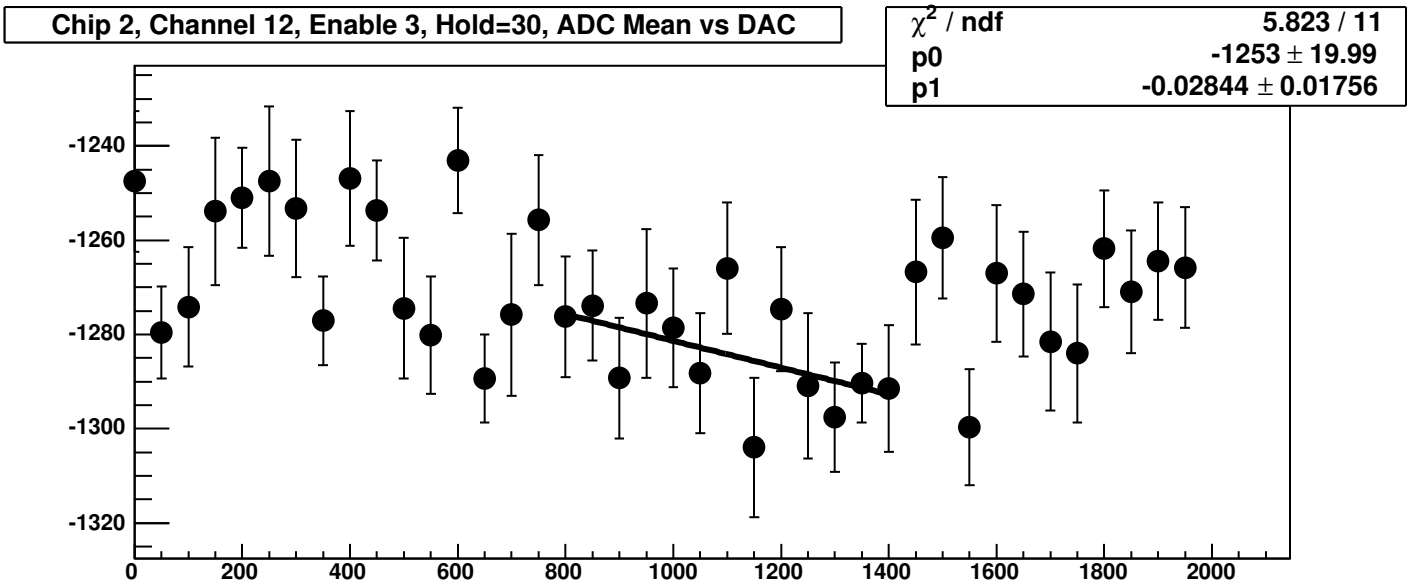


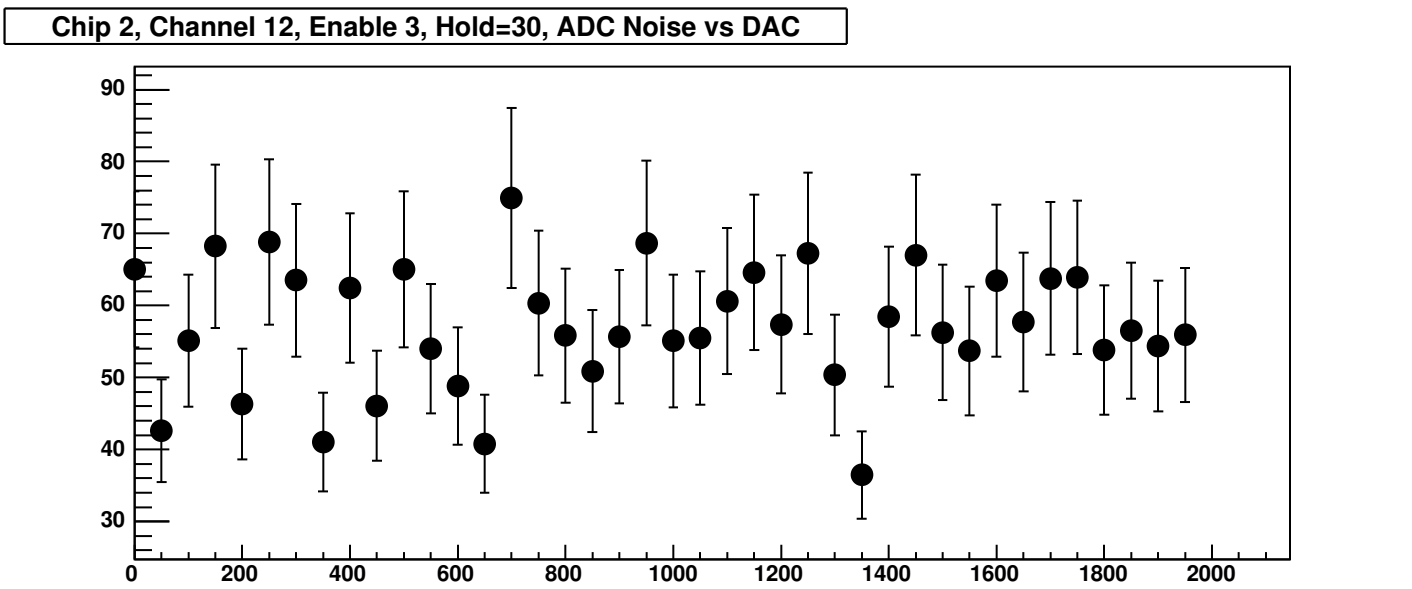
Chip 2, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC

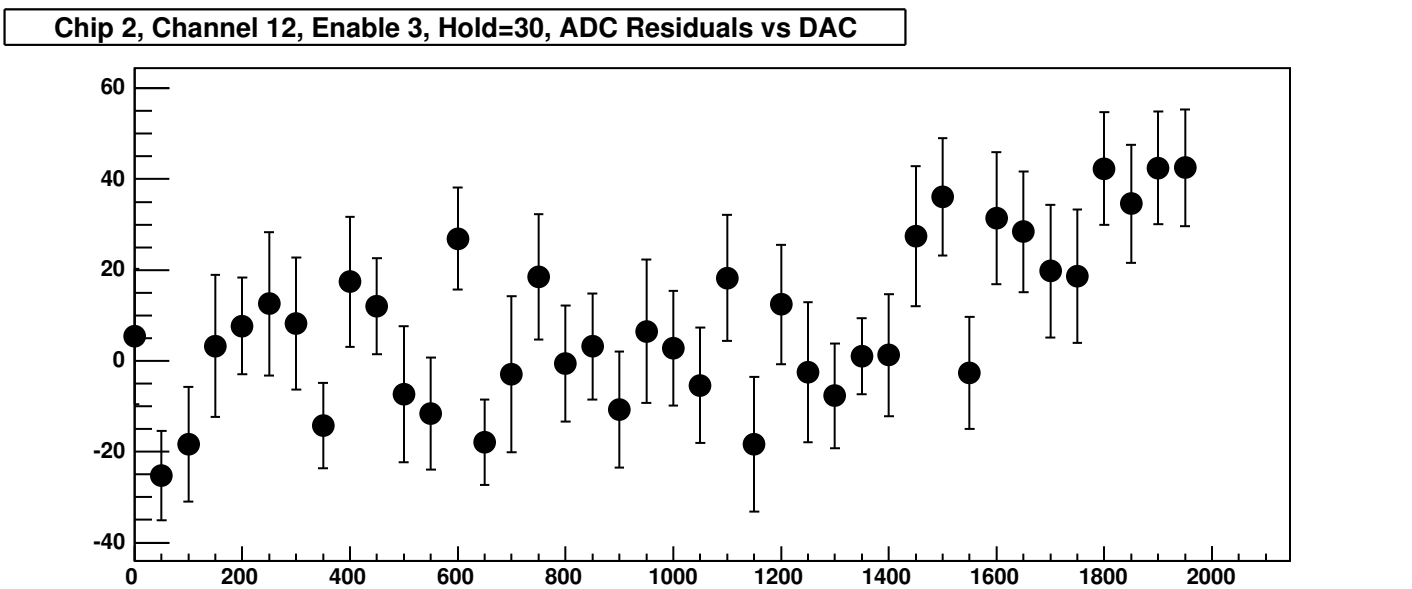


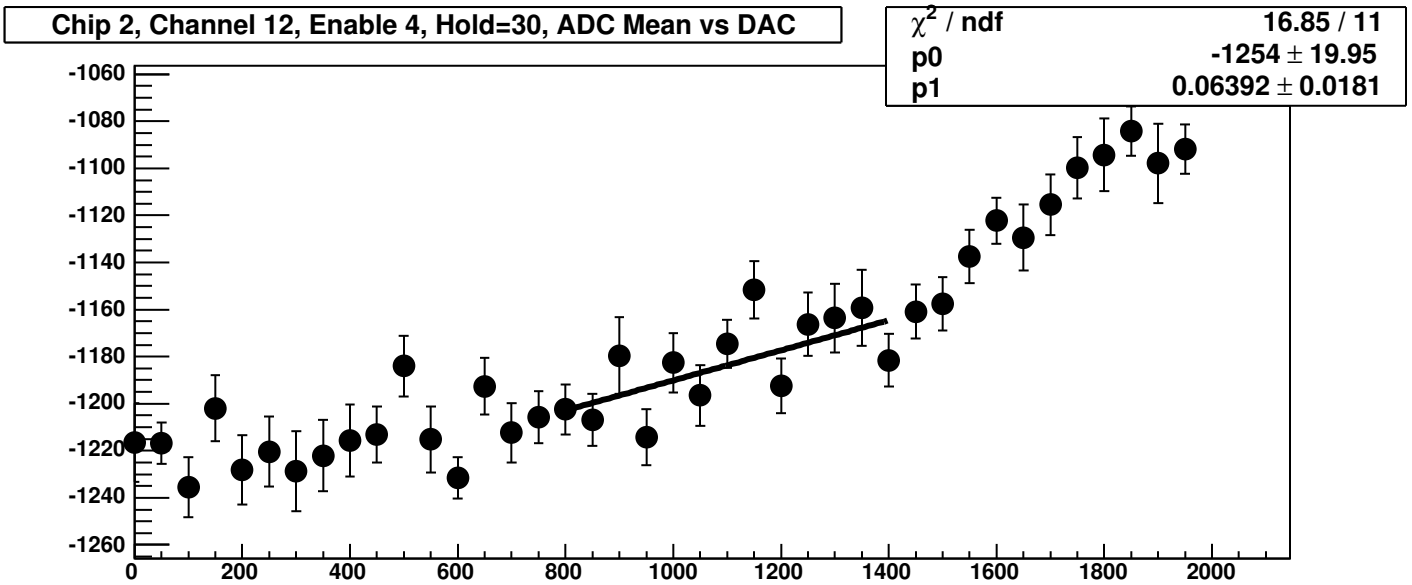
Chip 2, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC

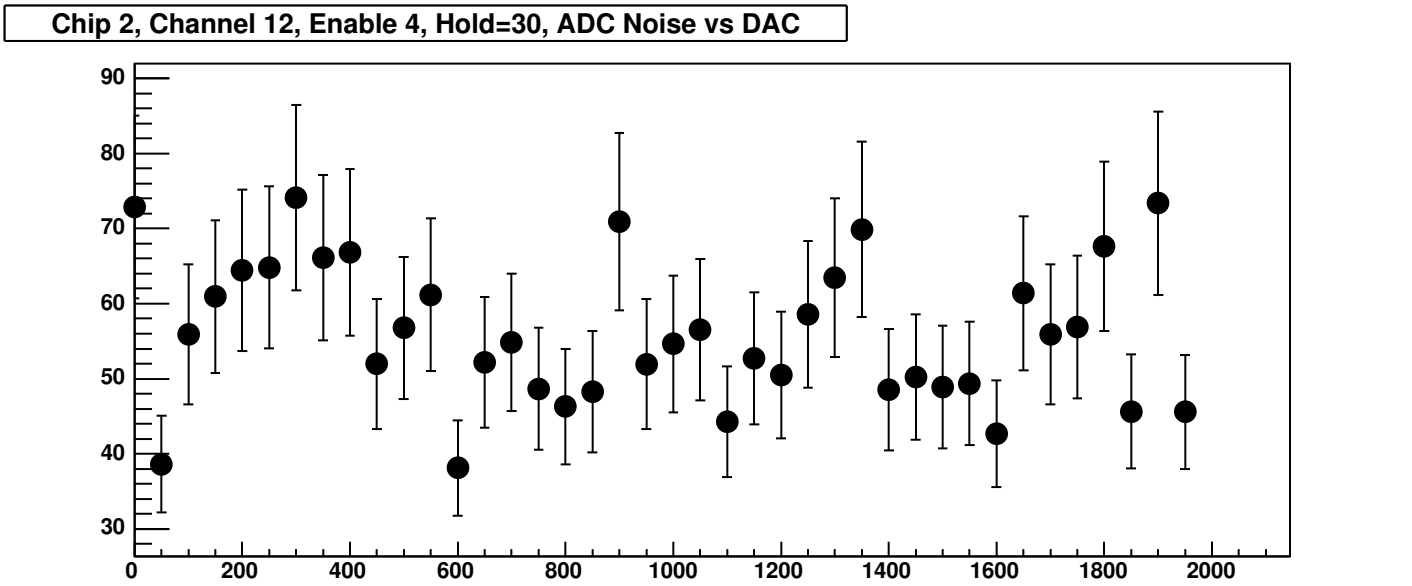


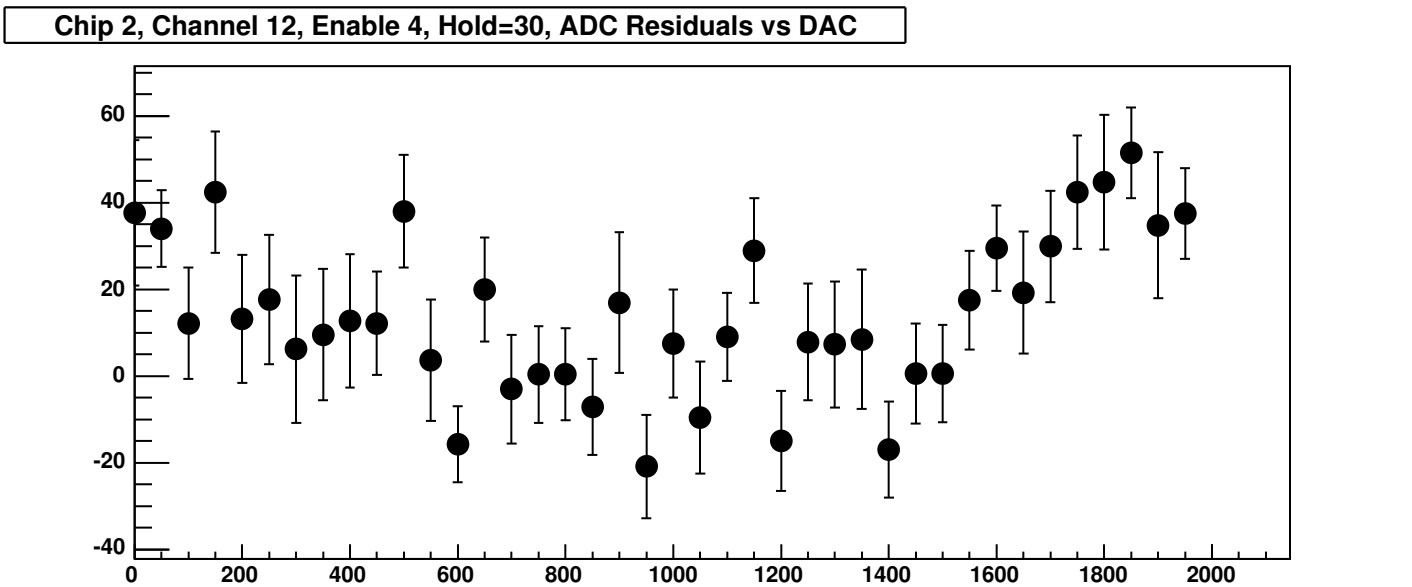

Chip 2, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5.823 / 11
p0 -1253 ± 19.99
p1 -0.02844 ± 0.01756
-1240
-1260
-1280
-1300
-1320
0
200
400
600
800
1000
1200
1400
1600
1800
2000

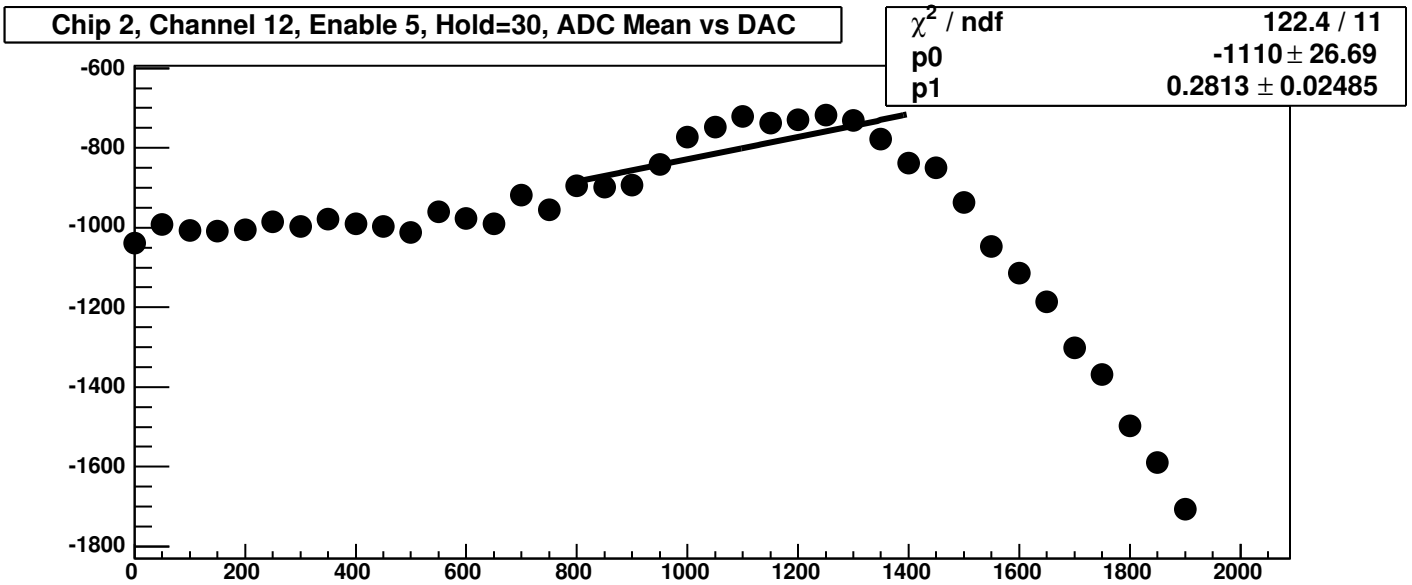

Chip 2, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

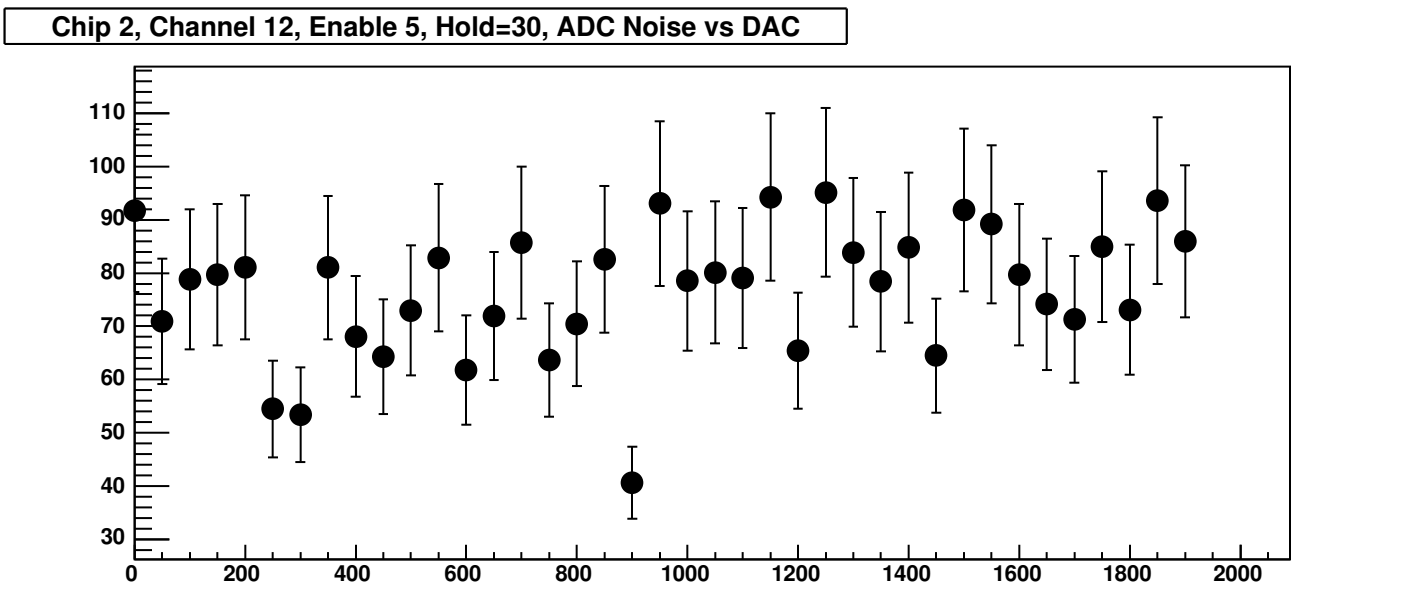

Chip 2, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

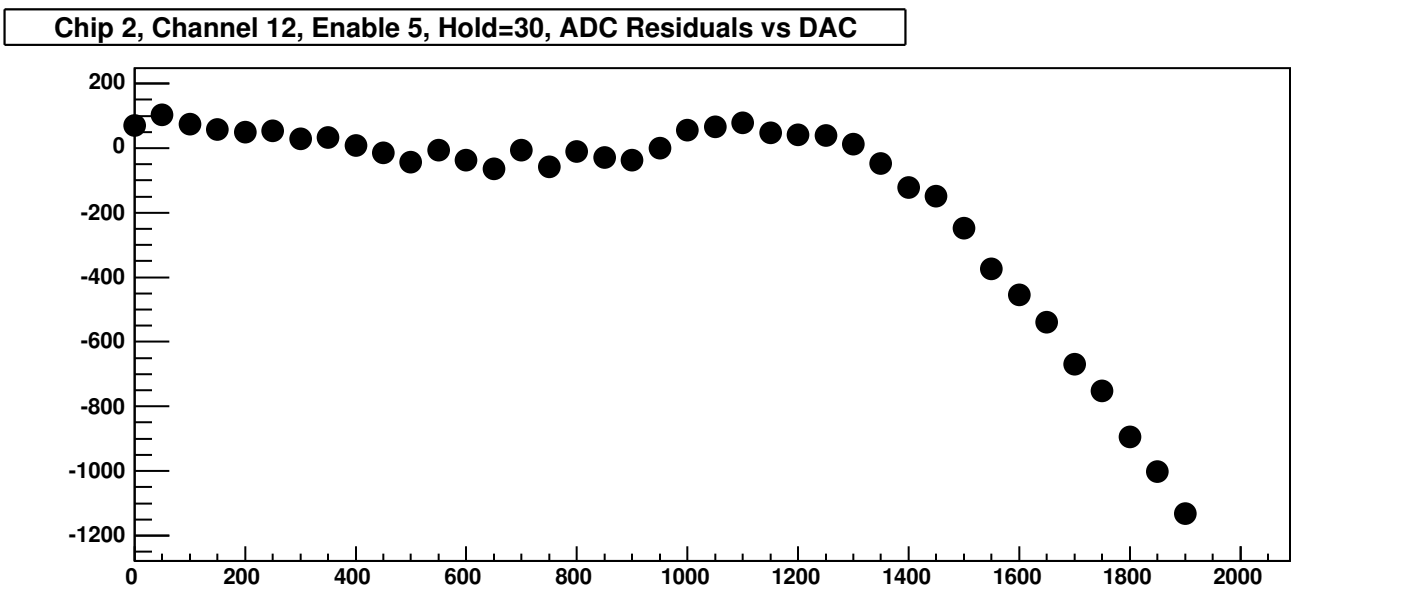

Chip 2, Channel 12, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 16.85 / 11
p0 -1254 ± 19.95
p1 0.06392 ± 0.0181
-1060
-1080
-1100
-1120
-1140
-1160
-1180
-1200
-1220
-1240
-1260
0
200
400
600
800
1000
1200
1400
1600
1800
2000

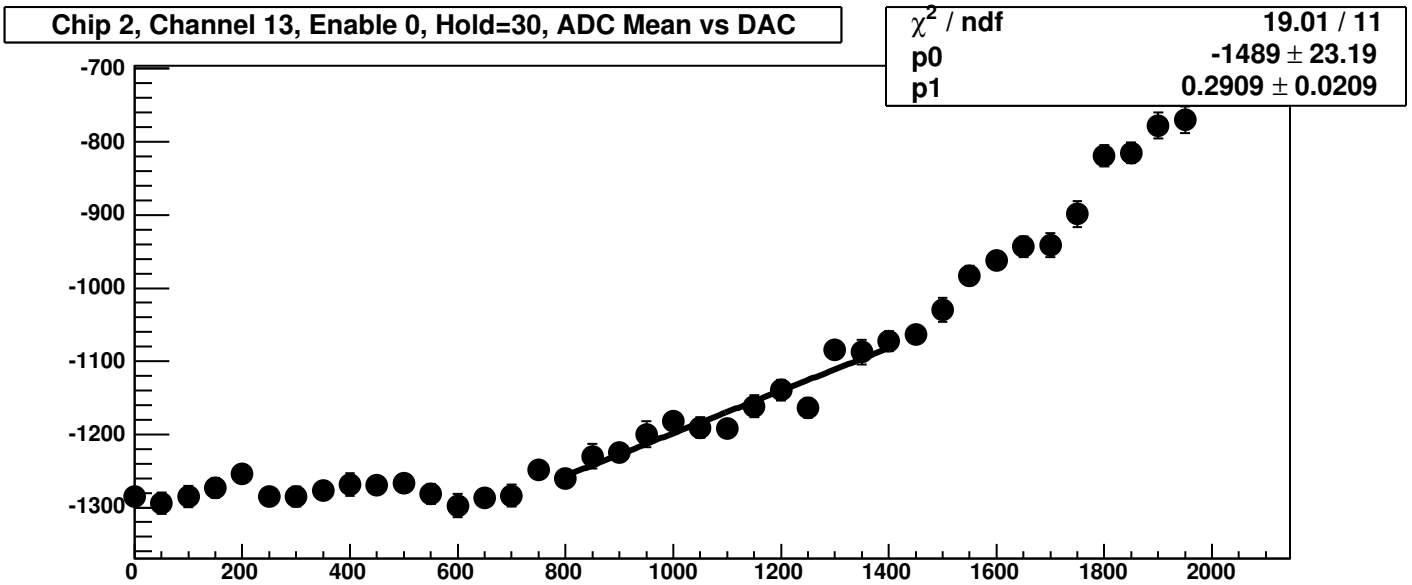

Chip 2, Channel 12, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

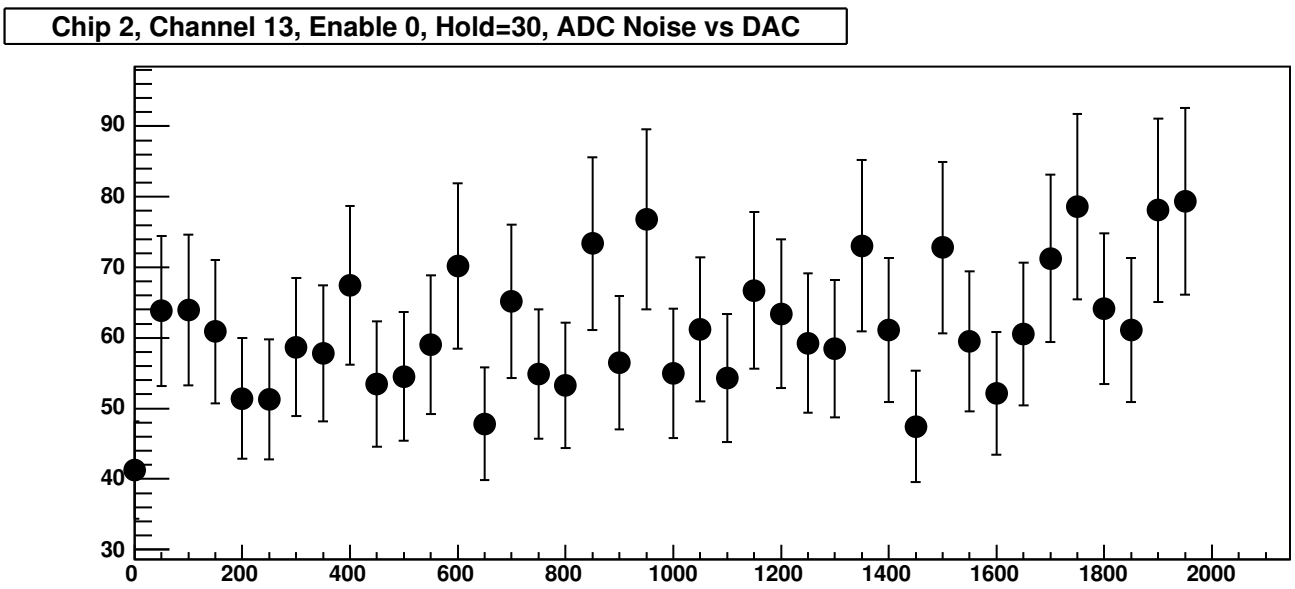

Chip 2, Channel 12, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

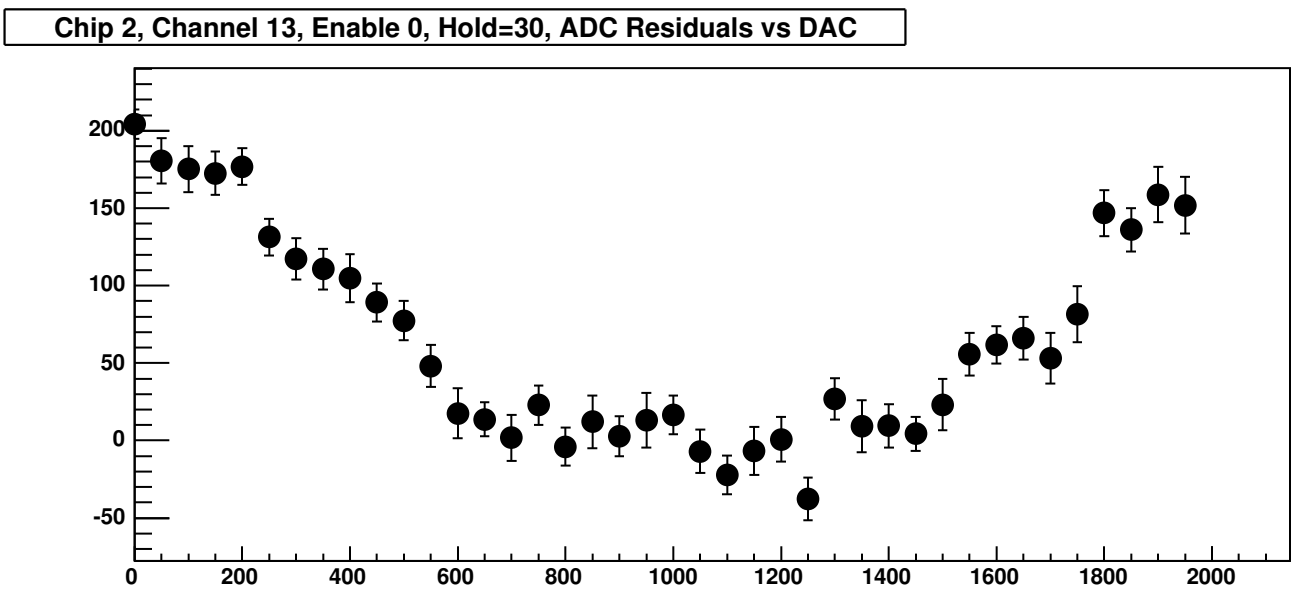

Chip 2, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 122.4 / 11
p0 -1110 ± 26.69
p1 0.2813 ± 0.02485
-600
-800
-1000
-1200
-1400
-1600
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

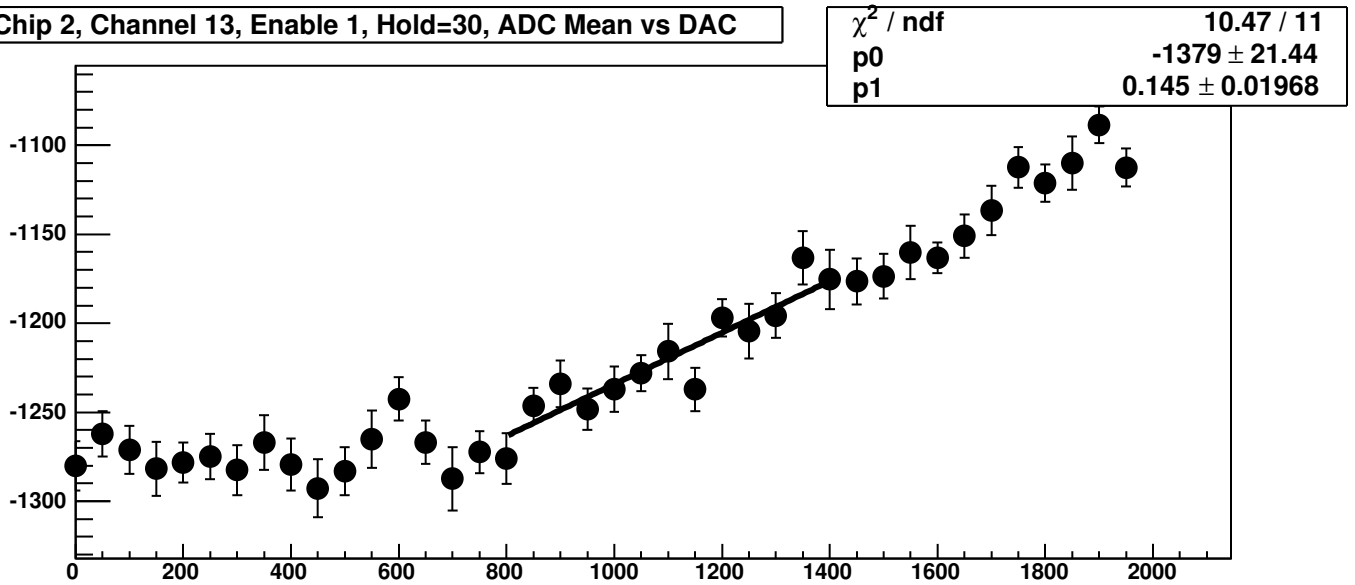

Chip 2, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 19.01 / 11
p0 -1489 ± 23.19
p1 0.2909 ± 0.0209
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

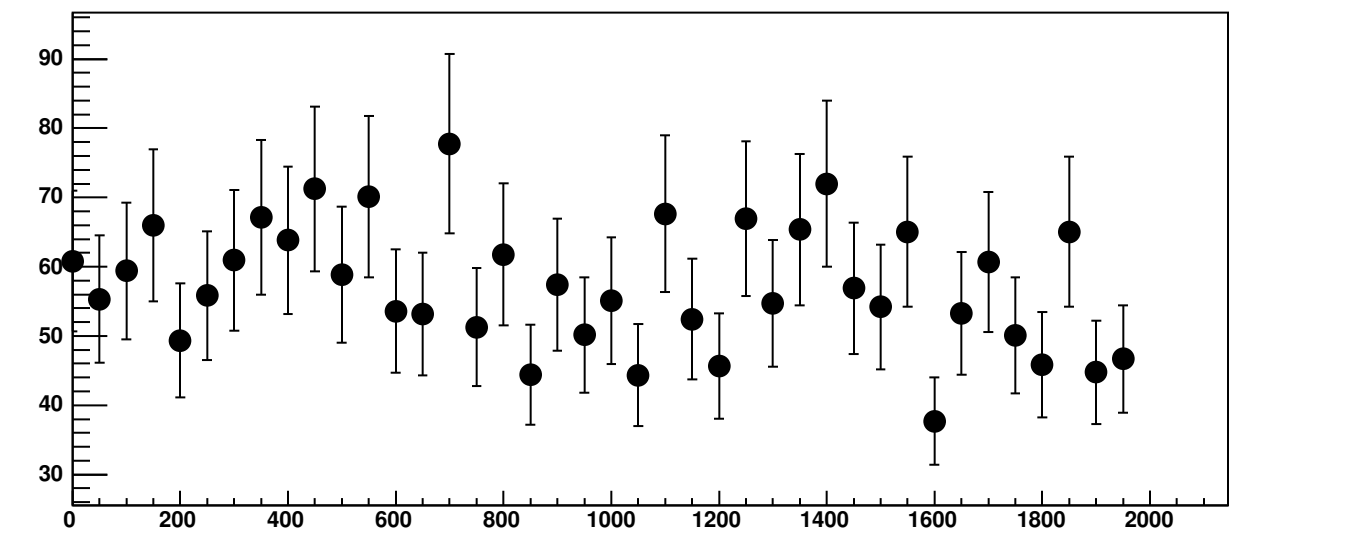

Chip 2, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

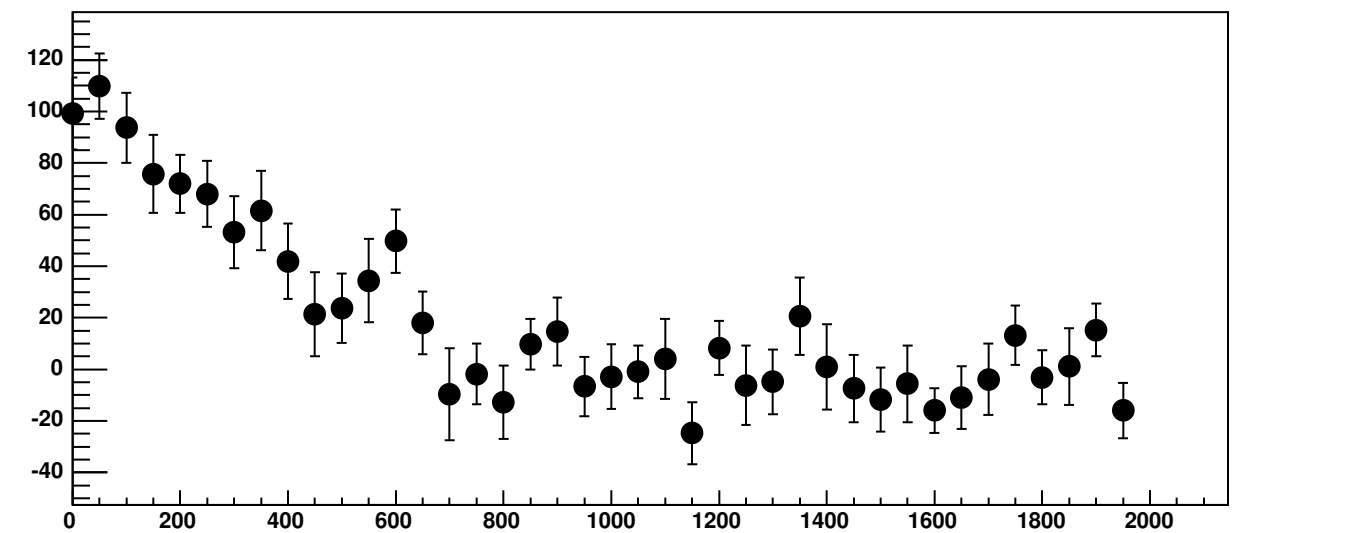
Chip 2, Channel 13, Enable 1, Hold=30, ADC Mean vs DAC

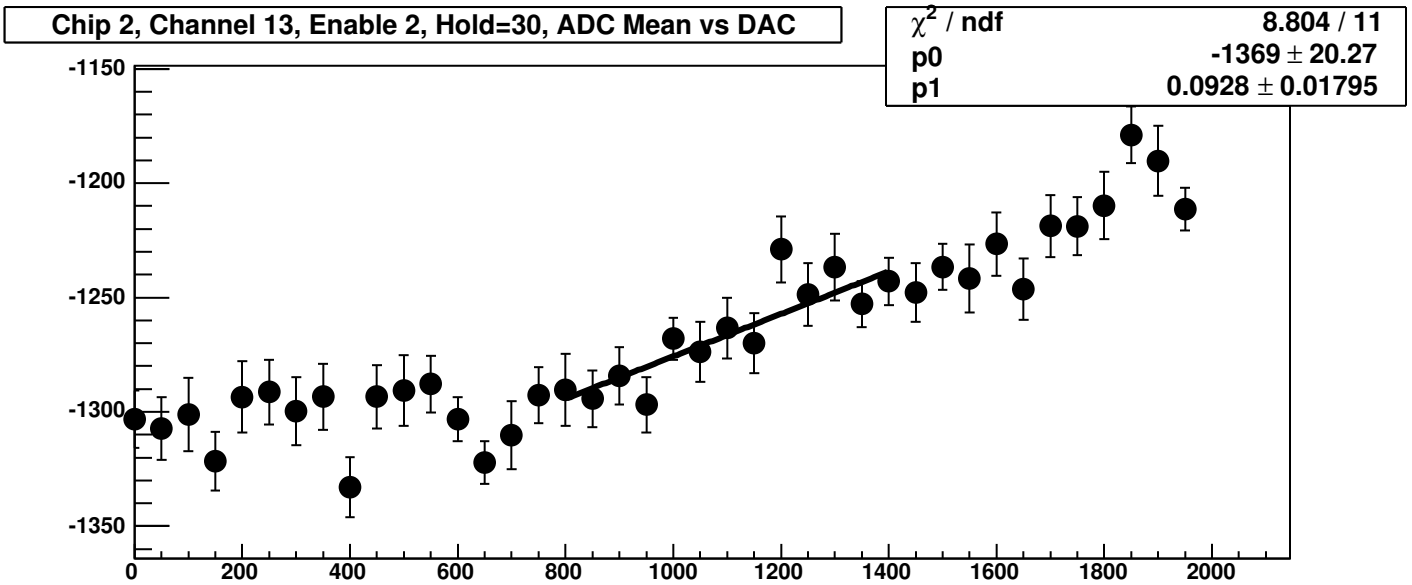


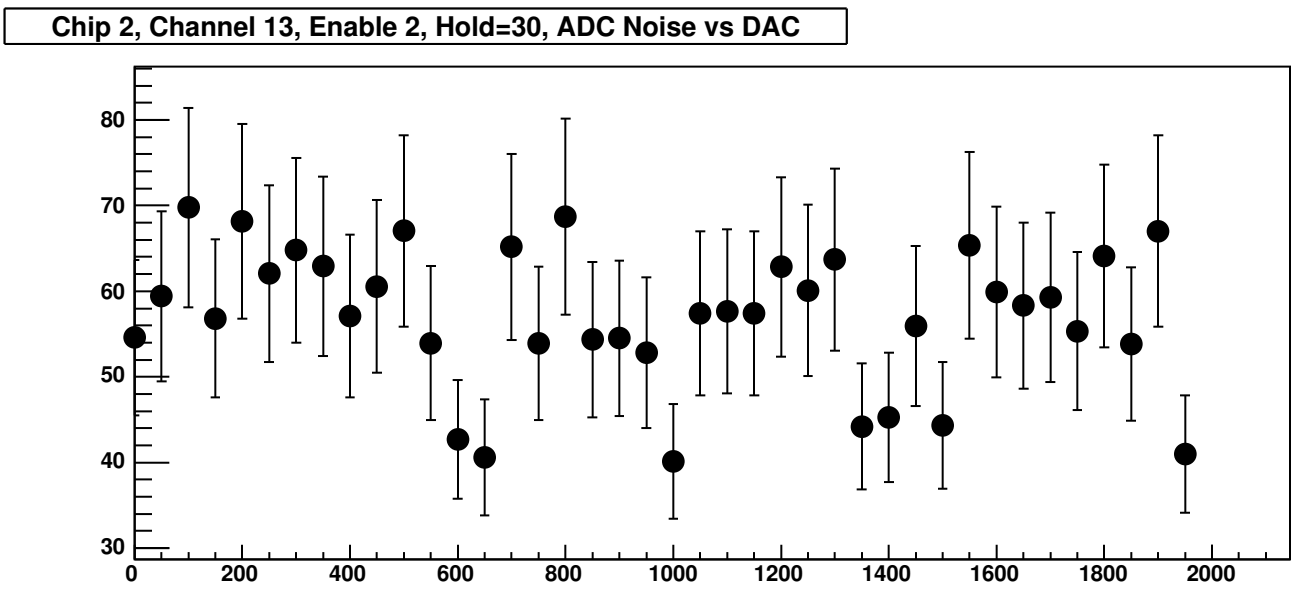
Chip 2, Channel 13, Enable 1, Hold=30, ADC Noise vs DAC

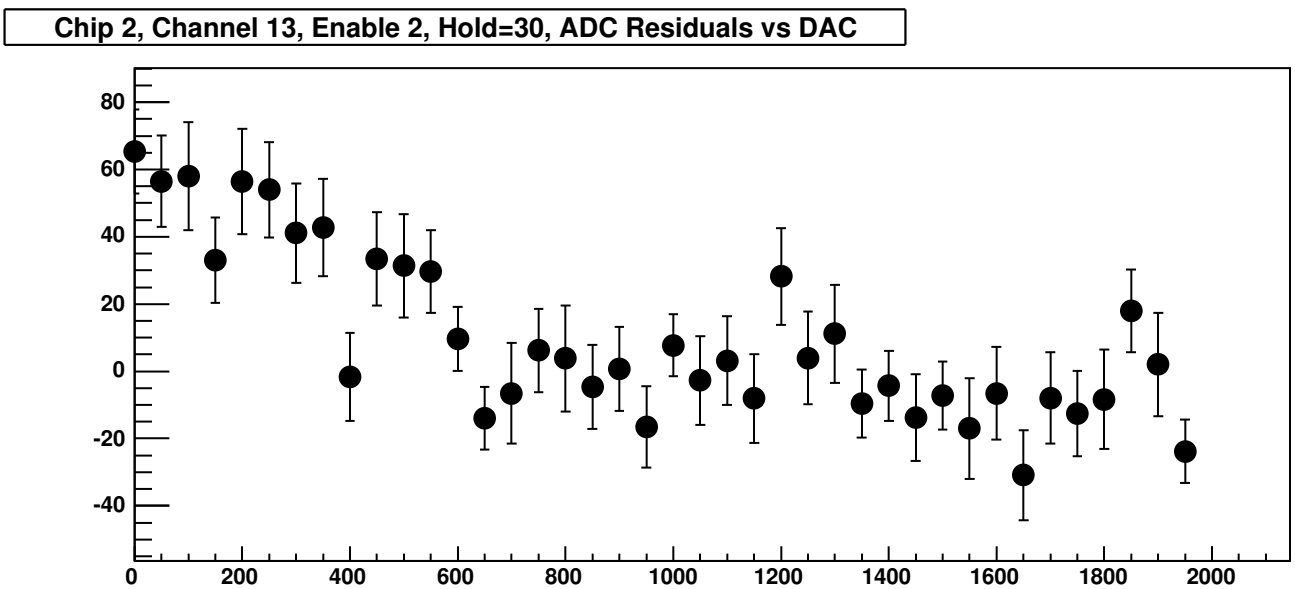


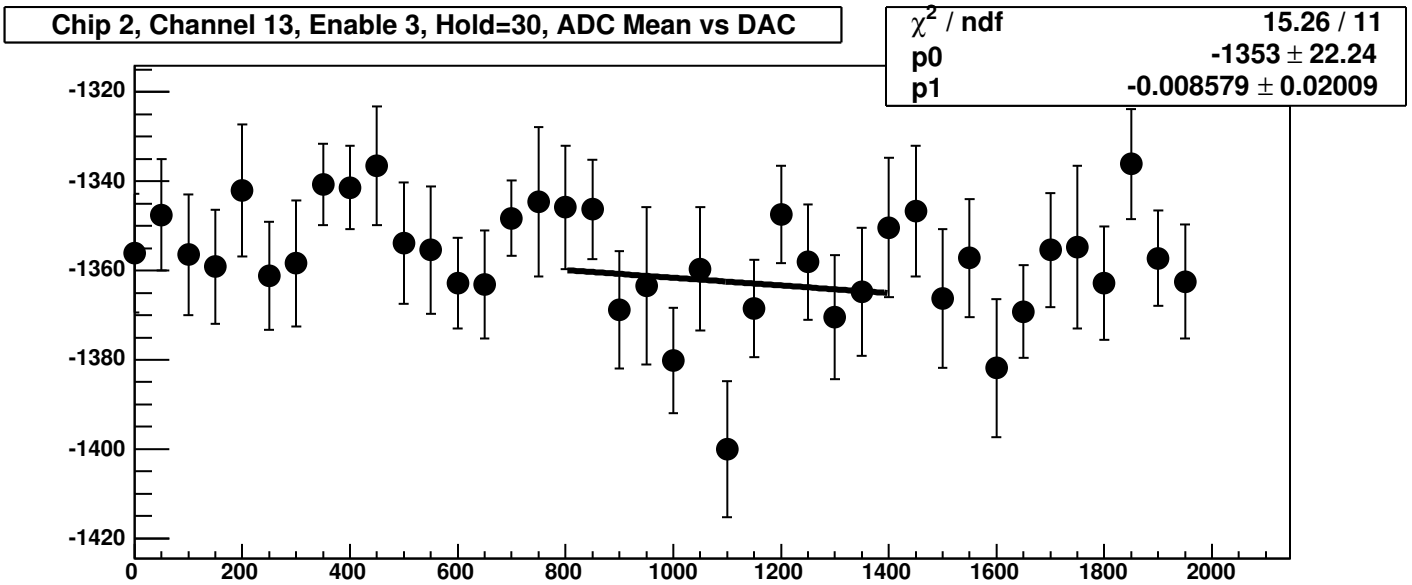
Chip 2, Channel 13, Enable 1, Hold=30, ADC Residuals vs DAC

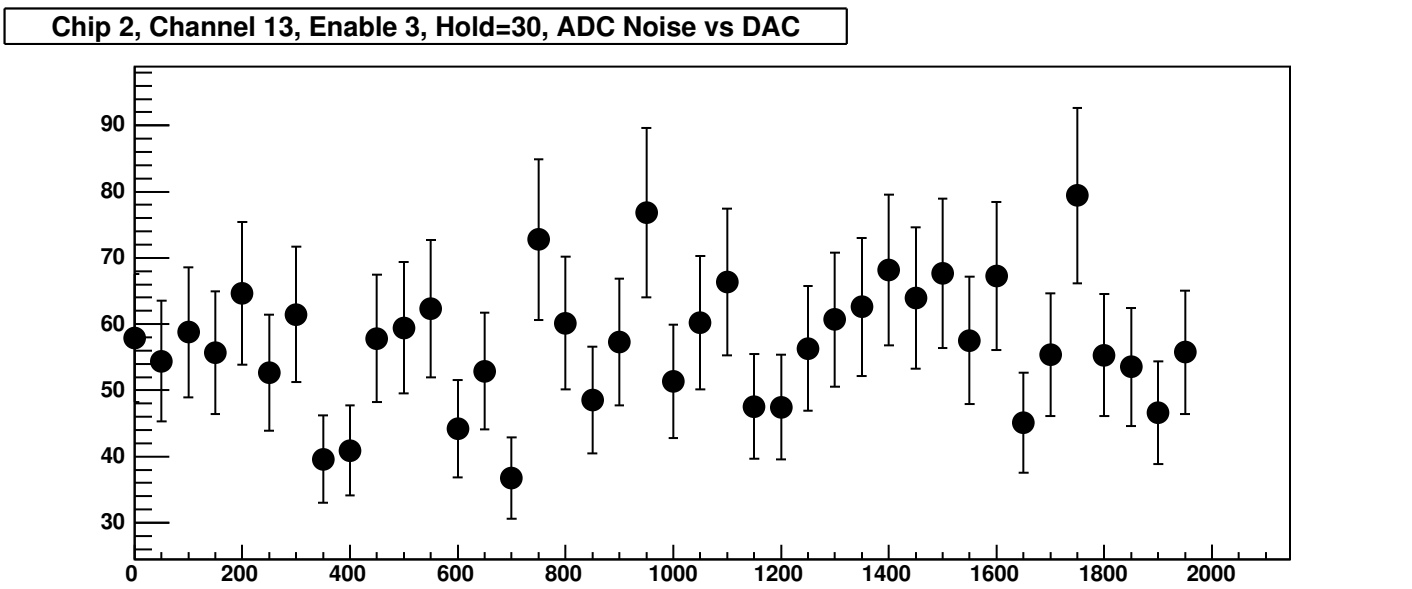


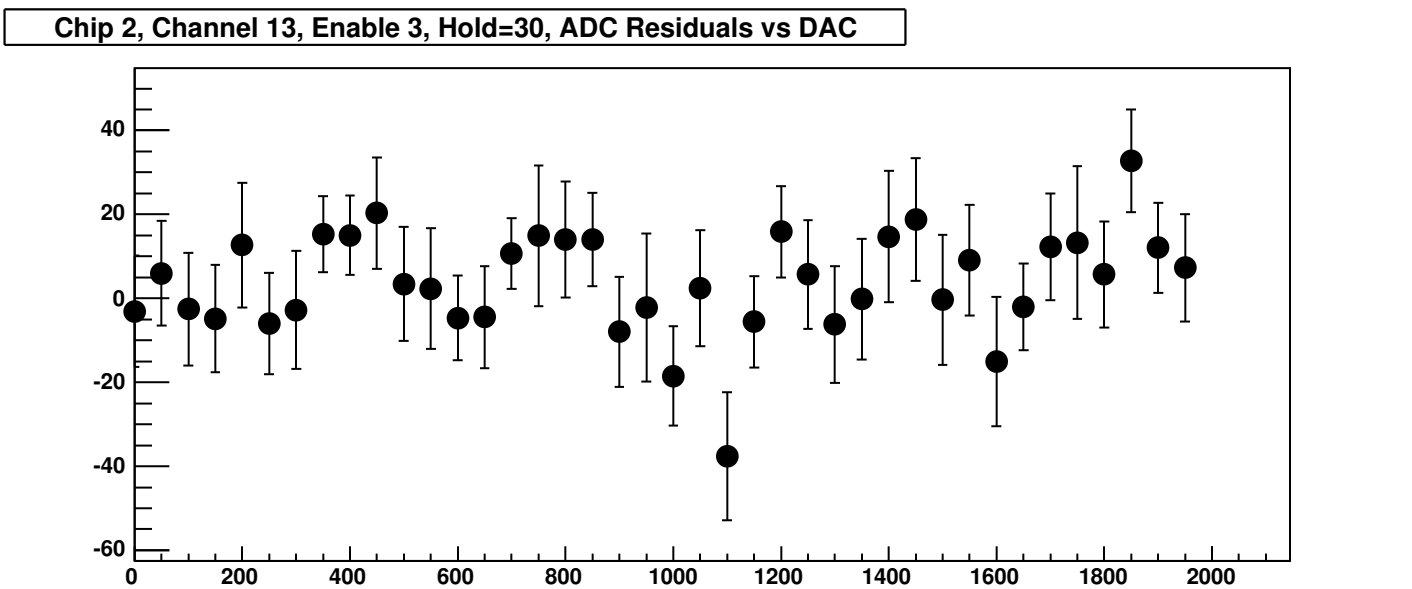

Chip 2, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 8.804 / 11
p0 -1369 ± 20.27
p1 0.0928 ± 0.01795
-1150
-1200
-1250
-1300
-1350
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

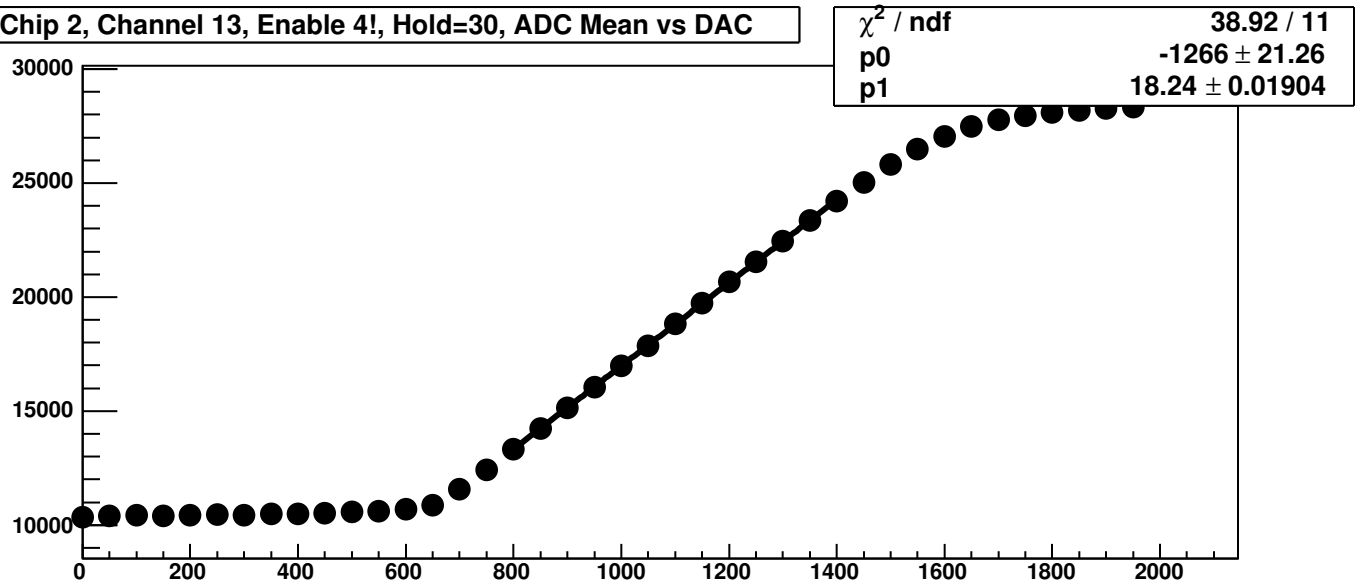

Chip 2, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 15.26 / 11
p0 -1353 ± 22.24
p1 -0.008579 ± 0.02009
-1320
-1340
-1360
-1380
-1400
-1420
0
200
400
600
800
1000
1200
1400
1600
1800
2000

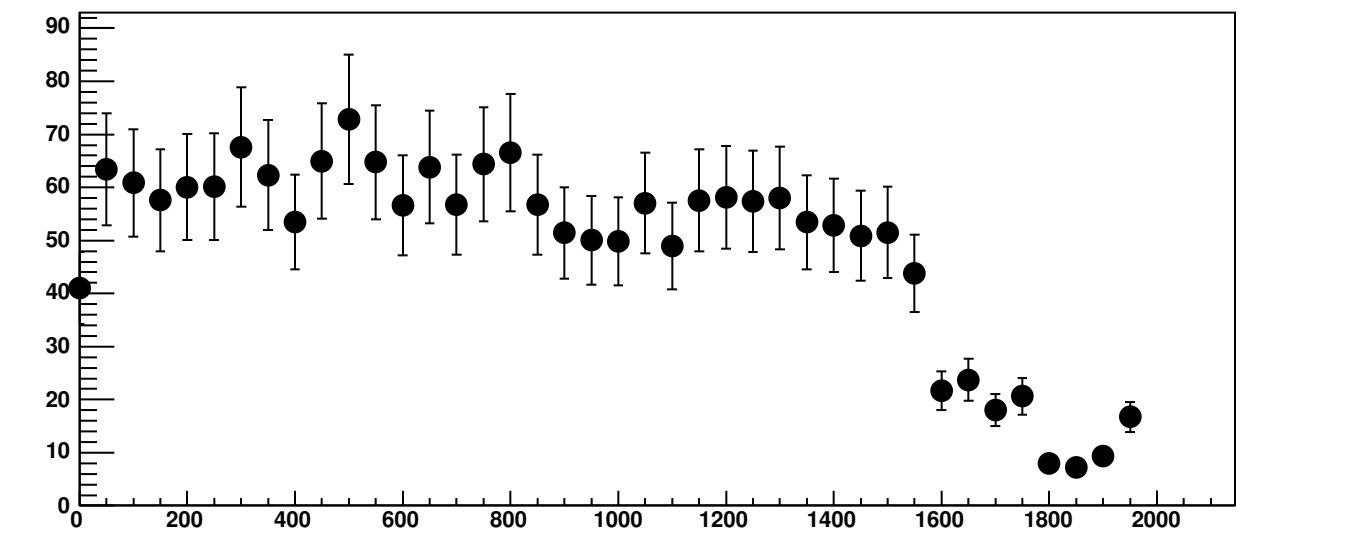

Chip 2, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

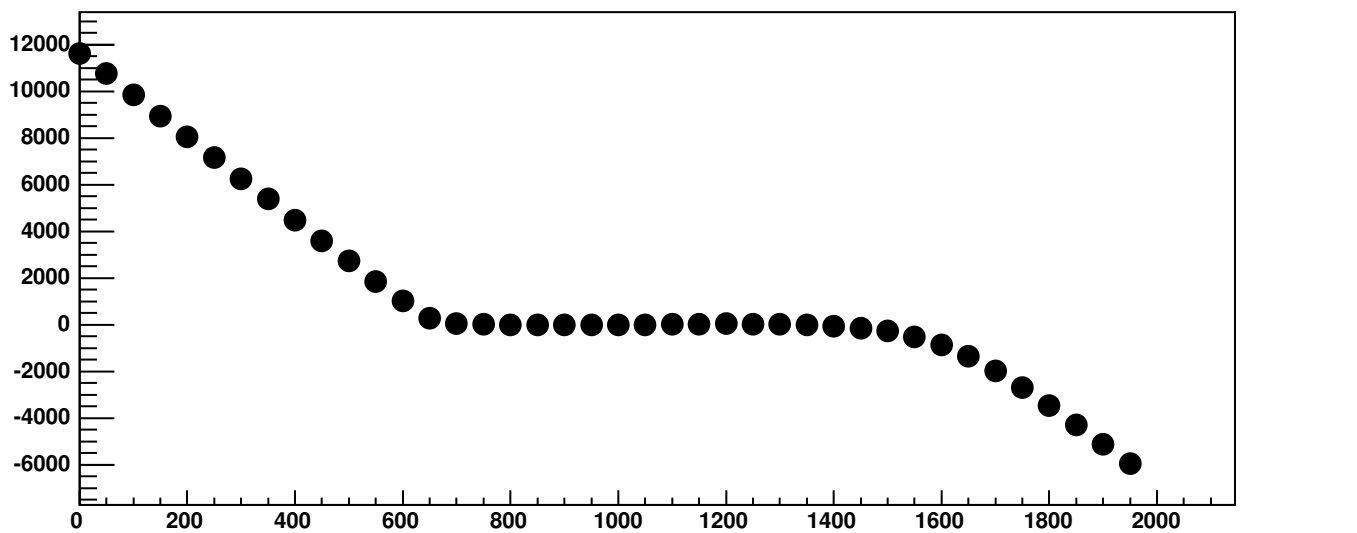
Chip 2, Channel 13, Enable 4!, Hold=30, ADC Mean vs DAC

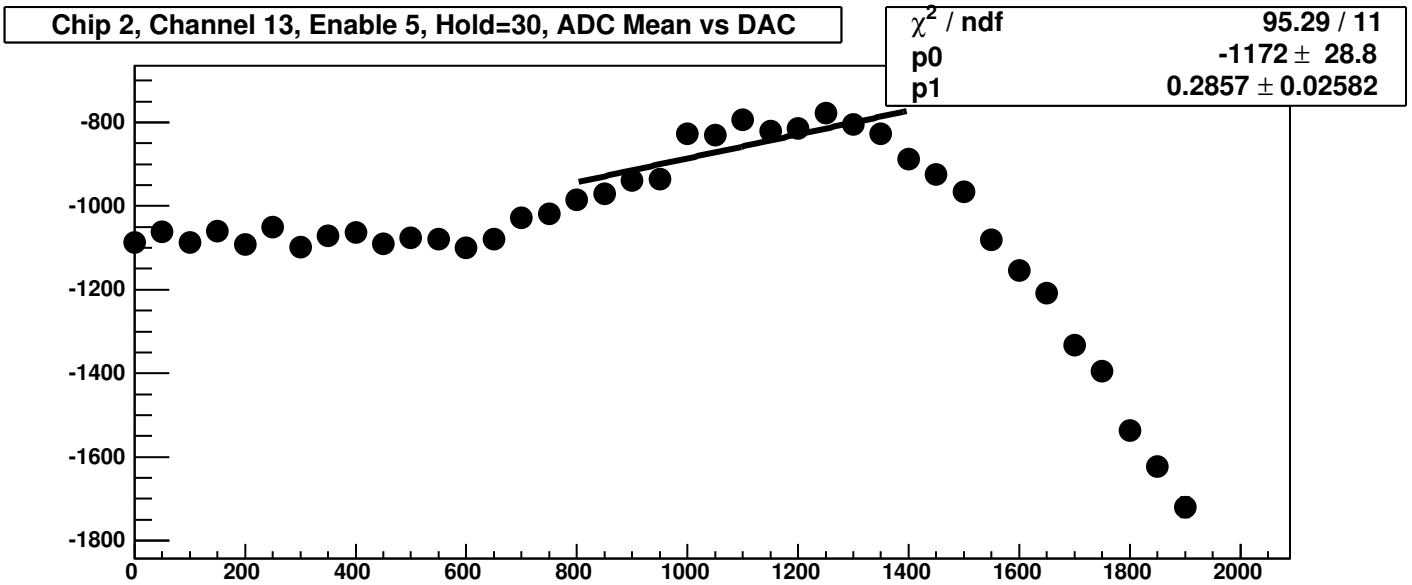


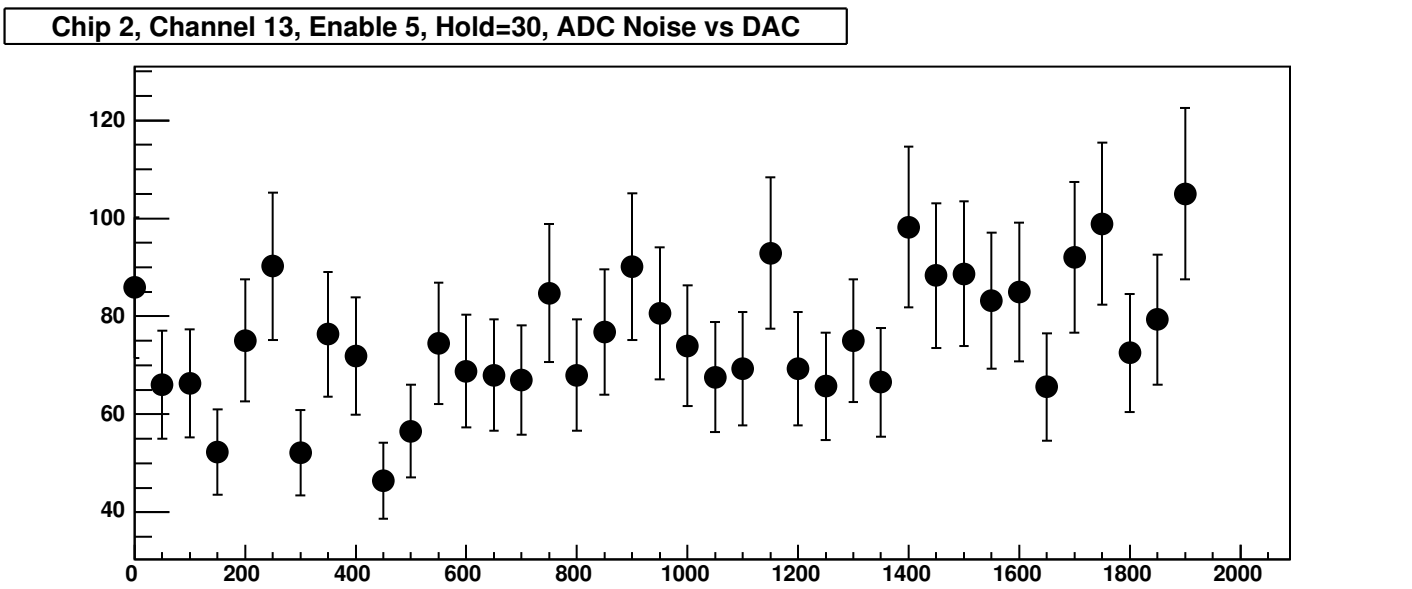
Chip 2, Channel 13, Enable 4!, Hold=30, ADC Noise vs DAC

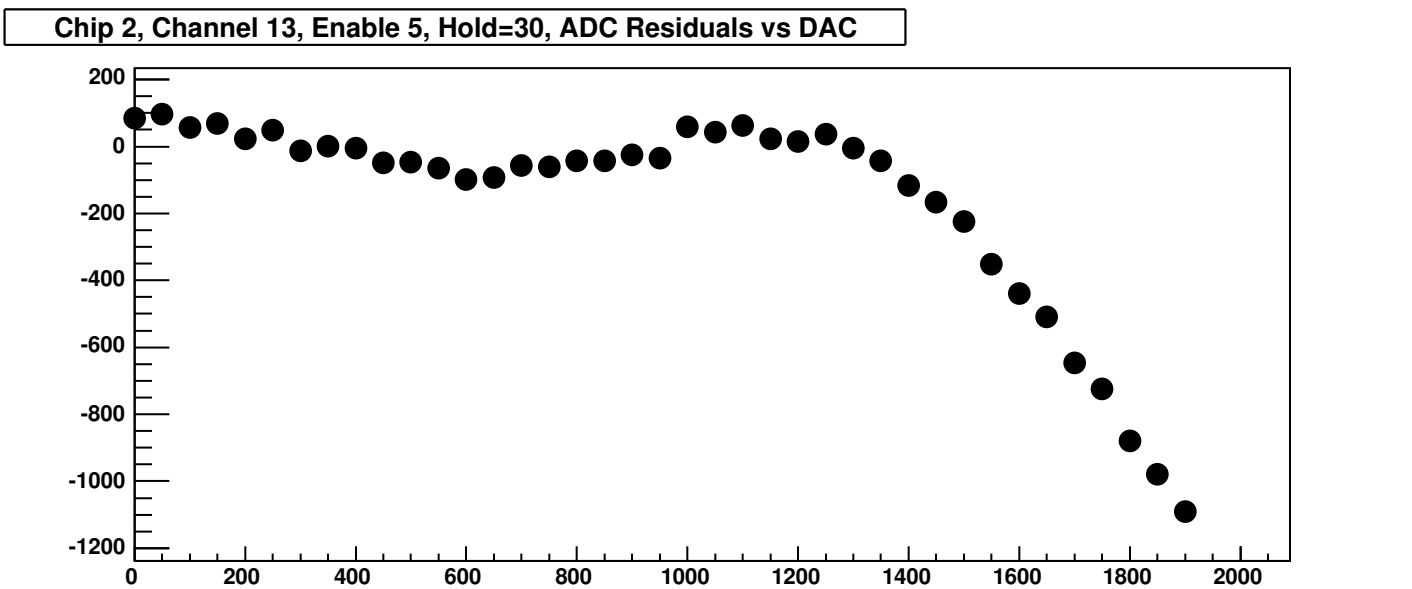


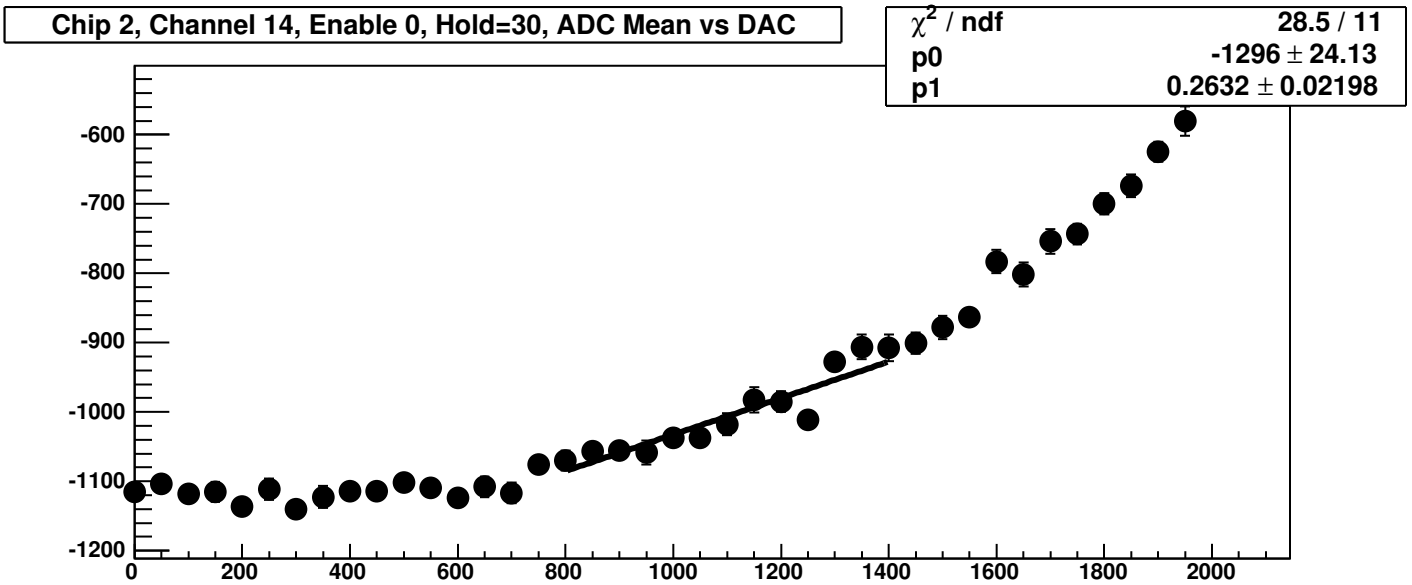
Chip 2, Channel 13, Enable 4!, Hold=30, ADC Residuals vs DAC

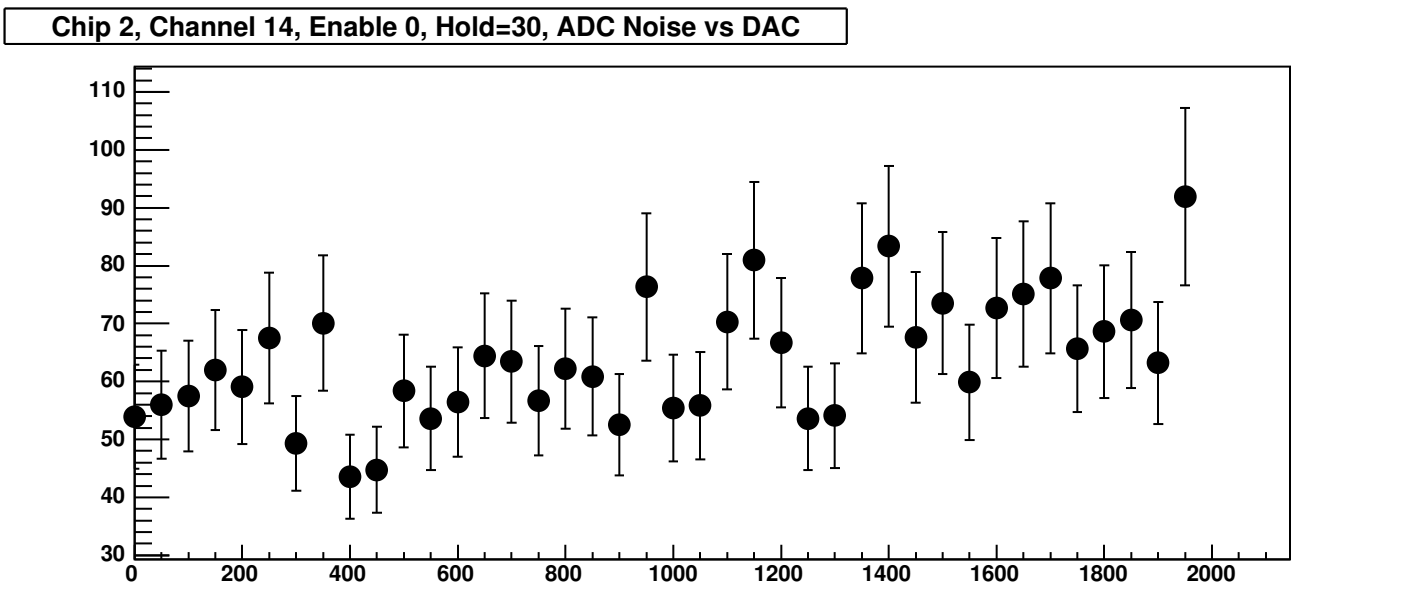


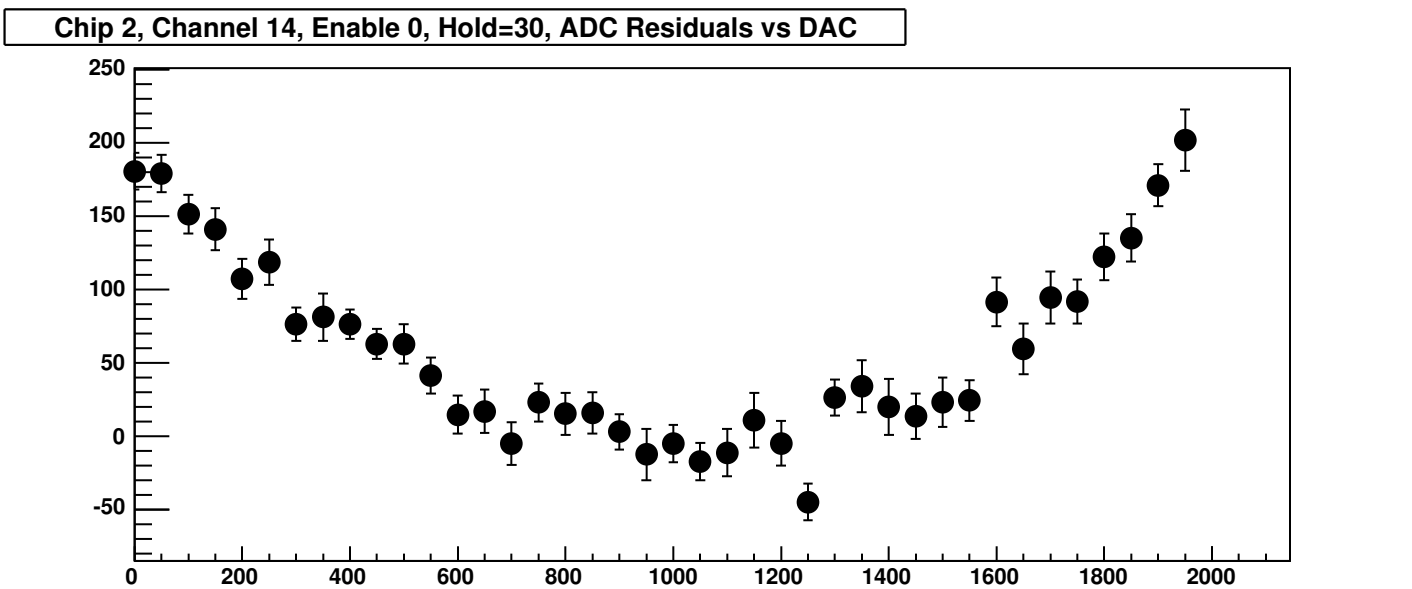

Chip 2, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 95.29 / 11
p0 -1172 ± 28.8
p1 0.2857 ± 0.02582
-800
-1000
-1200
-1400
-1600
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

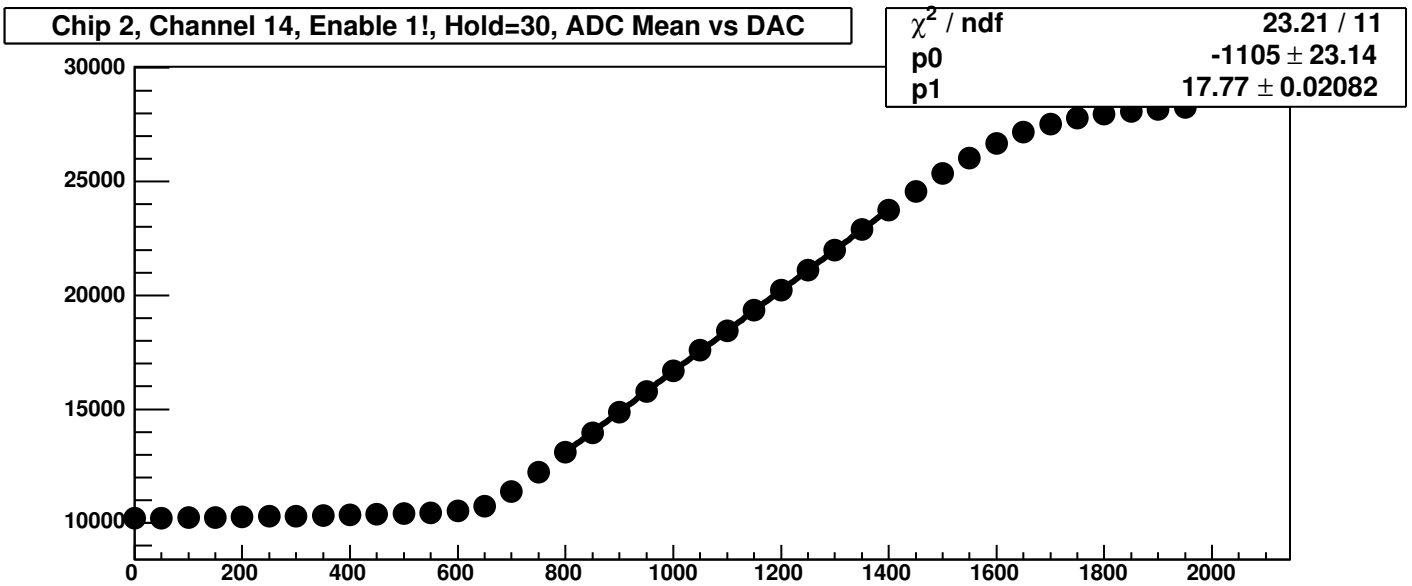

Chip 2, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

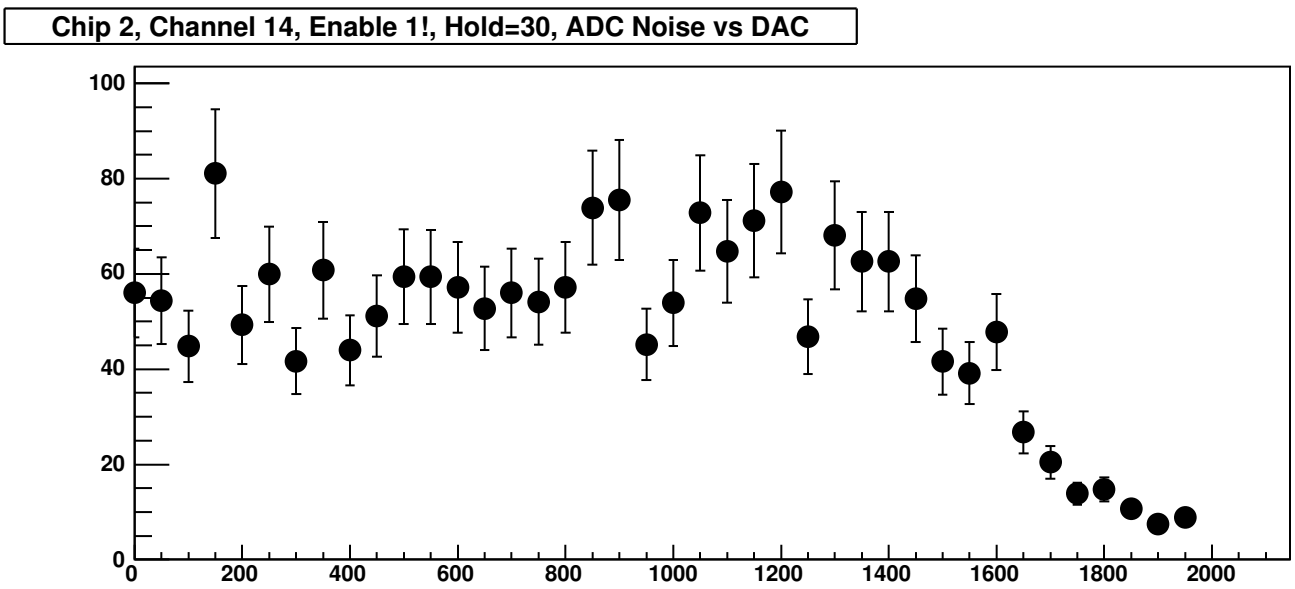

Chip 2, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

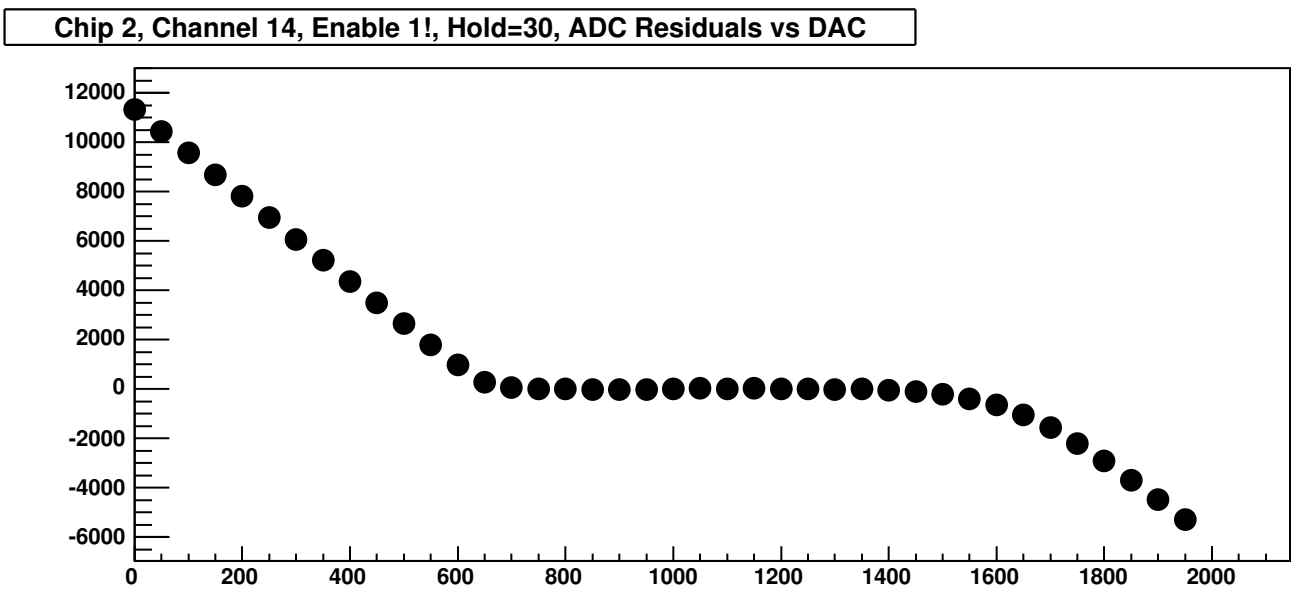

Chip 2, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 28.5 / 11
p0 -1296 ± 24.13
p1 0.2632 ± 0.02198
-600
-700
-800
-900
-1000
-1100
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

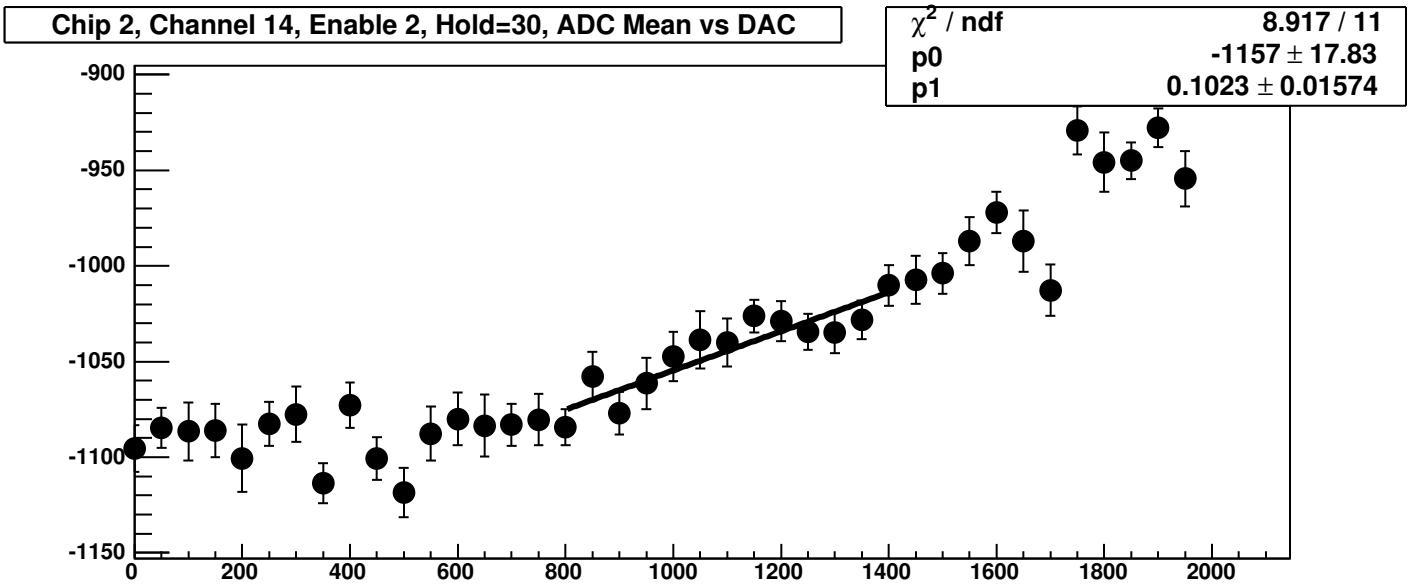

Chip 2, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

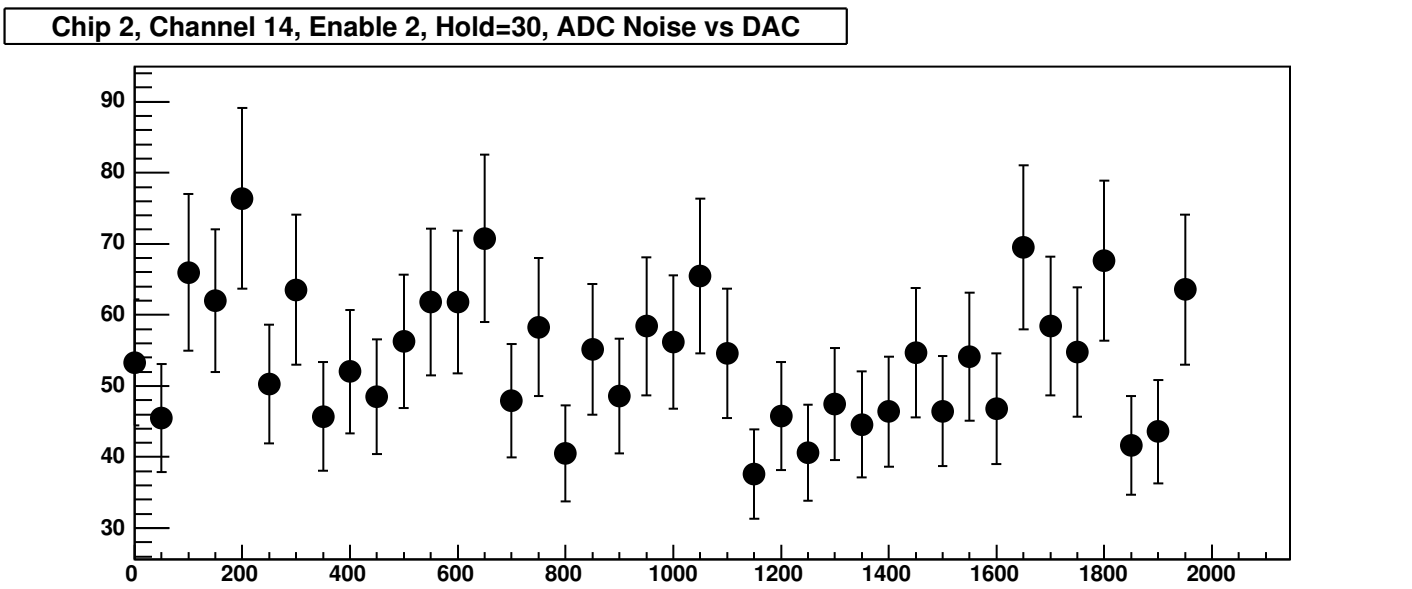

Chip 2, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

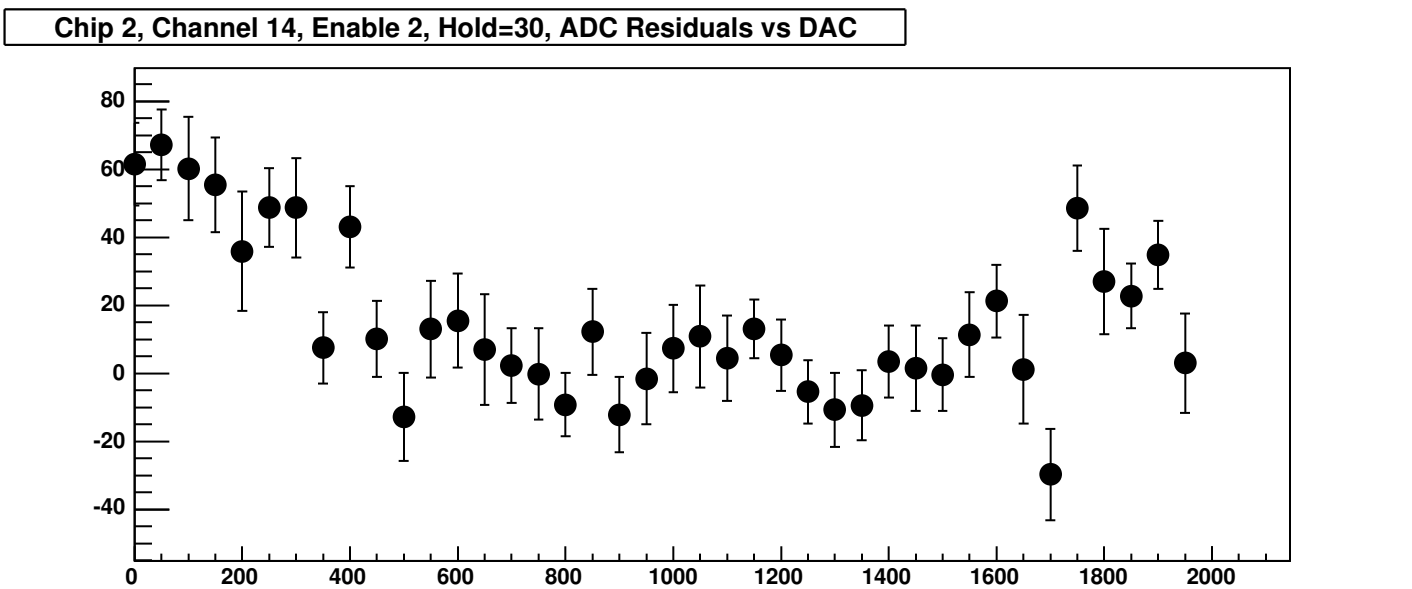

Chip 2, Channel 14, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 23.21 / 11
p0 -1105 ± 23.14
p1 17.77 ± 0.02082
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

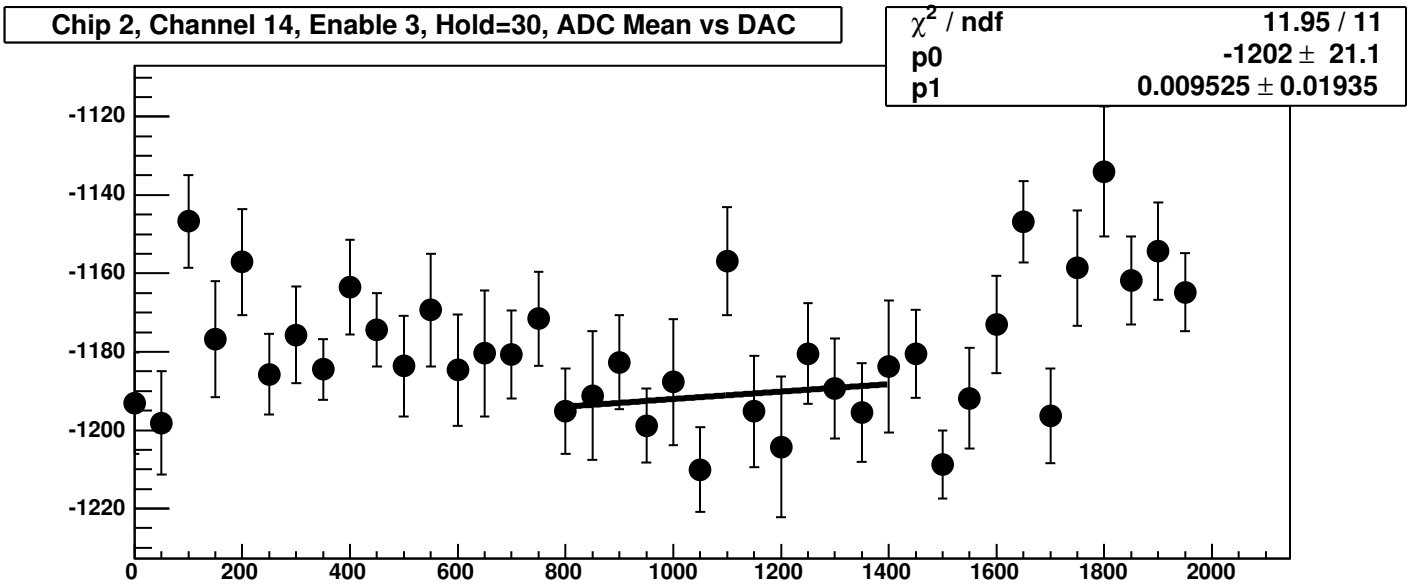

Chip 2, Channel 14, Enable 1!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

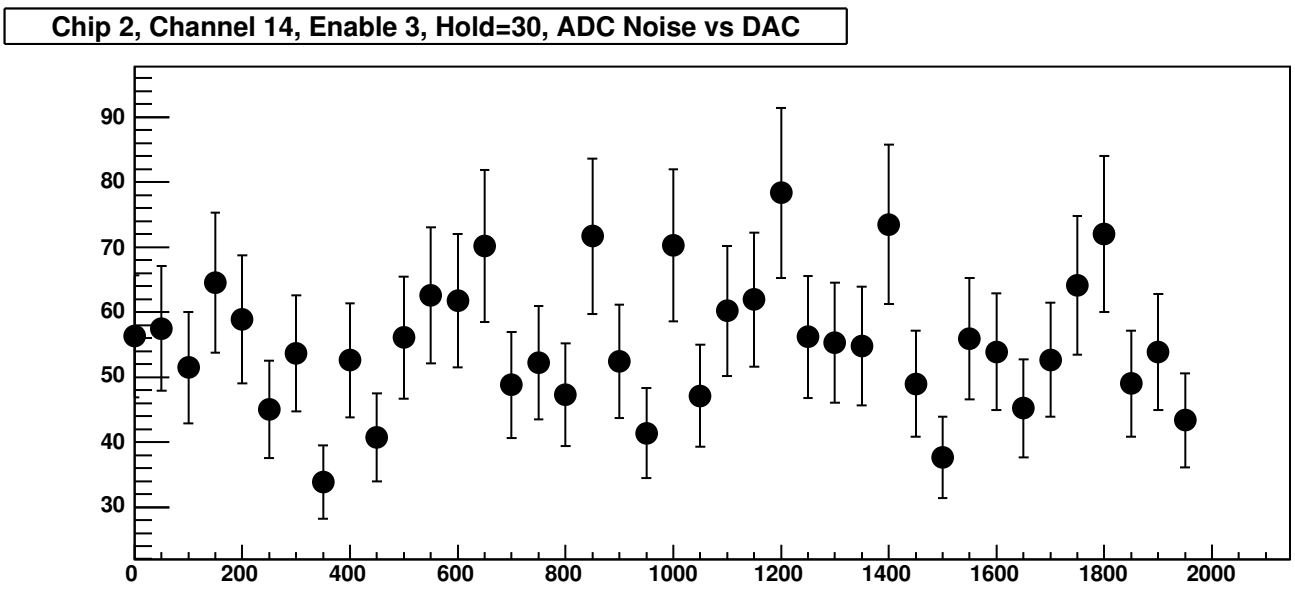

Chip 2, Channel 14, Enable 1!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

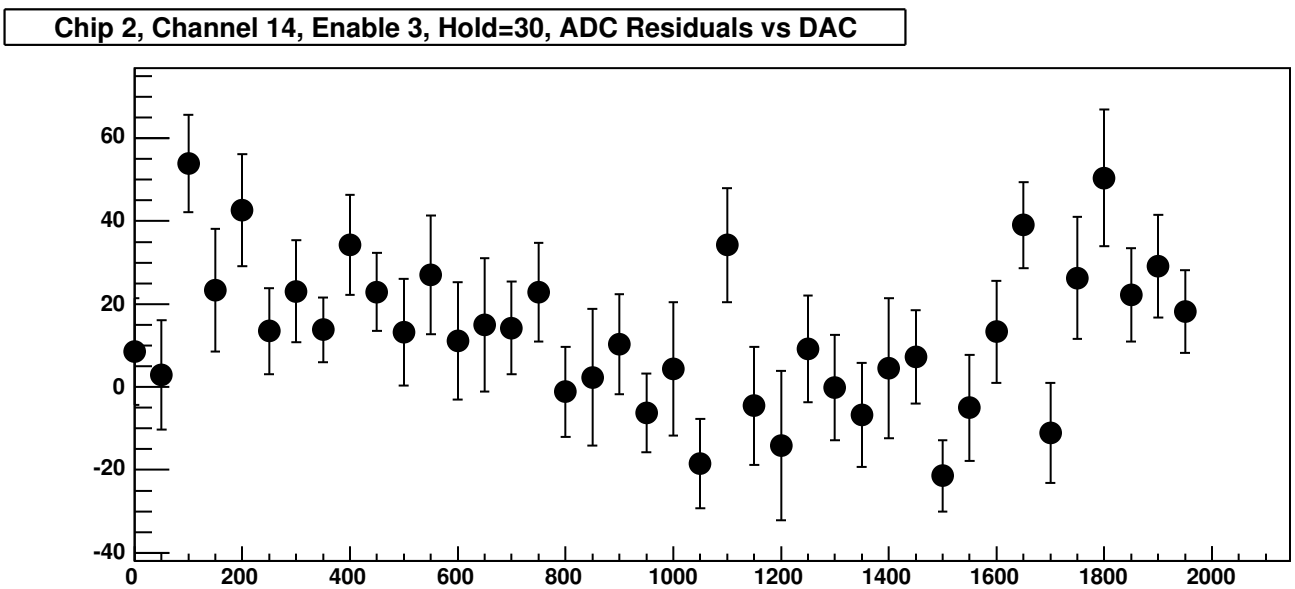

Chip 2, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 8.917 / 11
p0 -1157 ± 17.83
p1 0.1023 ± 0.01574
-900
-950
-1000
-1050
-1100
-1150
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

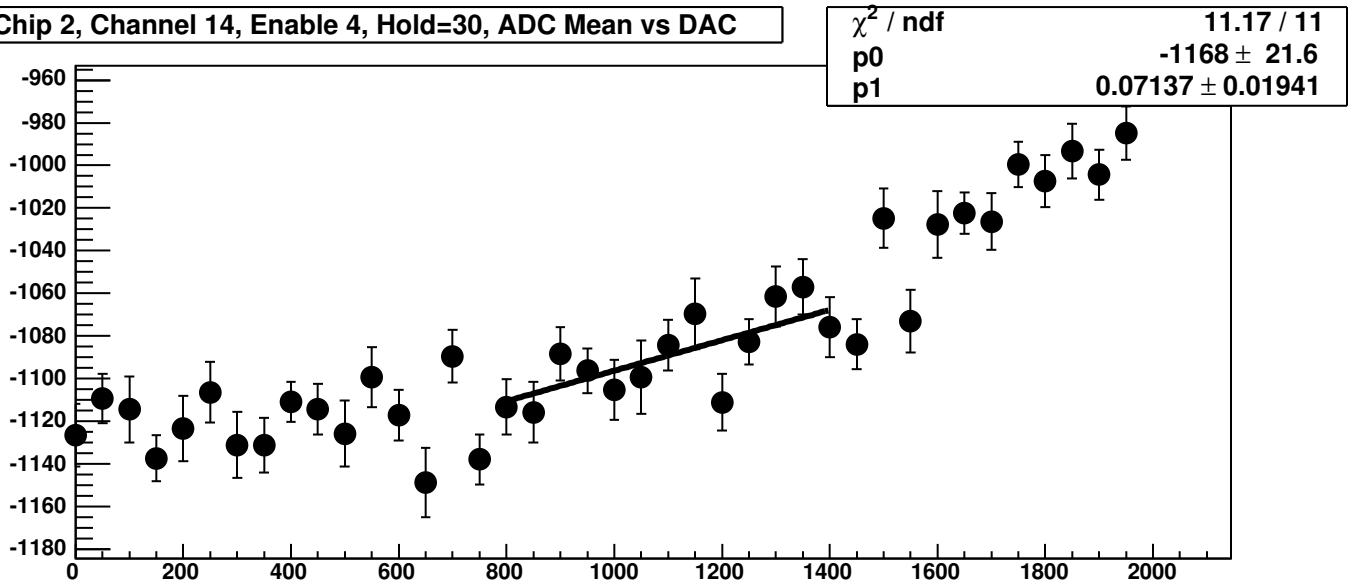

Chip 2, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 11.95 / 11
p0 -1202 ± 21.1
p1 0.009525 ± 0.01935
-1120
-1140
-1160
-1180
-1200
-1220
0
200
400
600
800
1000
1200
1400
1600
1800
2000

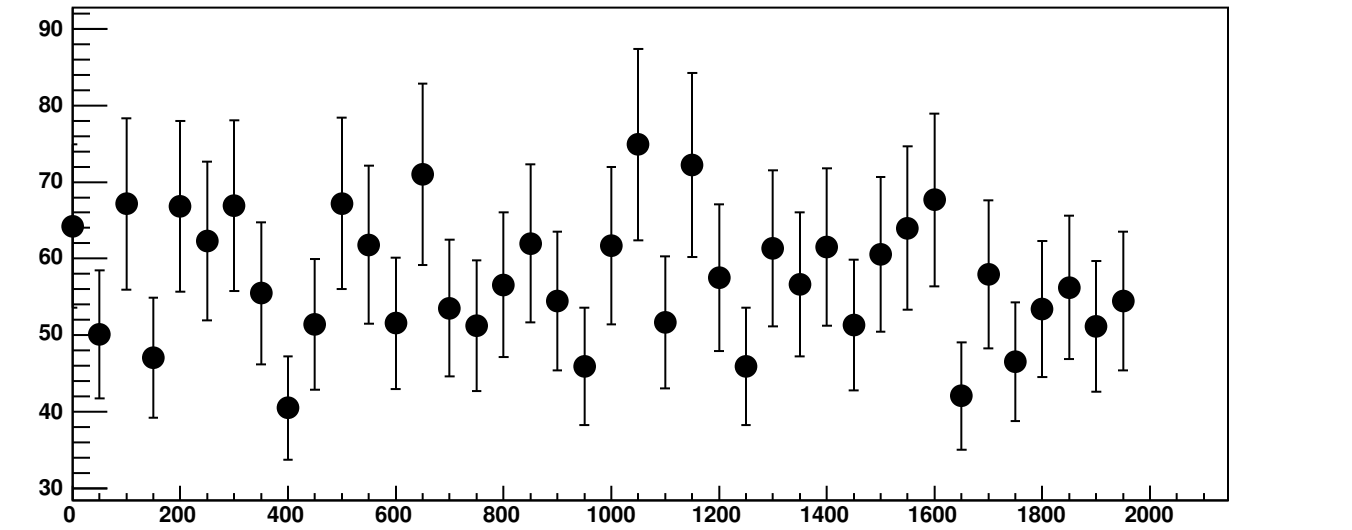

Chip 2, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

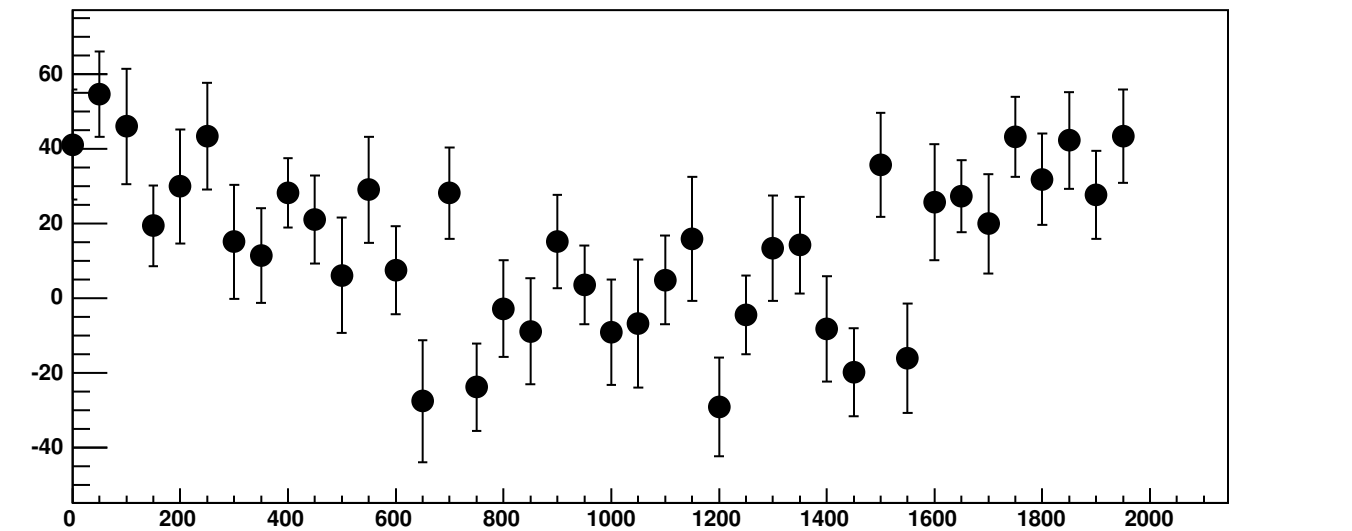
Chip 2, Channel 14, Enable 4, Hold=30, ADC Mean vs DAC

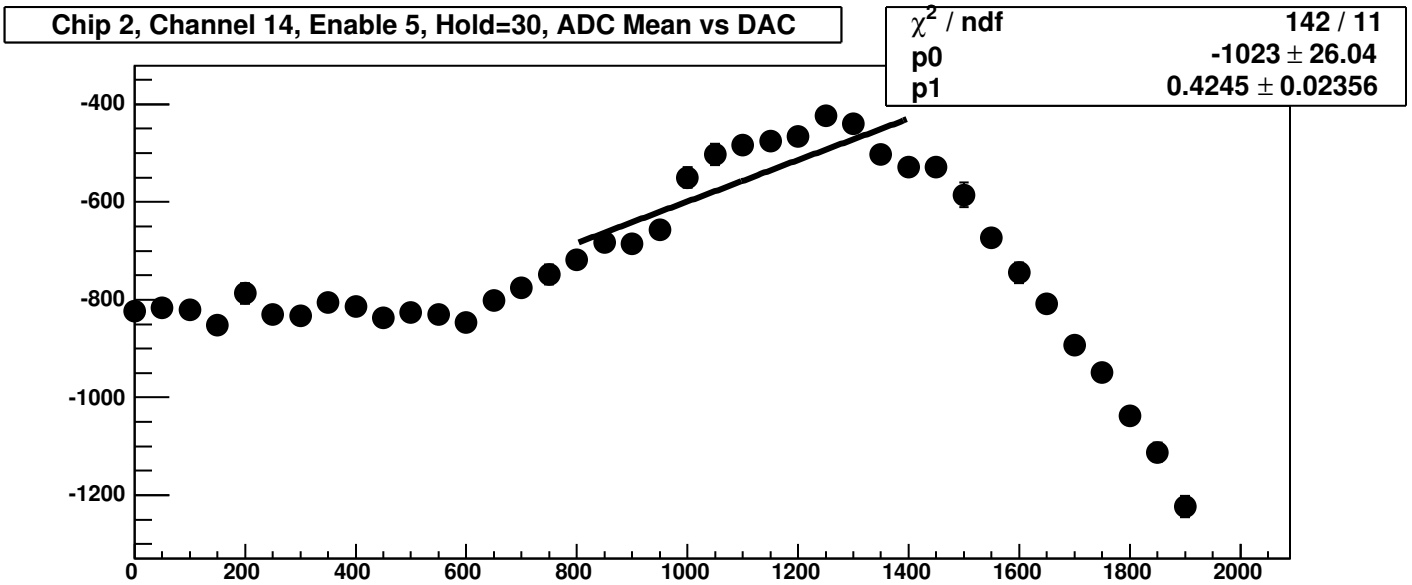


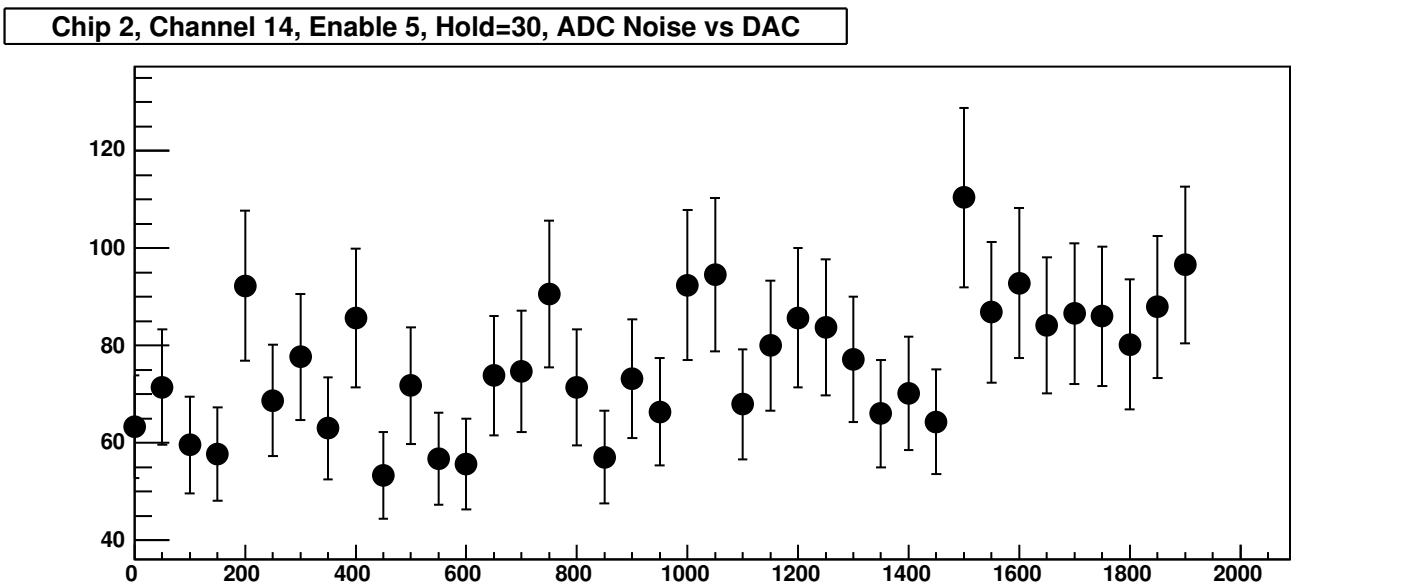
Chip 2, Channel 14, Enable 4, Hold=30, ADC Noise vs DAC

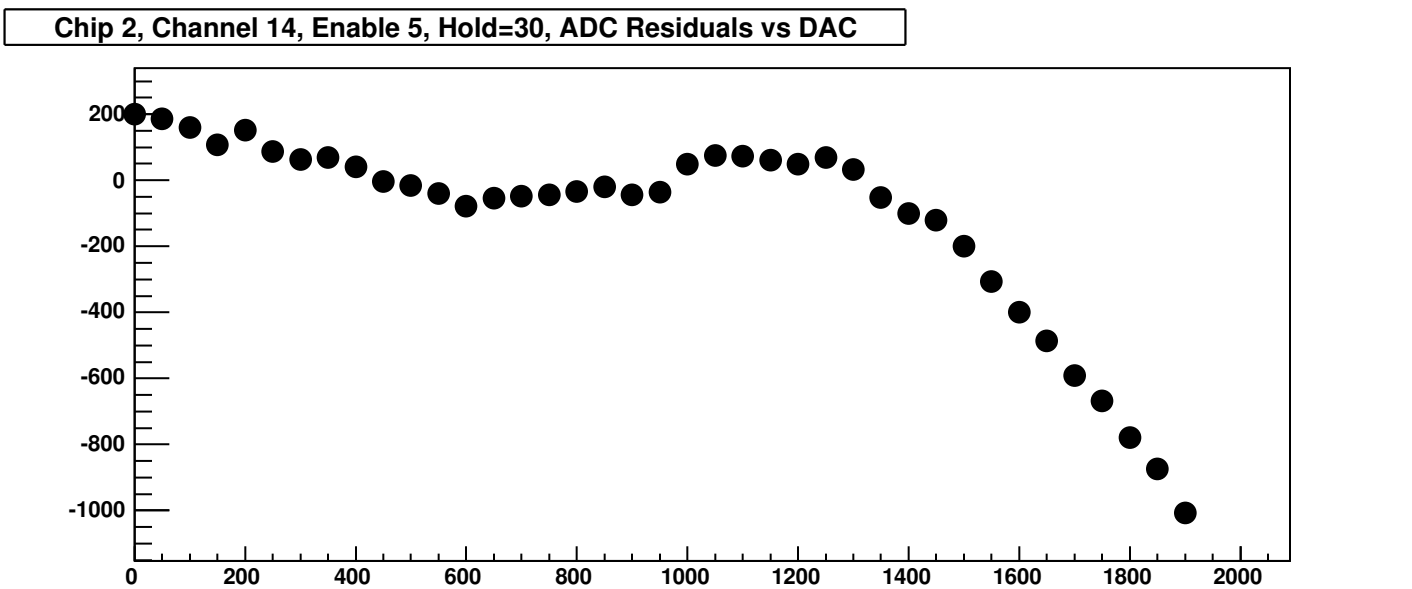


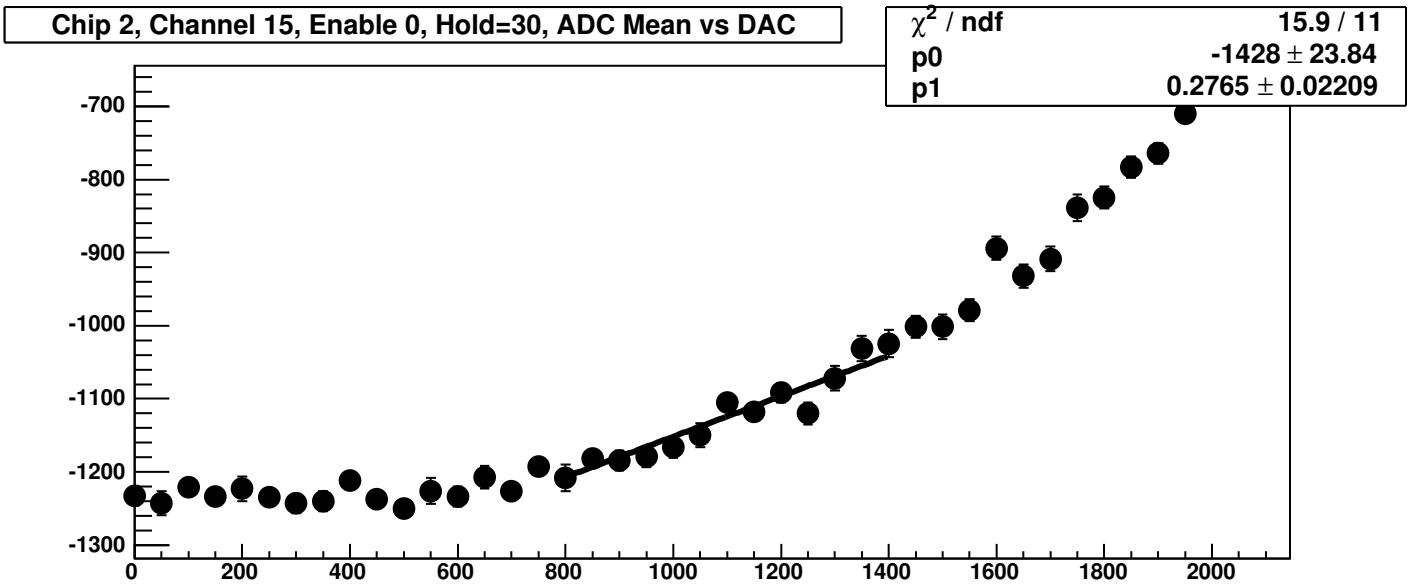
Chip 2, Channel 14, Enable 4, Hold=30, ADC Residuals vs DAC

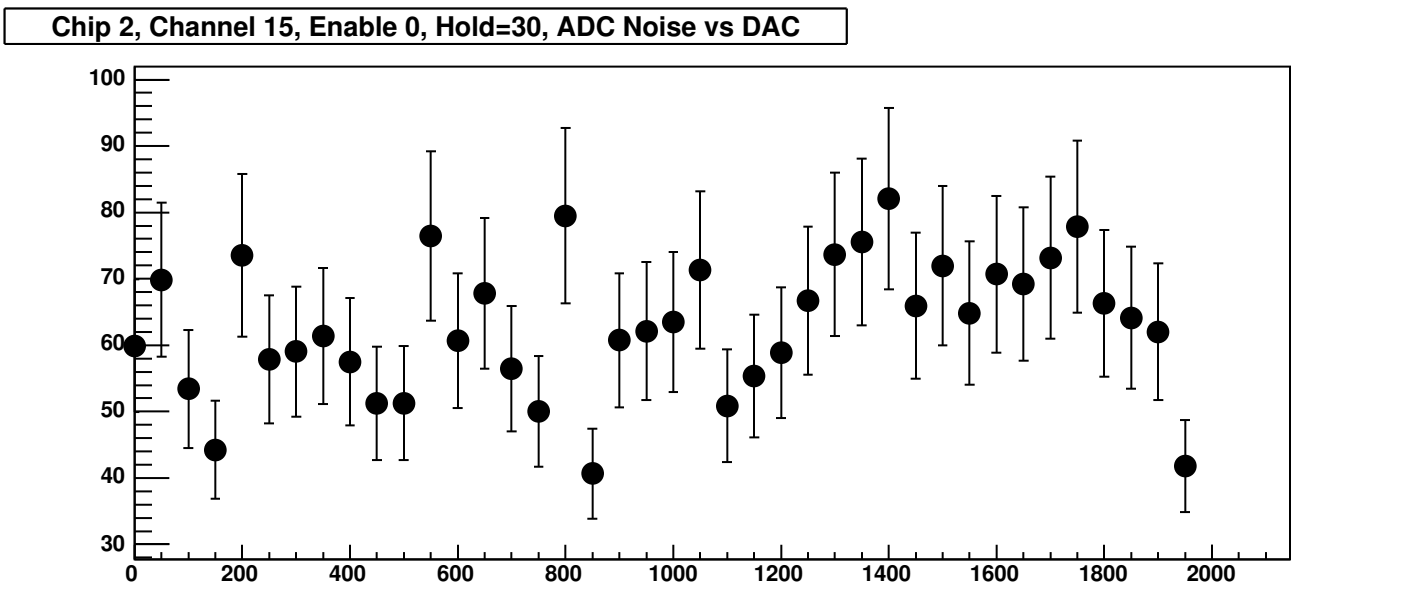


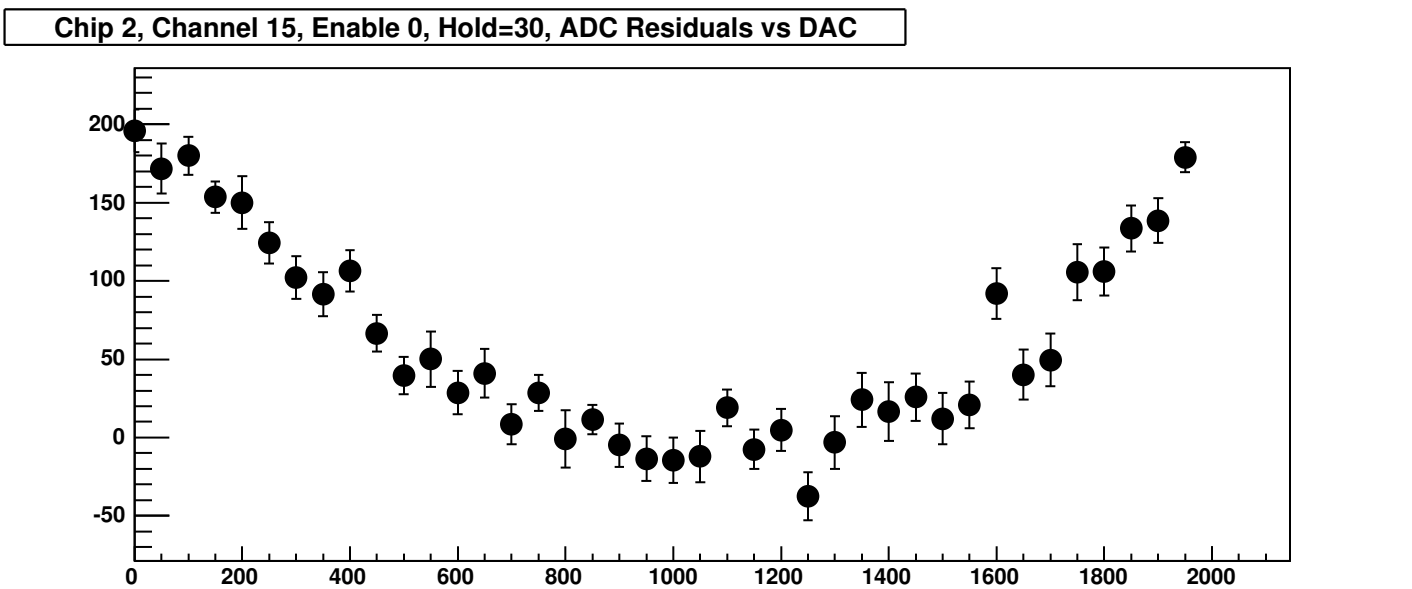

Chip 2, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 142 / 11
p0 -1023 ± 26.04
p1 0.4245 ± 0.02356
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

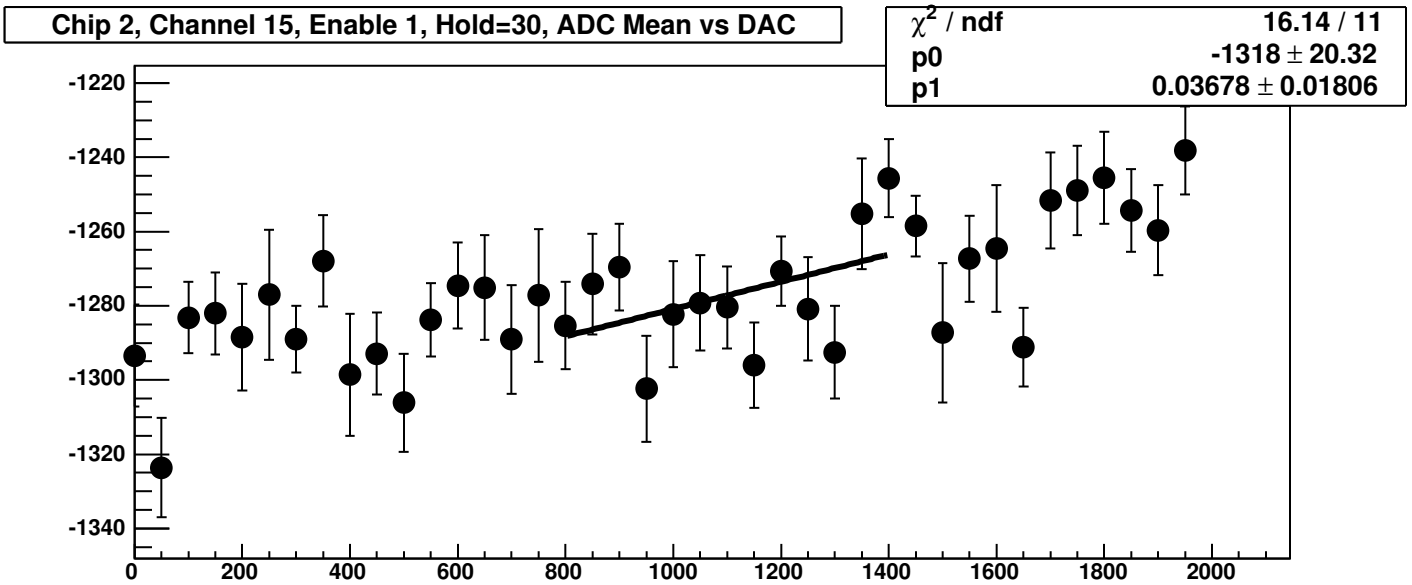

Chip 2, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

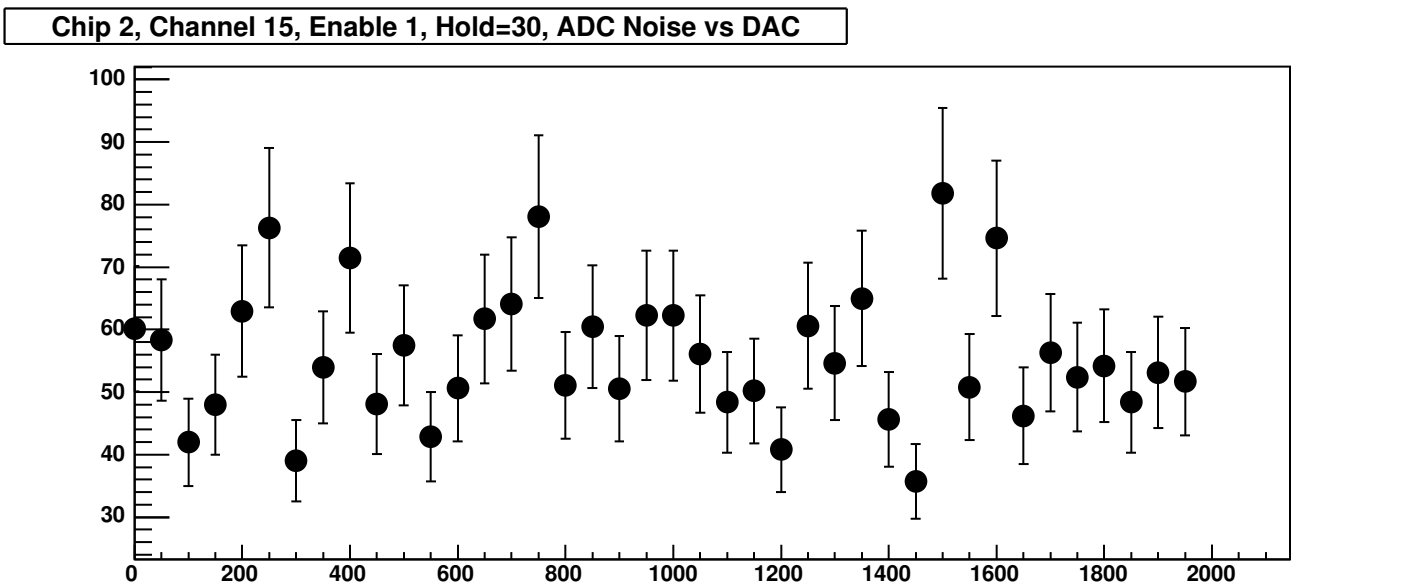

Chip 2, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

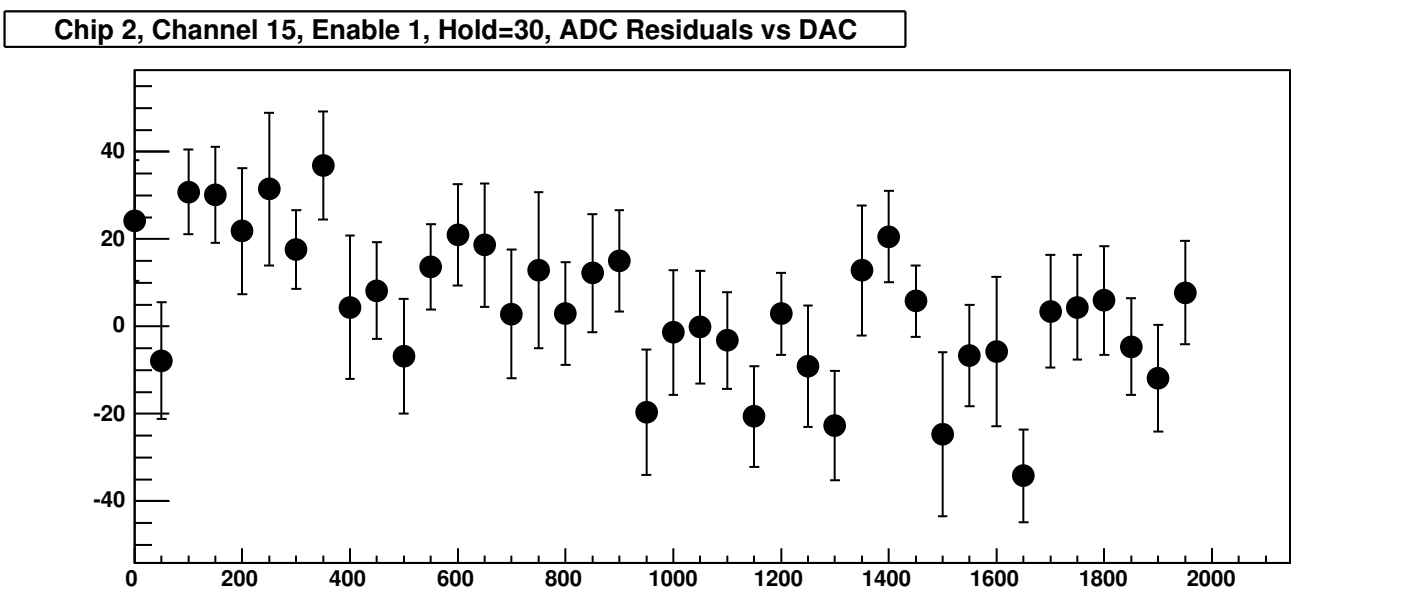

Chip 2, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 15.9 / 11
p0 -1428 ± 23.84
p1 0.2765 ± 0.02209
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

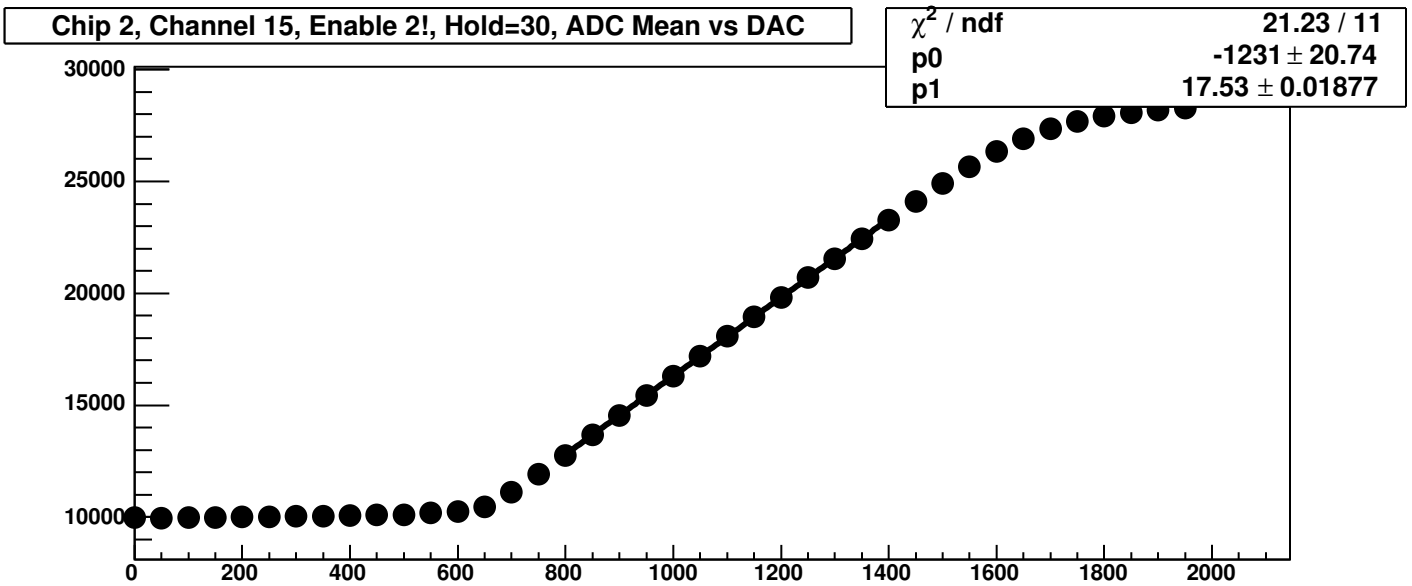

Chip 2, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

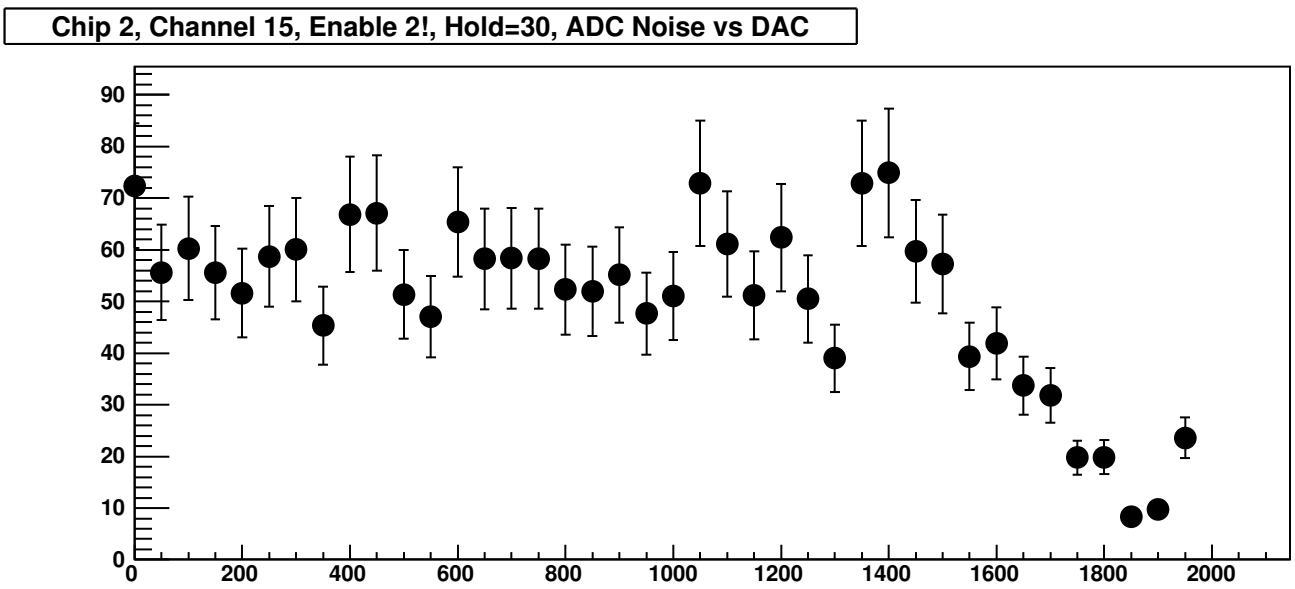

Chip 2, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

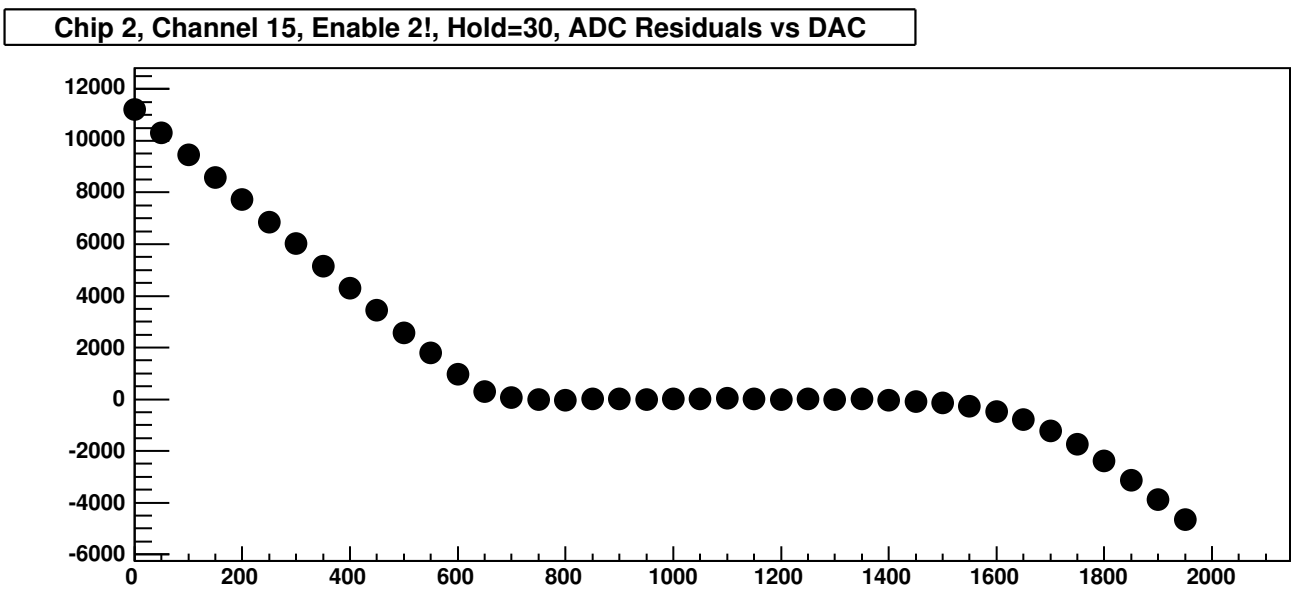

Chip 2, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 16.14 / 11
p0 -1318 ± 20.32
p1 0.03678 ± 0.01806
-1220
-1240
-1260
-1280
-1300
-1320
-1340
0
200
400
600
800
1000
1200
1400
1600
1800
2000

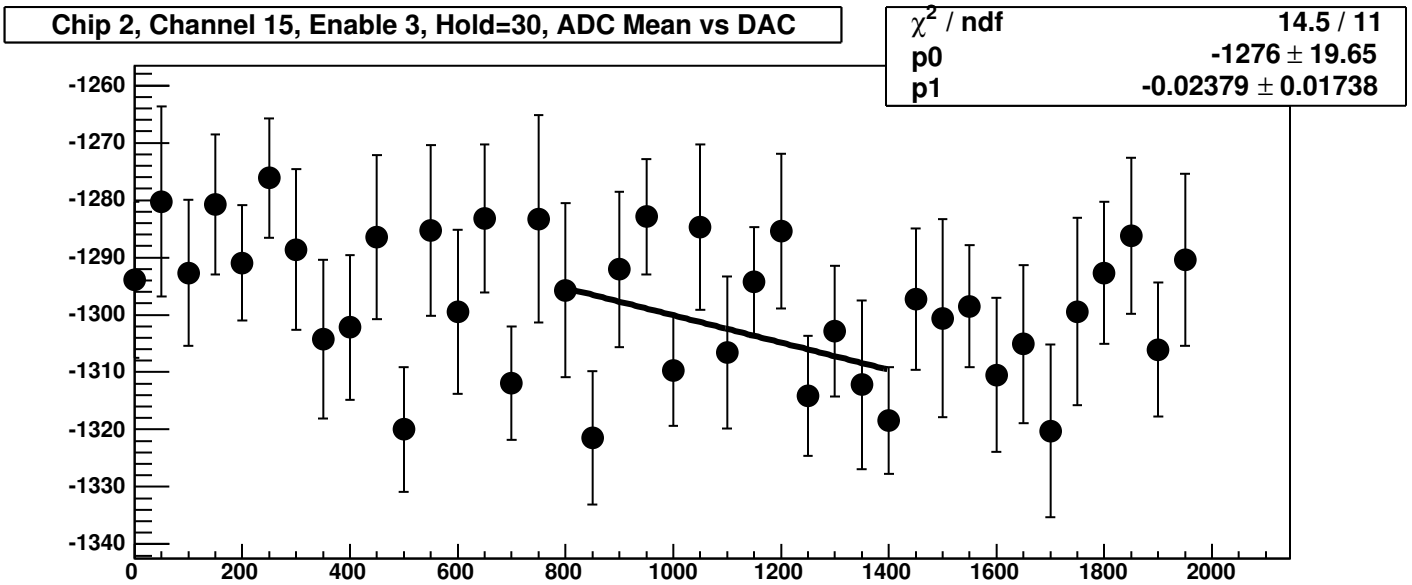

Chip 2, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

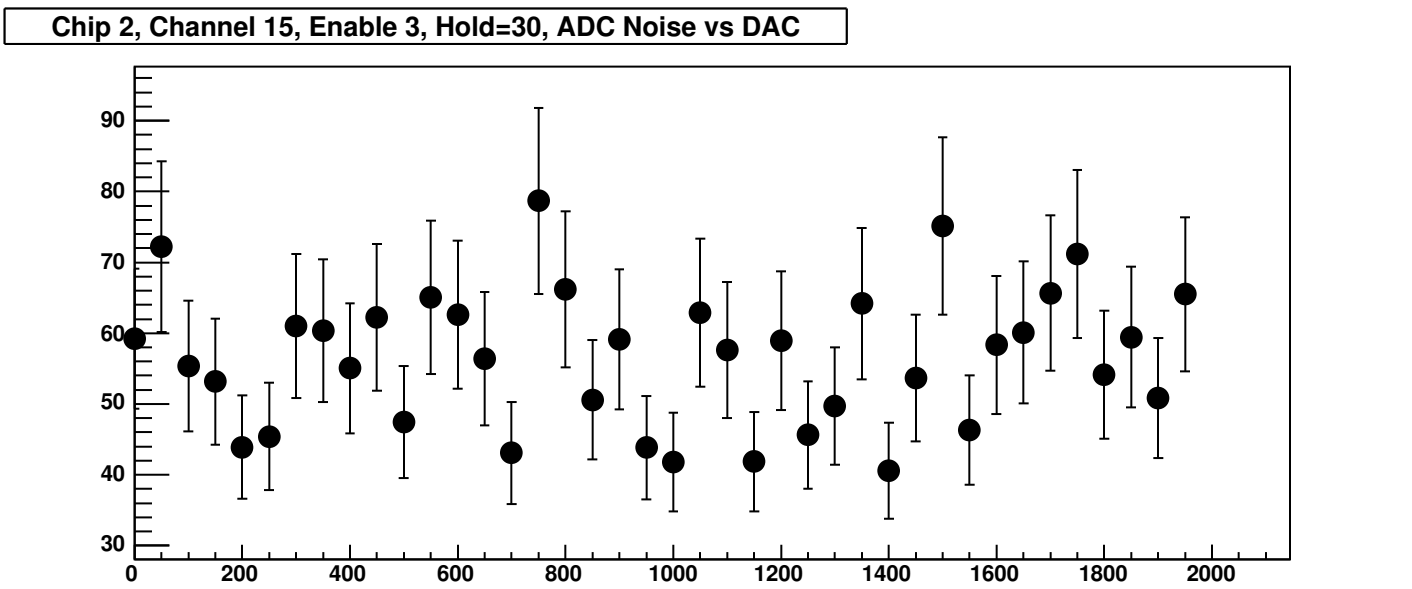

Chip 2, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

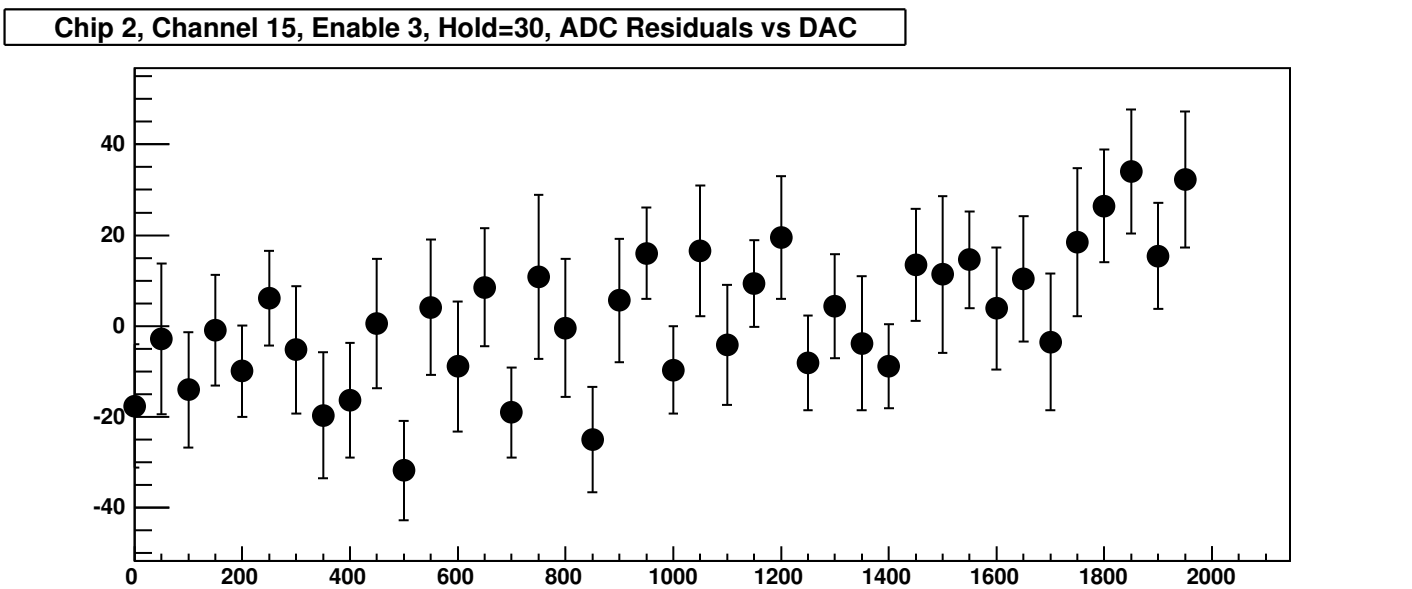

Chip 2, Channel 15, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 21.23 / 11
p0 -1231 ± 20.74
p1 17.53 ± 0.01877
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 15, Enable 2!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

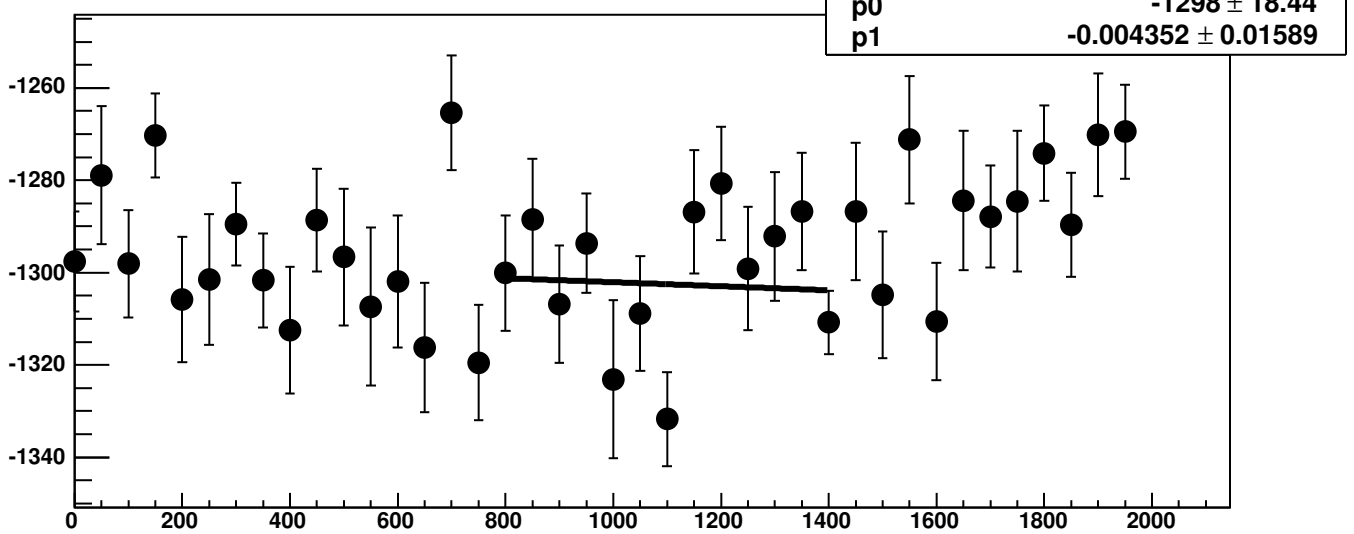

Chip 2, Channel 15, Enable 2!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 14.5 / 11
p0 -1276 ± 19.65
p1 -0.02379 ± 0.01738
-1260
-1270
-1280
-1290
-1300
-1310
-1320
-1330
-1340
0
200
400
600
800
1000
1200
1400
1600
1800
2000

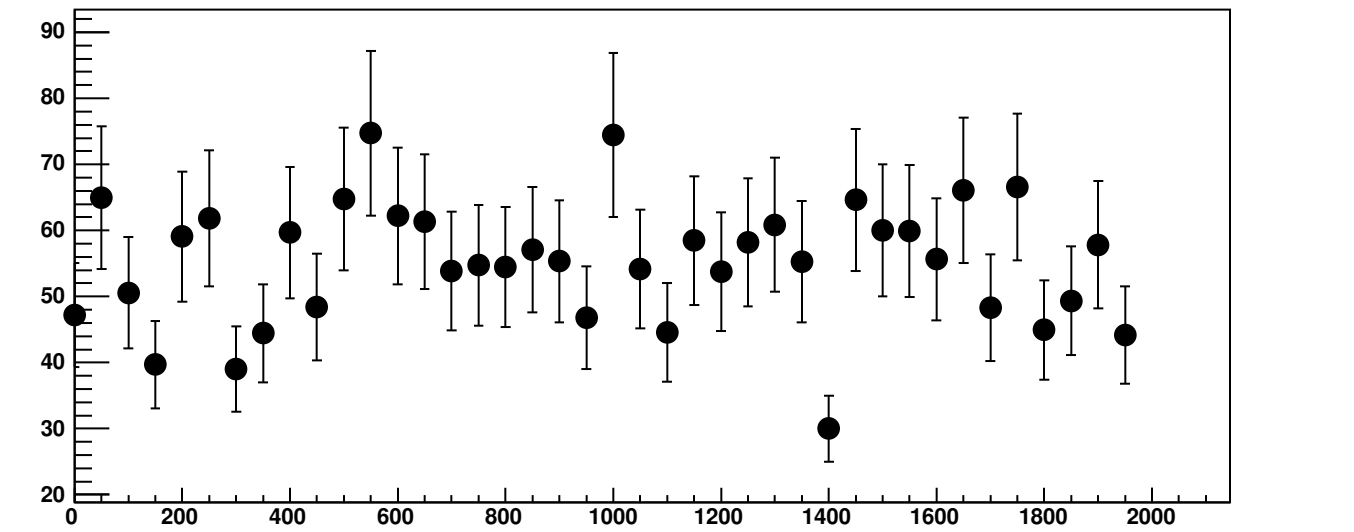

Chip 2, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

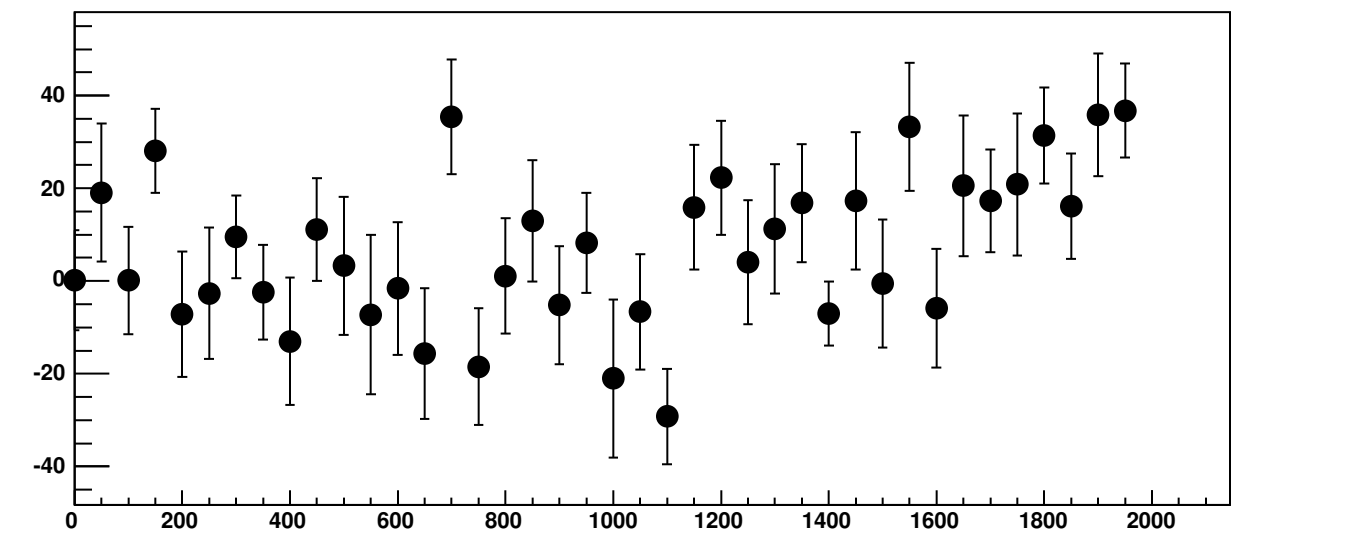
Chip 2, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC

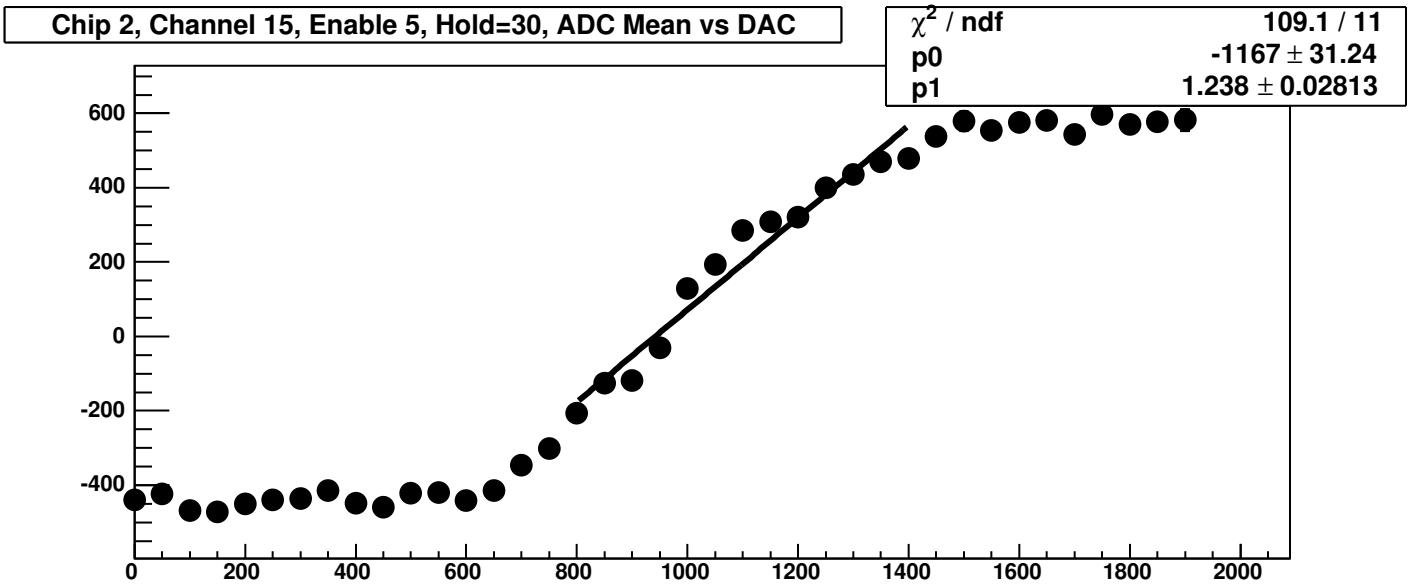


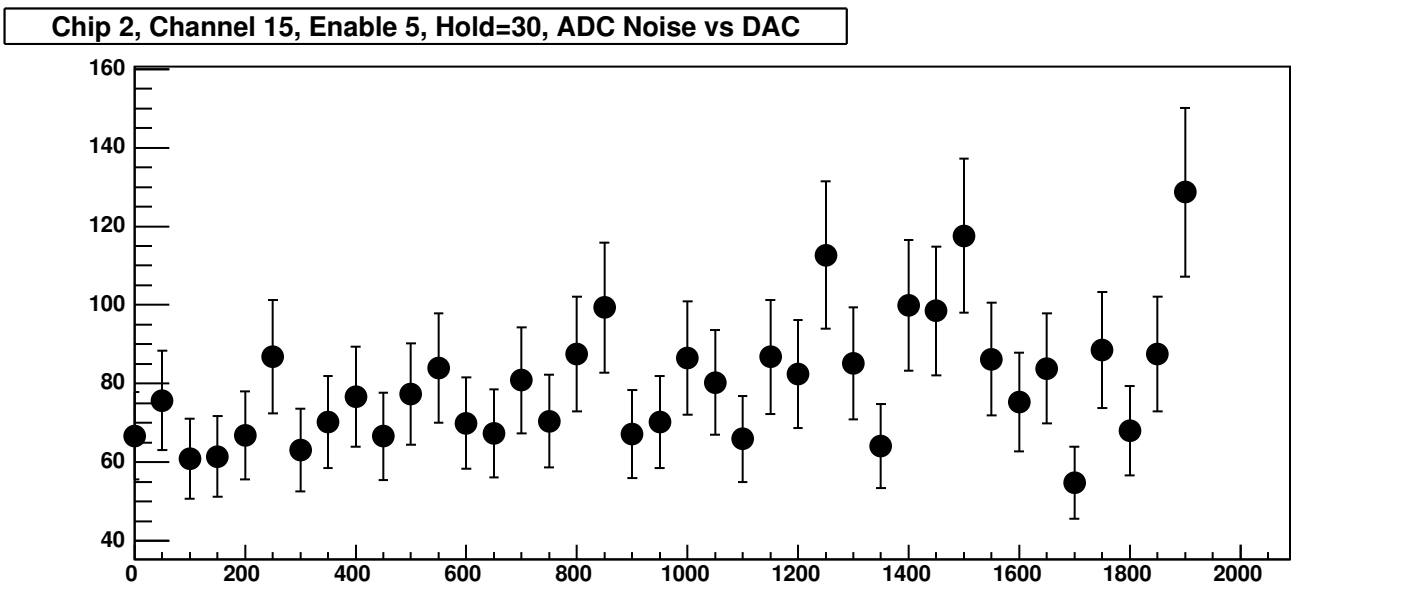
Chip 2, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC

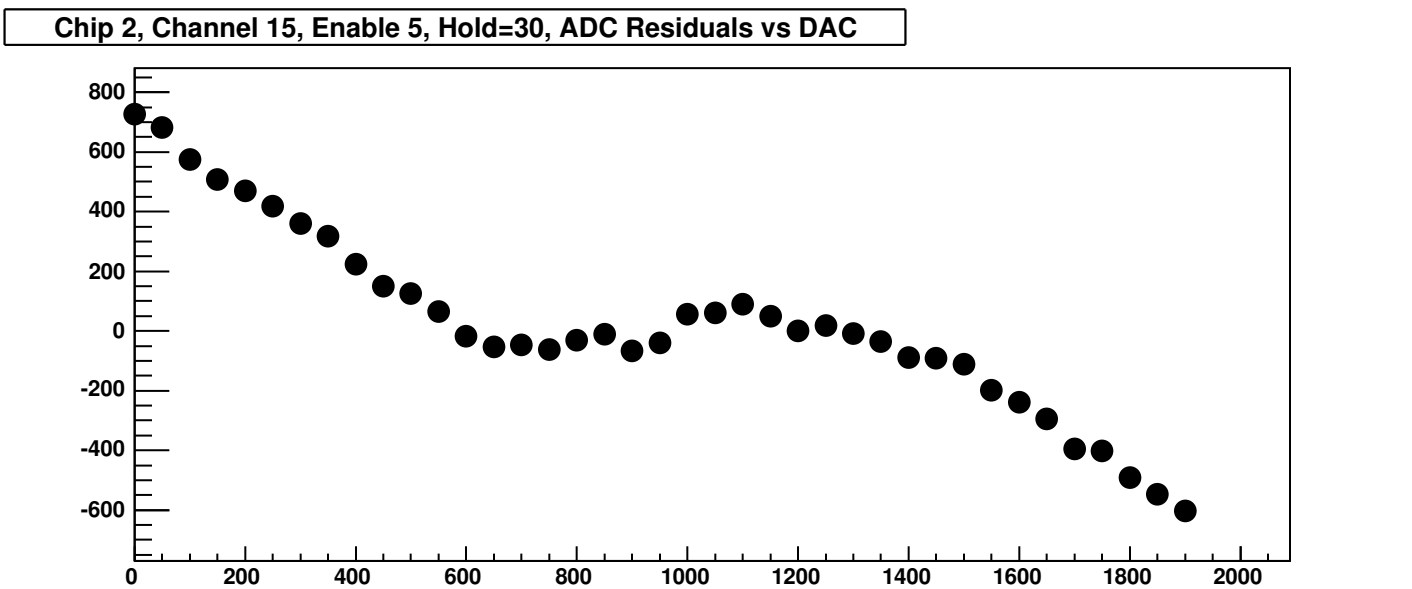


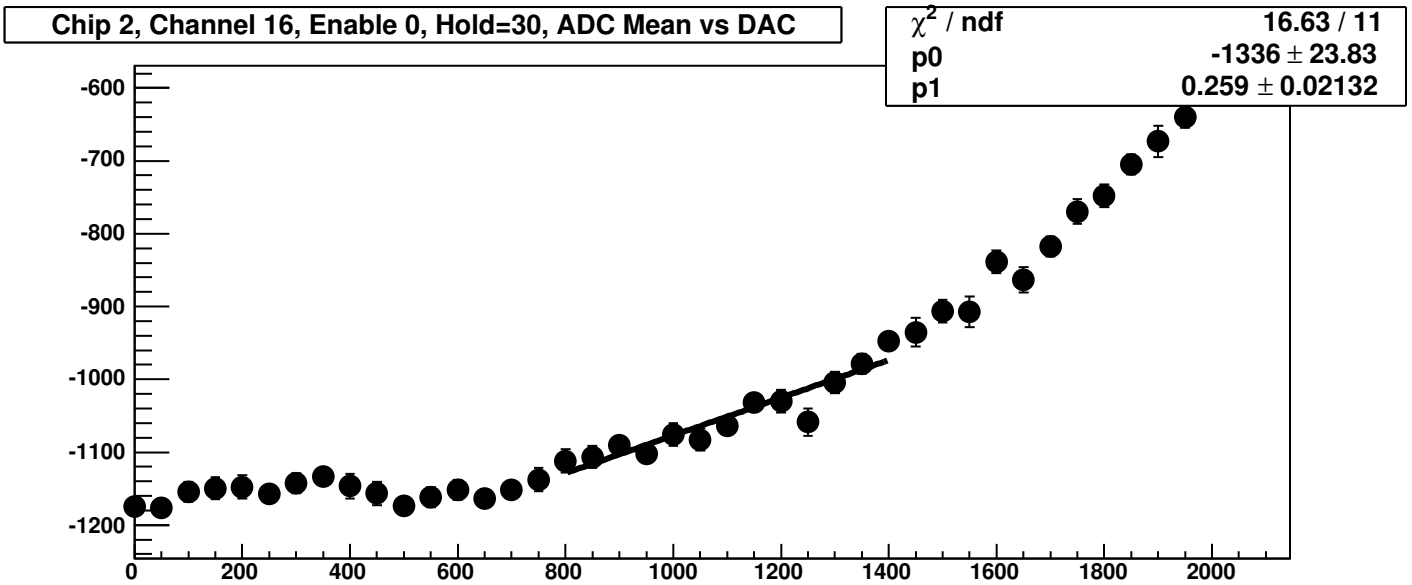
Chip 2, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC

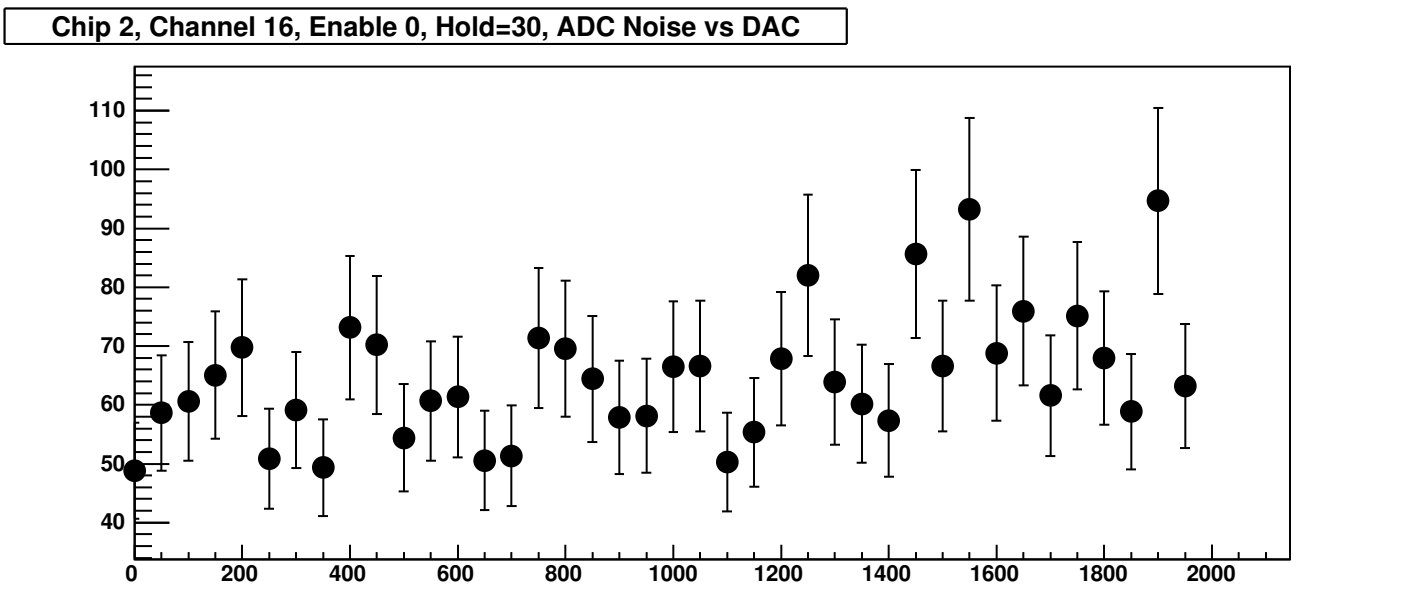


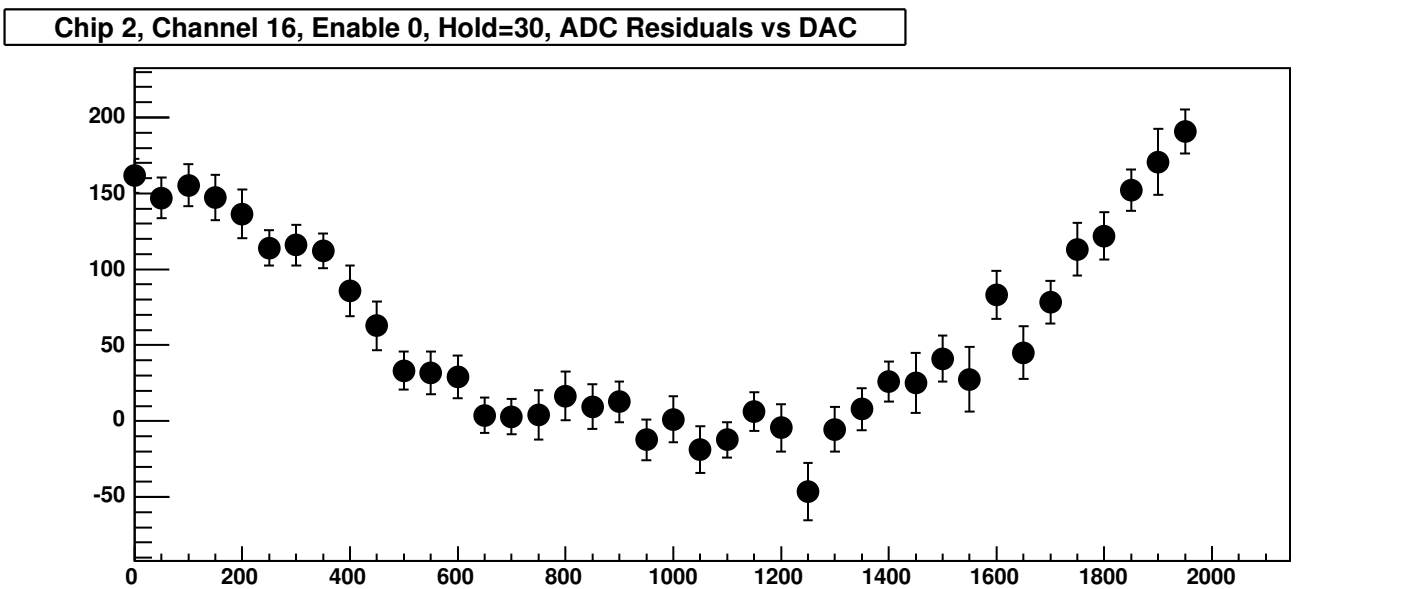

Chip 2, Channel 15, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 109.1 / 11
p0 -1167 ± 31.24
p1 1.238 ± 0.02813
600
400
200
0
-200
-400
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 15, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

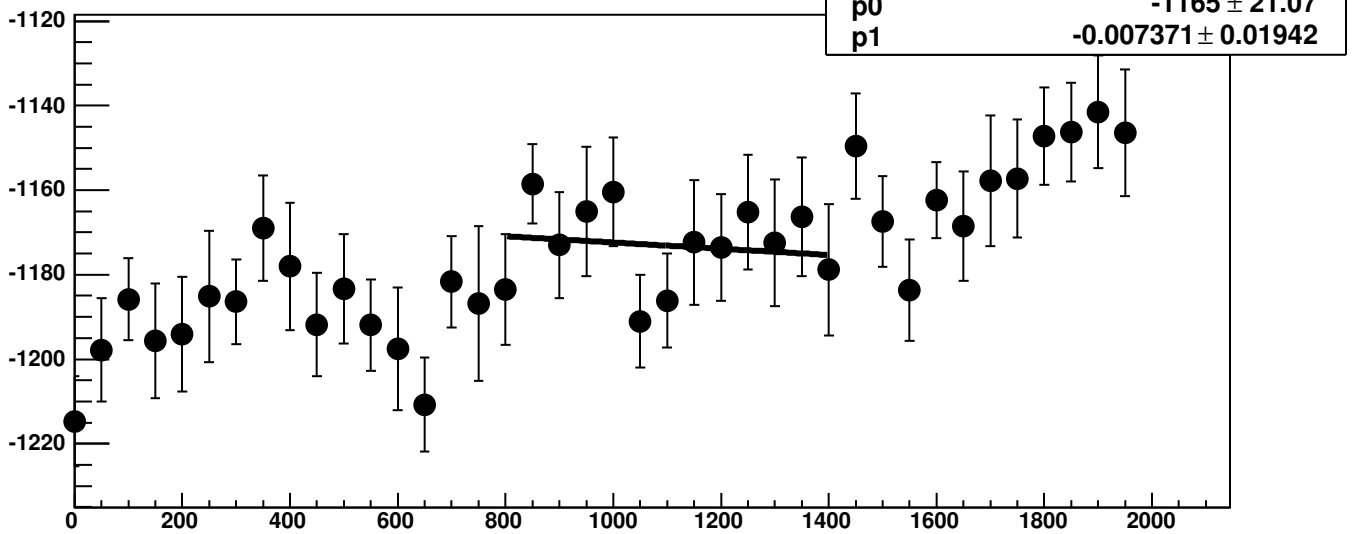

Chip 2, Channel 15, Enable 5, Hold=30, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 16.63 / 11
p0 -1336 ± 23.83
p1 0.259 ± 0.02132
-600
-700
-800
-900
-1000
-1100
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

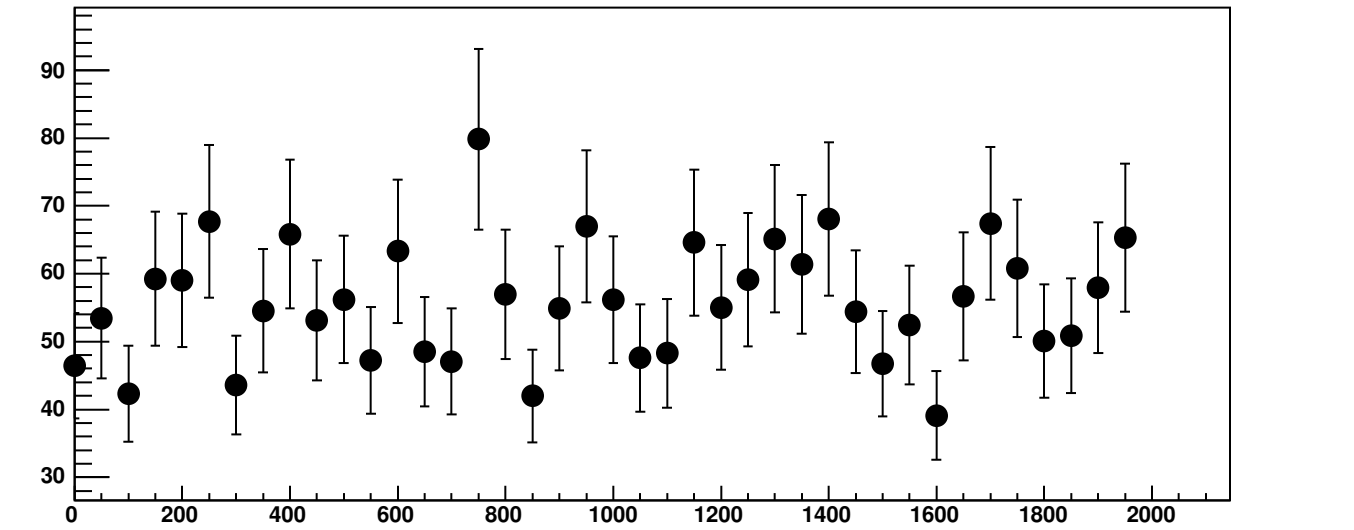

Chip 2, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

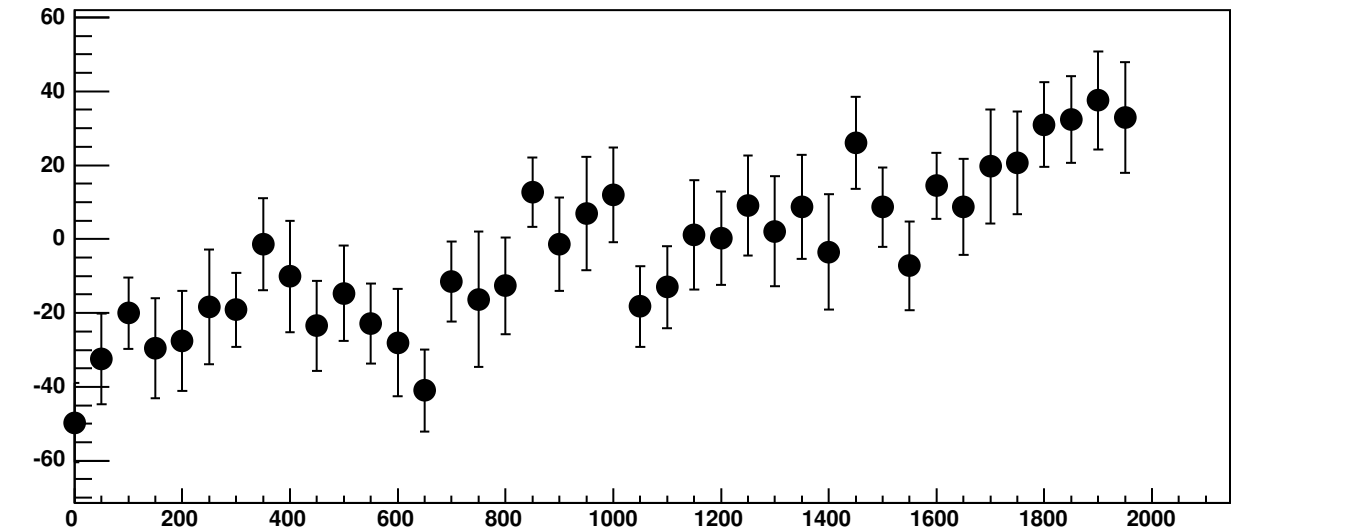
Chip 2, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC

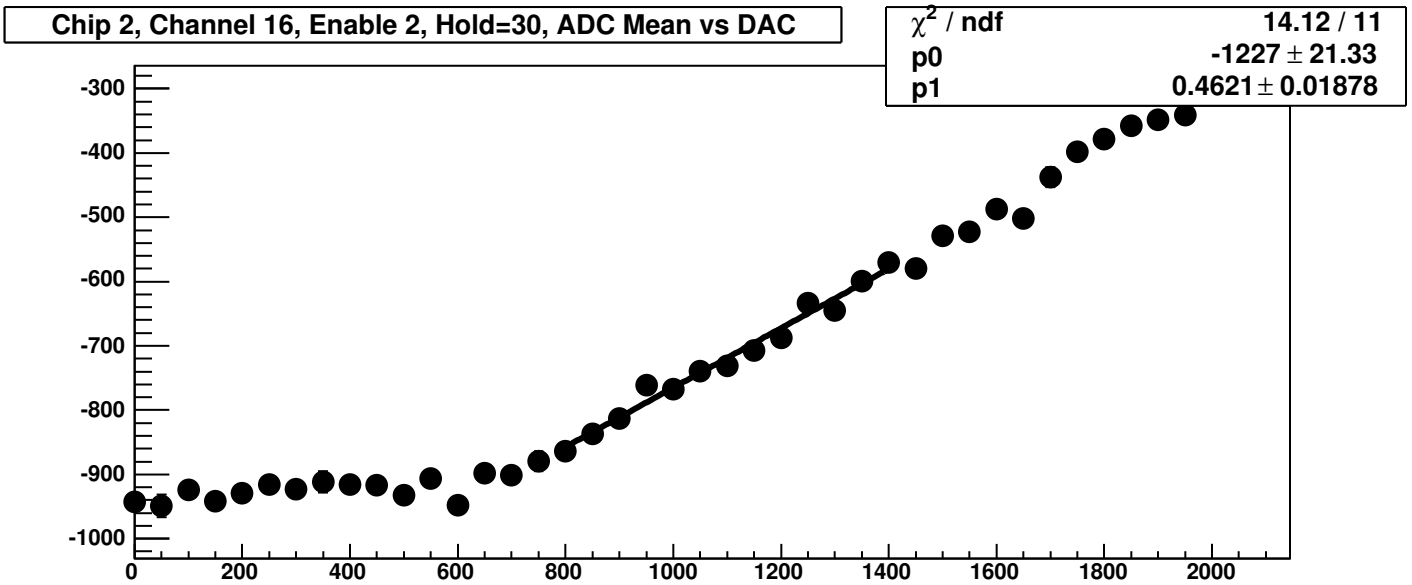


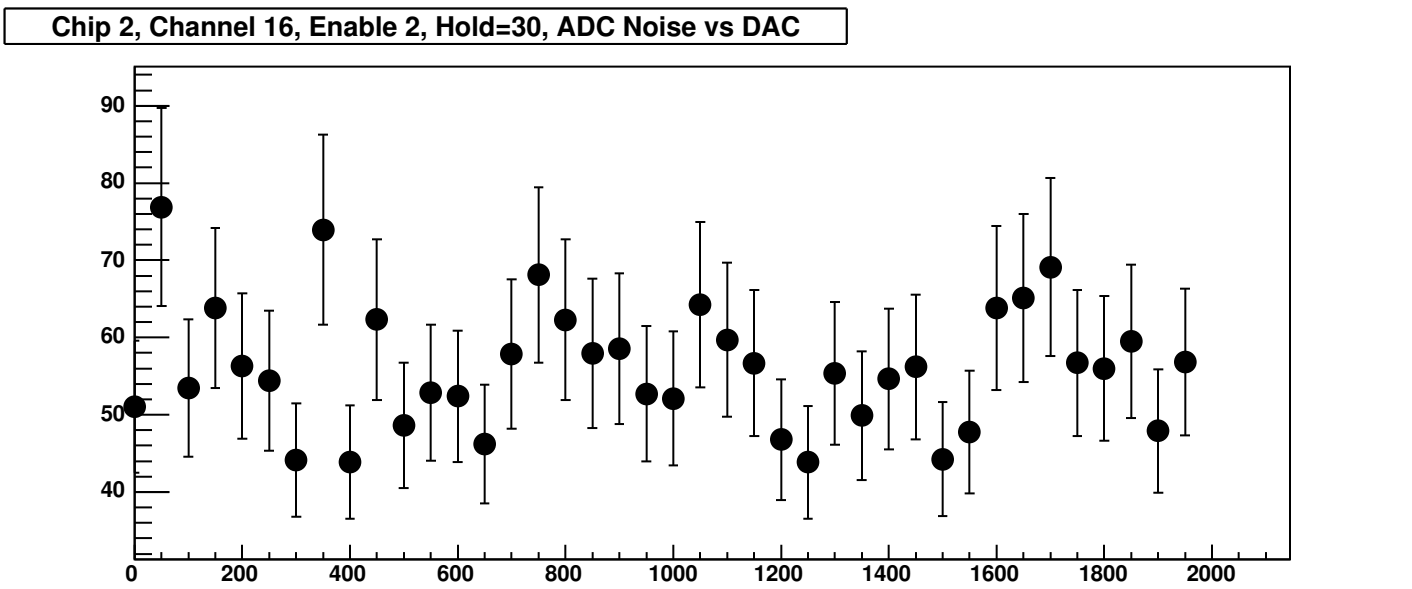
Chip 2, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC

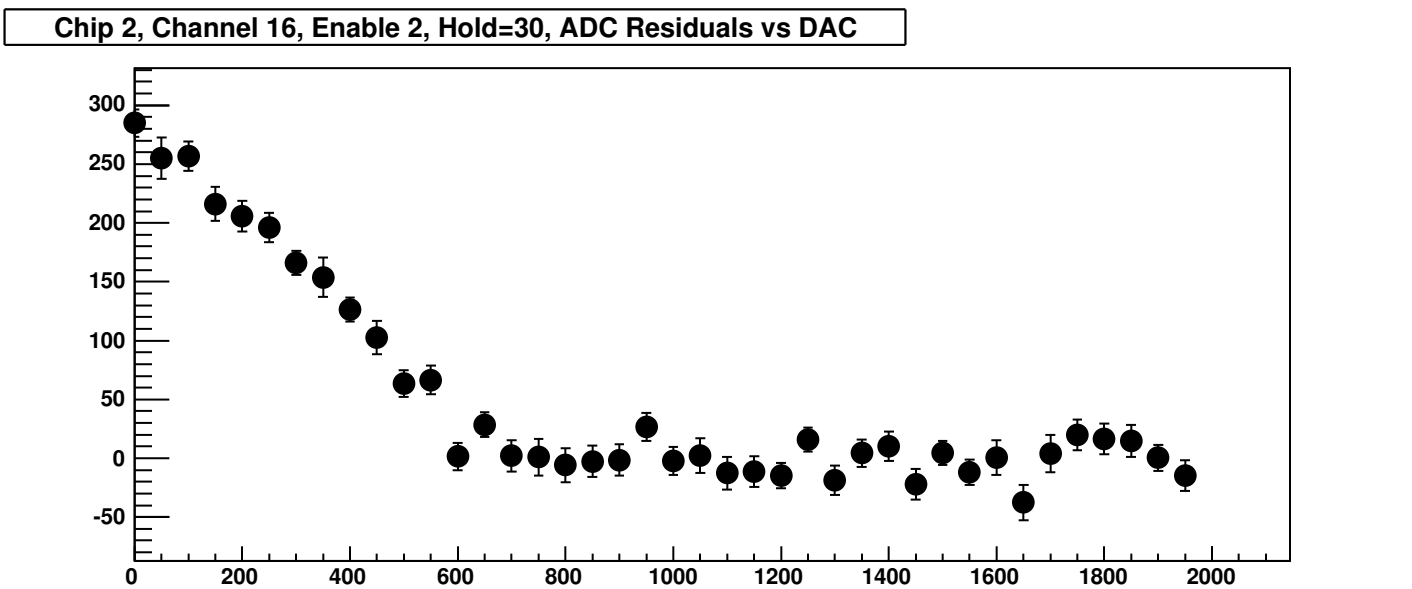


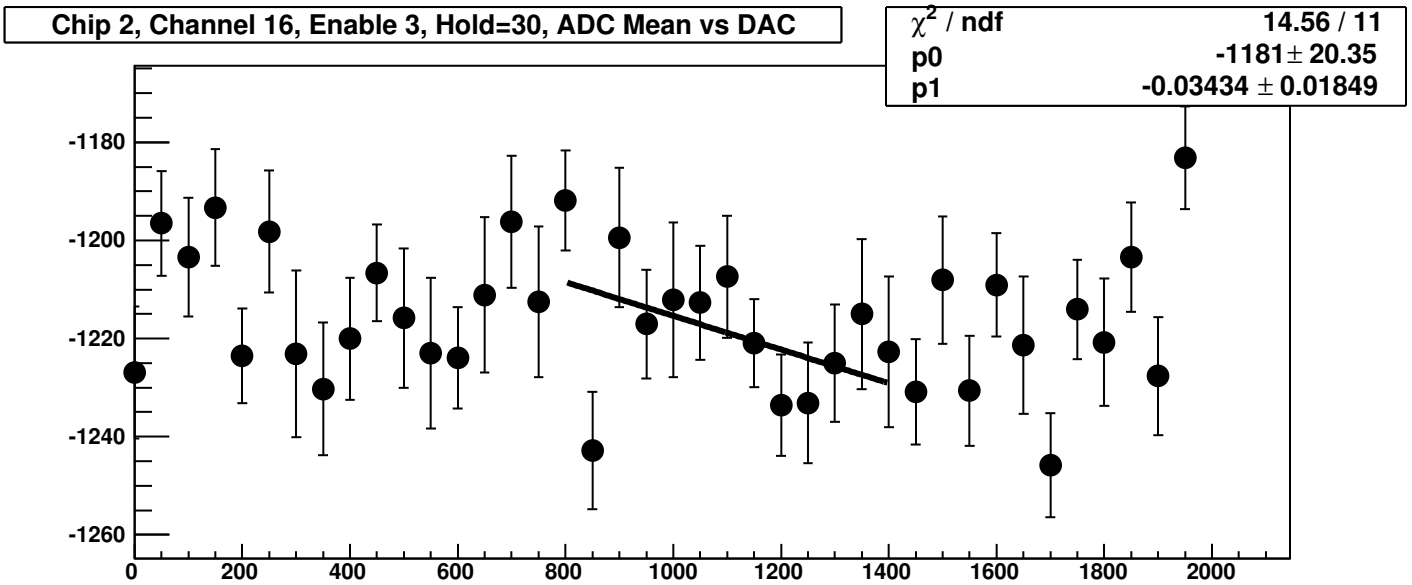
Chip 2, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC

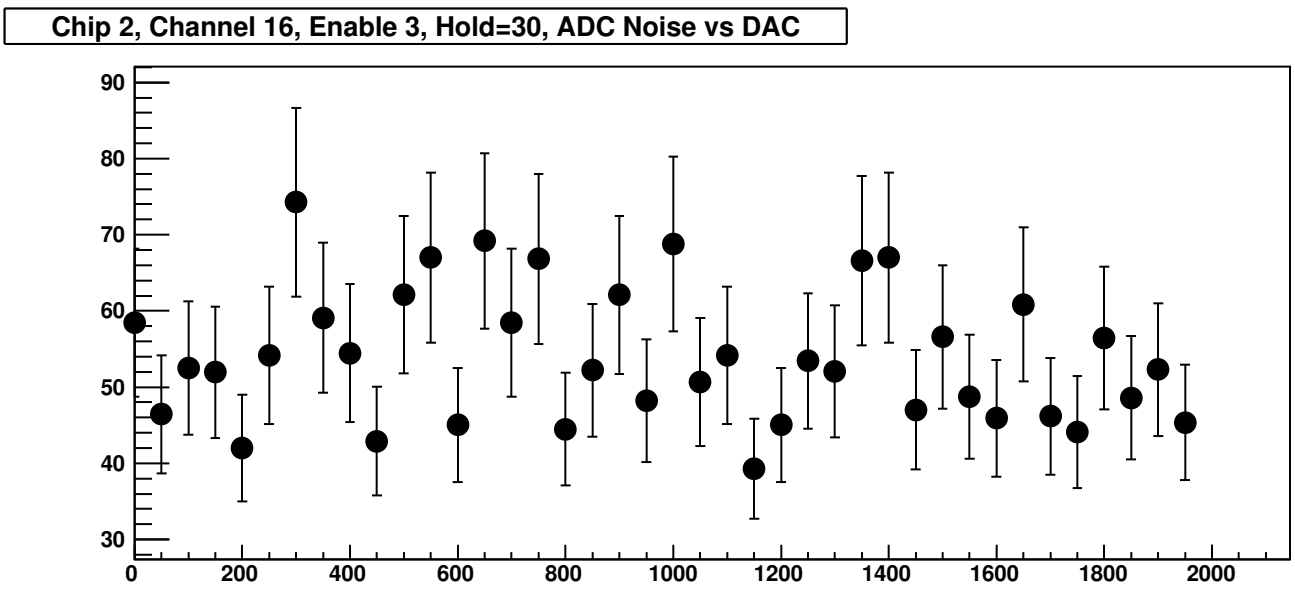


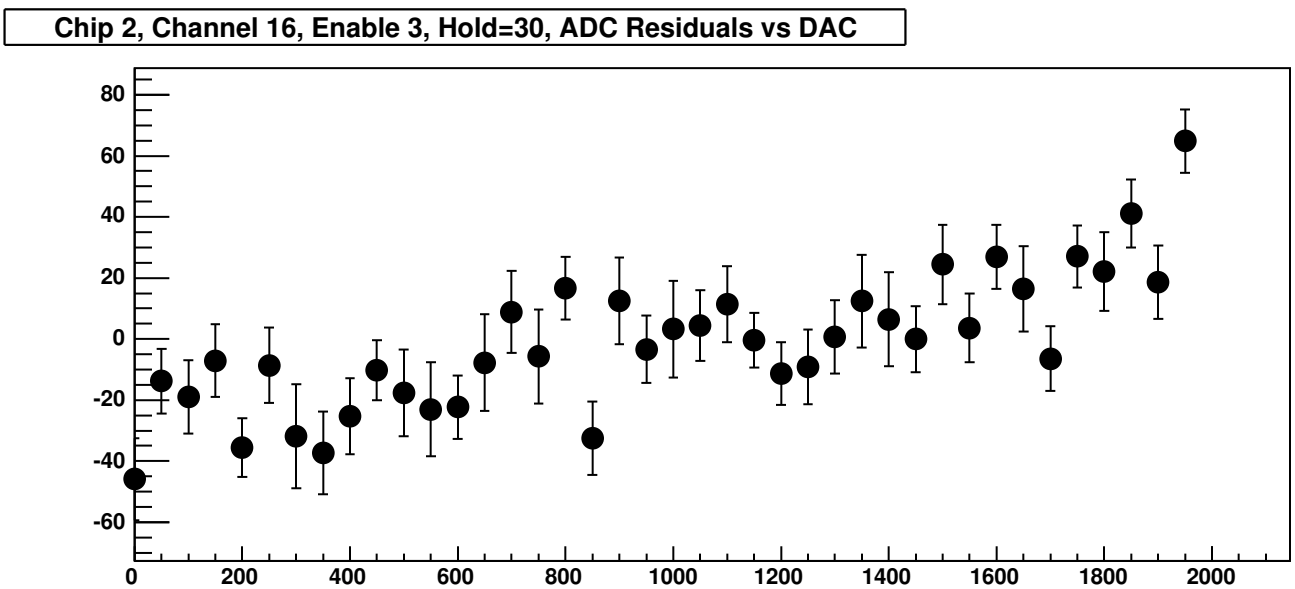

Chip 2, Channel 16, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 14.12 / 11
p0 -1227 ± 21.33
p1 0.4621 ± 0.01878
-300
-400
-500
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

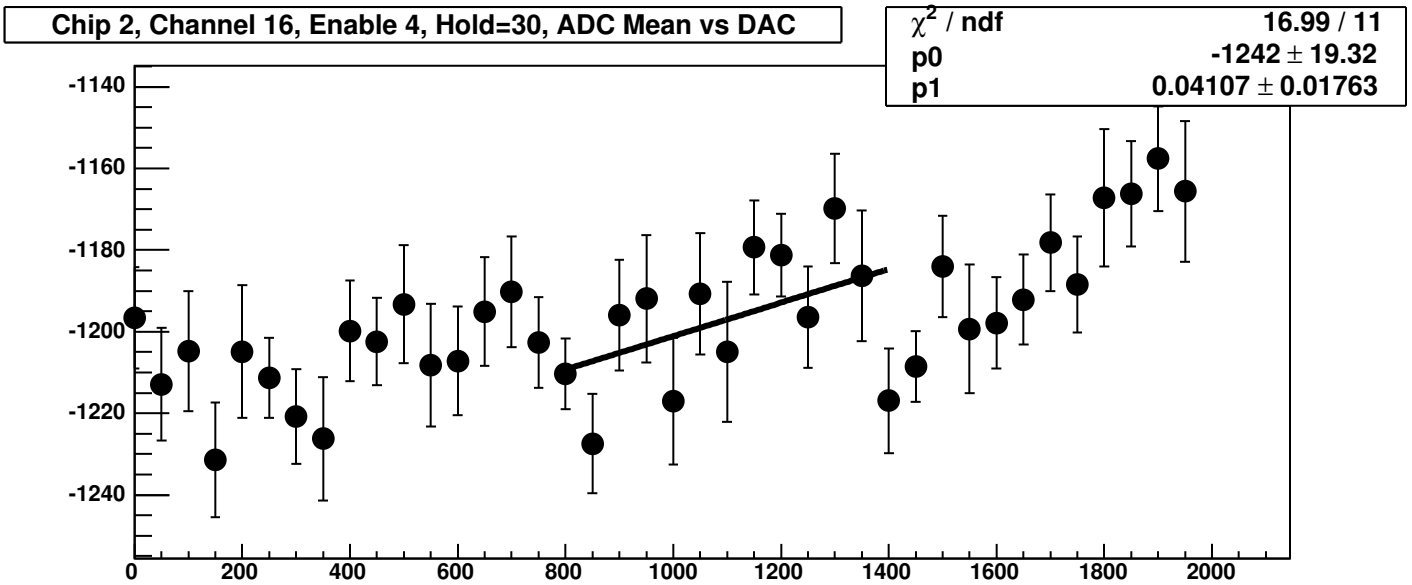

Chip 2, Channel 16, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

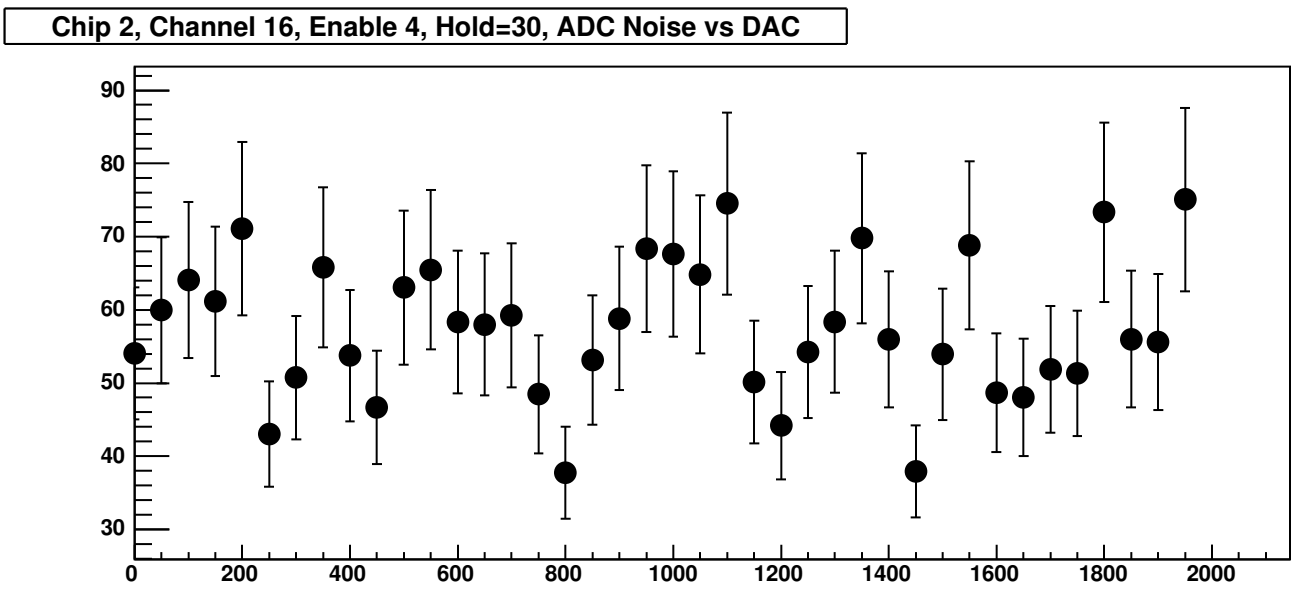

Chip 2, Channel 16, Enable 2, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

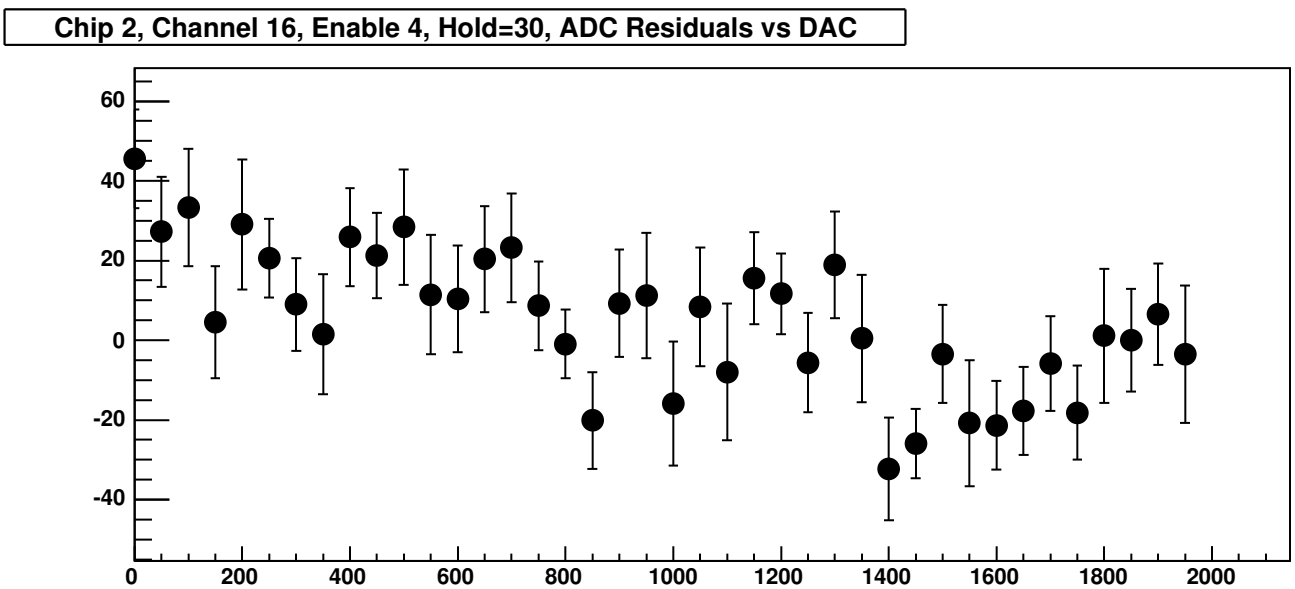

Chip 2, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 14.56 / 11
p0 -1181 ± 20.35
p1 -0.03434 ± 0.01849
-1180
-1200
-1220
-1240
-1260
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

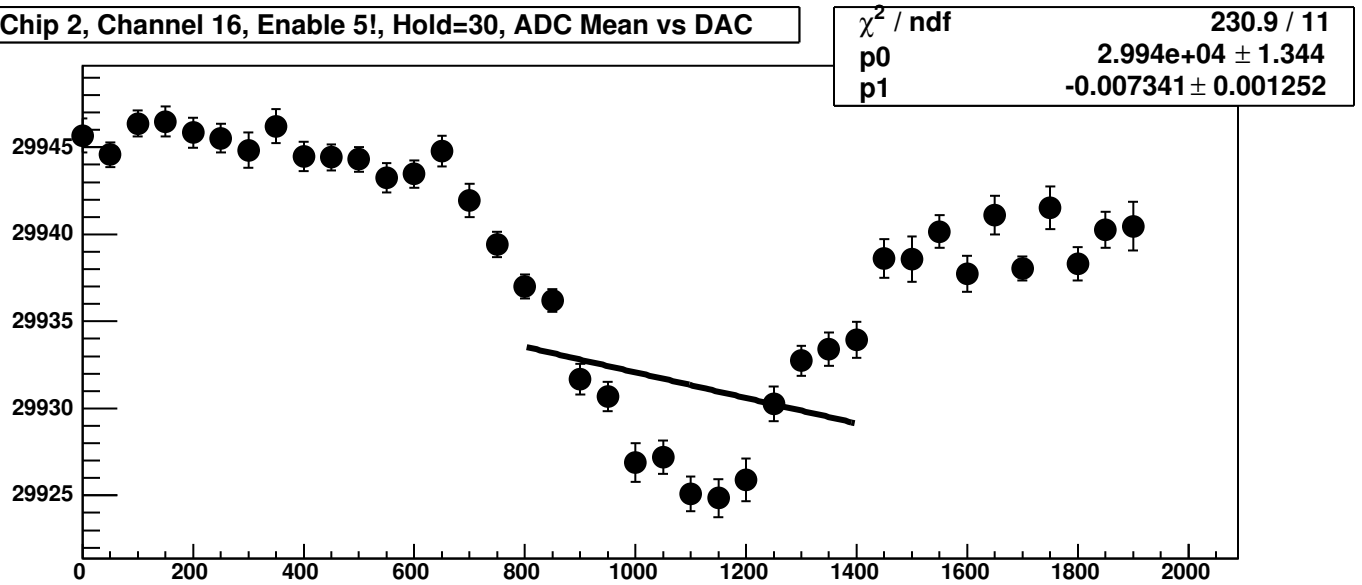

Chip 2, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 16.99 / 11
p0 -1242 ± 19.32
p1 0.04107 ± 0.01763
-1140
-1160
-1180
-1200
-1220
-1240
0
200
400
600
800
1000
1200
1400
1600
1800
2000

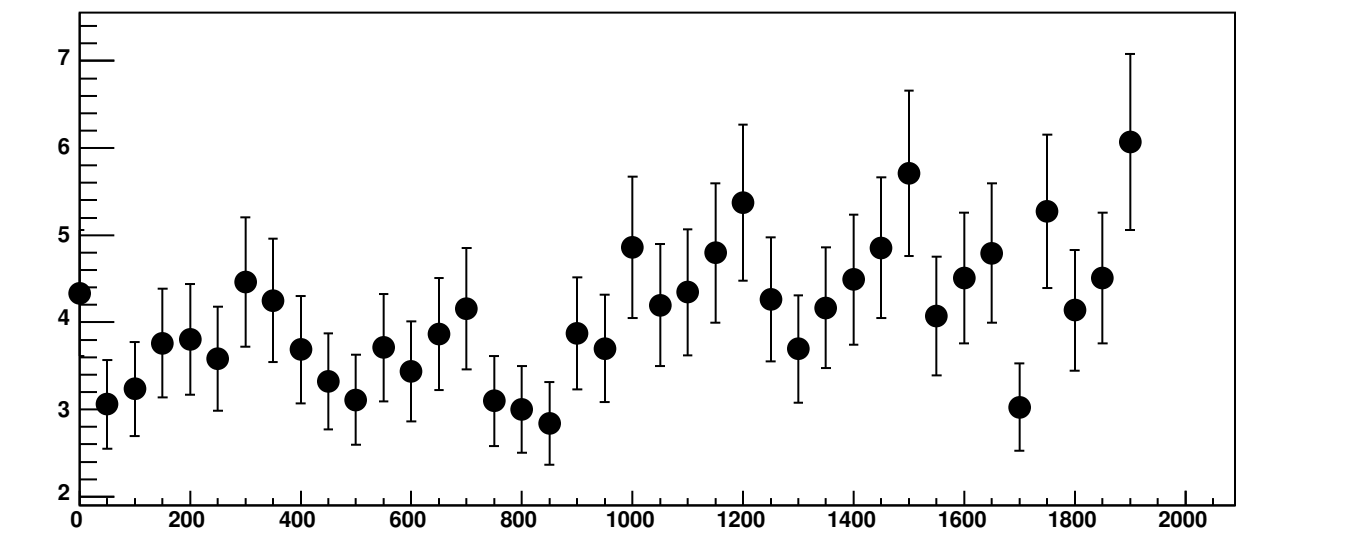

Chip 2, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

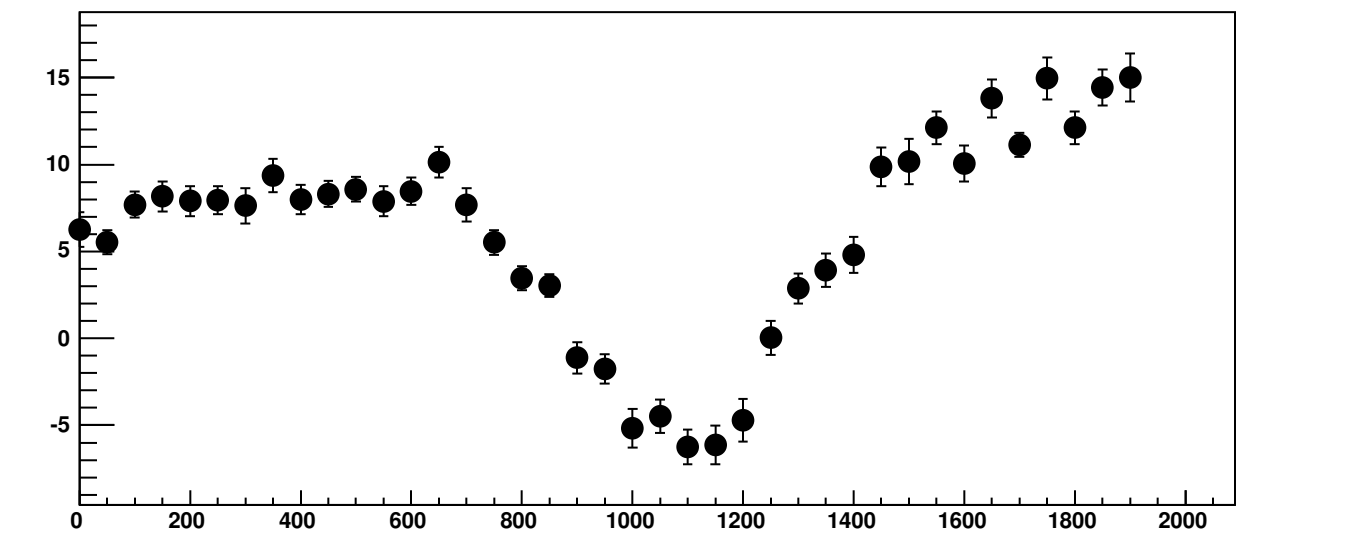
Chip 2, Channel 16, Enable 5!, Hold=30, ADC Mean vs DAC

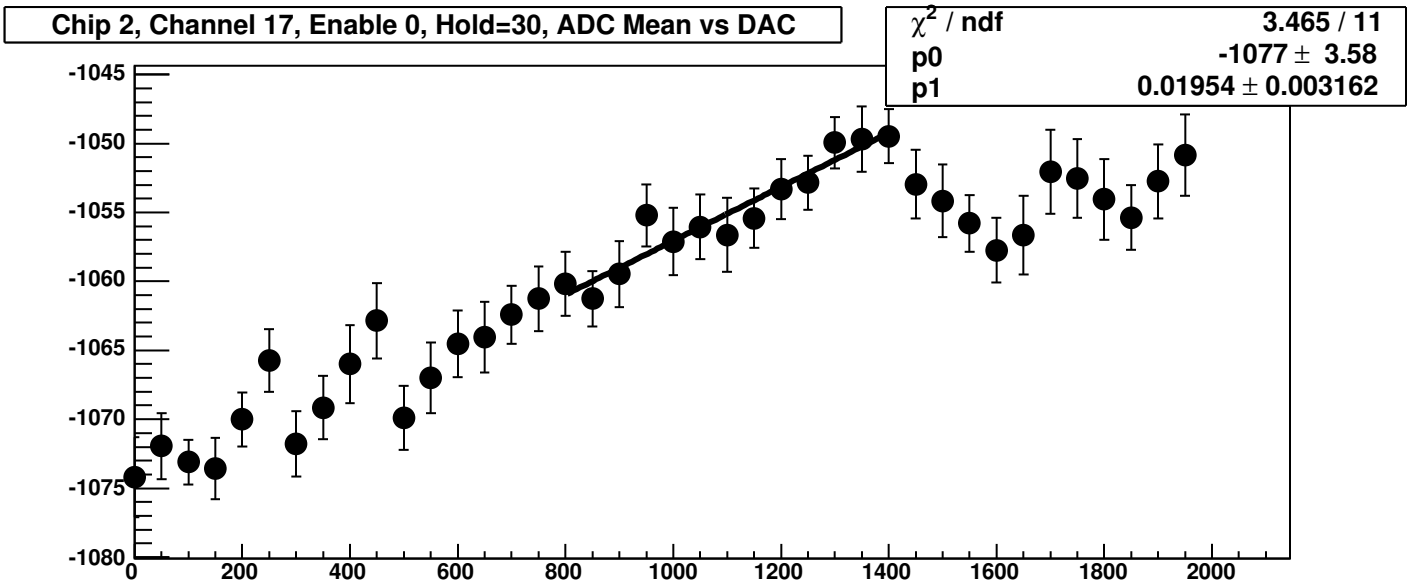


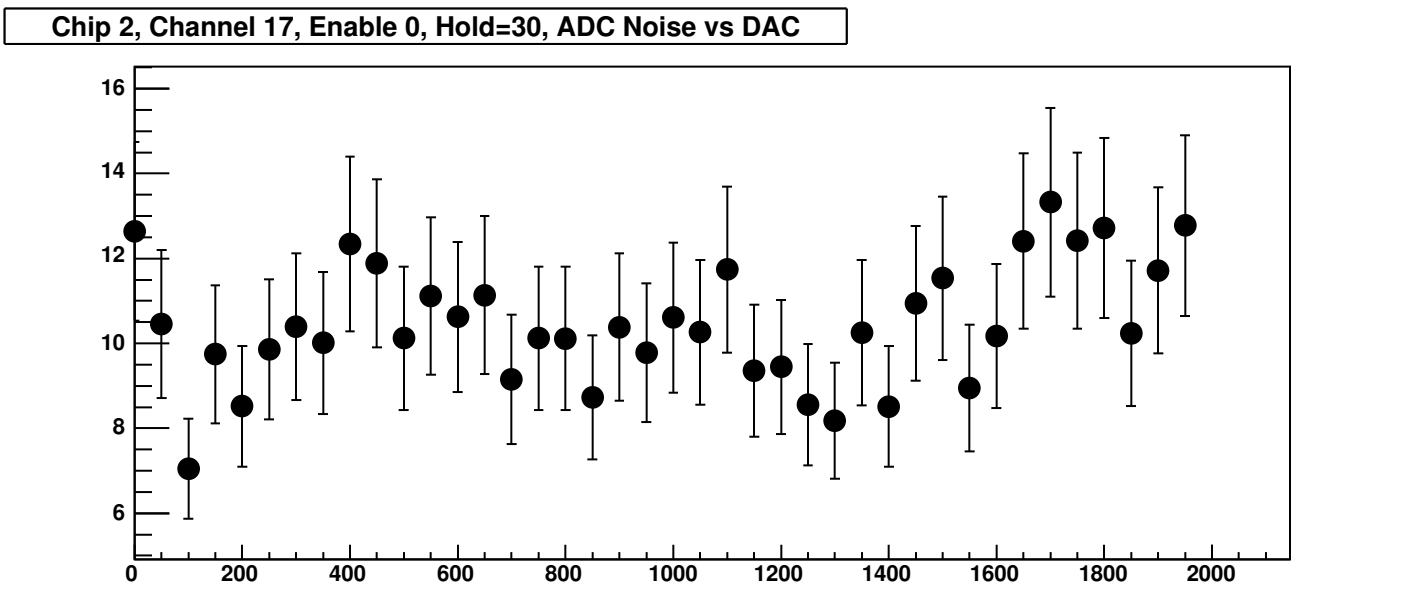
Chip 2, Channel 16, Enable 5!, Hold=30, ADC Noise vs DAC

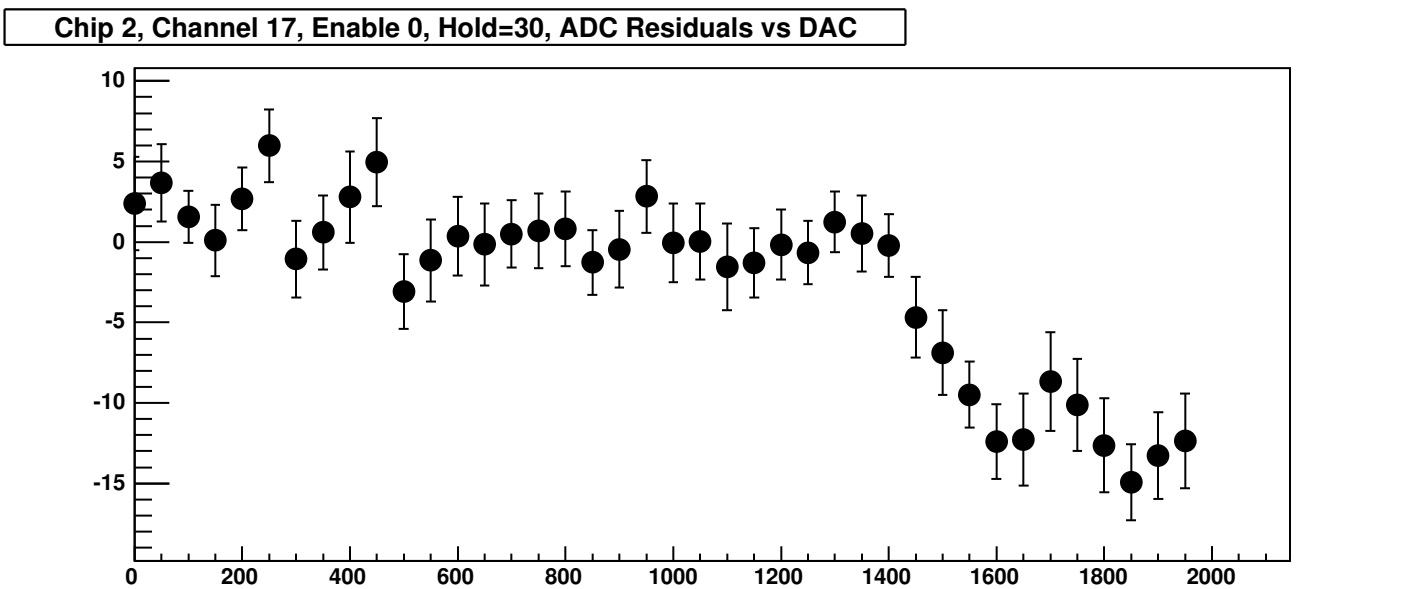


Chip 2, Channel 16, Enable 5!, Hold=30, ADC Residuals vs DAC

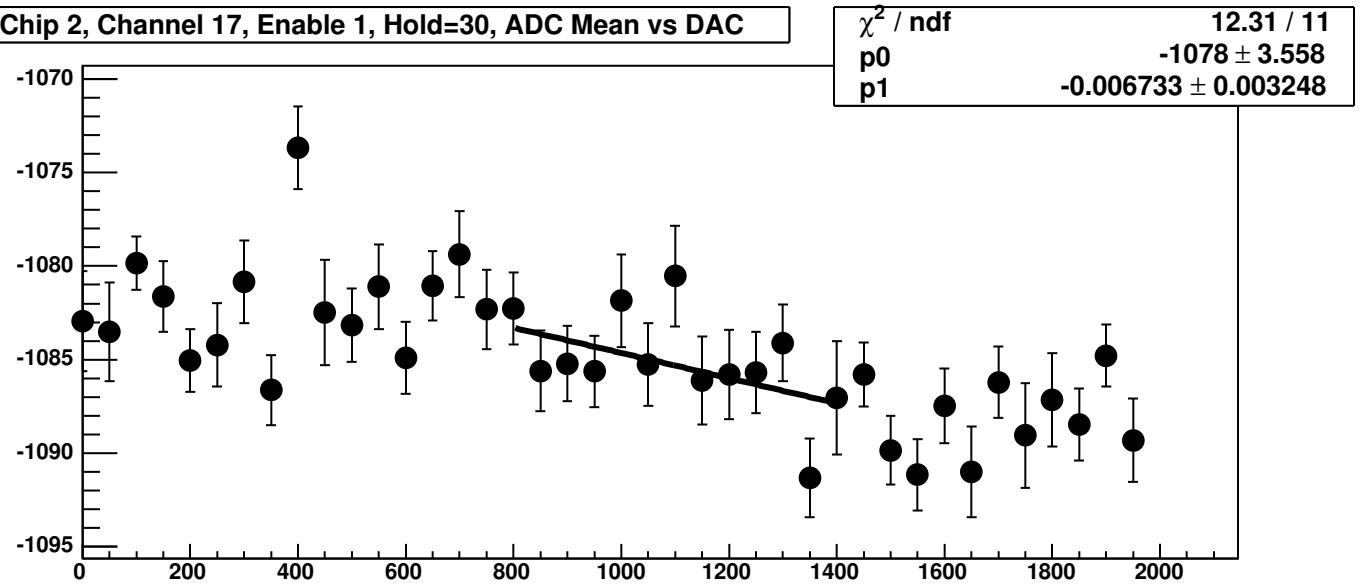



Chip 2, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 3.465 / 11
p0 -1077 ± 3.58
p1 0.01954 ± 0.003162
-1045
-1050
-1055
-1060
-1065
-1070
-1075
-1080
0
200
400
600
800
1000
1200
1400
1600
1800
2000

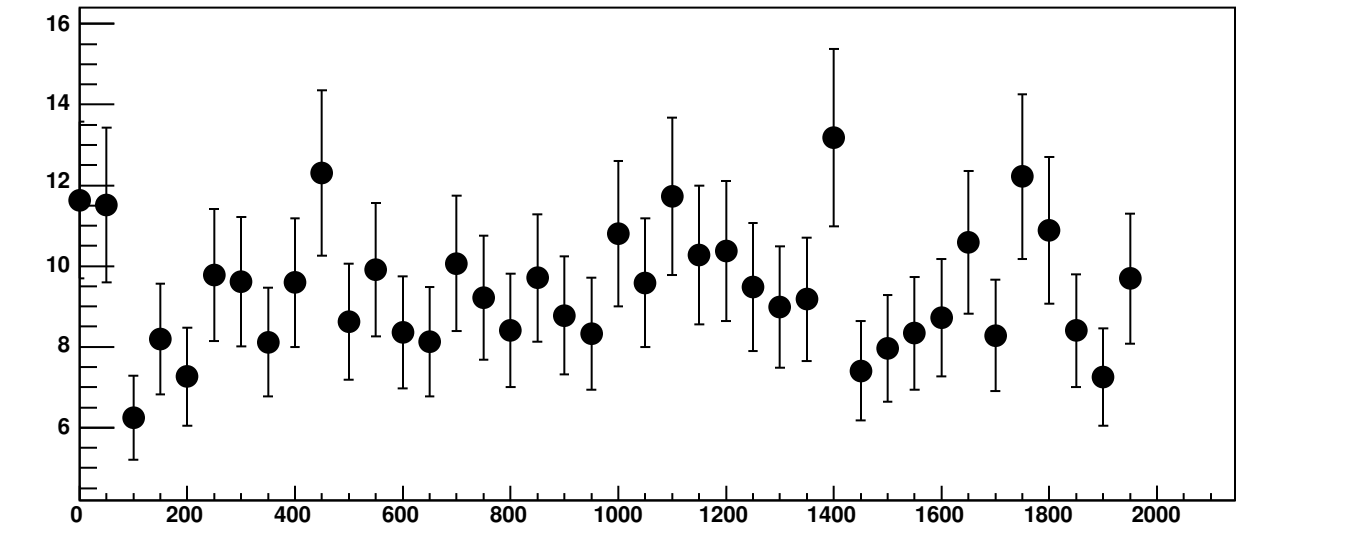

Chip 2, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
200
400
600
800
1000
1200
1400
1600
1800
2000

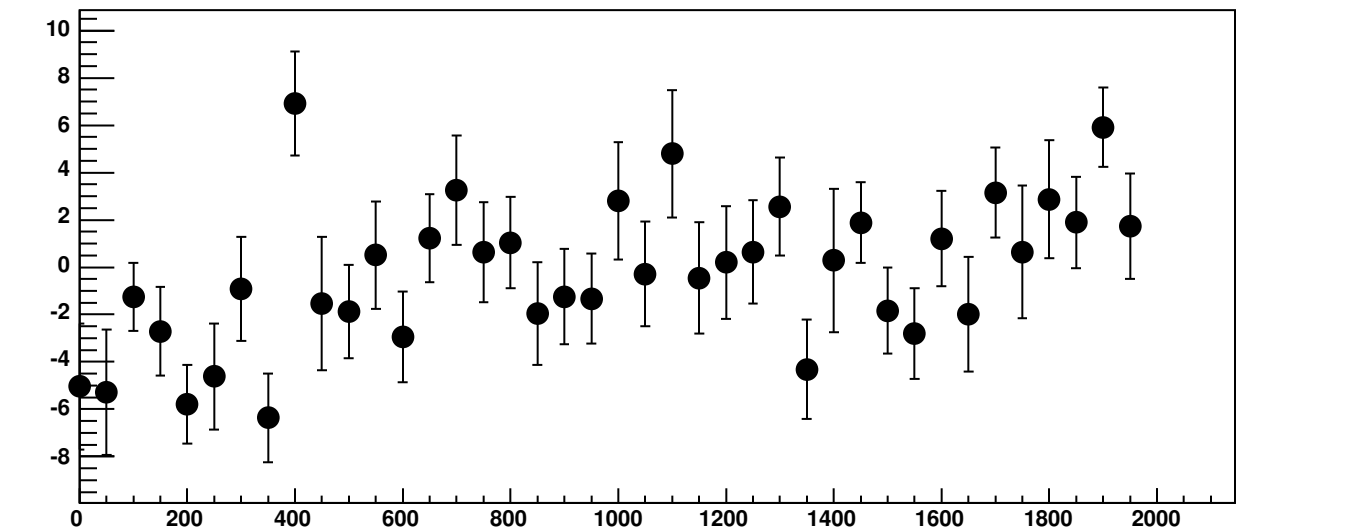
Chip 2, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC

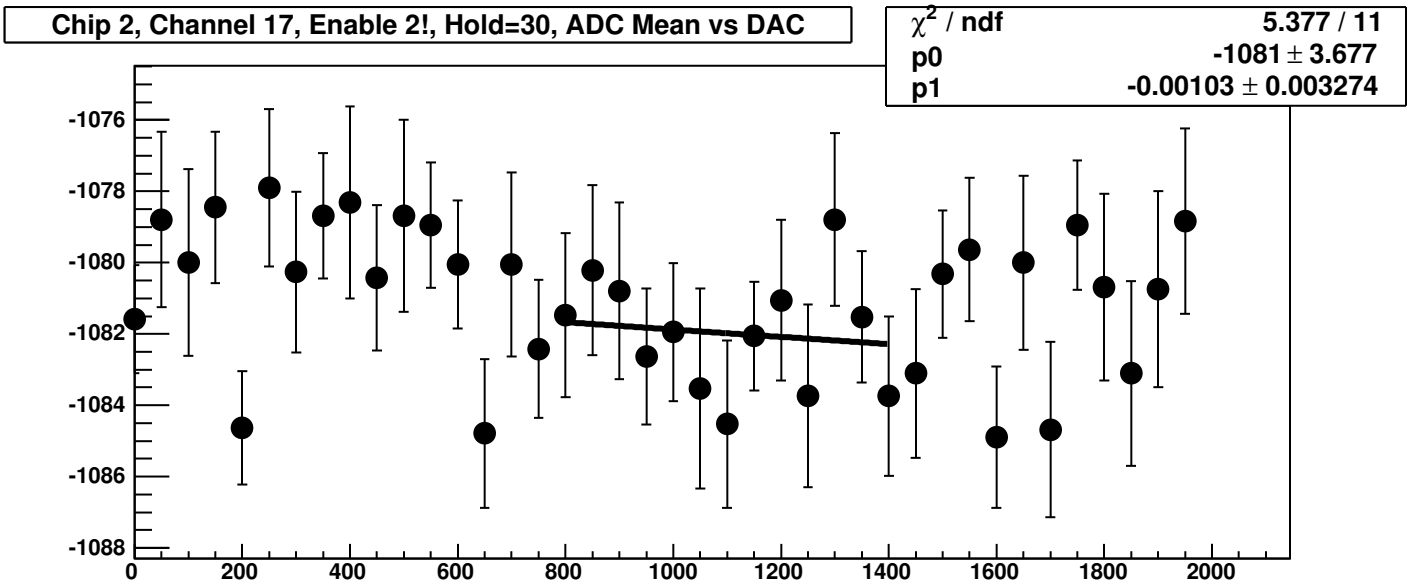


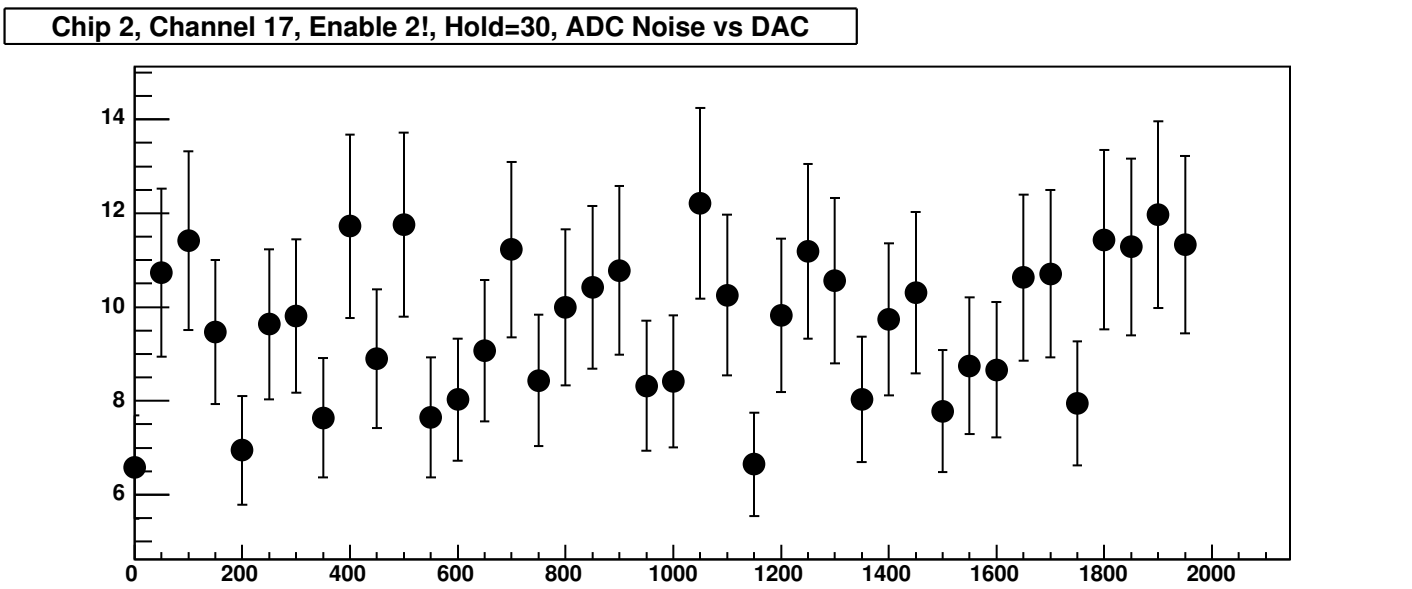
Chip 2, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC

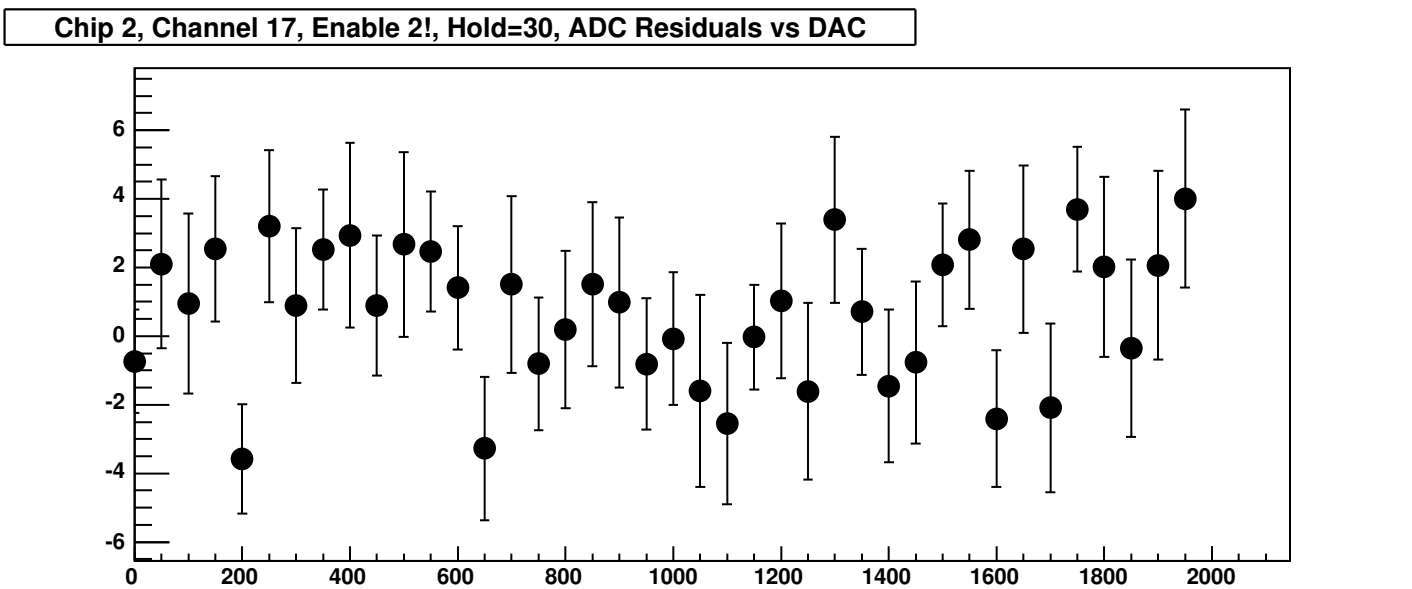


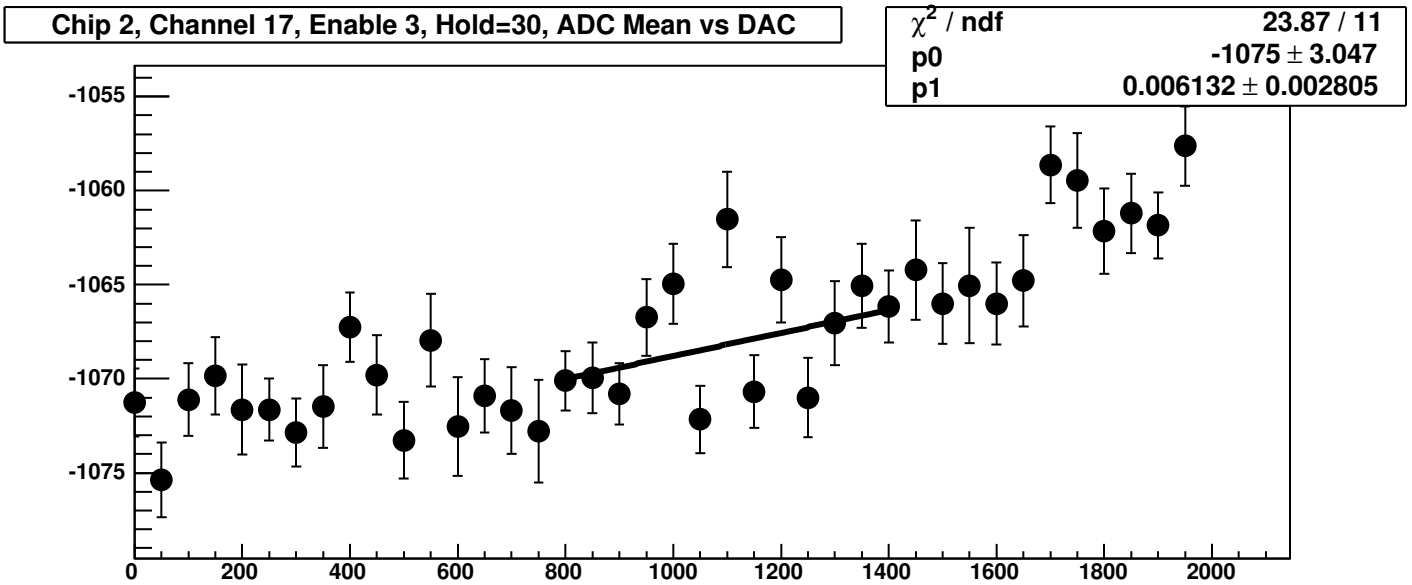
Chip 2, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC

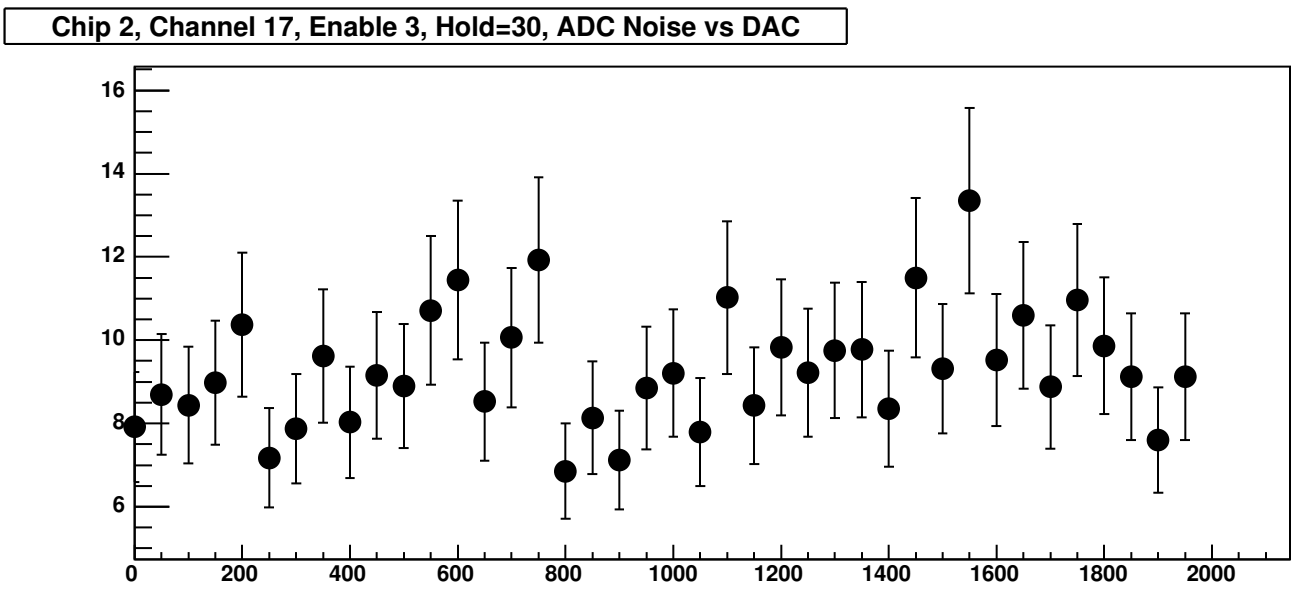


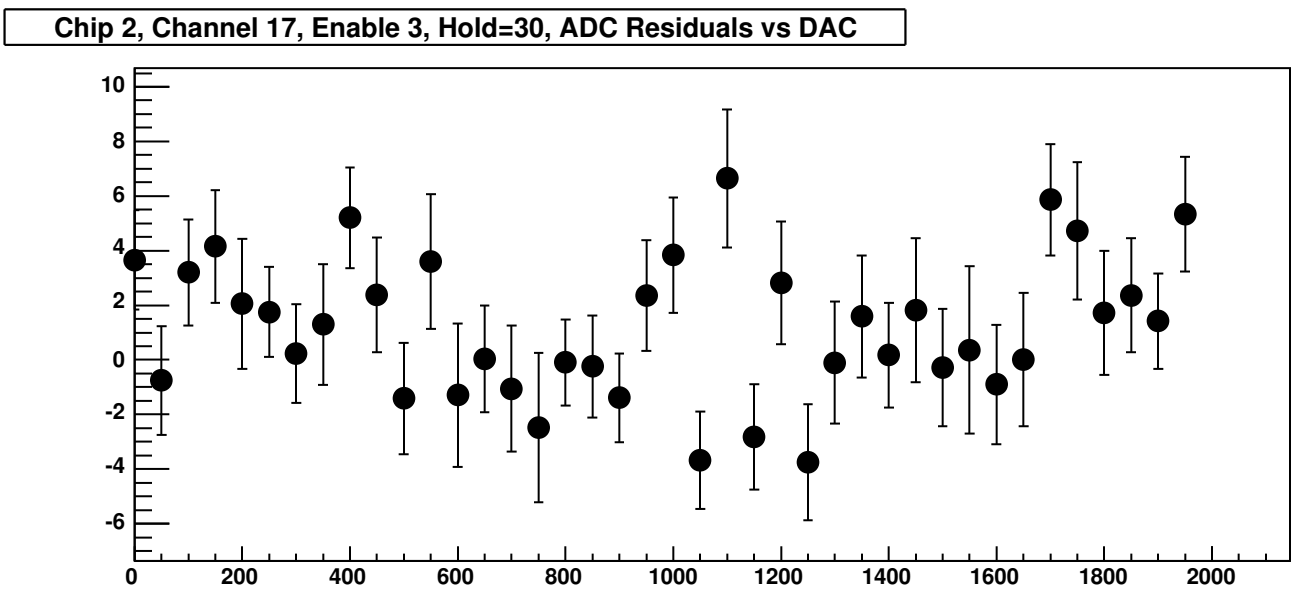

Chip 2, Channel 17, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 5.377 / 11
p0 -1081 ± 3.677
p1 -0.00103 ± 0.003274
-1076
-1078
-1080
-1082
-1084
-1086
-1088
0
200
400
600
800
1000
1200
1400
1600
1800
2000

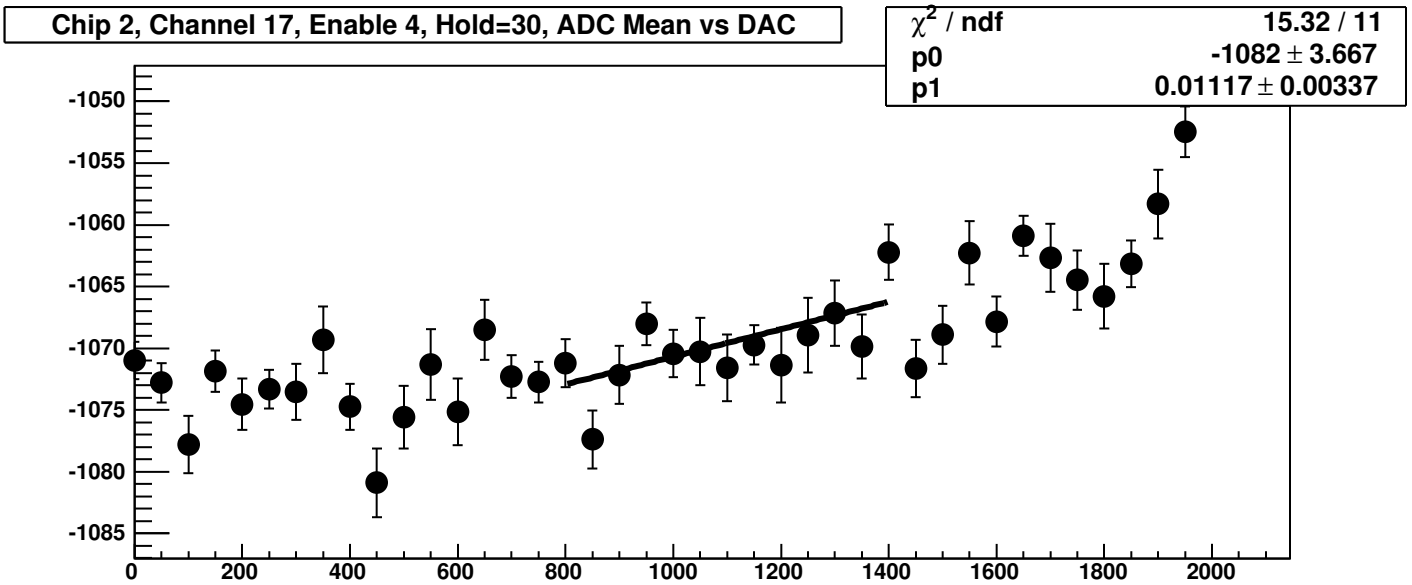

Chip 2, Channel 17, Enable 2!, Hold=30, ADC Noise vs DAC
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

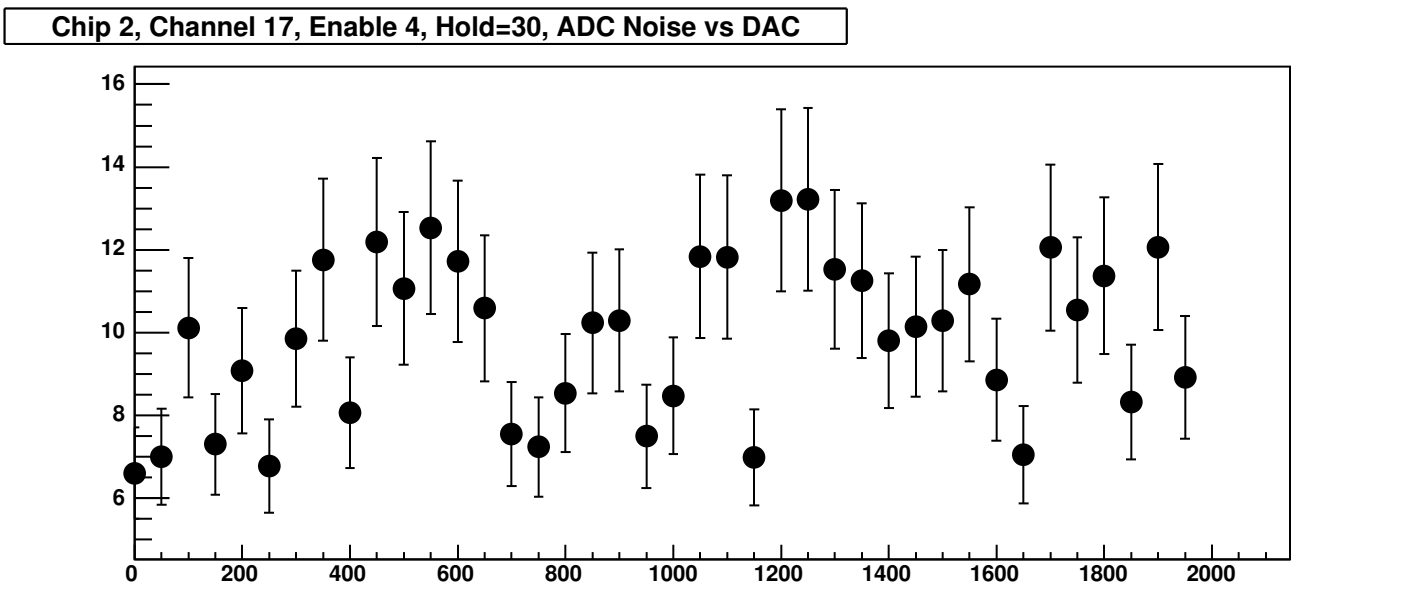

Chip 2, Channel 17, Enable 2!, Hold=30, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

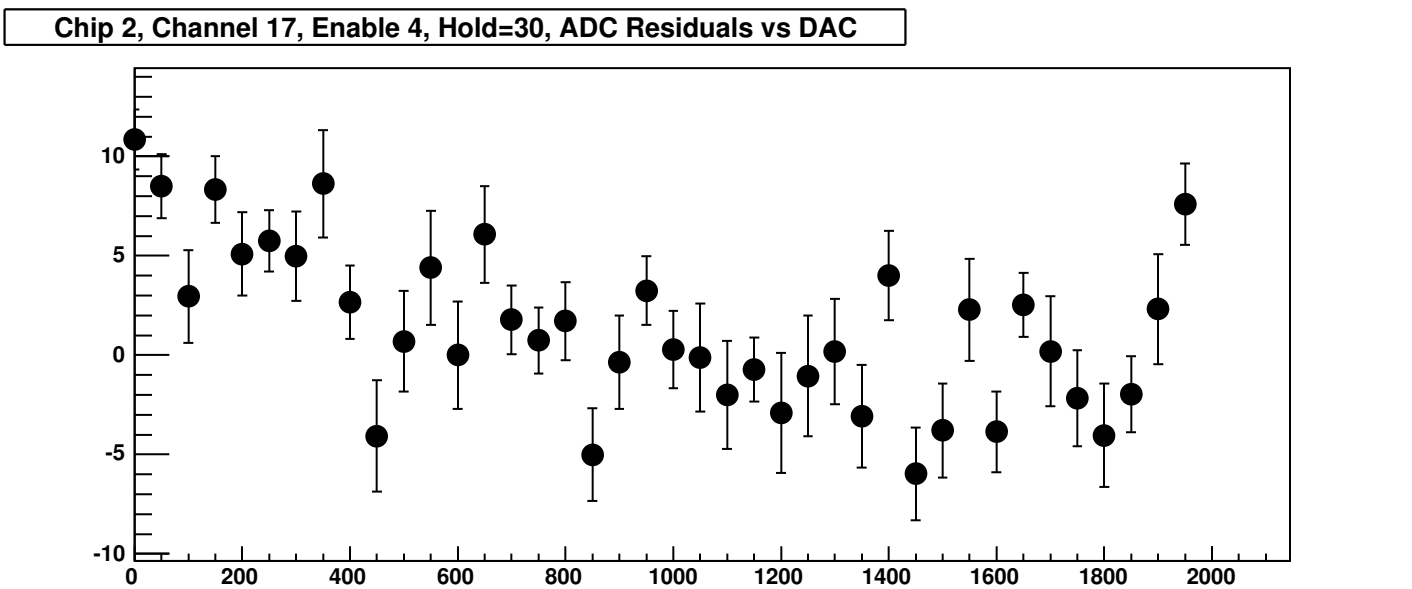

Chip 2, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 23.87 / 11
p0 -1075 ± 3.047
p1 0.006132 ± 0.002805
-1055
-1060
-1065
-1070
-1075
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

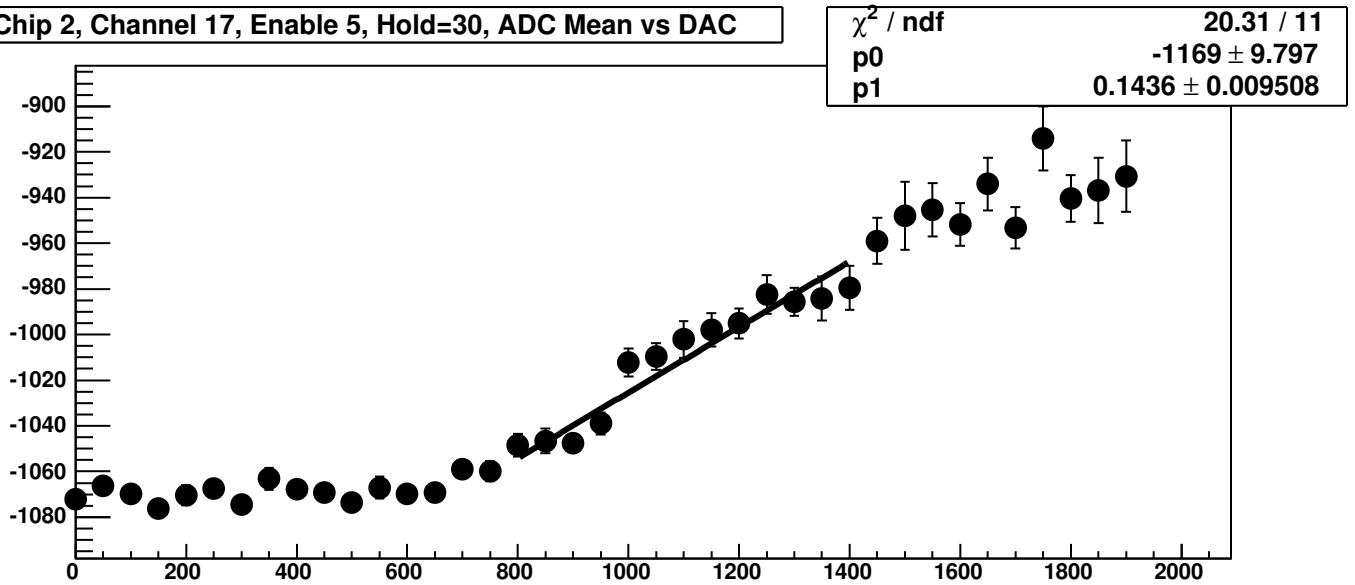

Chip 2, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 15.32 / 11
p0 -1082 ± 3.667
p1 0.01117 ± 0.00337
-1050
-1055
-1060
-1065
-1070
-1075
-1080
-1085
0
200
400
600
800
1000
1200
1400
1600
1800
2000

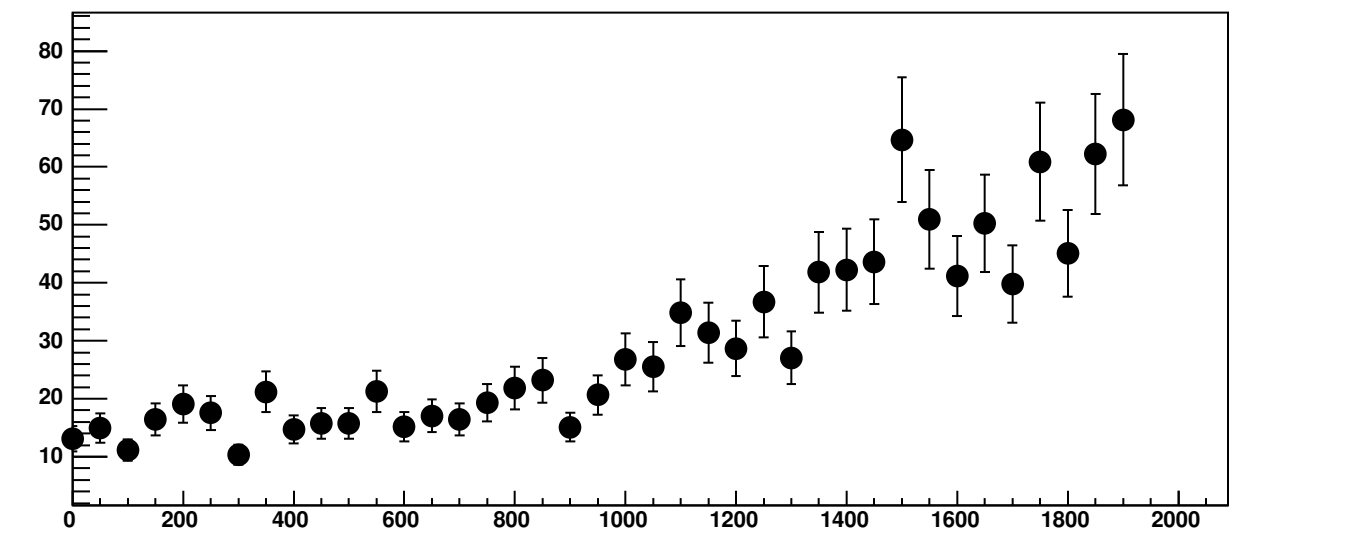

Chip 2, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 2, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

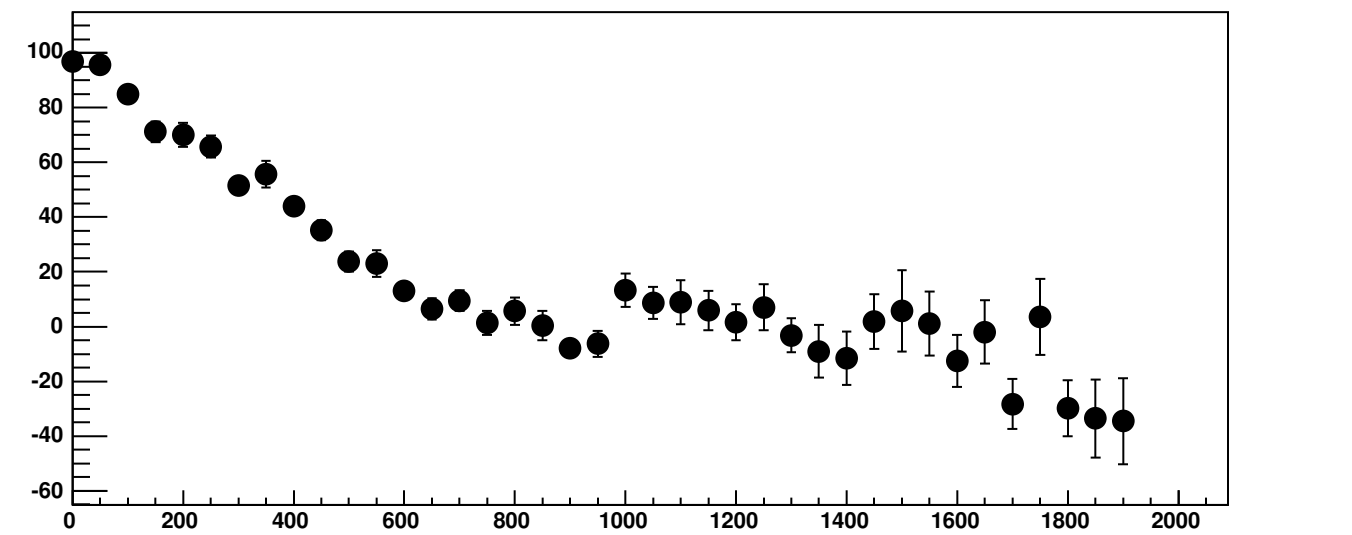
Chip 2, Channel 17, Enable 5, Hold=30, ADC Mean vs DAC

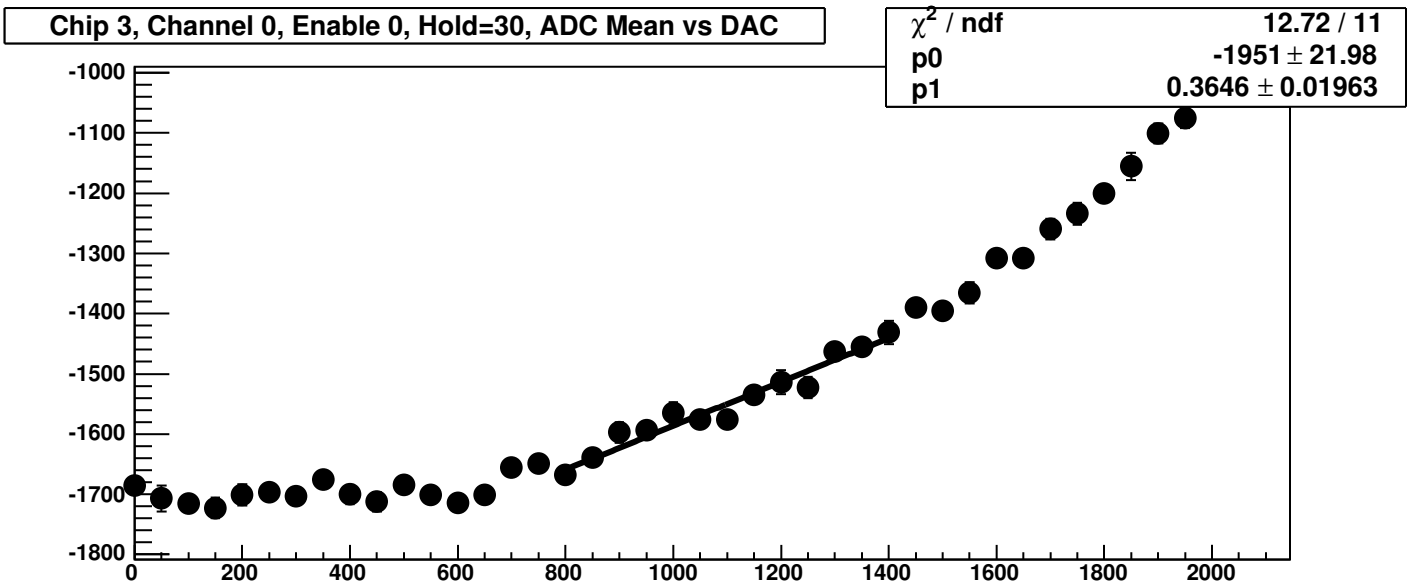


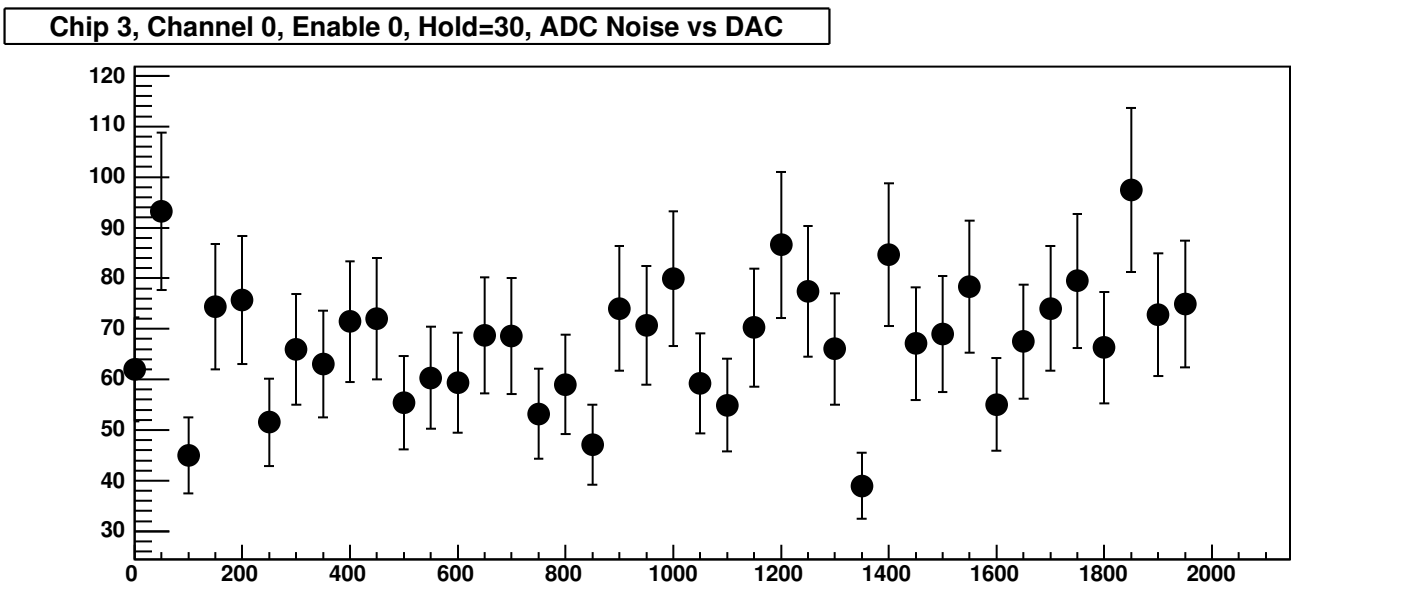
Chip 2, Channel 17, Enable 5, Hold=30, ADC Noise vs DAC

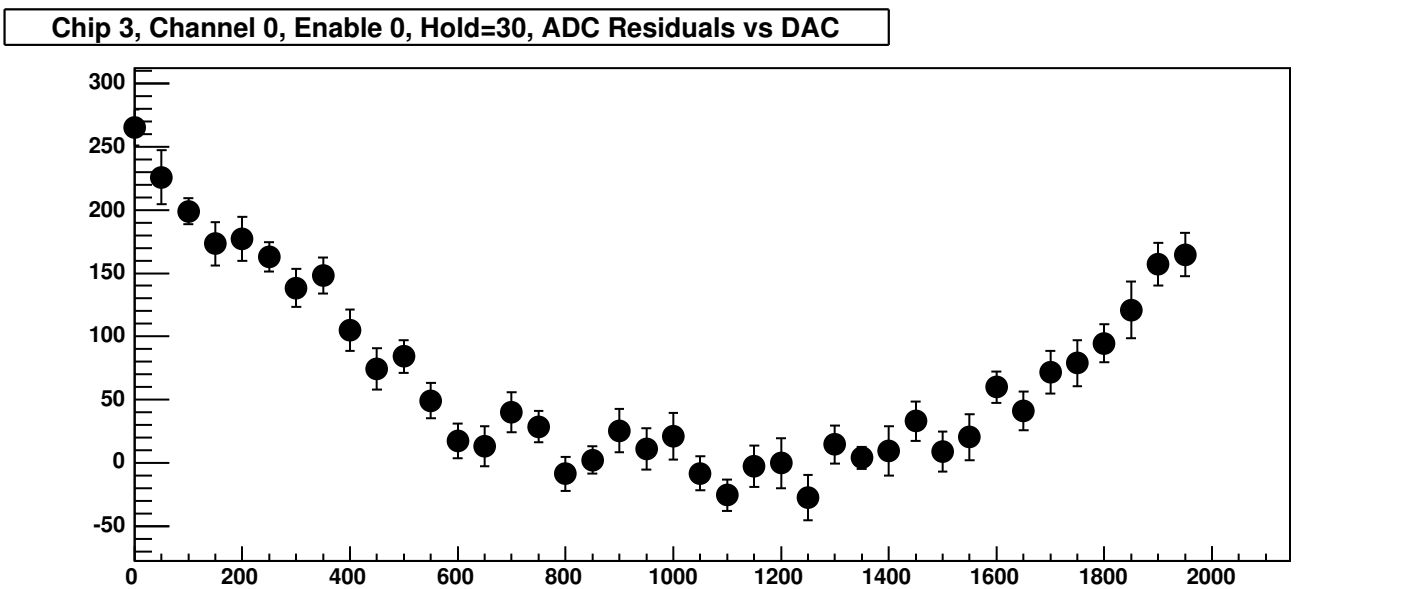


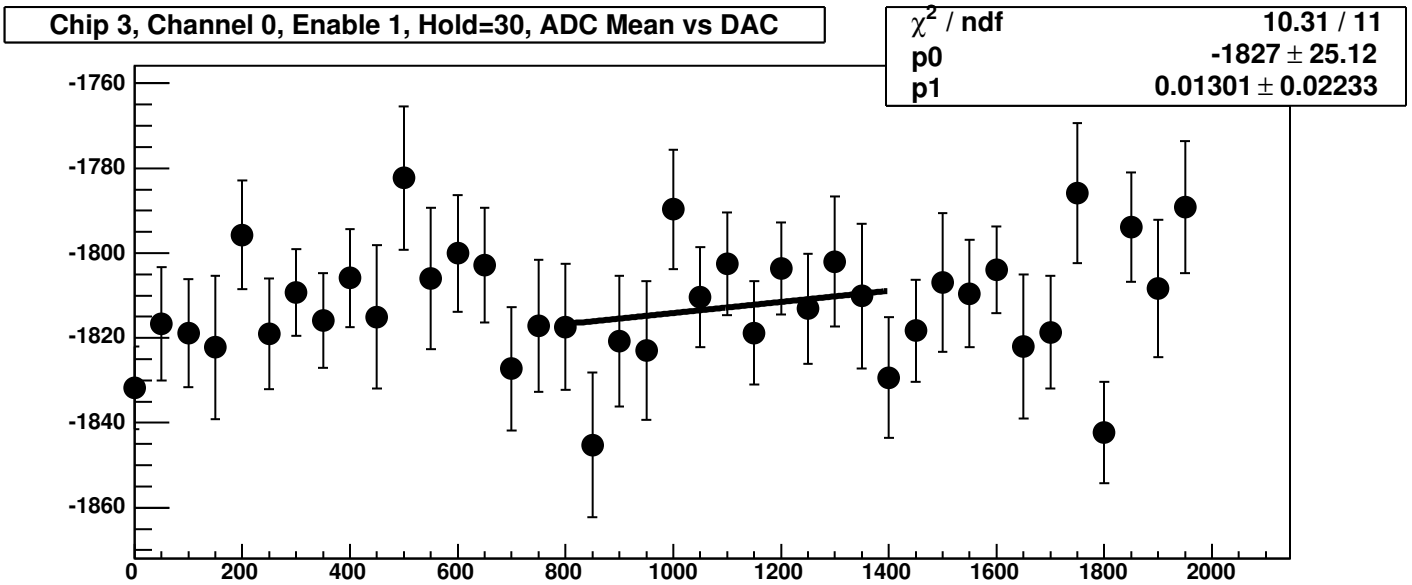
Chip 2, Channel 17, Enable 5, Hold=30, ADC Residuals vs DAC

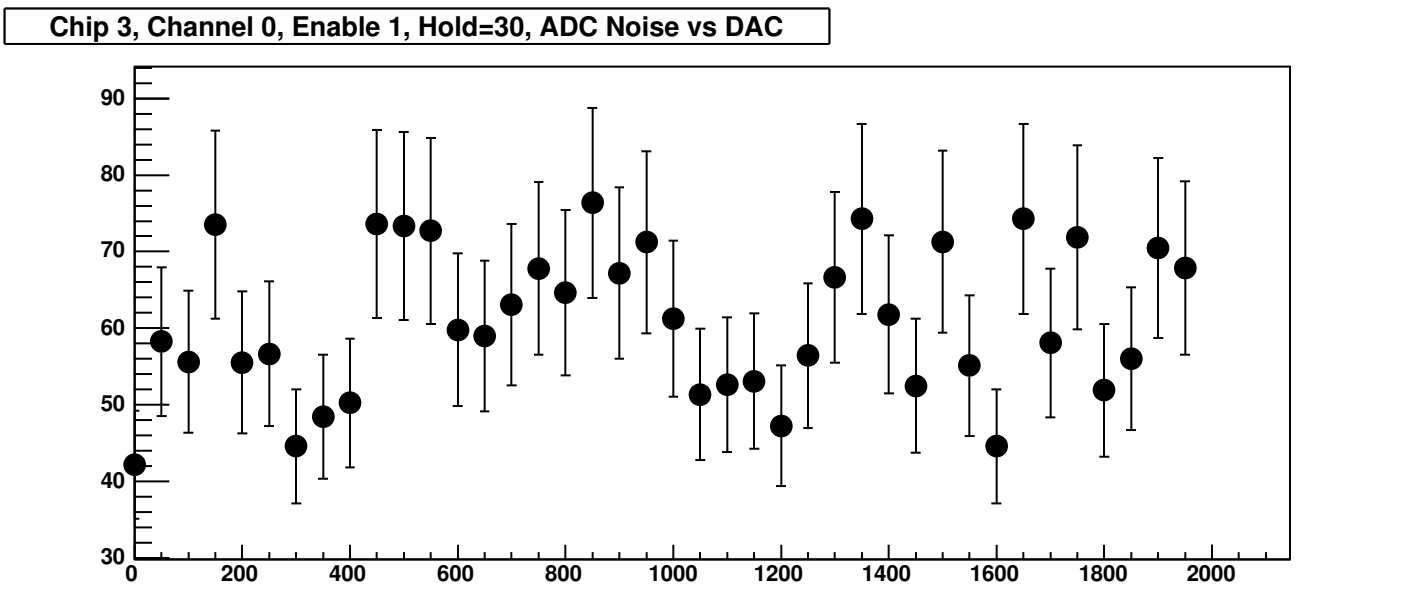


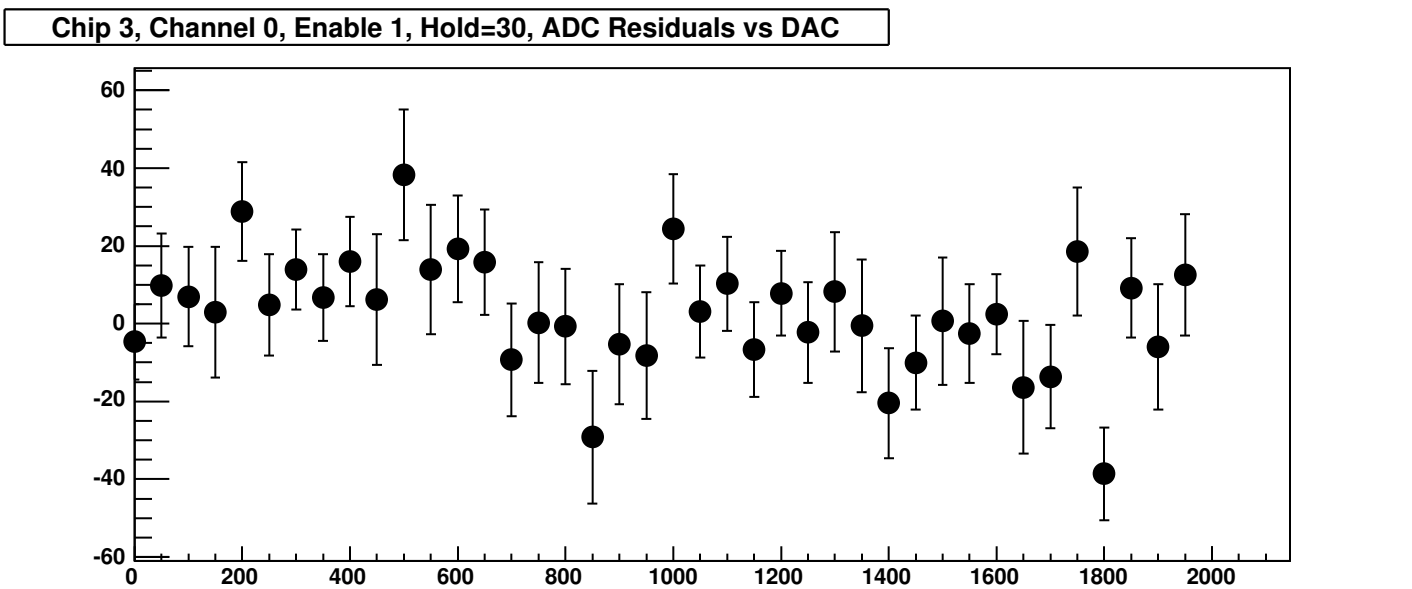

Chip 3, Channel 0, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 12.72 / 11
p0 -1951 ± 21.98
p1 0.3646 ± 0.01963
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

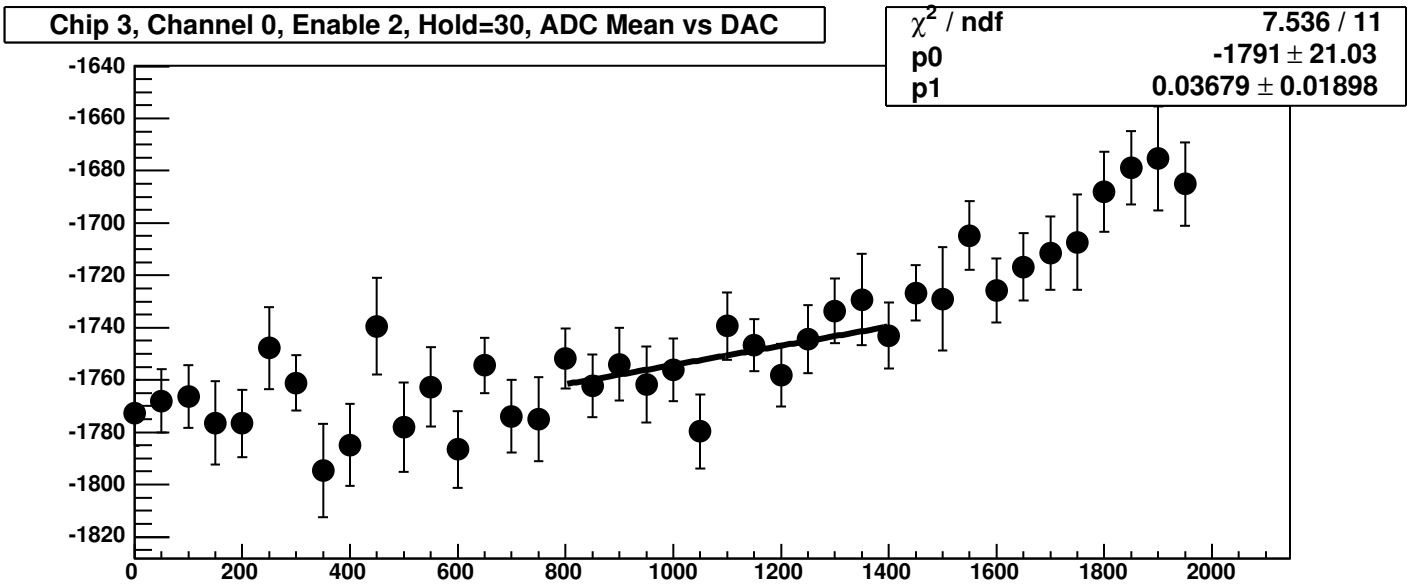

Chip 3, Channel 0, Enable 0, Hold=30, ADC Noise vs DAC
120
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

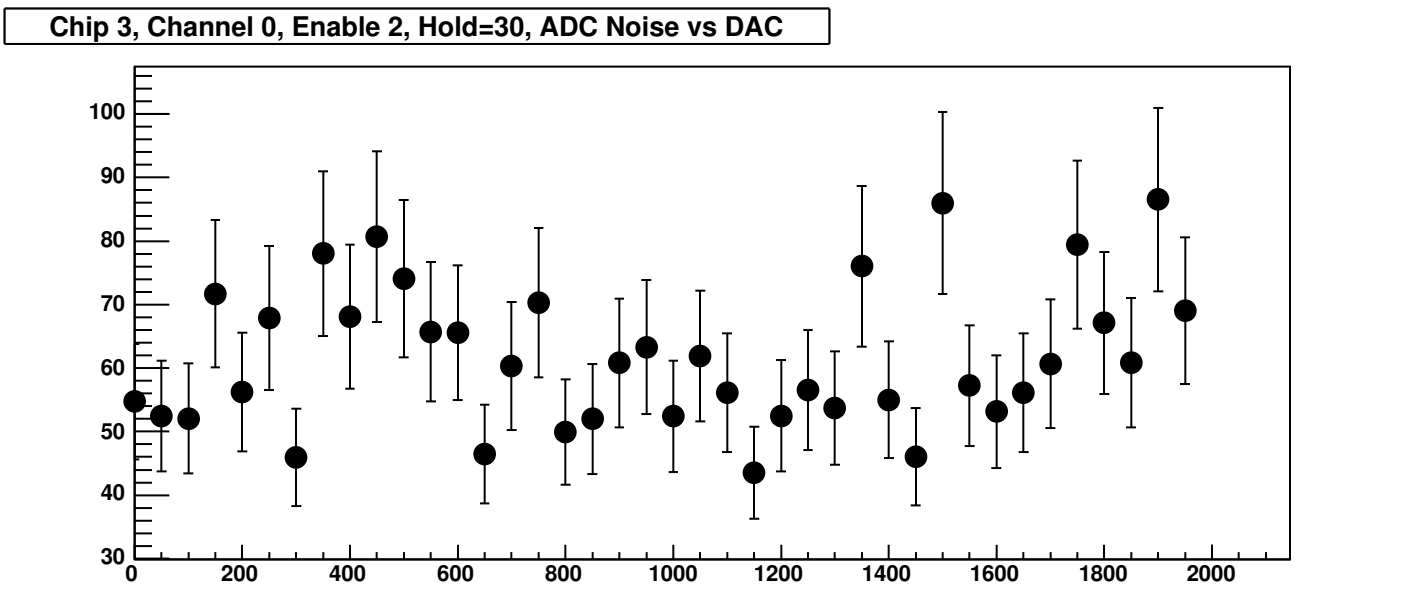

Chip 3, Channel 0, Enable 0, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

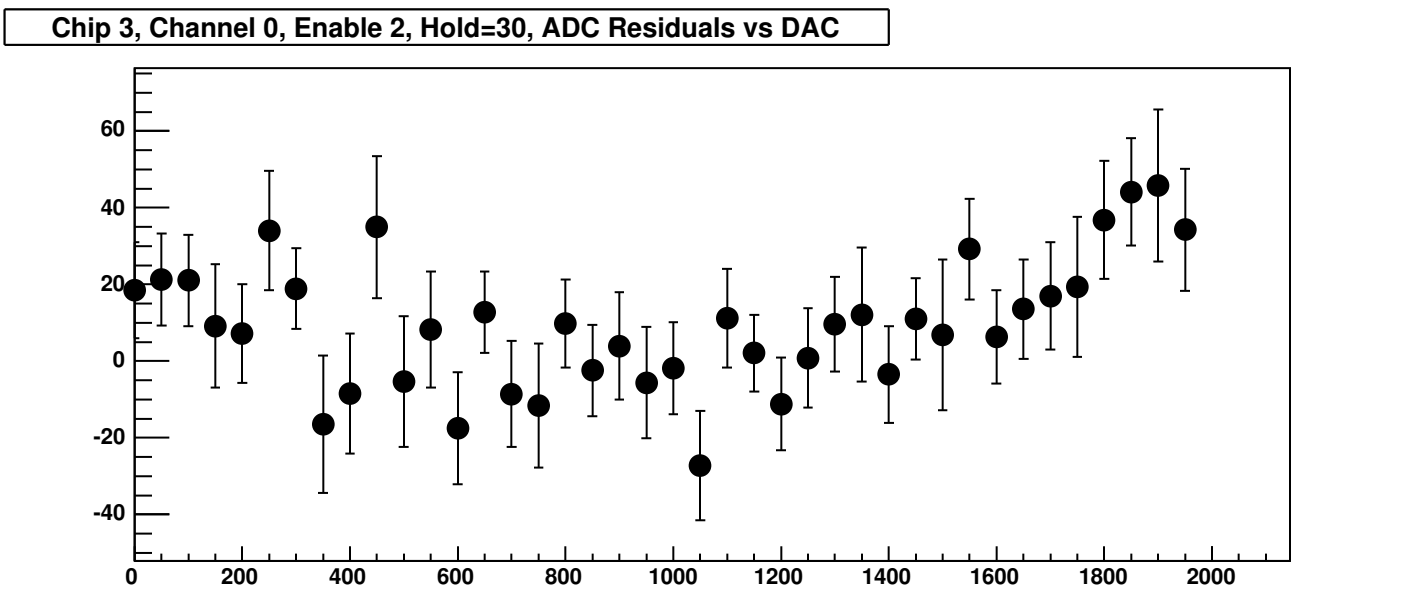

Chip 3, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 10.31 / 11
p0 -1827 ± 25.12
p1 0.01301 ± 0.02233
-1760
-1780
-1800
-1820
-1840
-1860
0
200
400
600
800
1000
1200
1400
1600
1800
2000

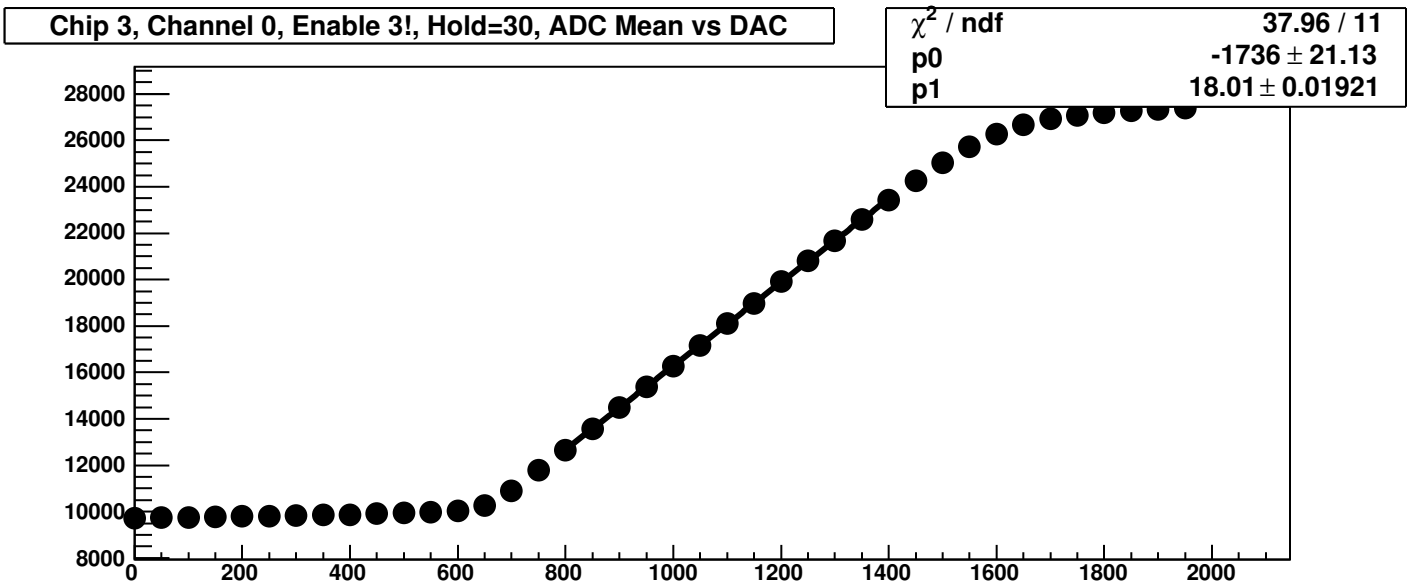

Chip 3, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

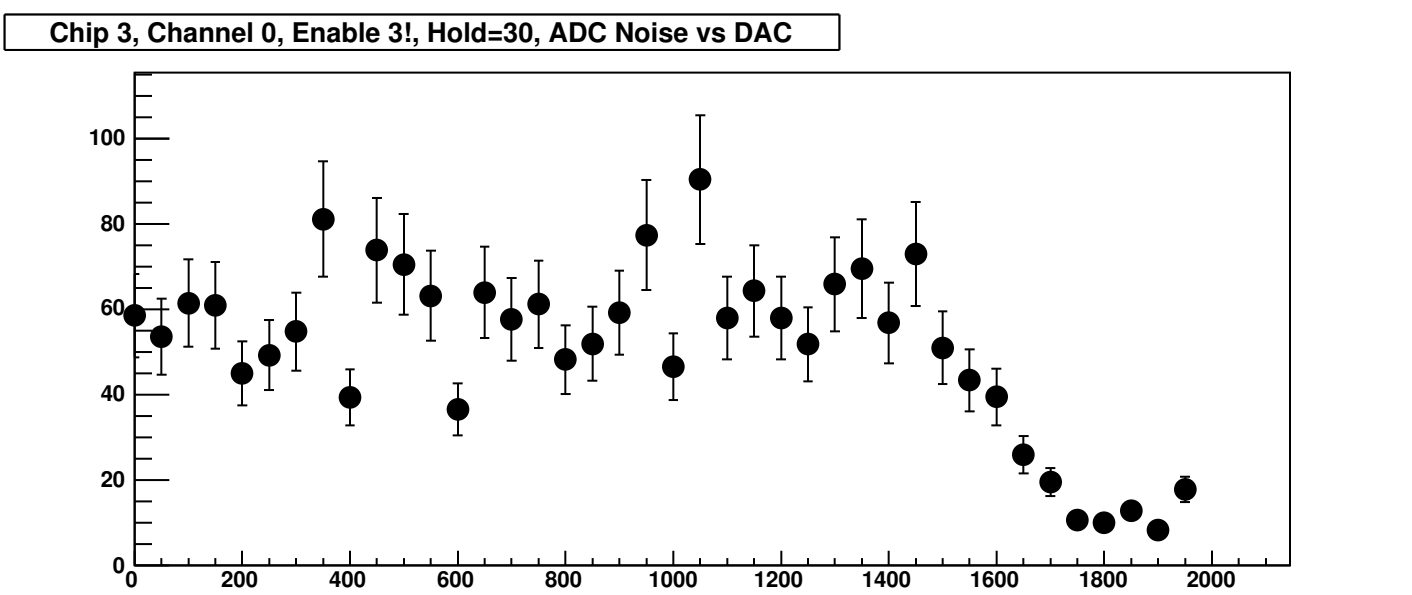

Chip 3, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

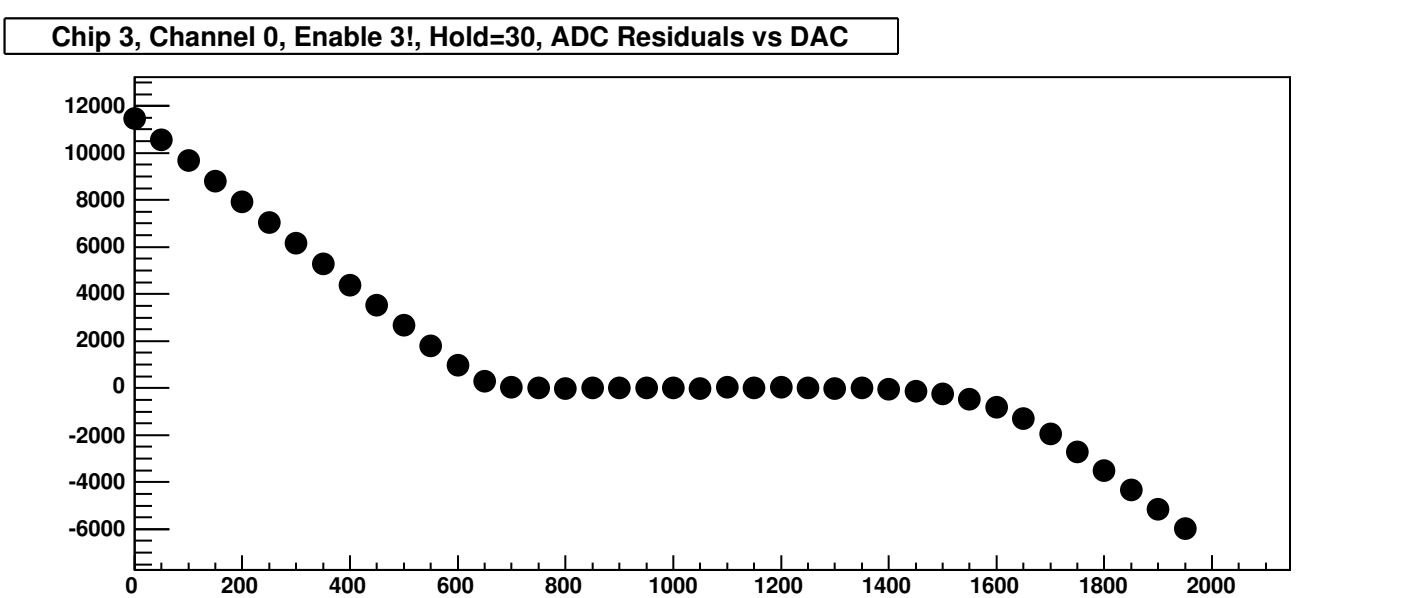

Chip 3, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 7.536 / 11
p0 -1791 ± 21.03
p1 0.03679 ± 0.01898
-1640
-1660
-1680
-1700
-1720
-1740
-1760
-1780
-1800
-1820
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

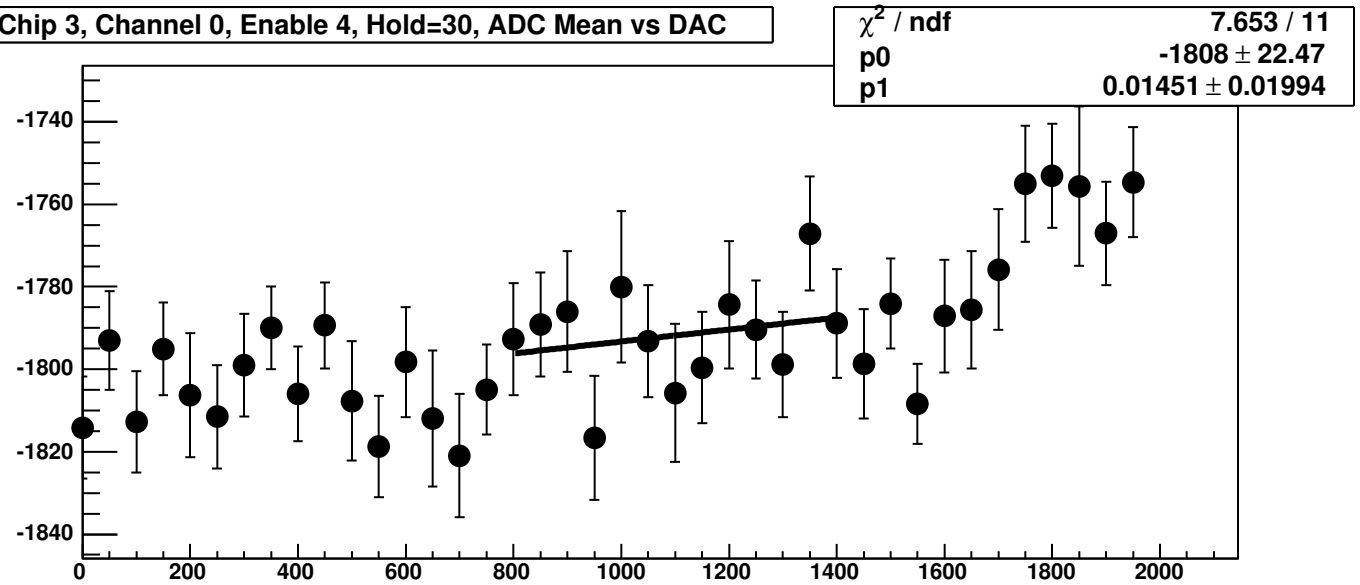

Chip 3, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 0, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 37.96 / 11
p0 -1736 ± 21.13
p1 18.01 ± 0.01921
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

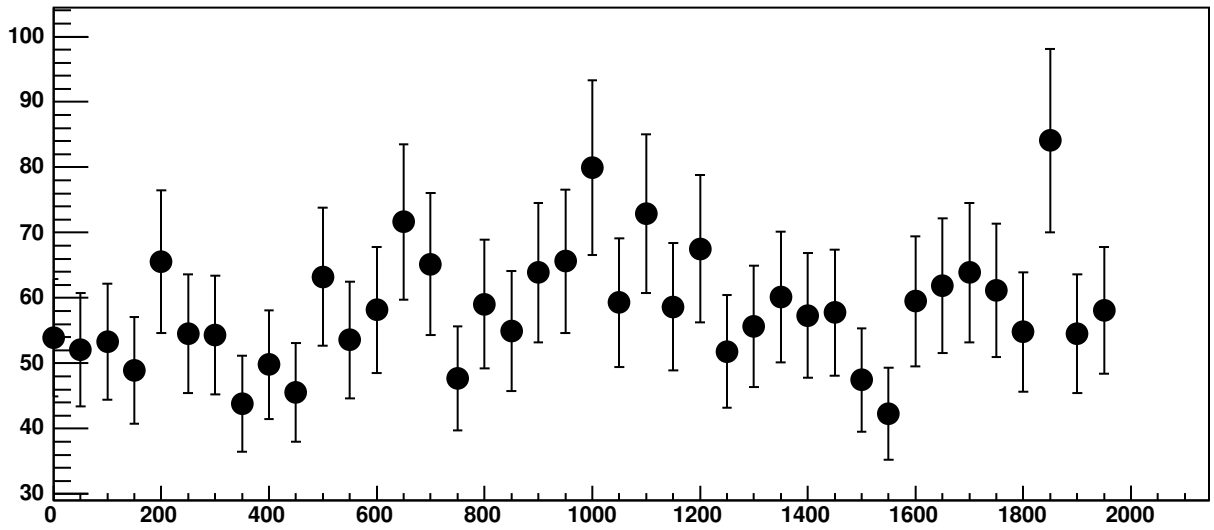

Chip 3, Channel 0, Enable 3!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 0, Enable 3!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

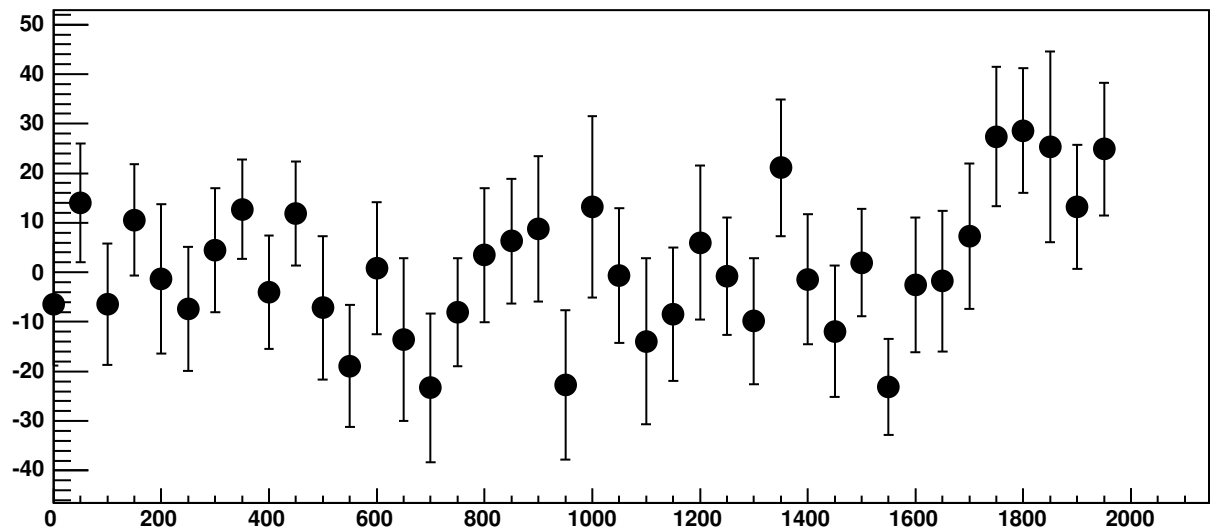
Chip 3, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC

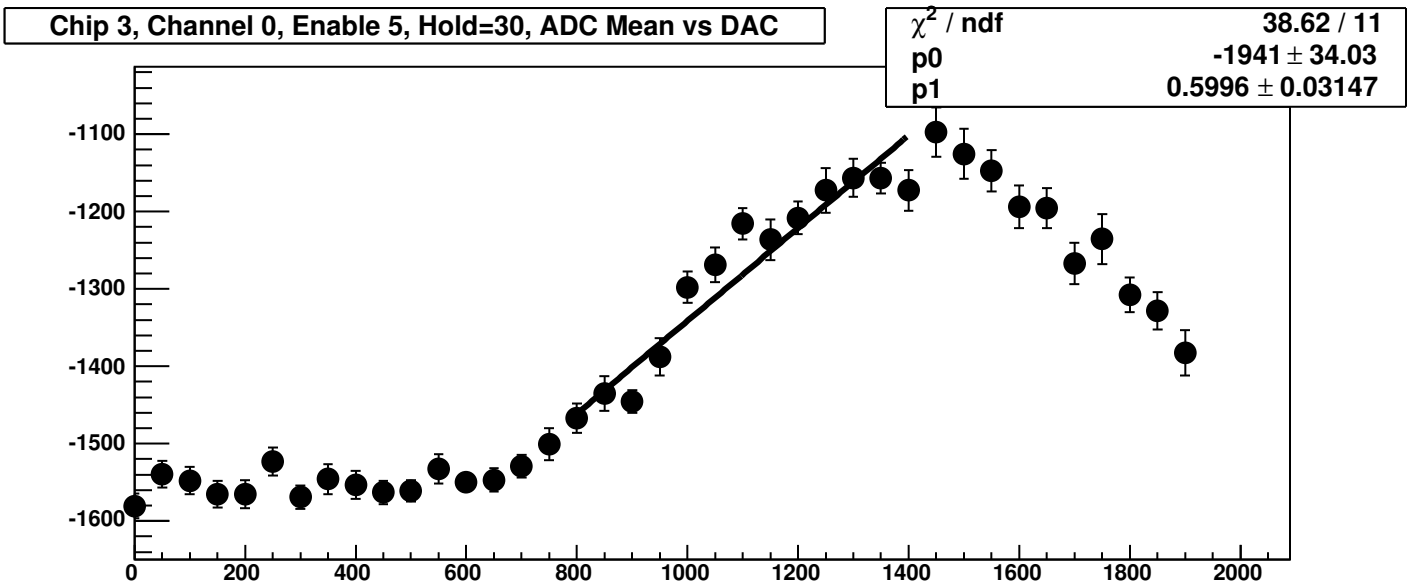


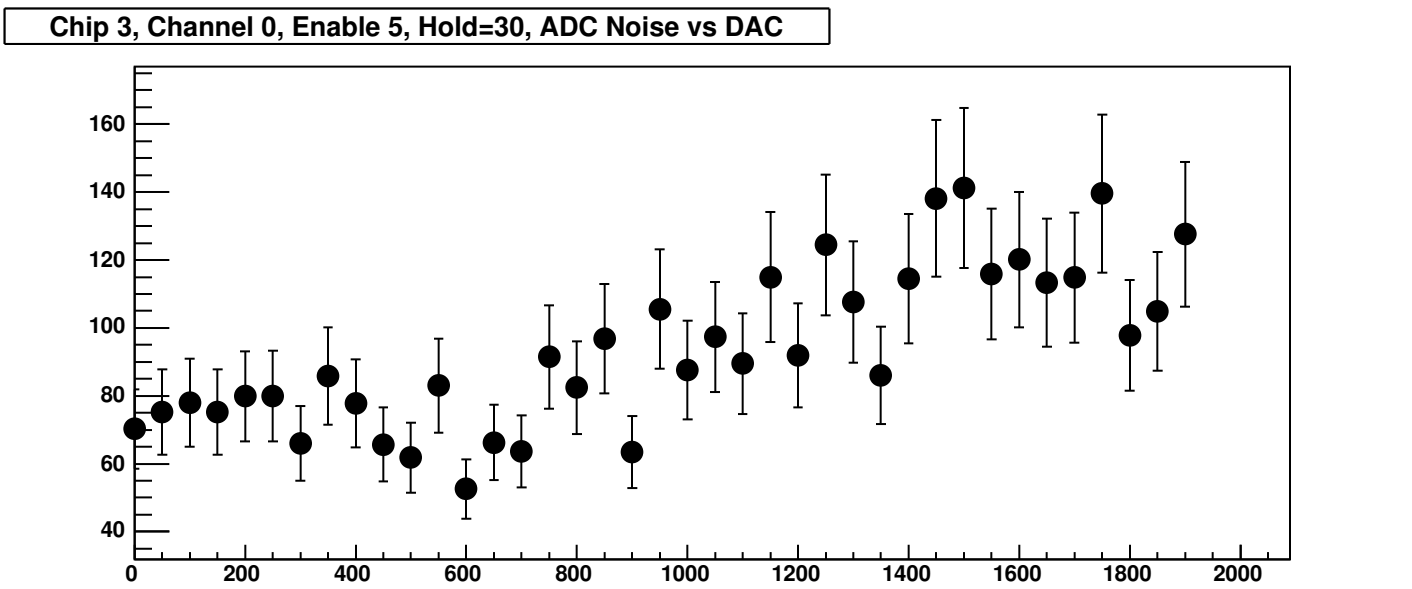
Chip 3, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC

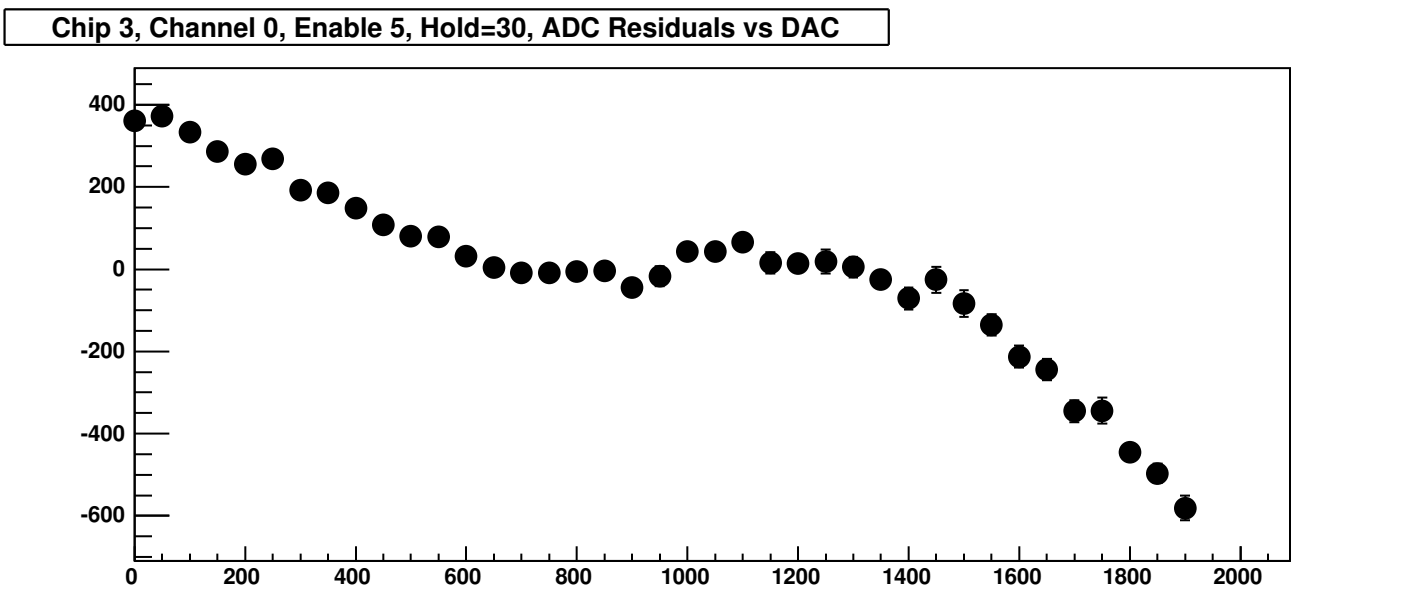


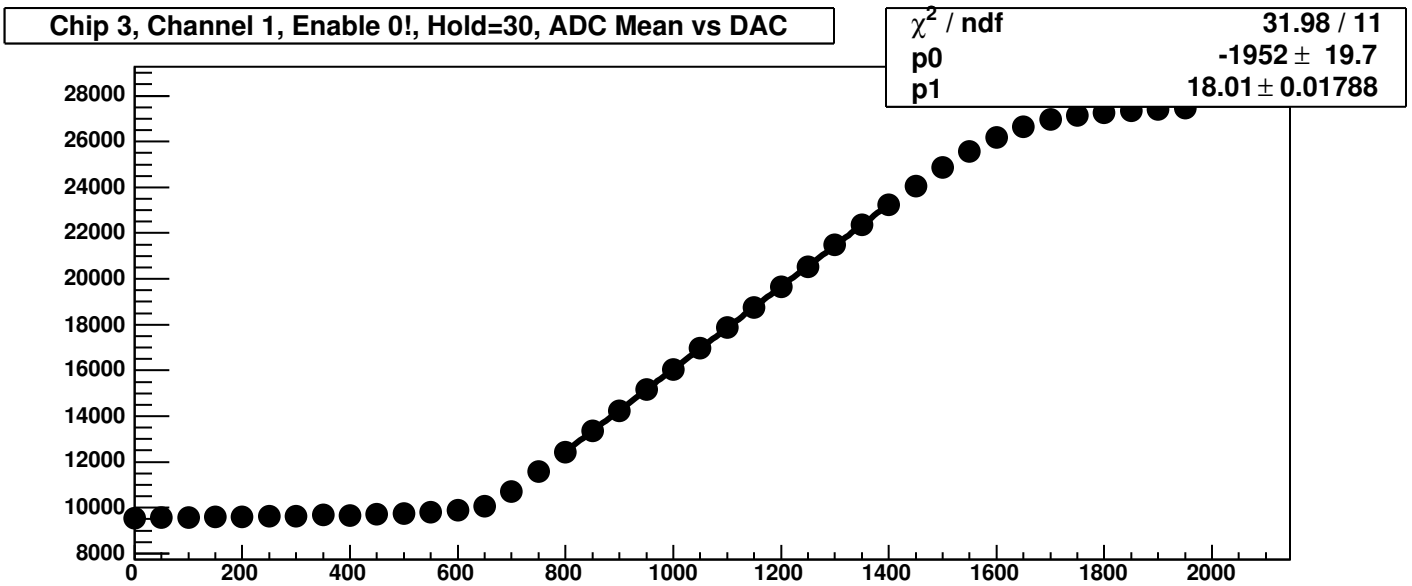
Chip 3, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC

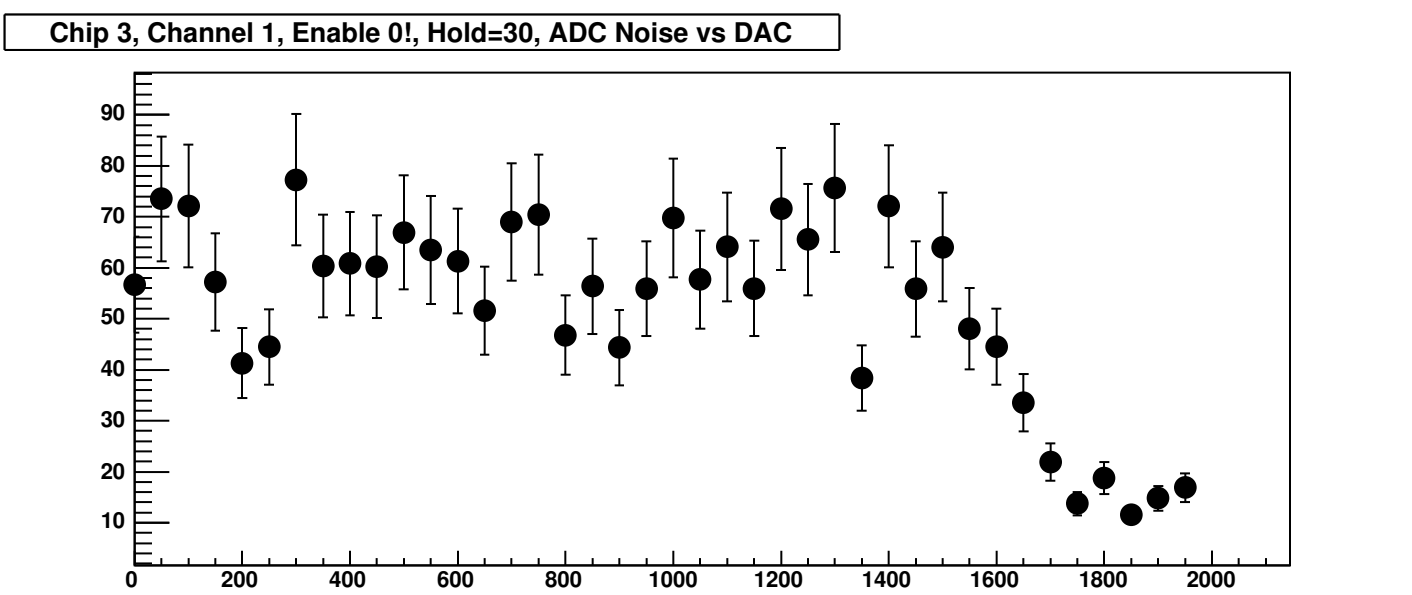


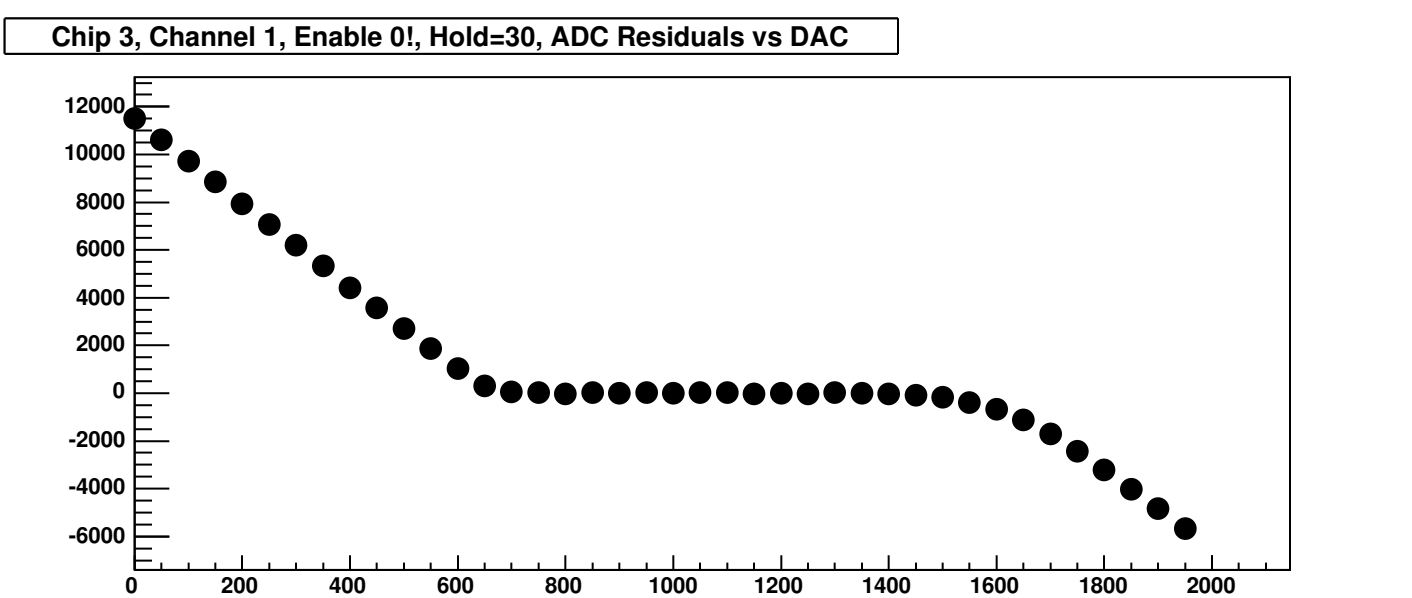

Chip 3, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 38.62 / 11
p0 -1941 ± 34.03
p1 0.5996 ± 0.03147
-1100
-1200
-1300
-1400
-1500
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

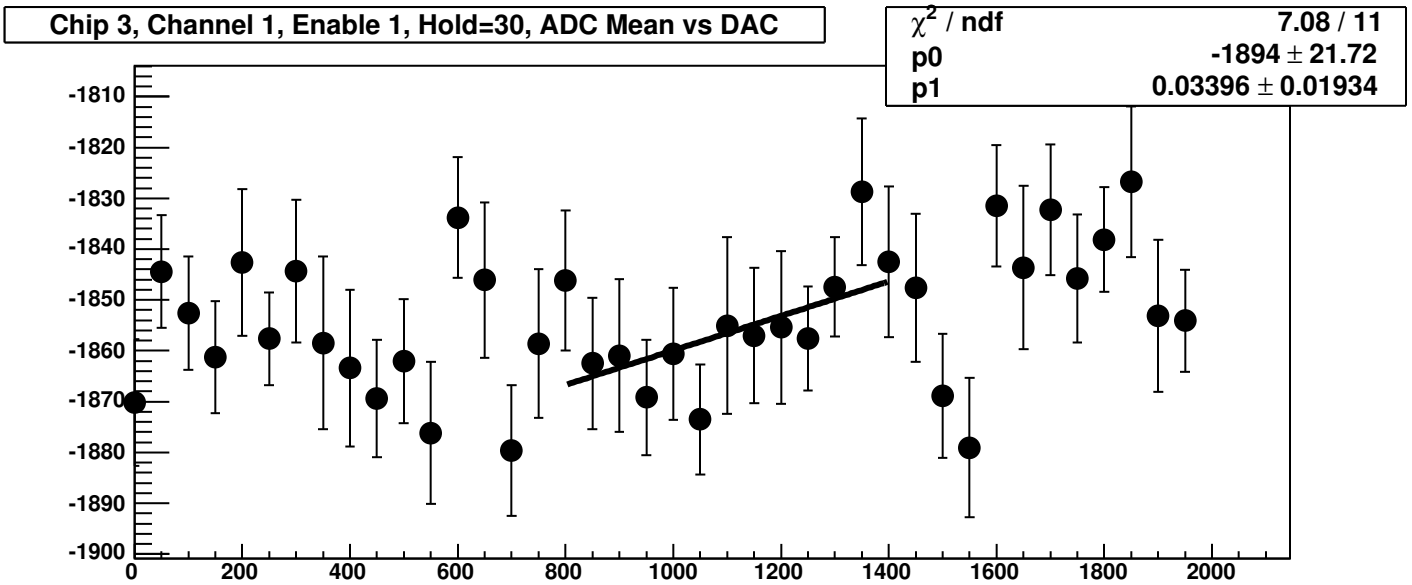

Chip 3, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

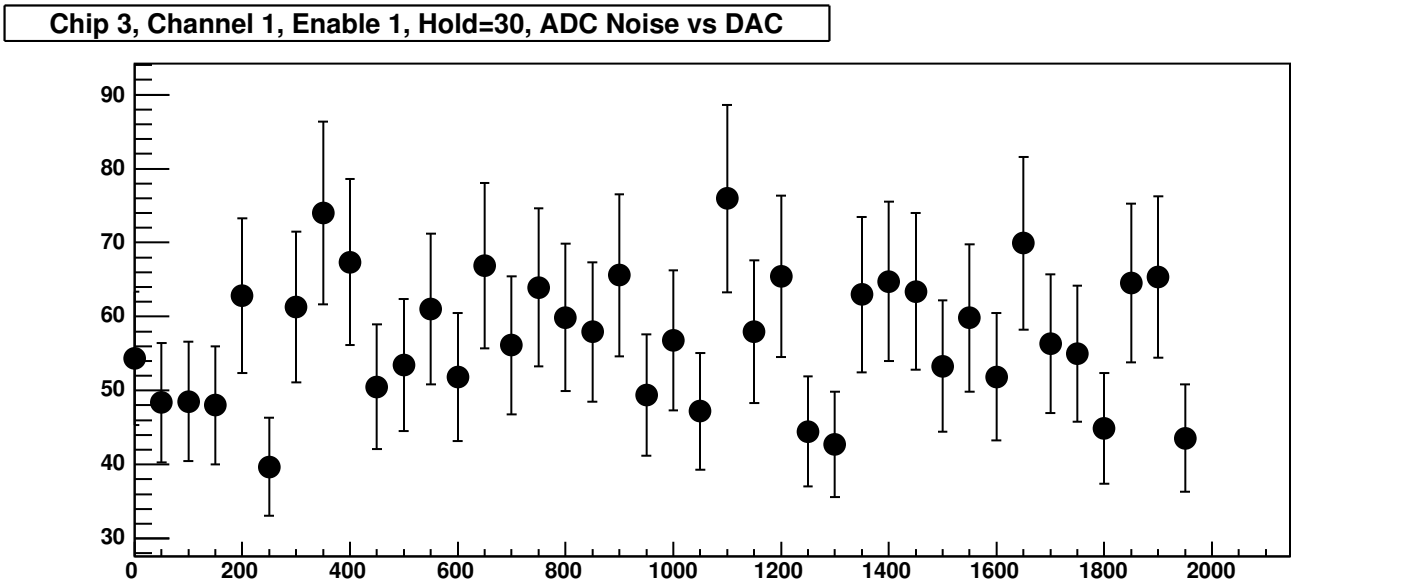

Chip 3, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

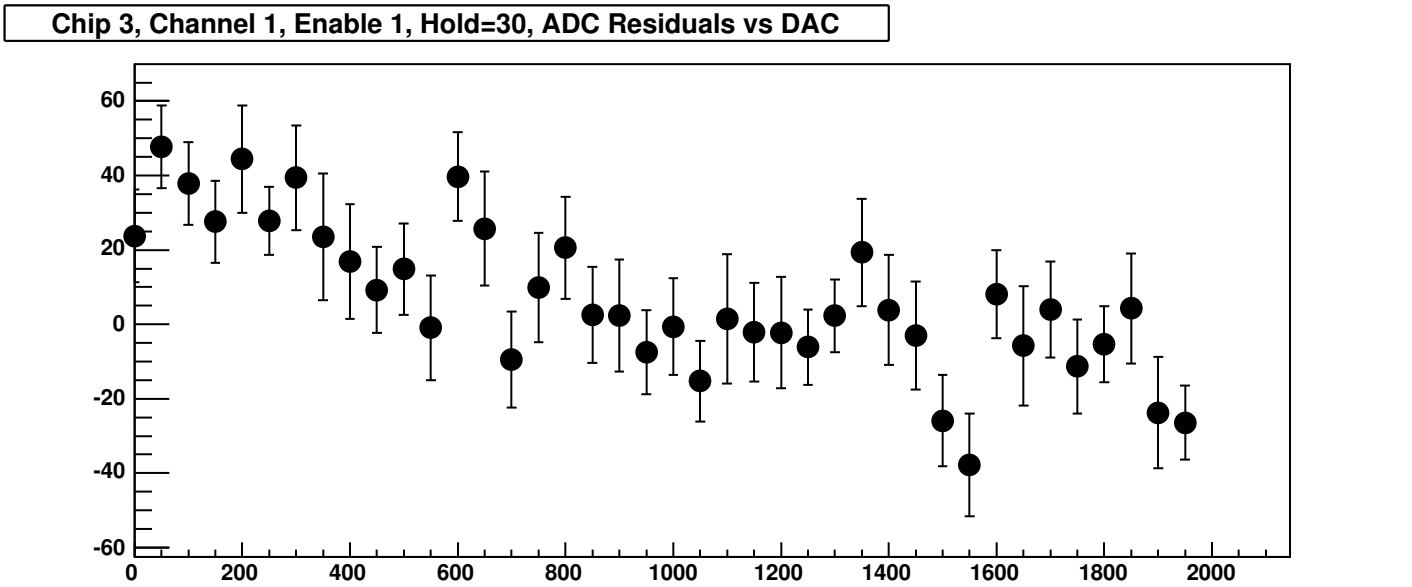

Chip 3, Channel 1, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 31.98 / 11
p0 -1952 ± 19.7
p1 18.01 ± 0.01788
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

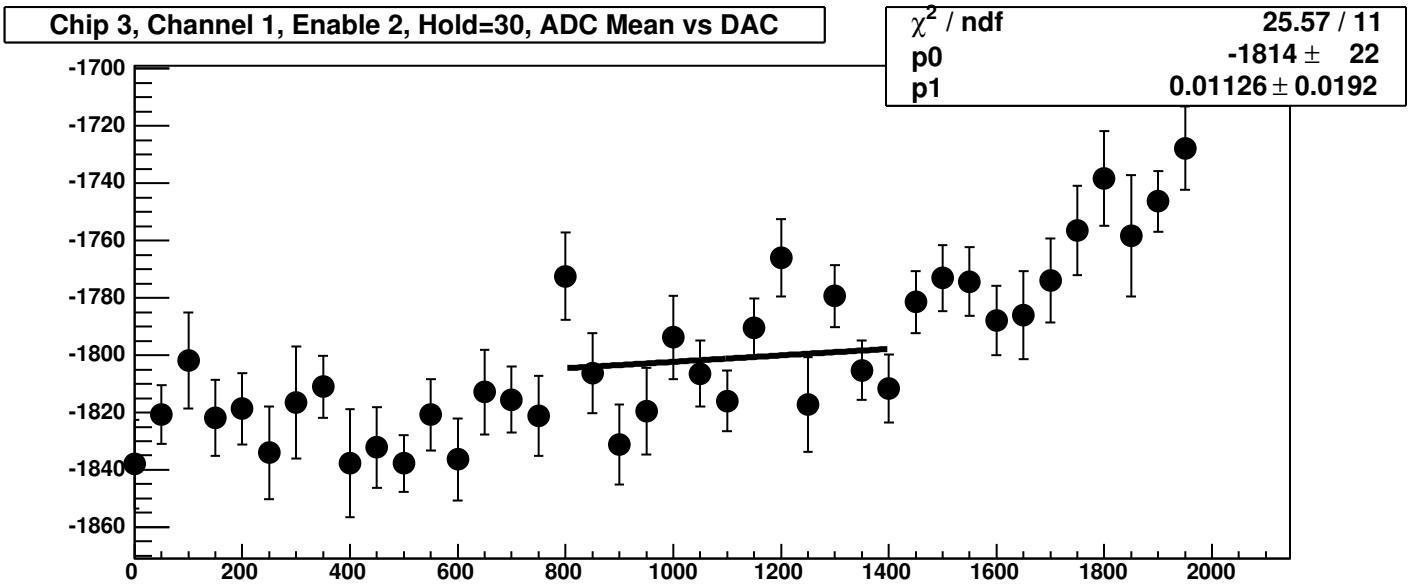

Chip 3, Channel 1, Enable 0!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

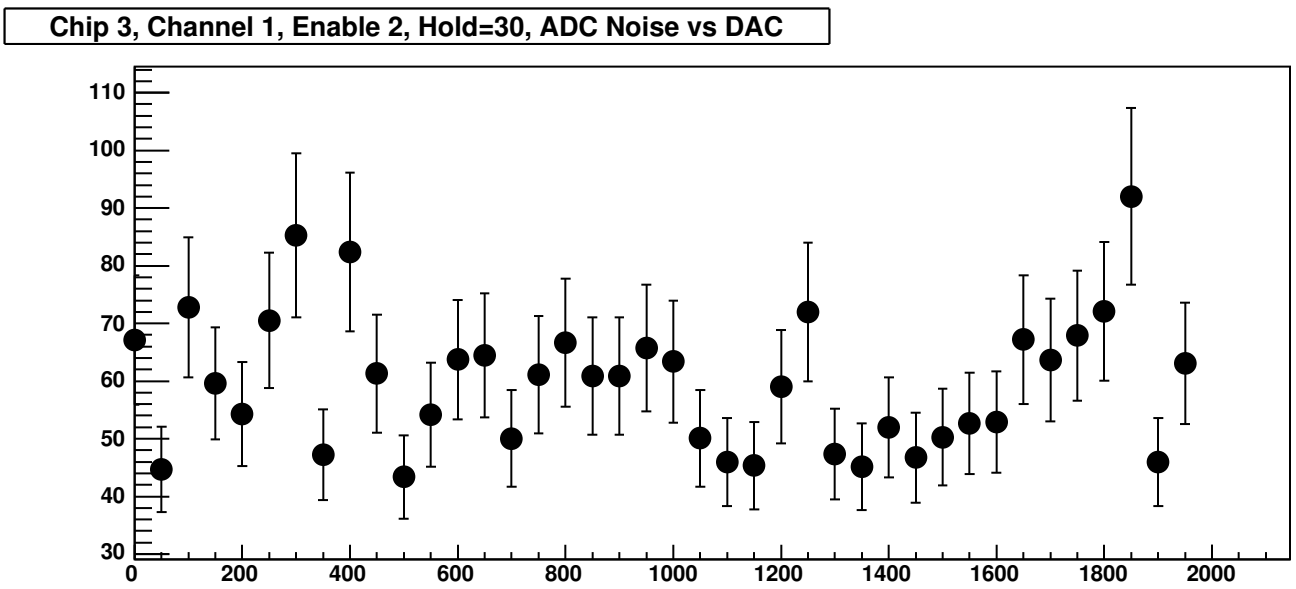

Chip 3, Channel 1, Enable 0!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

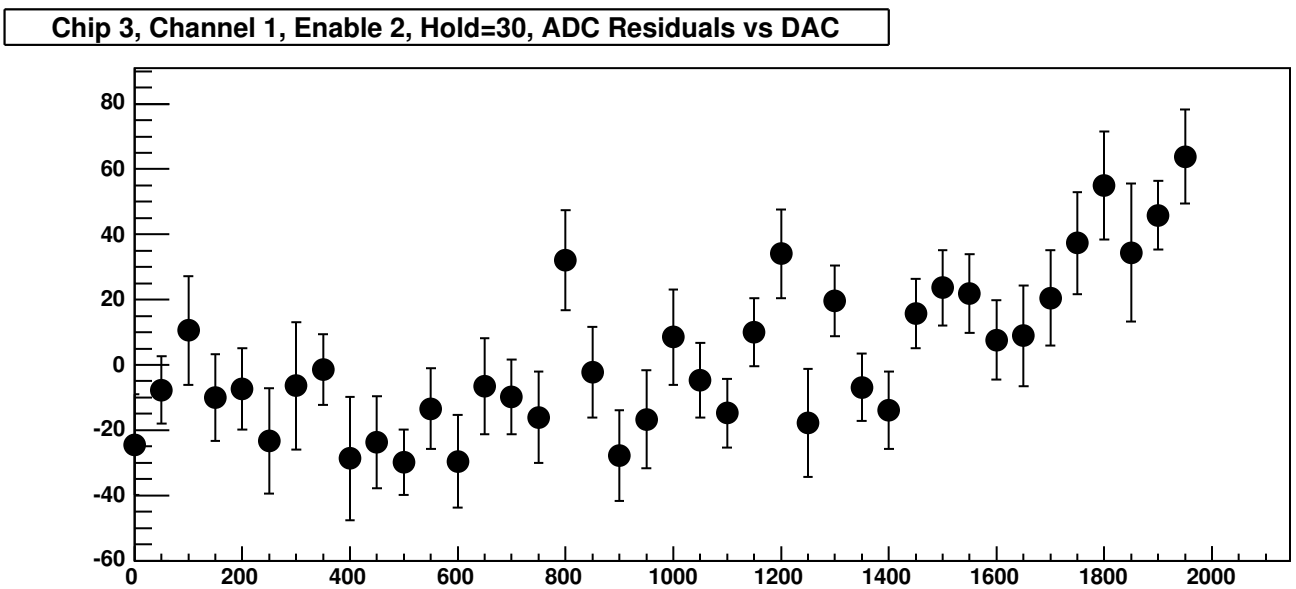

Chip 3, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7.08 / 11
p0 -1894 ± 21.72
p1 0.03396 ± 0.01934
-1810
-1820
-1830
-1840
-1850
-1860
-1870
-1880
-1890
-1900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

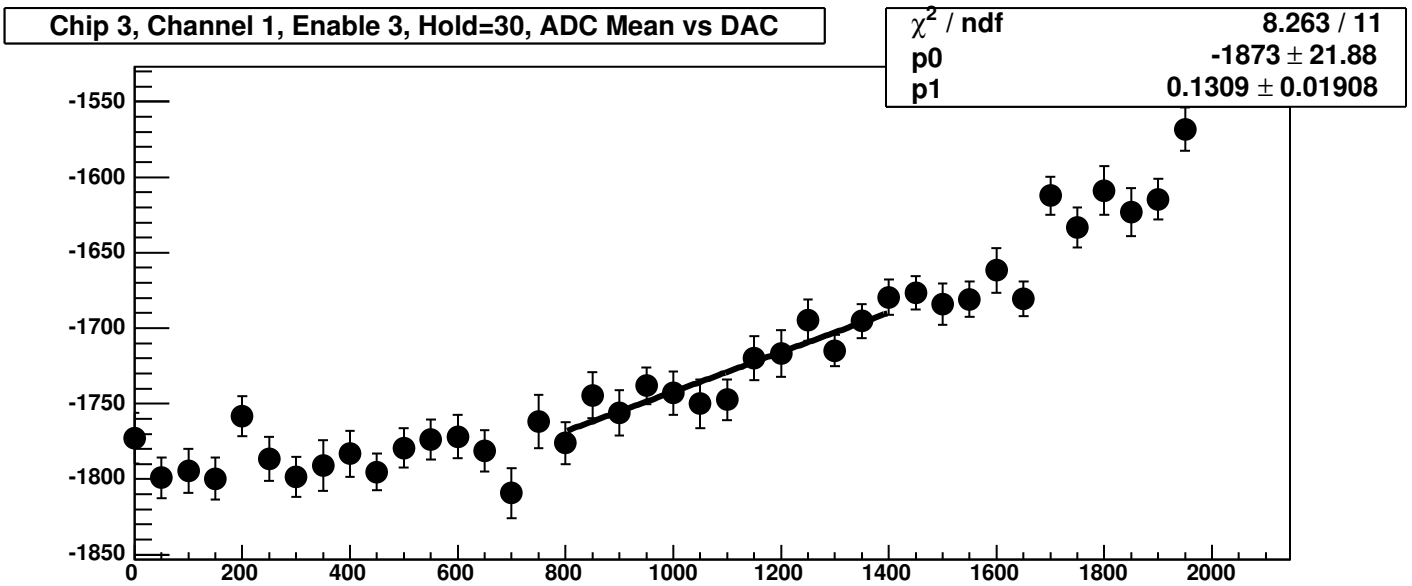

Chip 3, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

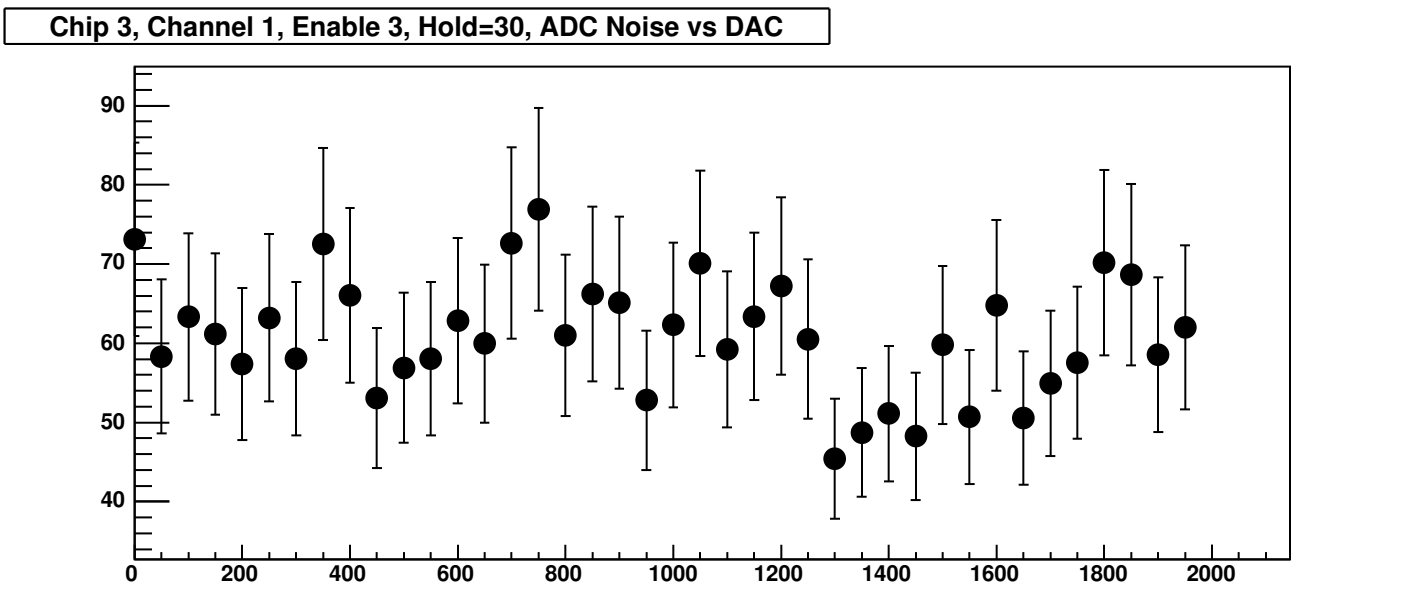

Chip 3, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

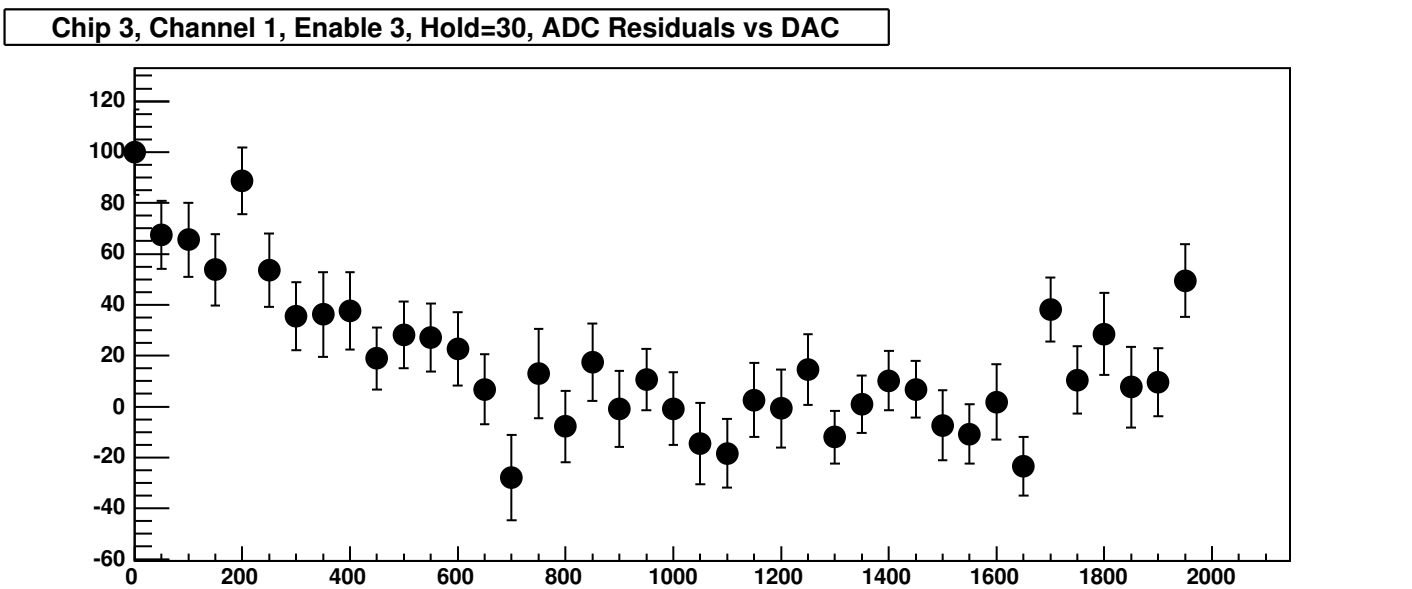

Chip 3, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 25.57 / 11
p0 -1814 ± 22
p1 0.01126 ± 0.0192
-1700
-1720
-1740
-1760
-1780
-1800
-1820
-1840
-1860
0
200
400
600
800
1000
1200
1400
1600
1800
2000

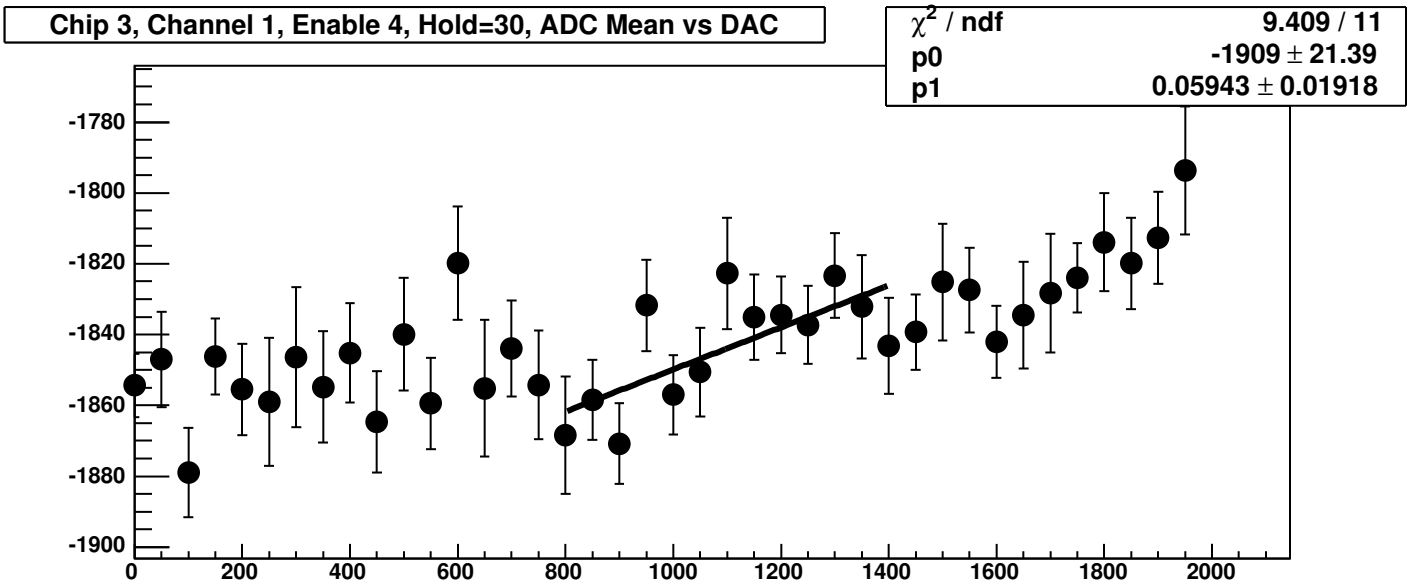

Chip 3, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

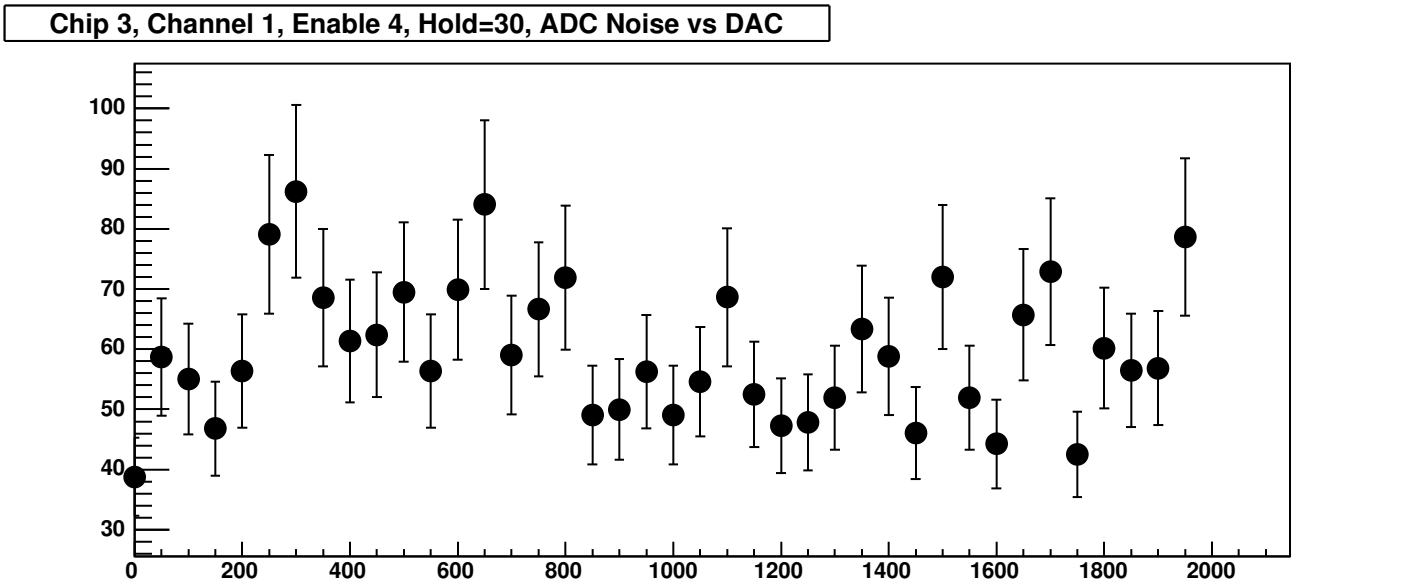

Chip 3, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

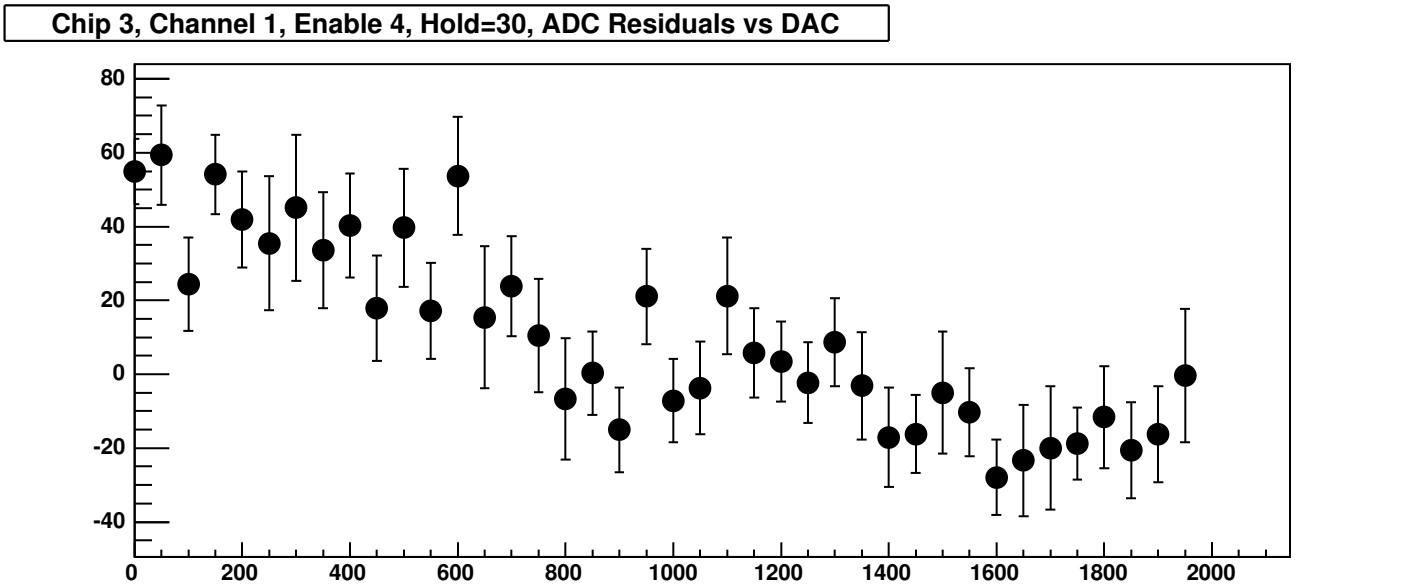

Chip 3, Channel 1, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 8.263 / 11
p0 -1873 ± 21.88
p1 0.1309 ± 0.01908
-1550
-1600
-1650
-1700
-1750
-1800
-1850
0
200
400
600
800
1000
1200
1400
1600
1800
2000

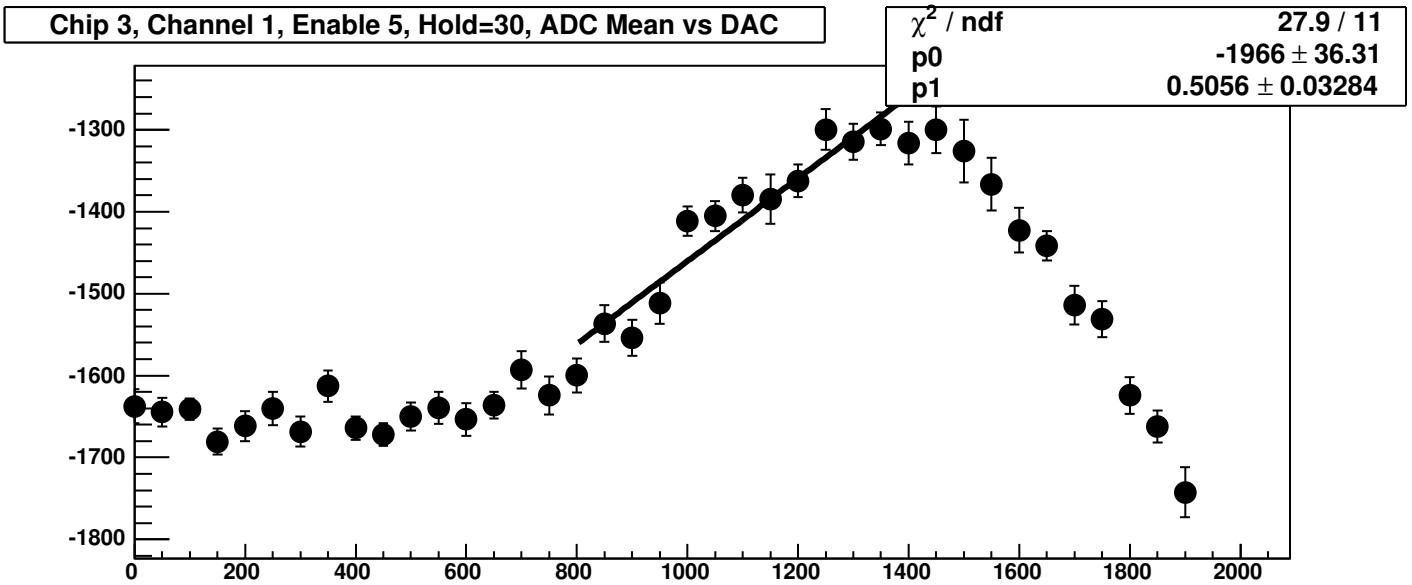

Chip 3, Channel 1, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

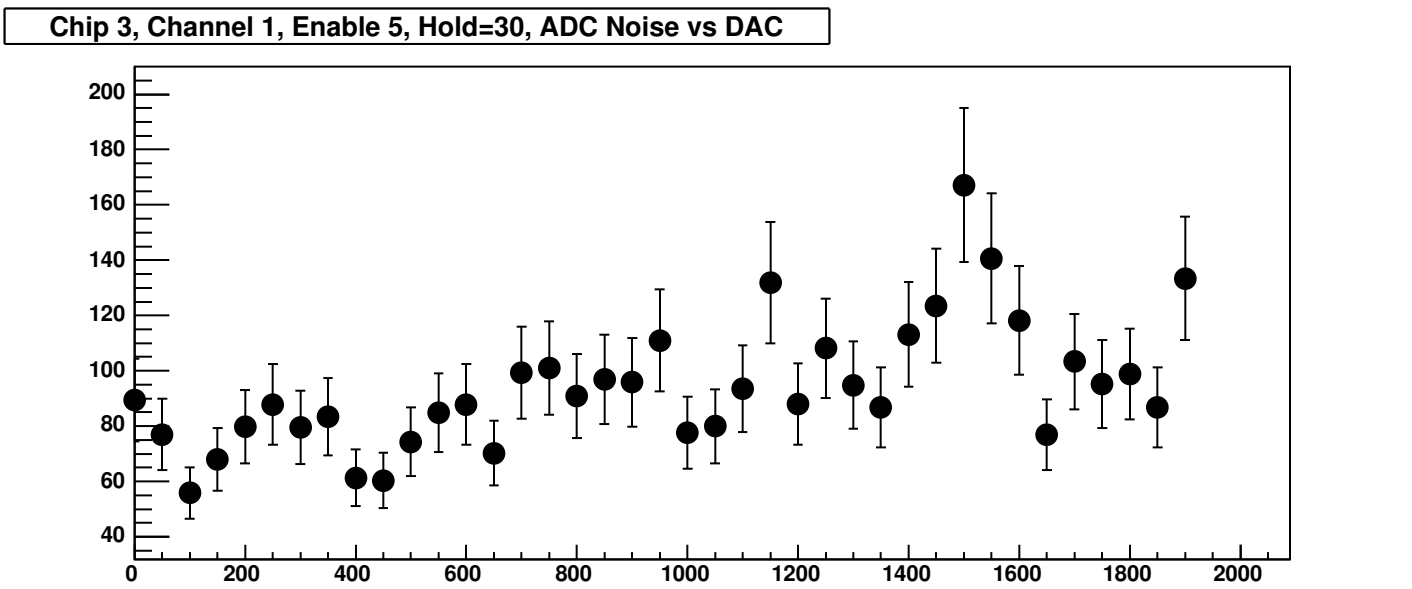

Chip 3, Channel 1, Enable 3, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

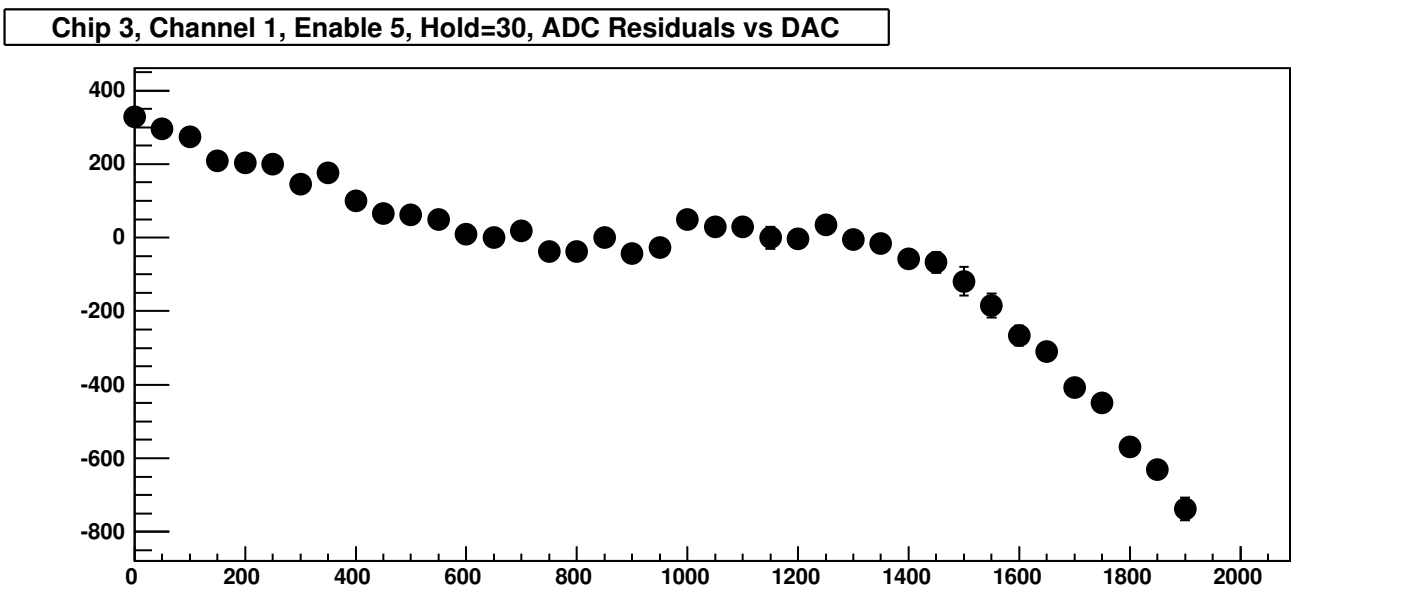

Chip 3, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 9.409 / 11
p0 -1909 ± 21.39
p1 0.05943 ± 0.01918
-1780
-1800
-1820
-1840
-1860
-1880
-1900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

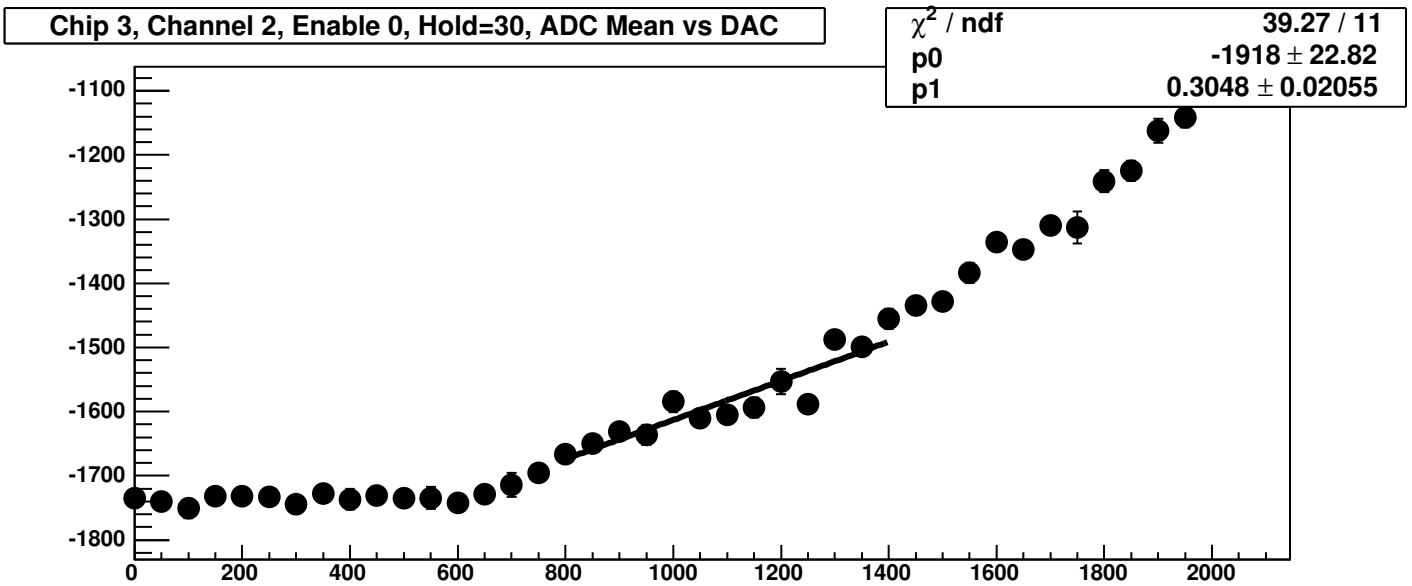

Chip 3, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

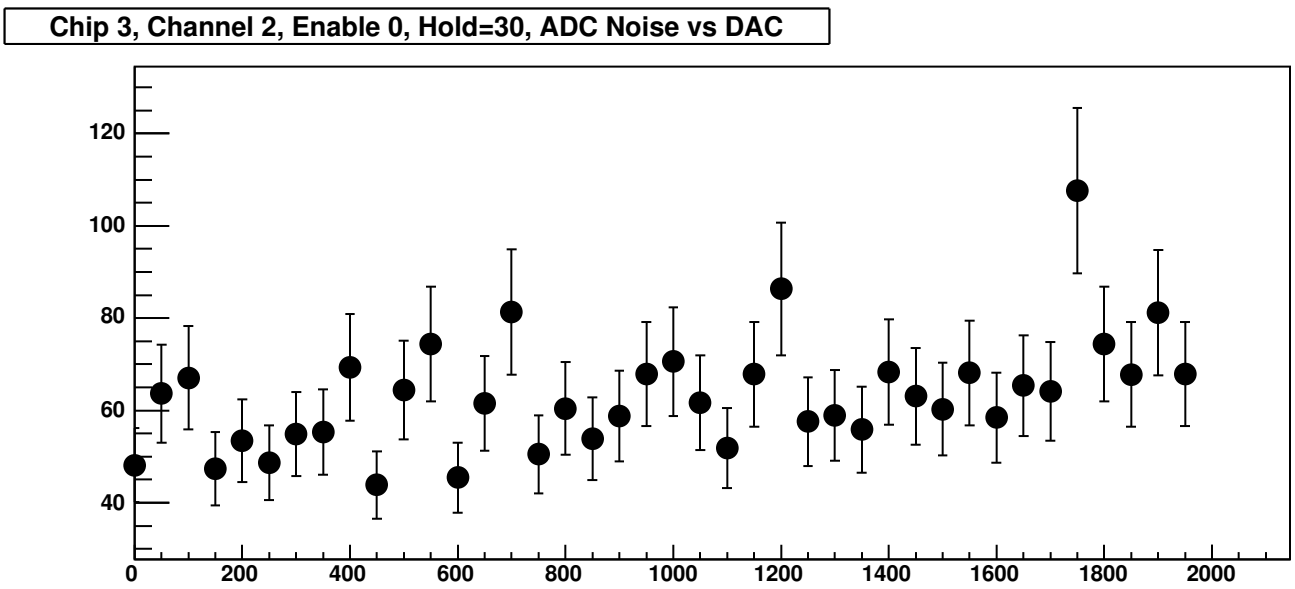

Chip 3, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

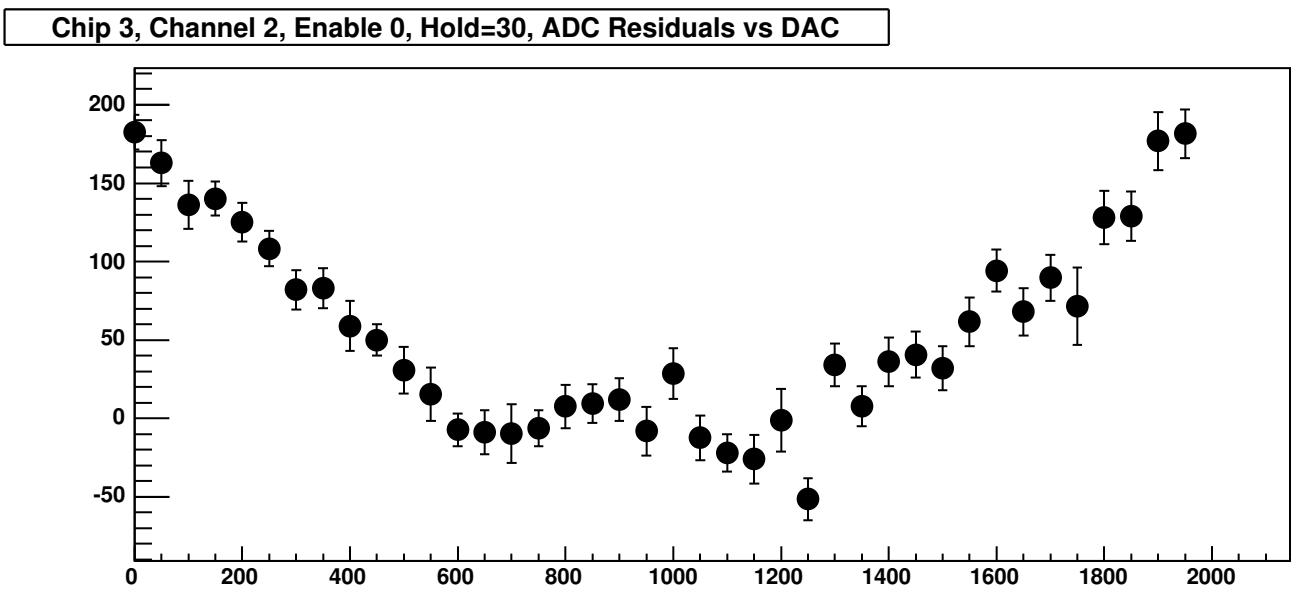

Chip 3, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 27.9 / 11
p0 -1966 ± 36.31
p1 0.5056 ± 0.03284
-1300
-1400
-1500
-1600
-1700
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

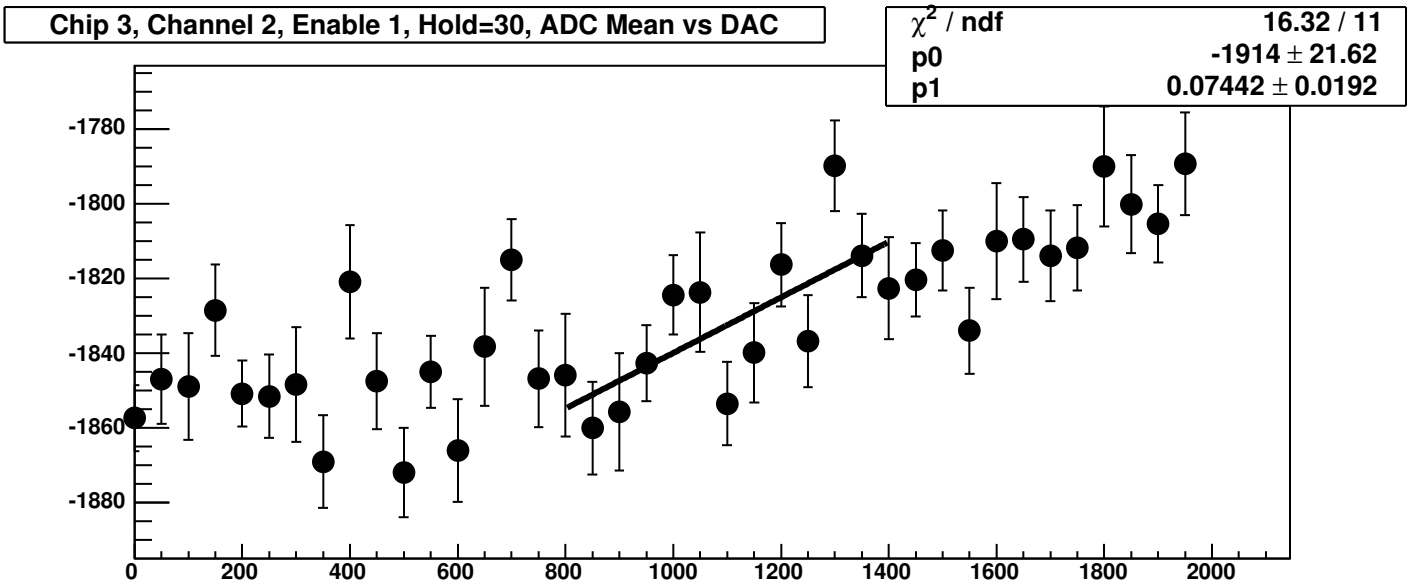

Chip 3, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

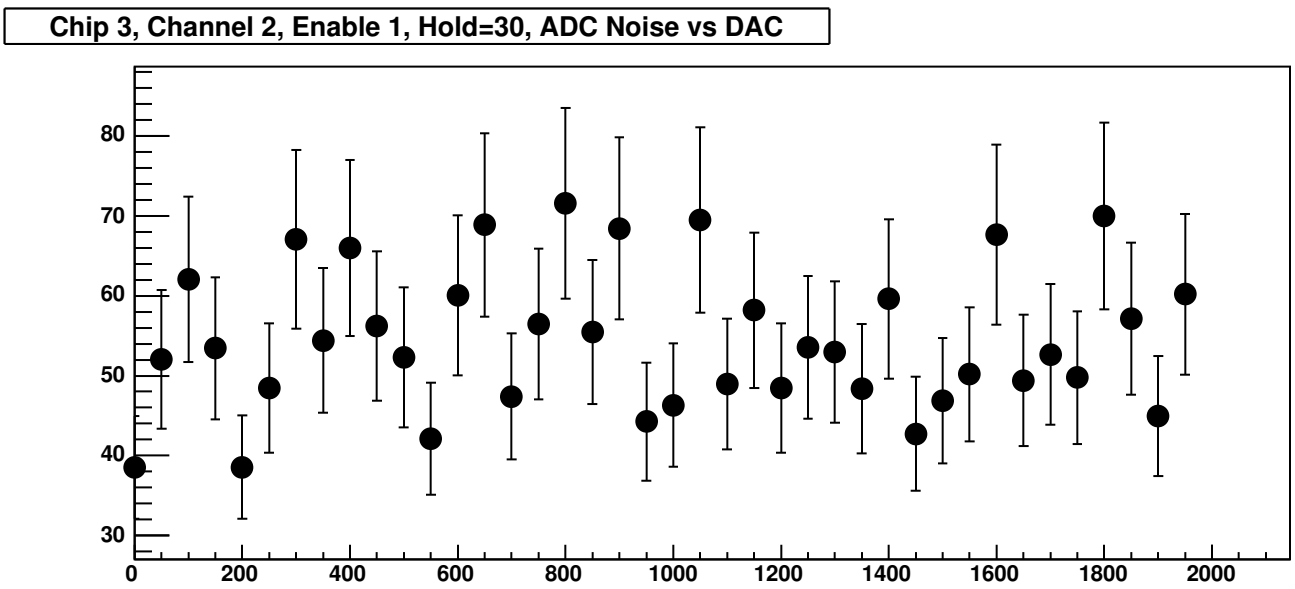

Chip 3, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

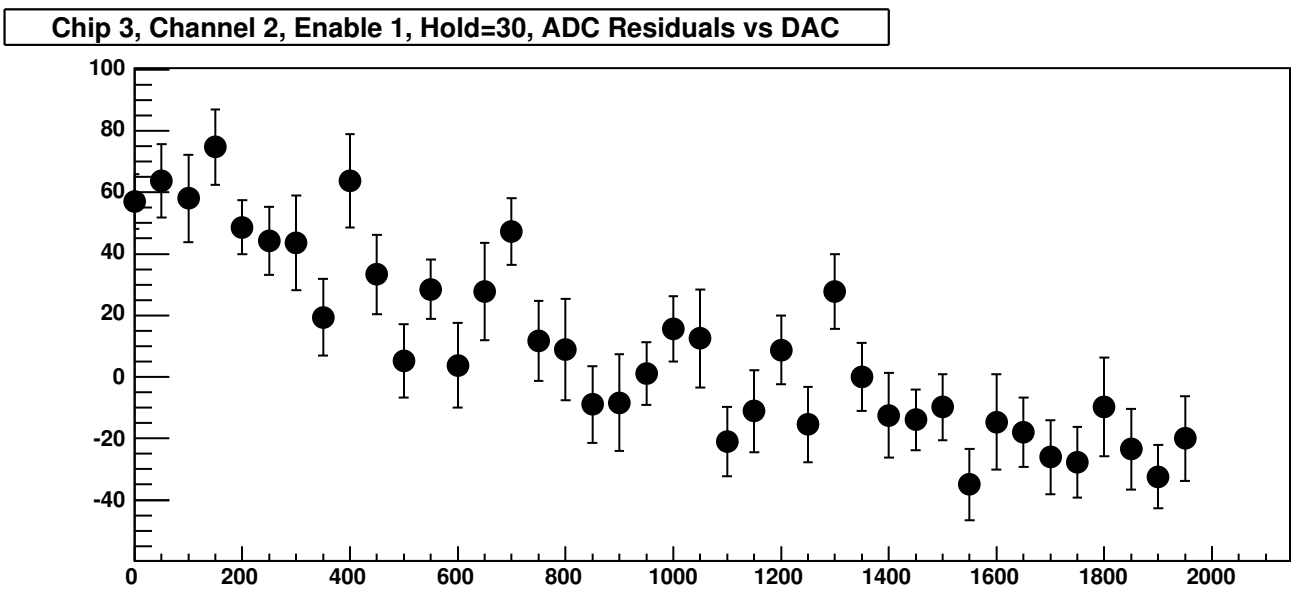

Chip 3, Channel 2, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 39.27 / 11
p0 -1918 ± 22.82
p1 0.3048 ± 0.02055
-1100
-1200
-1300
-1400
-1500
-1600
-1700
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

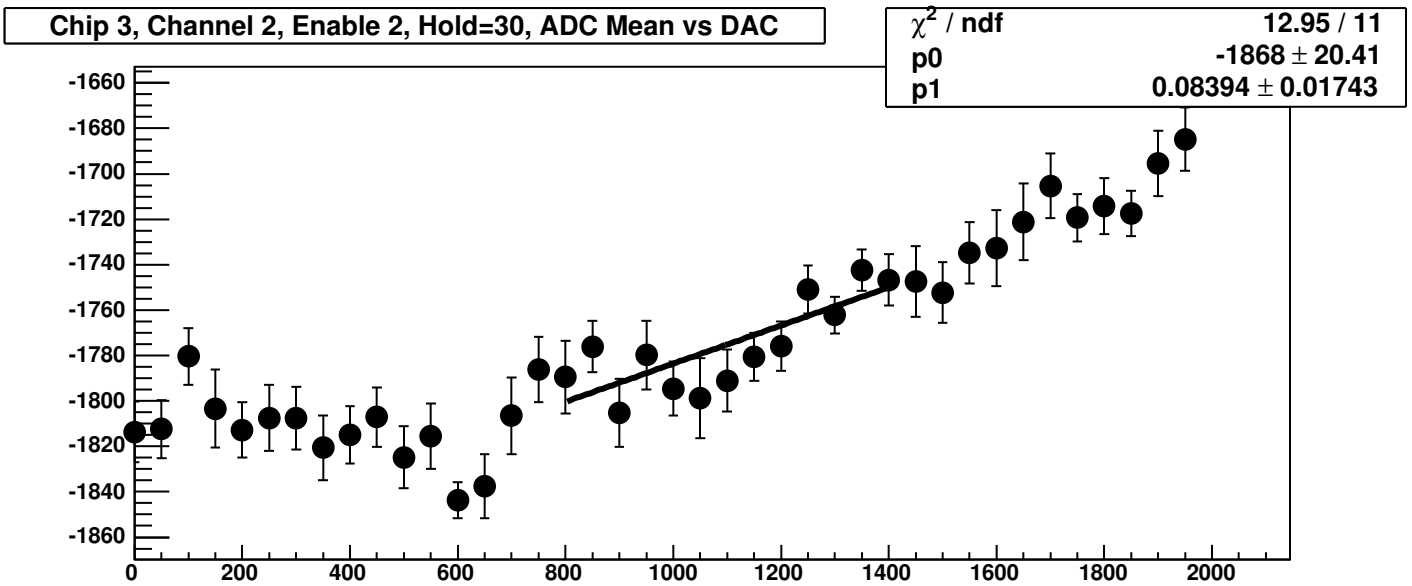

Chip 3, Channel 2, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

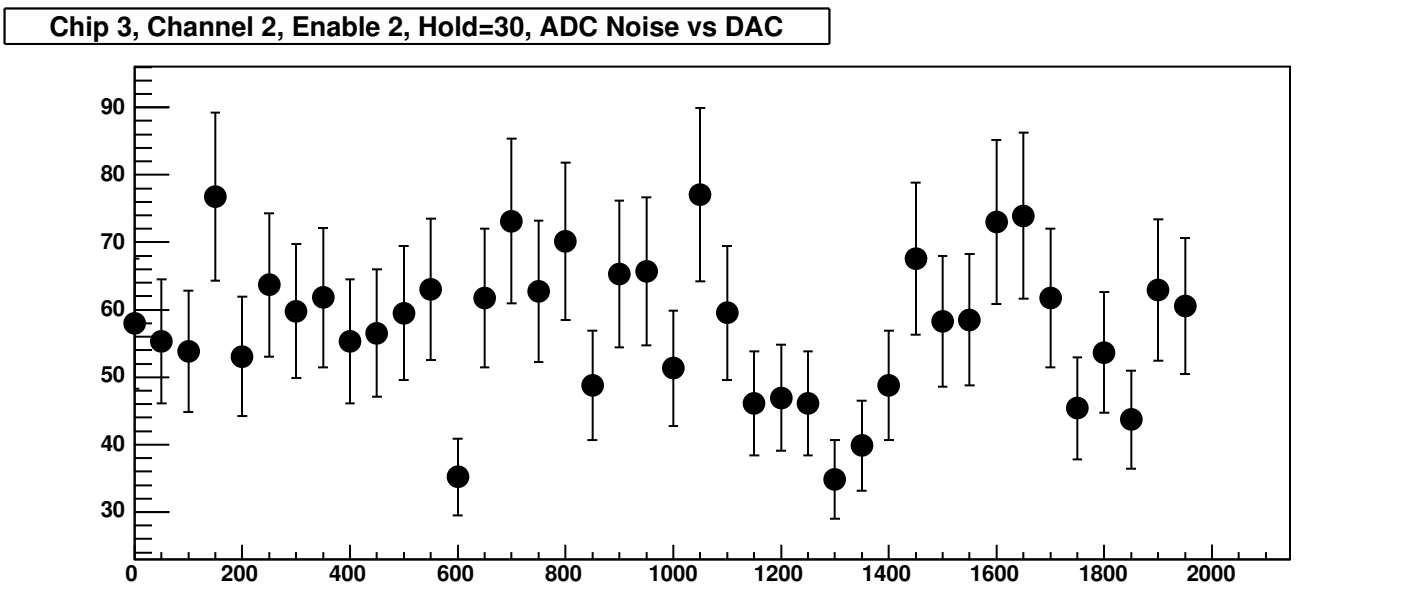

Chip 3, Channel 2, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

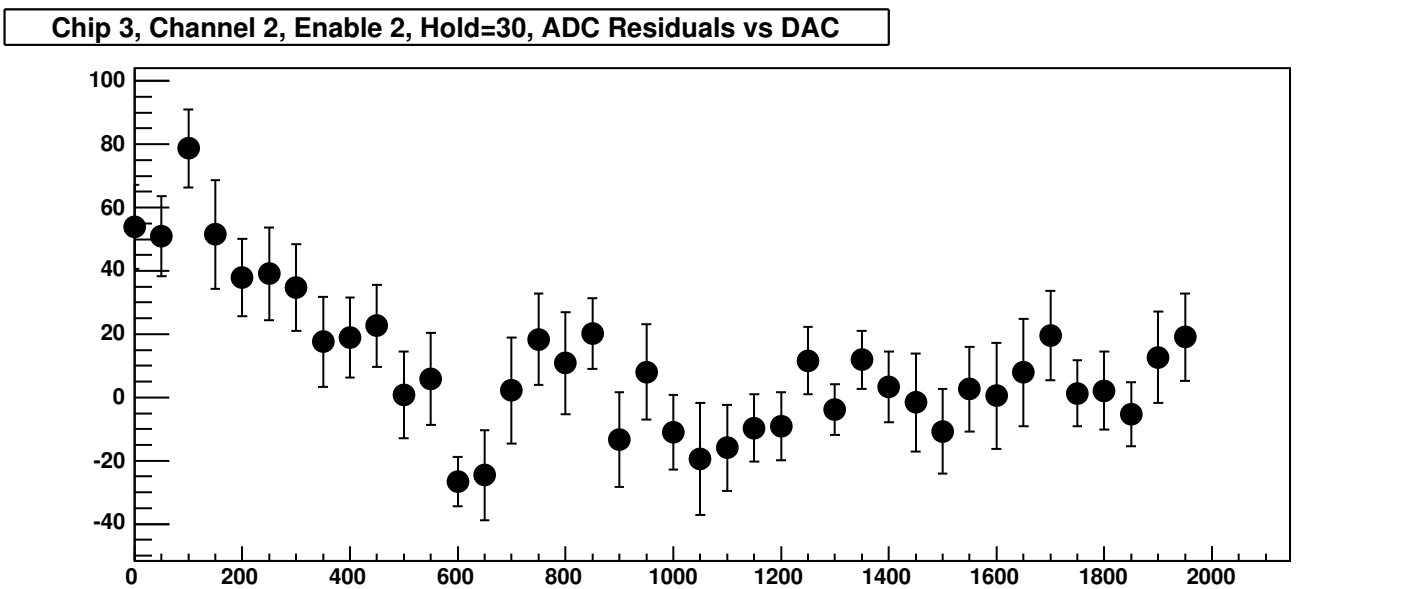

Chip 3, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 16.32 / 11
p0 -1914 ± 21.62
p1 0.07442 ± 0.0192
-1780
-1800
-1820
-1840
-1860
-1880
0
200
400
600
800
1000
1200
1400
1600
1800
2000

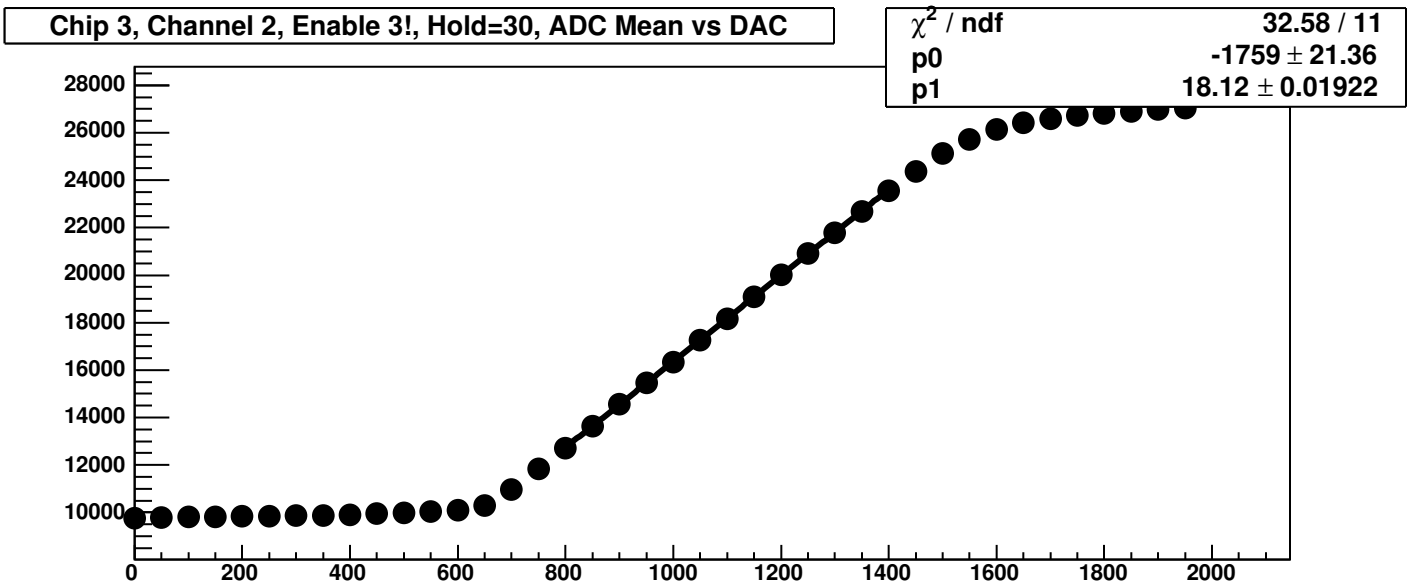

Chip 3, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

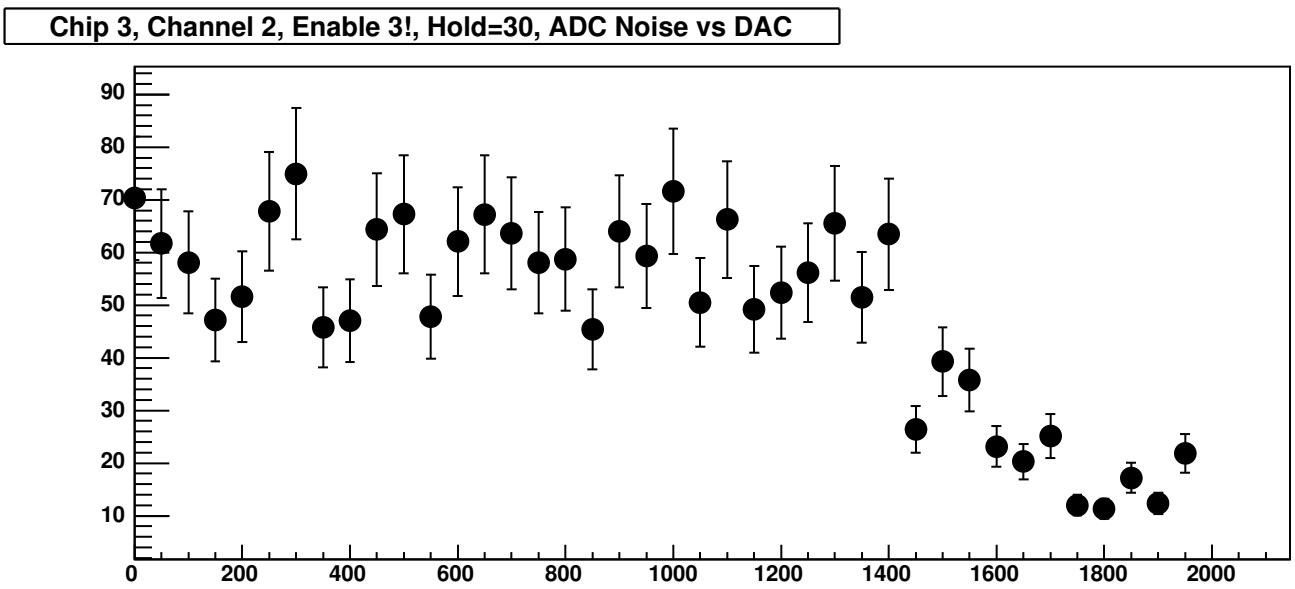

Chip 3, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

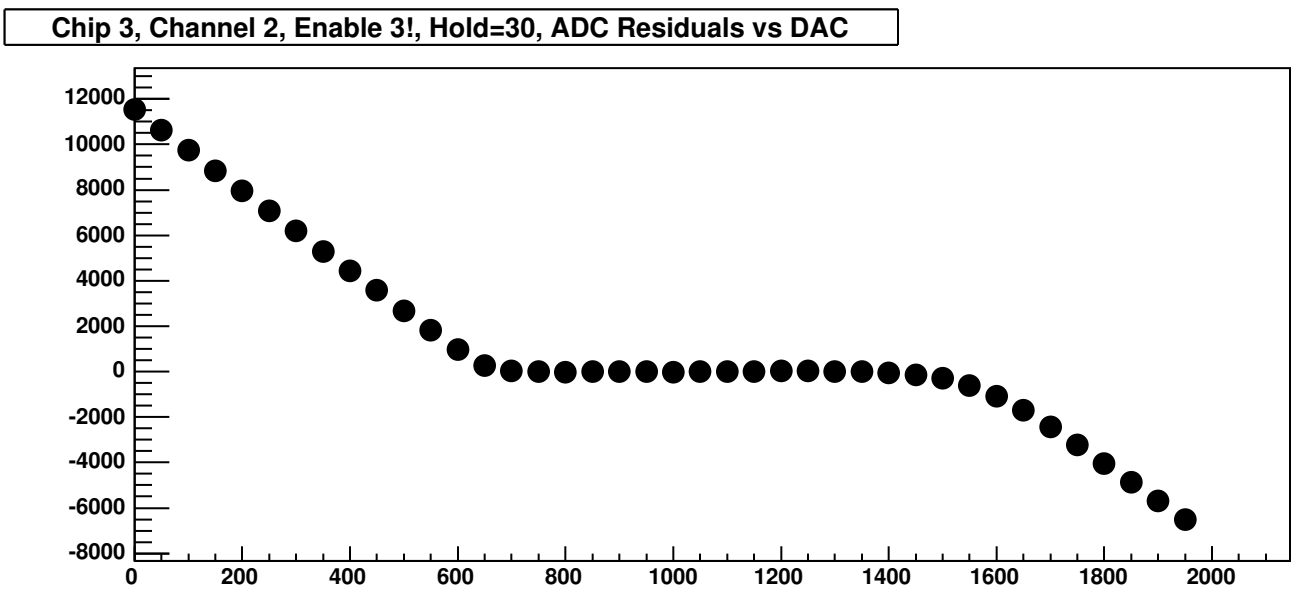

Chip 3, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 12.95 / 11
p0 -1868 ± 20.41
p1 0.08394 ± 0.01743
-1660
-1680
-1700
-1720
-1740
-1760
-1780
-1800
-1820
-1840
-1860
0
200
400
600
800
1000
1200
1400
1600
1800
2000

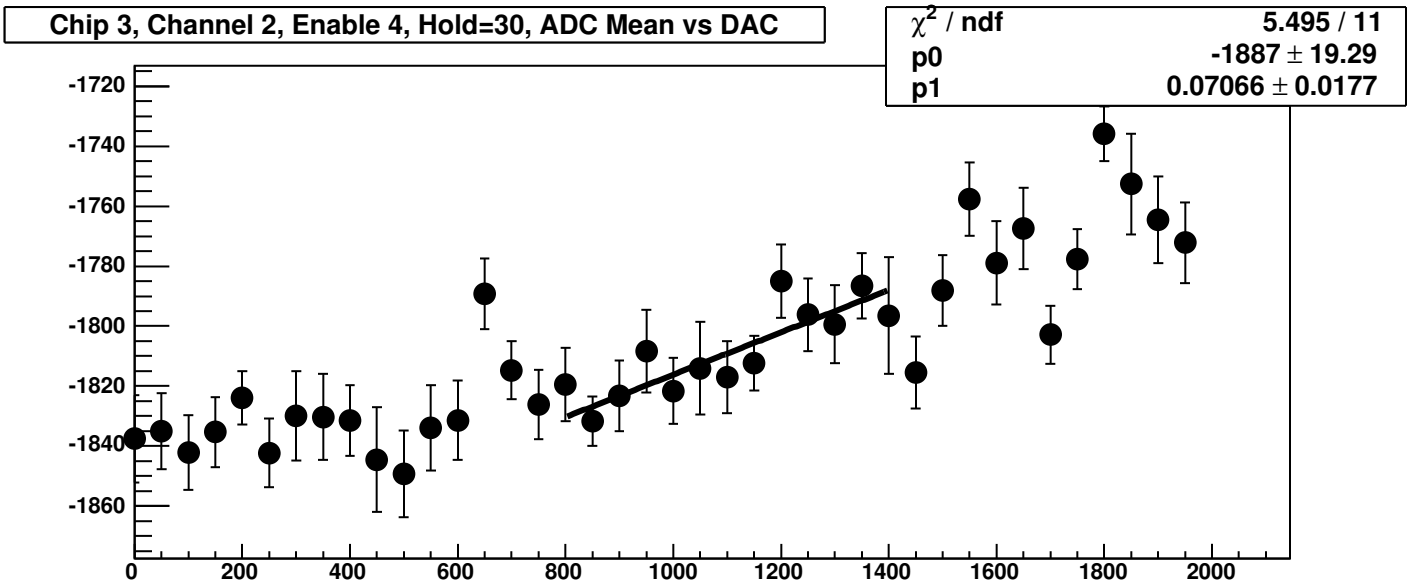

Chip 3, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

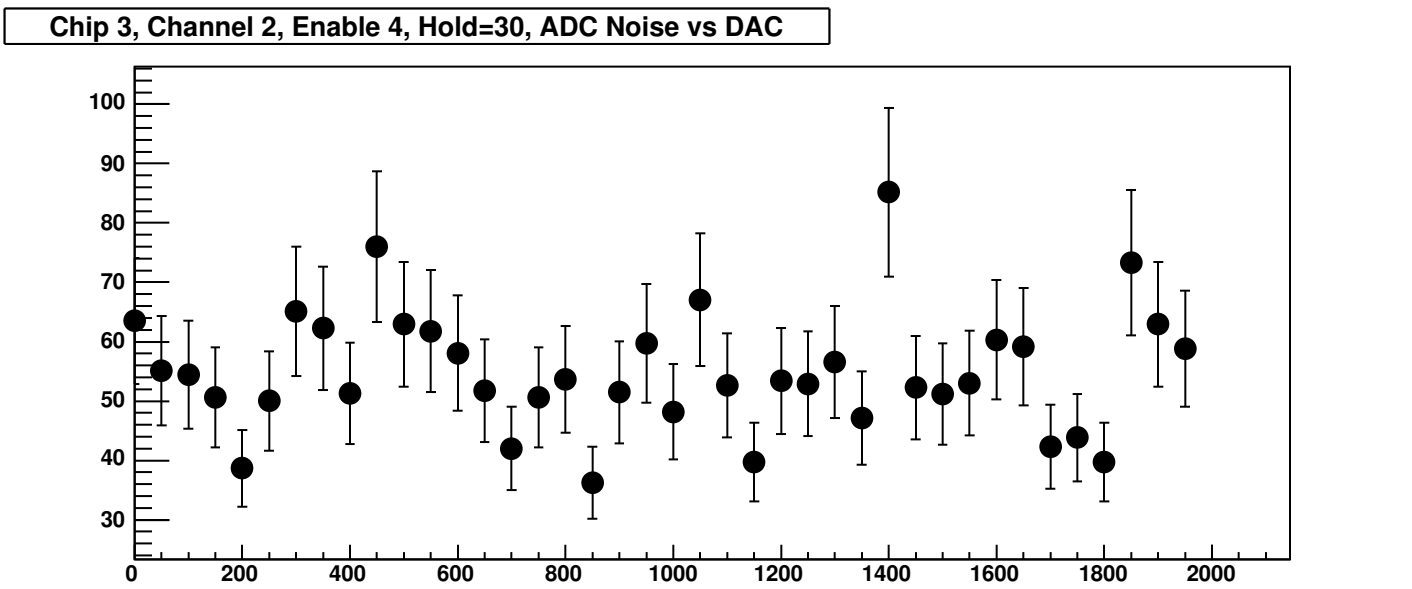

Chip 3, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

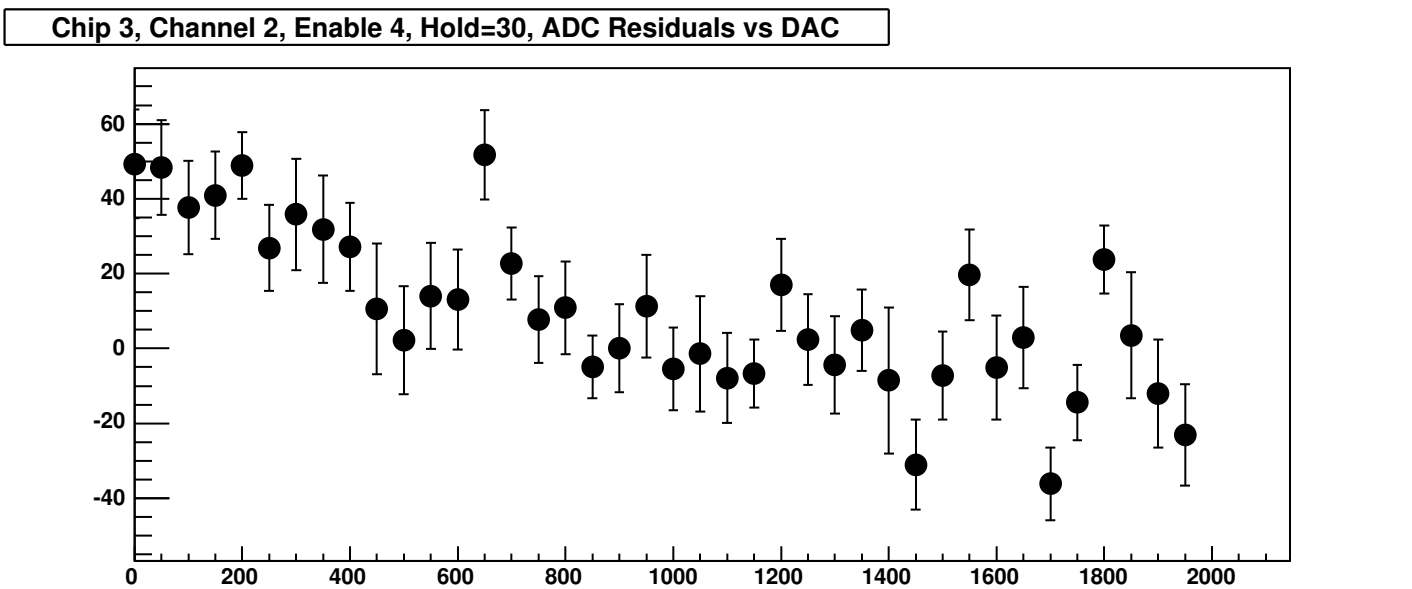

Chip 3, Channel 2, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 32.58 / 11
p0 -1759 ± 21.36
p1 18.12 ± 0.01922
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

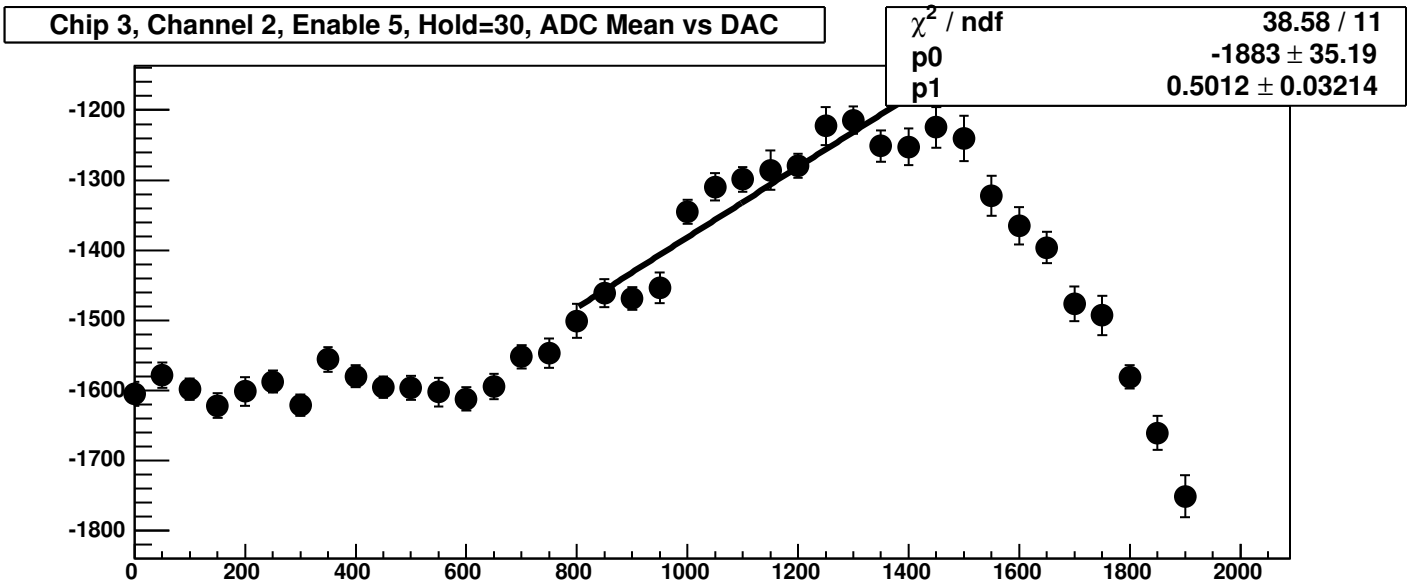

Chip 3, Channel 2, Enable 3!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

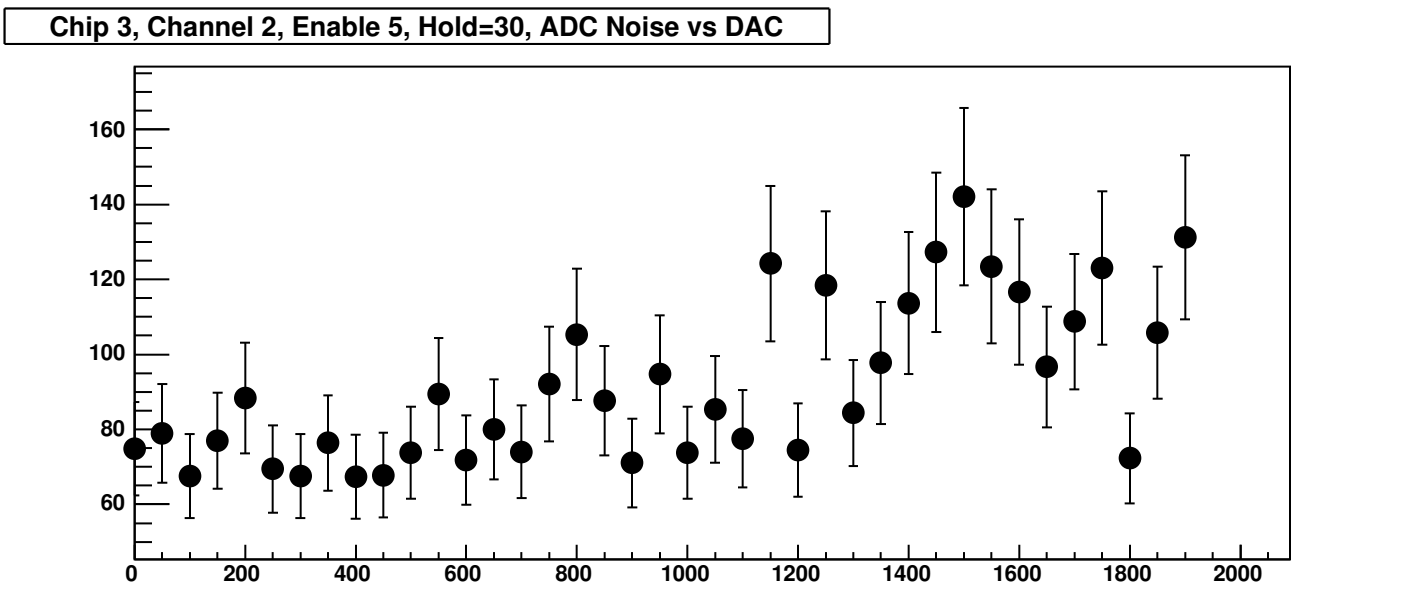

Chip 3, Channel 2, Enable 3!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

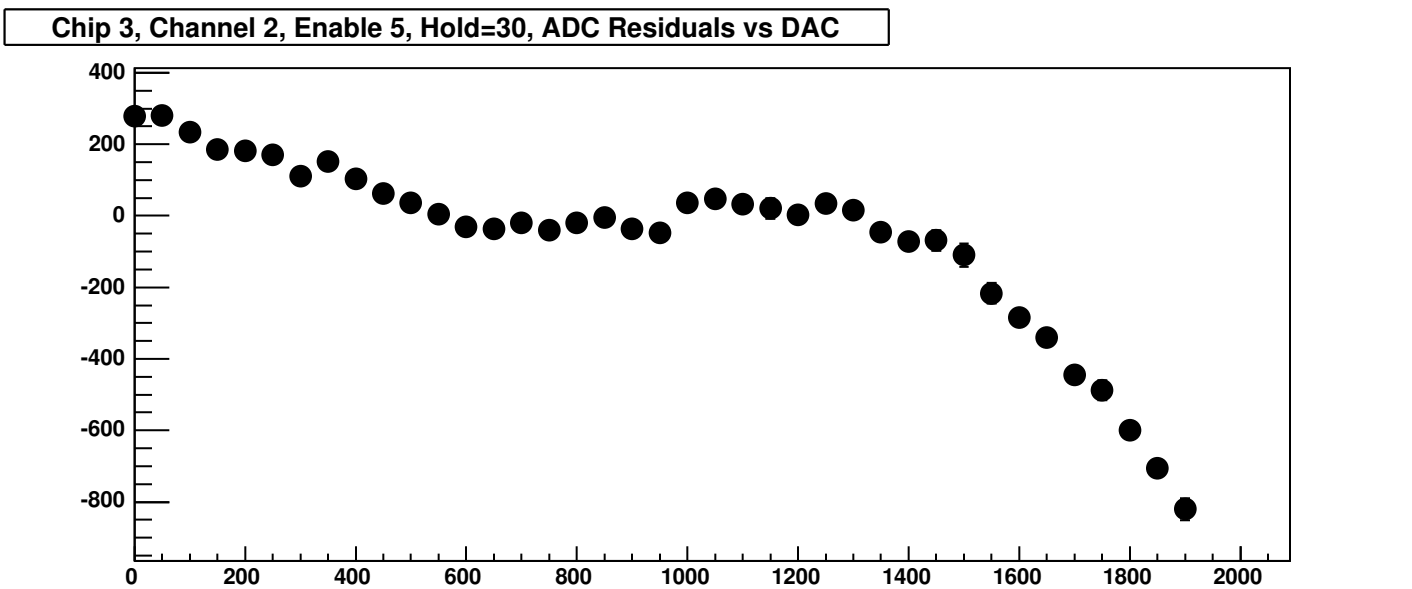

Chip 3, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5.495 / 11
p0 -1887 ± 19.29
p1 0.07066 ± 0.0177
-1720
-1740
-1760
-1780
-1800
-1820
-1840
-1860
0
200
400
600
800
1000
1200
1400
1600
1800
2000

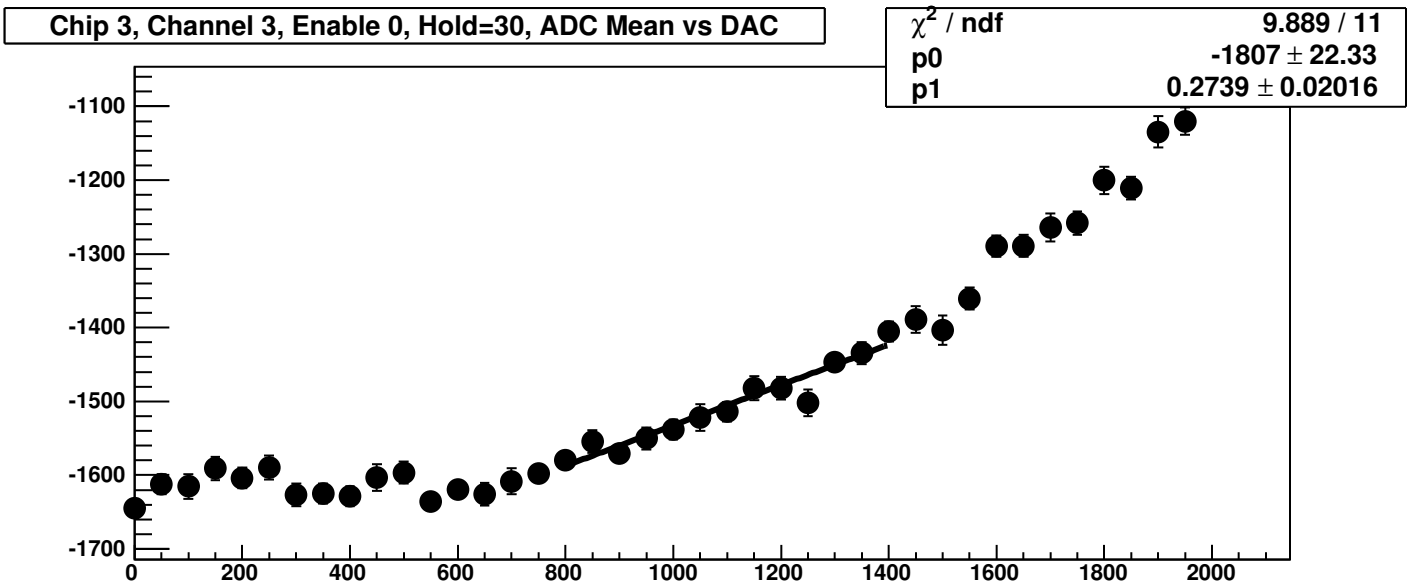

Chip 3, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

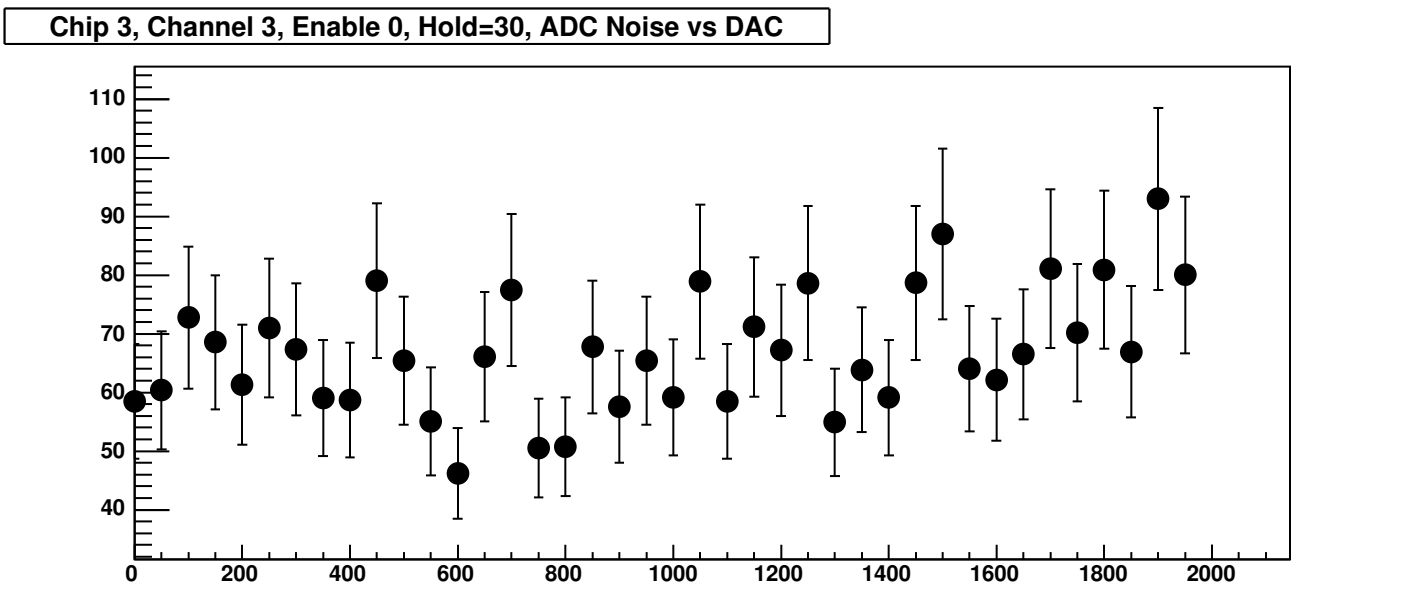

Chip 3, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

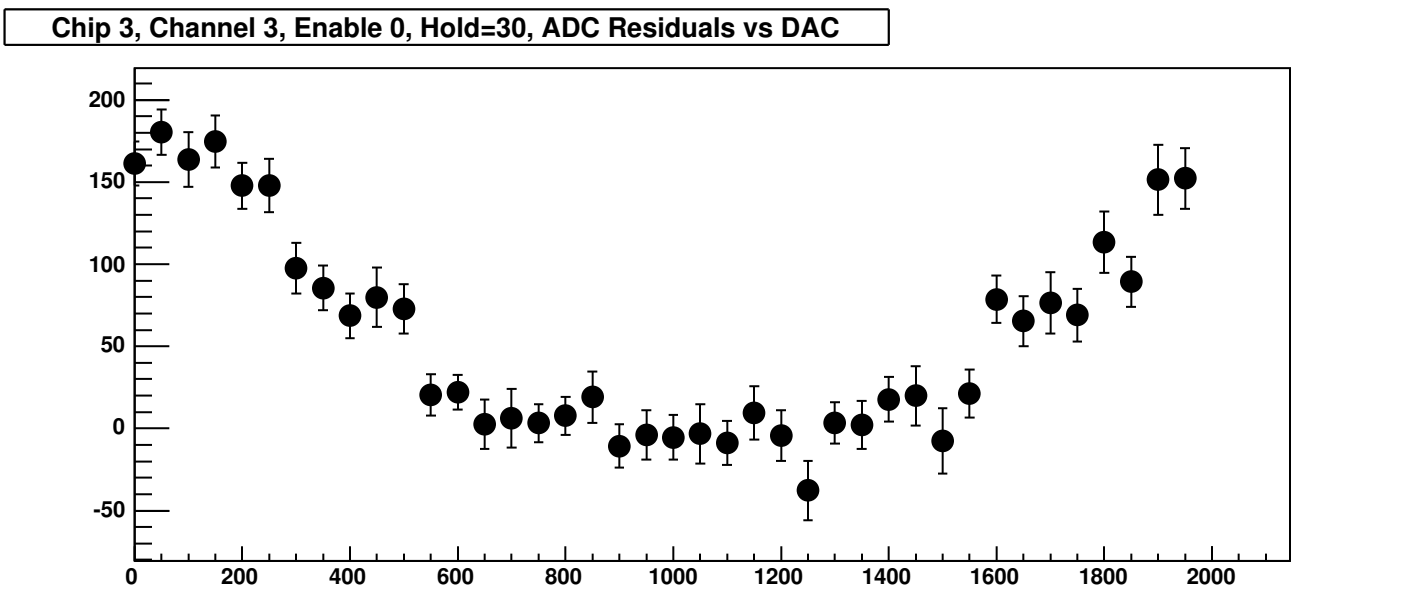

Chip 3, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 38.58 / 11
p0 -1883 ± 35.19
p1 0.5012 ± 0.03214
-1200
-1300
-1400
-1500
-1600
-1700
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

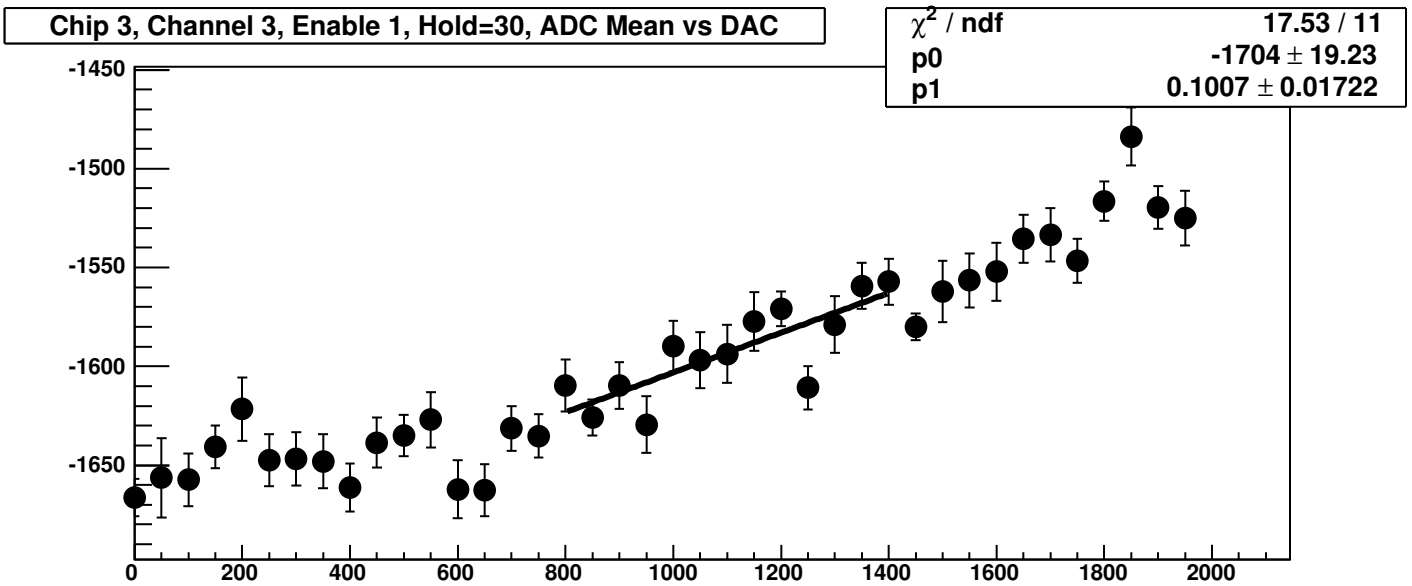

Chip 3, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

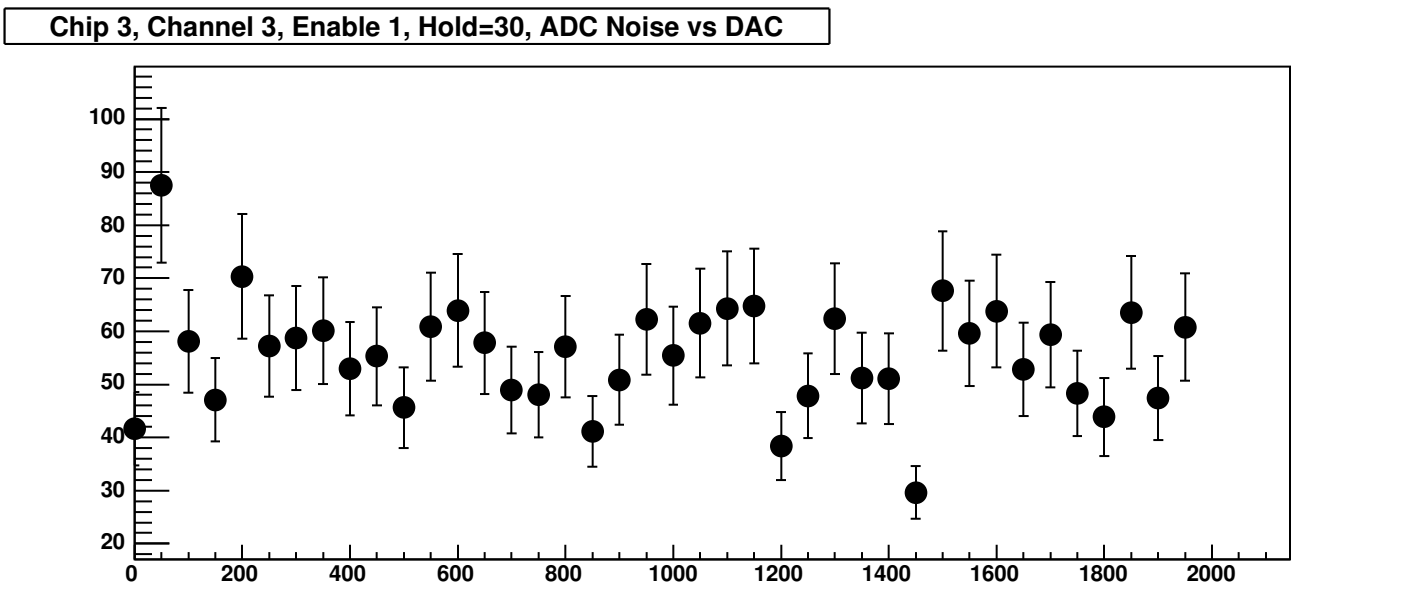

Chip 3, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

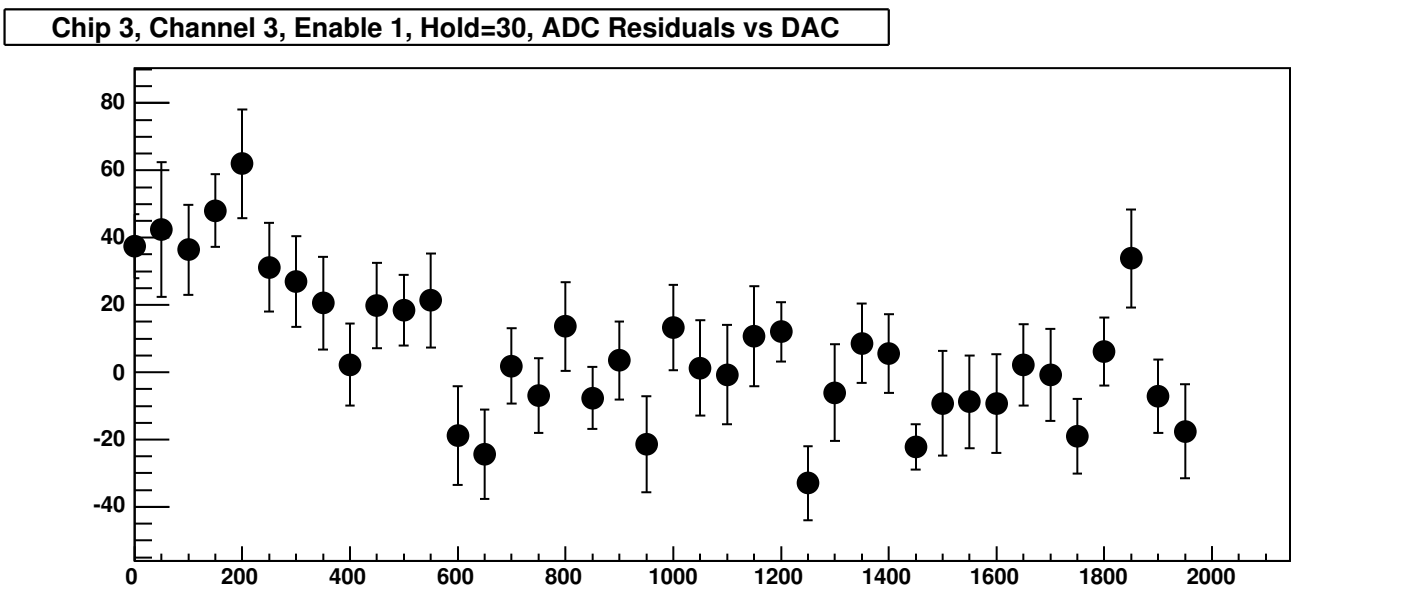

Chip 3, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 9.889 / 11
p0 -1807 ± 22.33
p1 0.2739 ± 0.02016
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

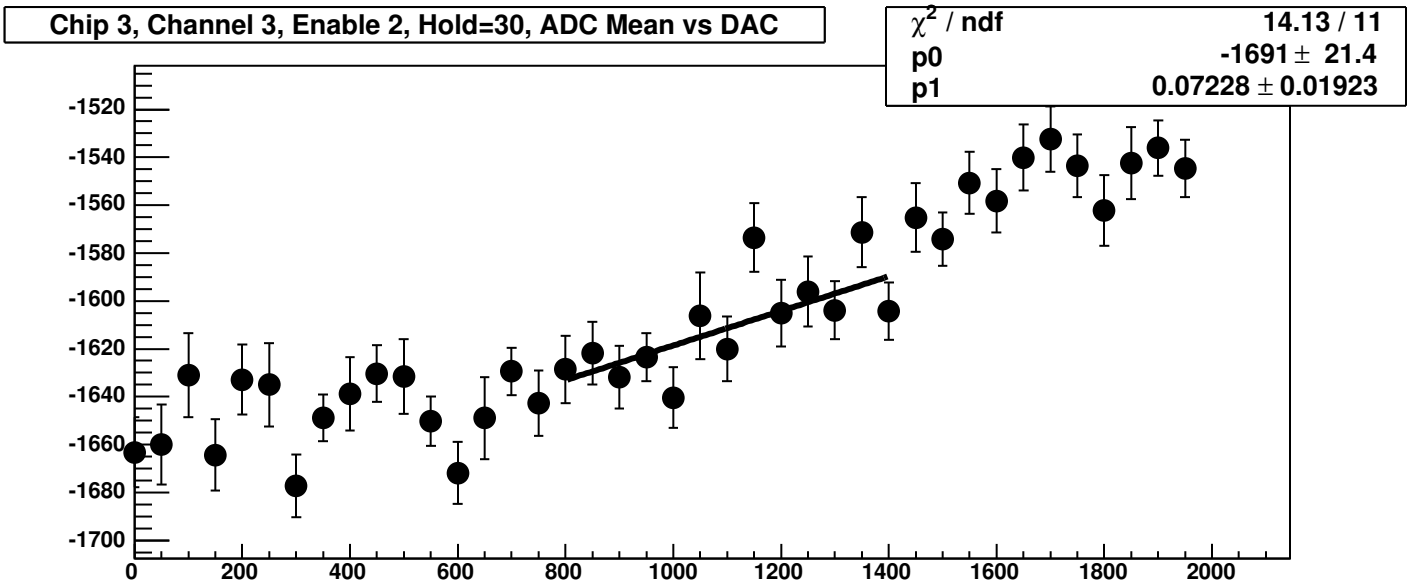

Chip 3, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

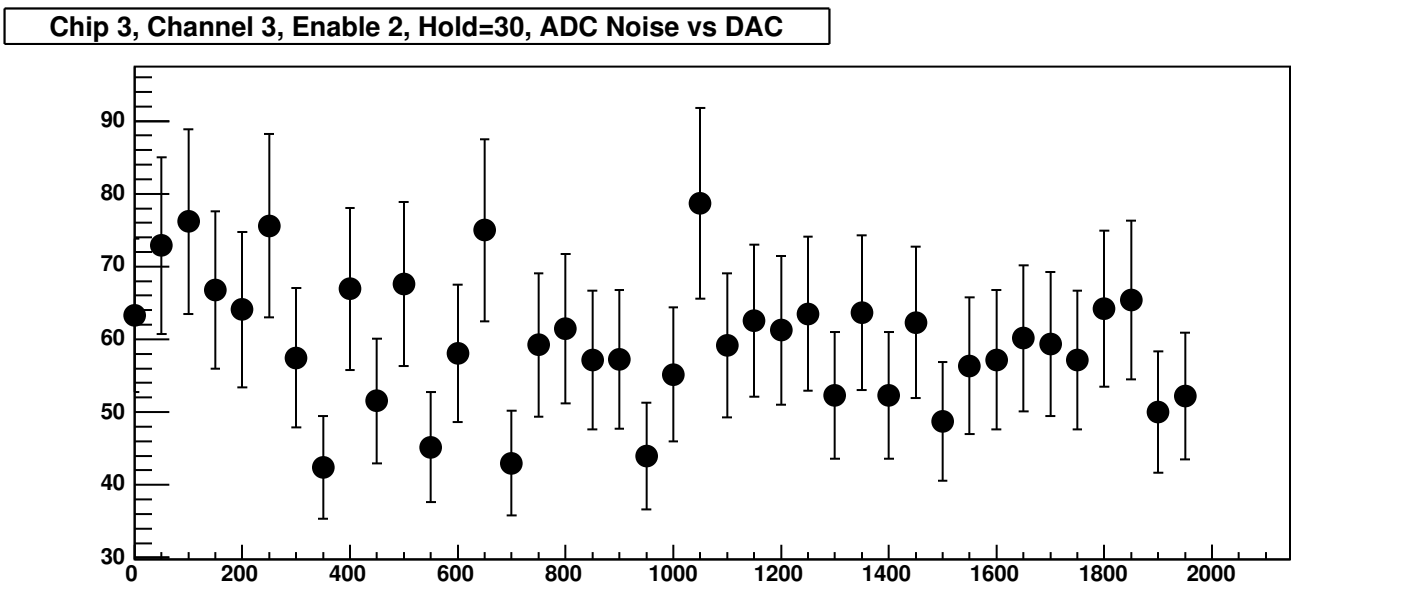

Chip 3, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

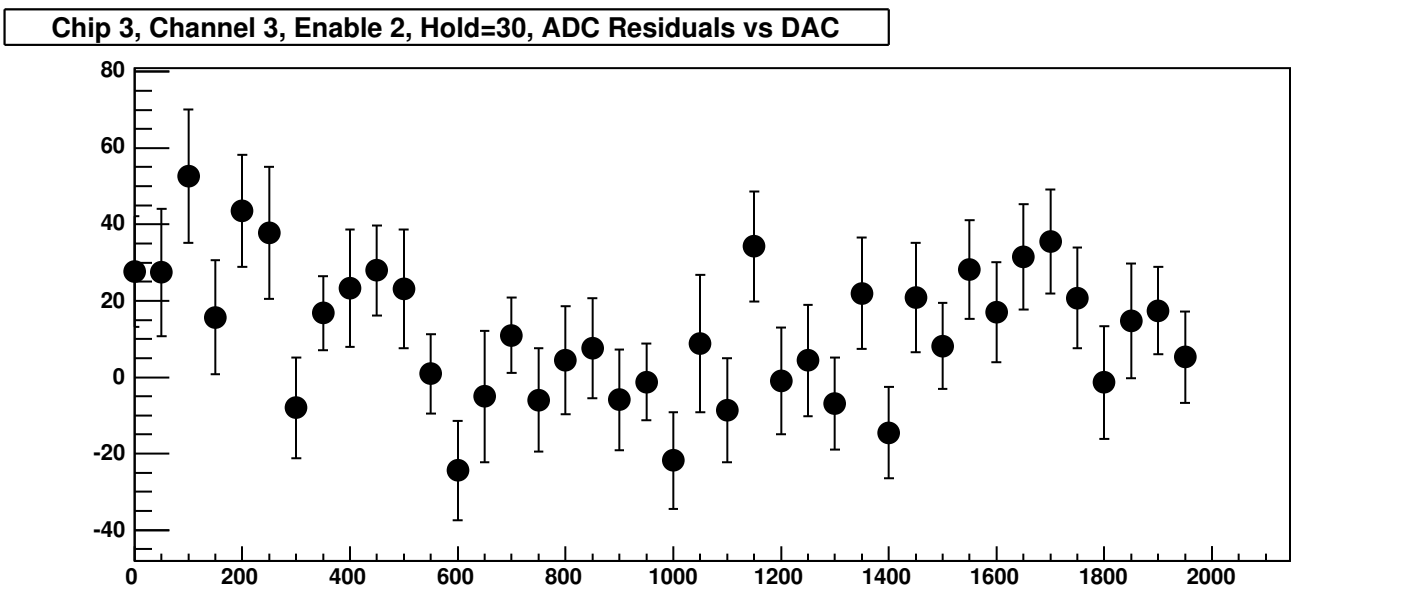

Chip 3, Channel 3, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 17.53 / 11
p0 -1704 ± 19.23
p1 0.1007 ± 0.01722
-1450
-1500
-1550
-1600
-1650
0
200
400
600
800
1000
1200
1400
1600
1800
2000

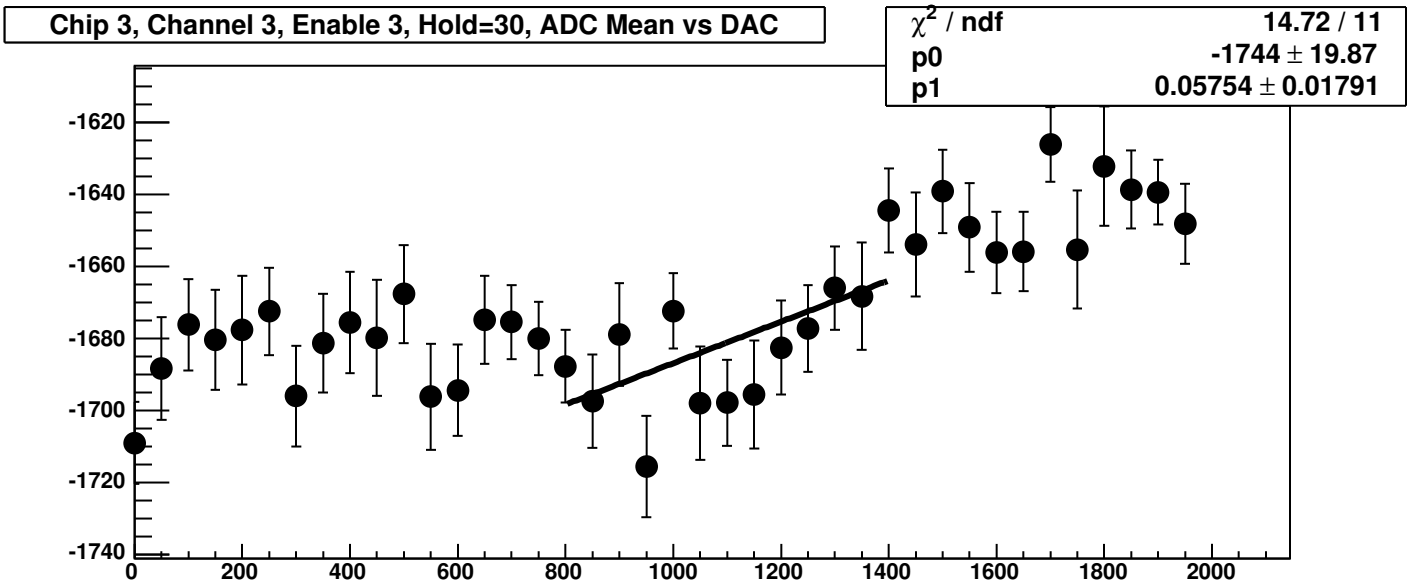

Chip 3, Channel 3, Enable 1, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

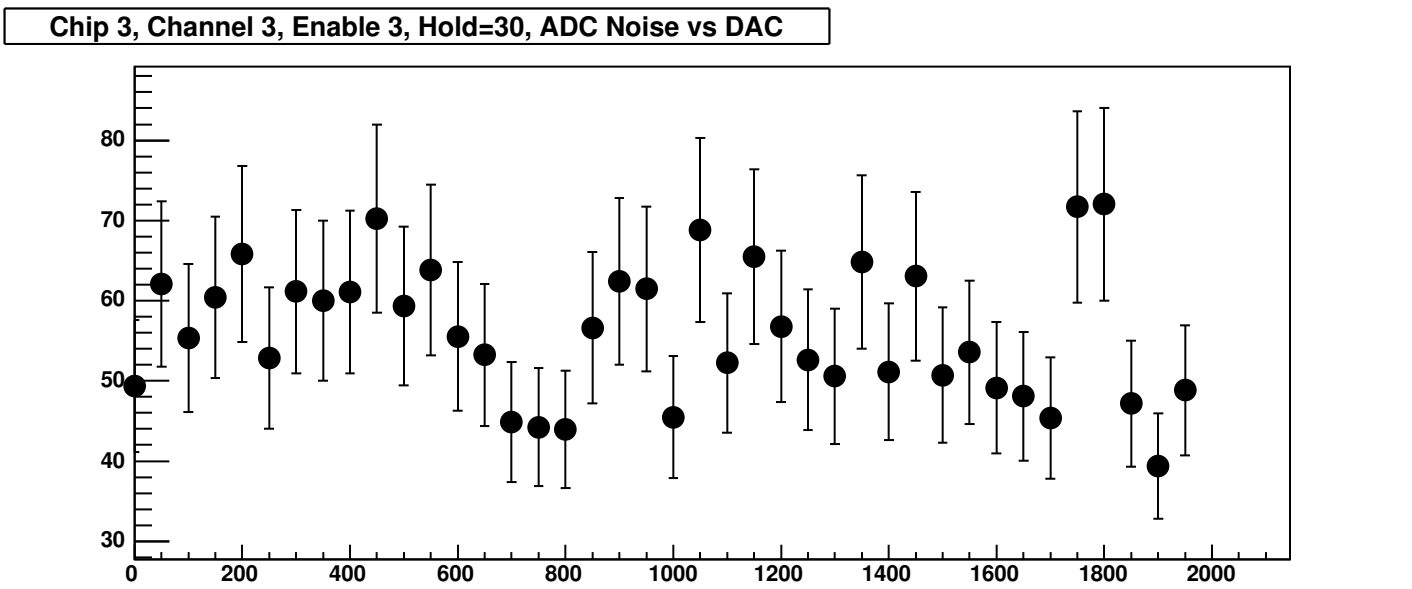

Chip 3, Channel 3, Enable 1, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

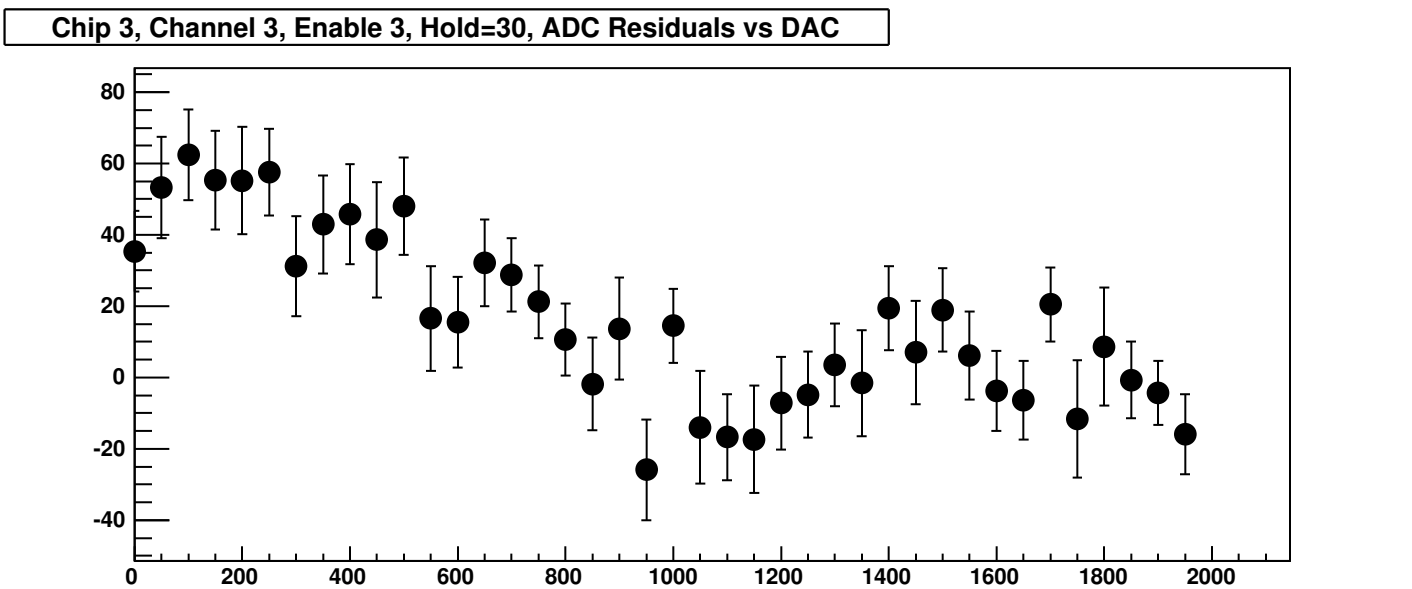

Chip 3, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 14.13 / 11
p0 -1691 ± 21.4
p1 0.07228 ± 0.01923
-1520
-1540
-1560
-1580
-1600
-1620
-1640
-1660
-1680
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

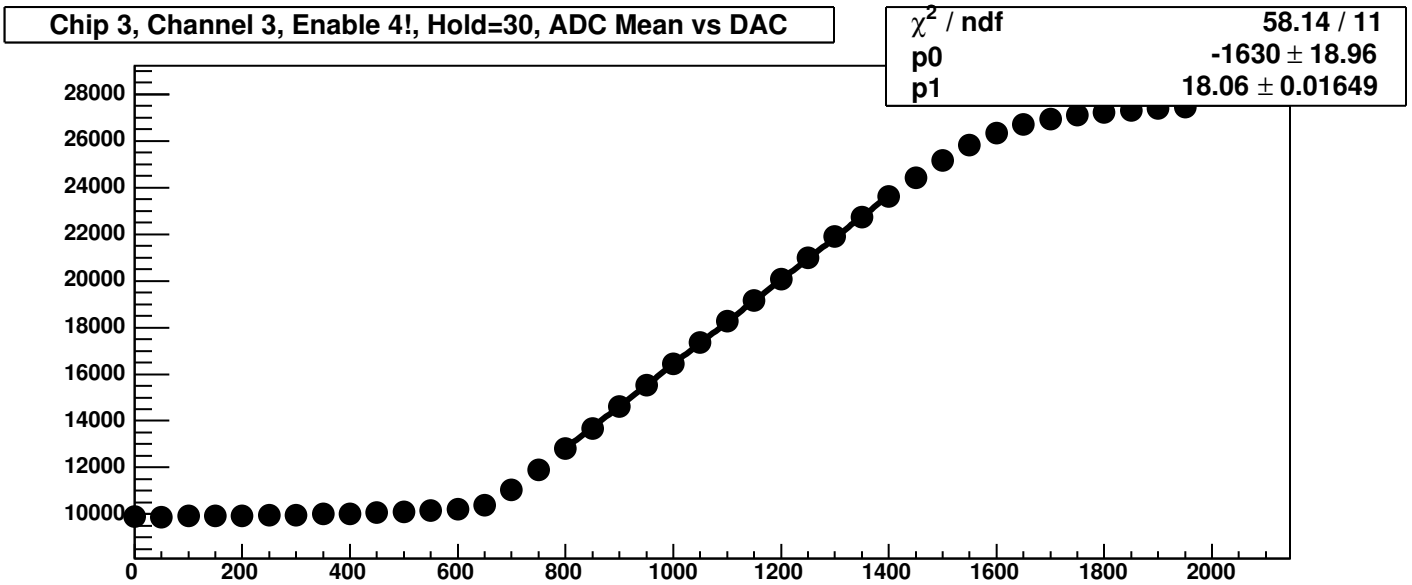

Chip 3, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

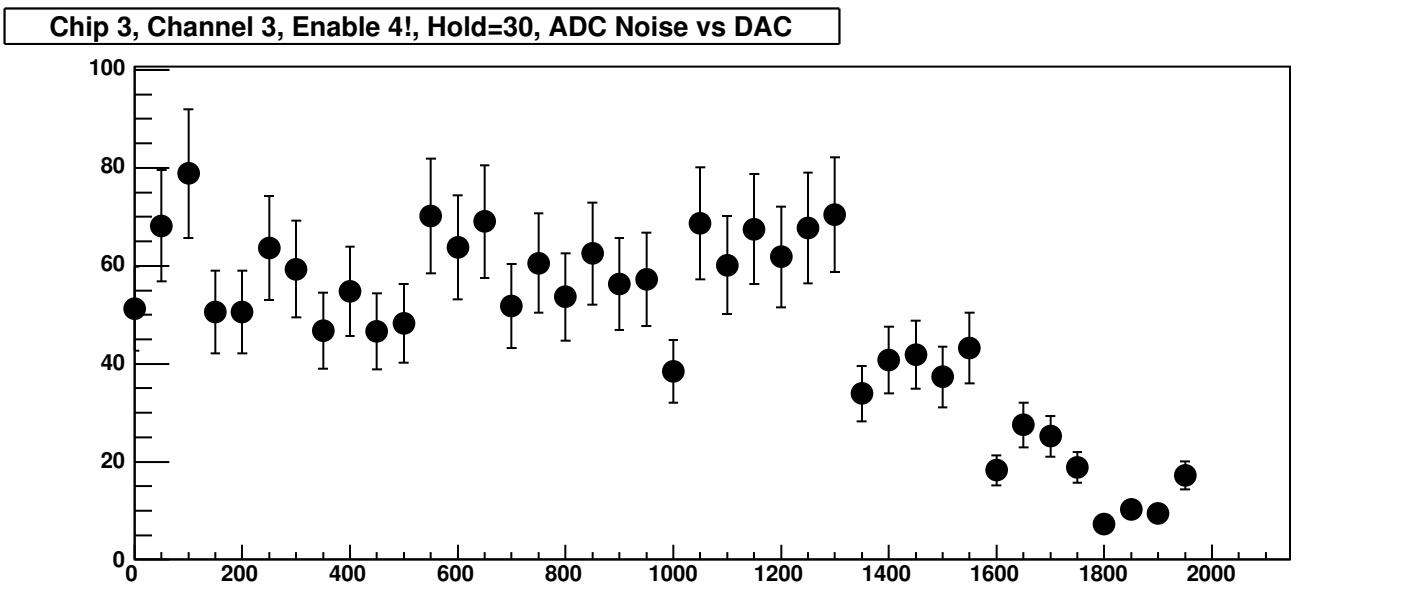

Chip 3, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

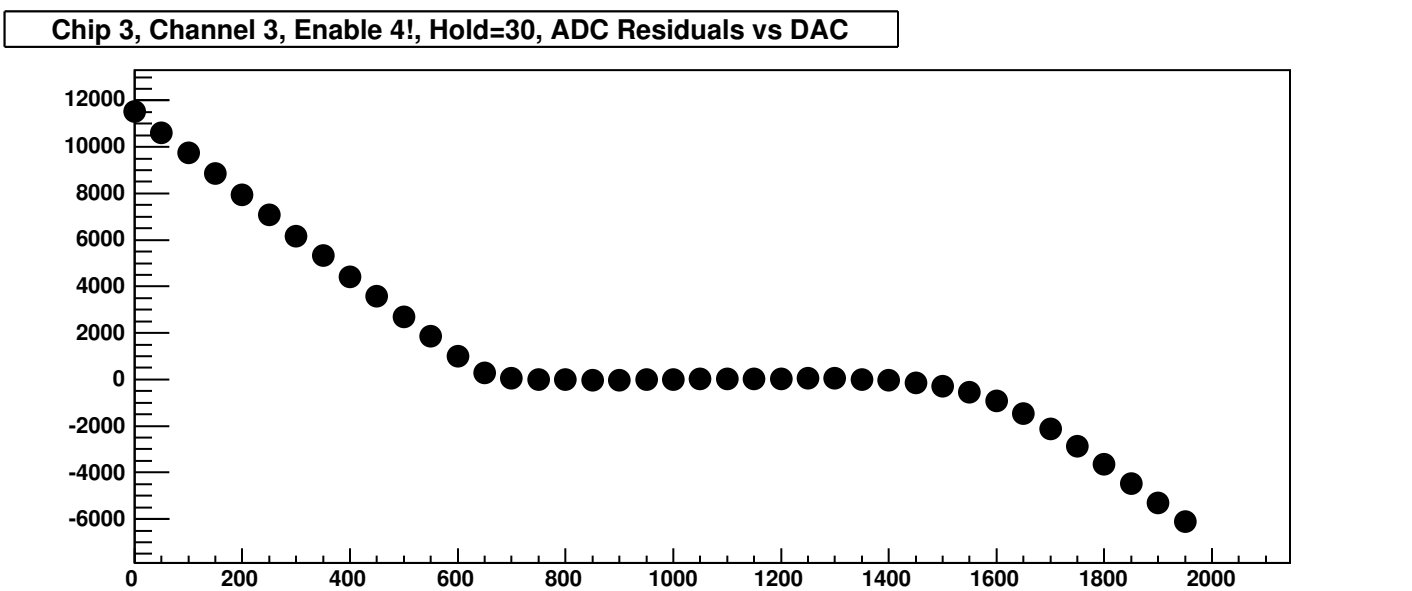

Chip 3, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 14.72 / 11
p0 -1744 ± 19.87
p1 0.05754 ± 0.01791
-1620
-1640
-1660
-1680
-1700
-1720
-1740
0
200
400
600
800
1000
1200
1400
1600
1800
2000

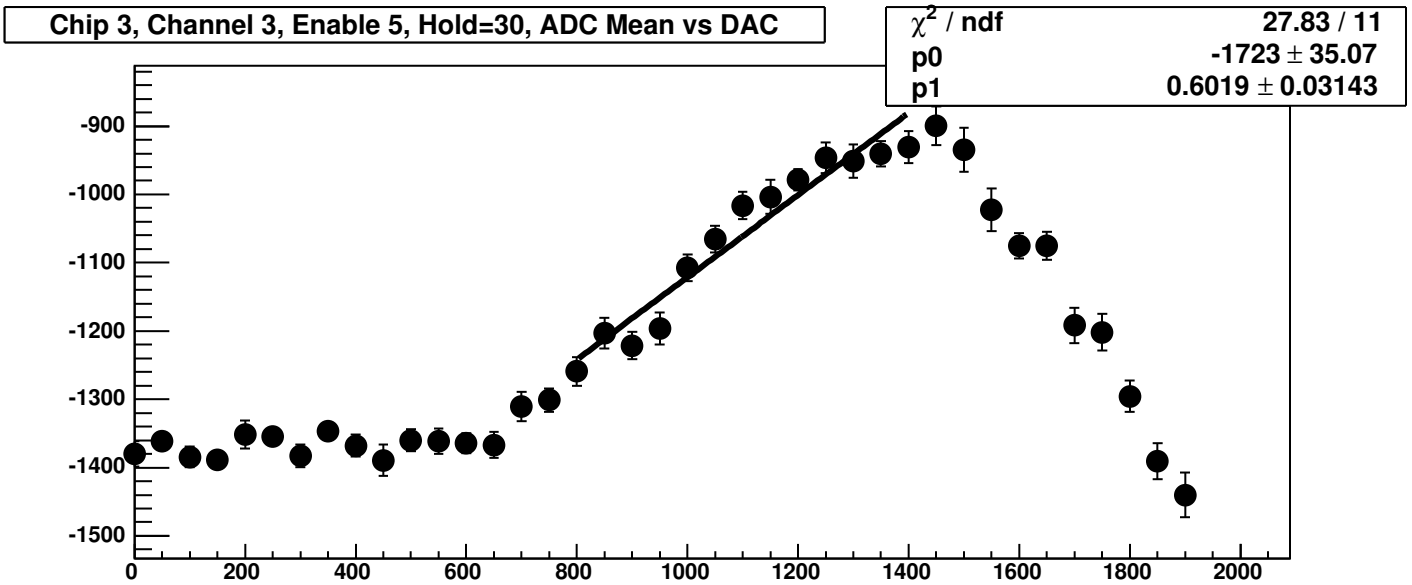

Chip 3, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

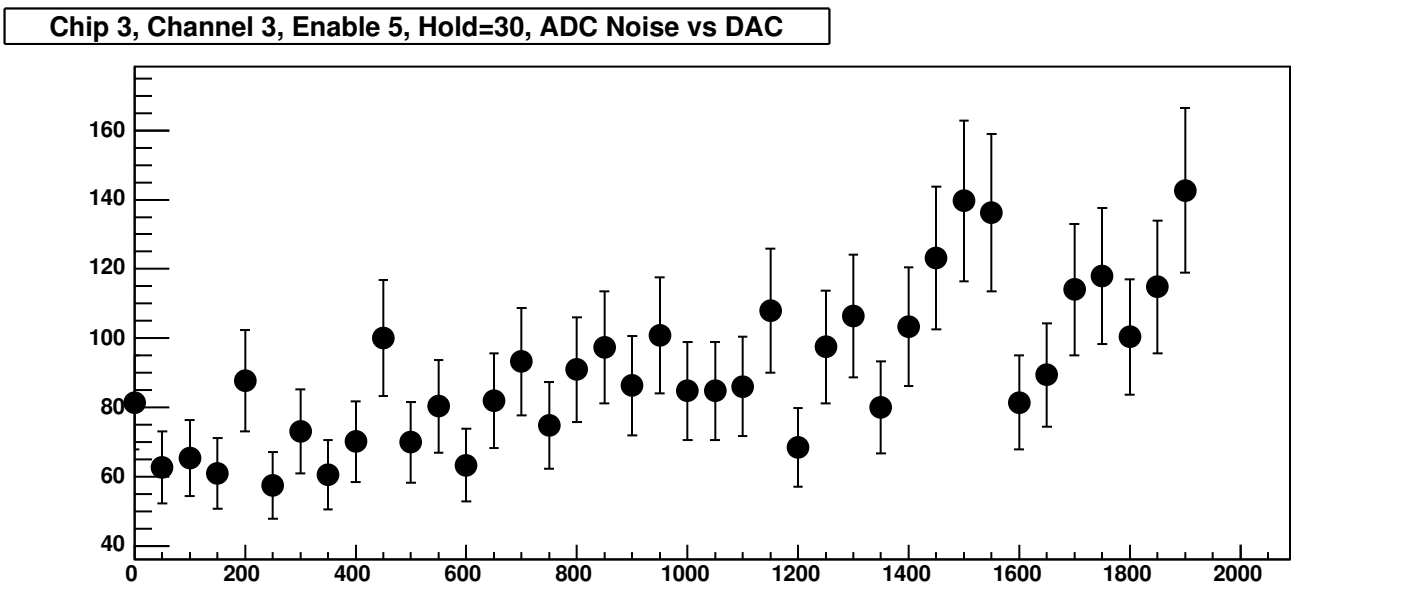

Chip 3, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

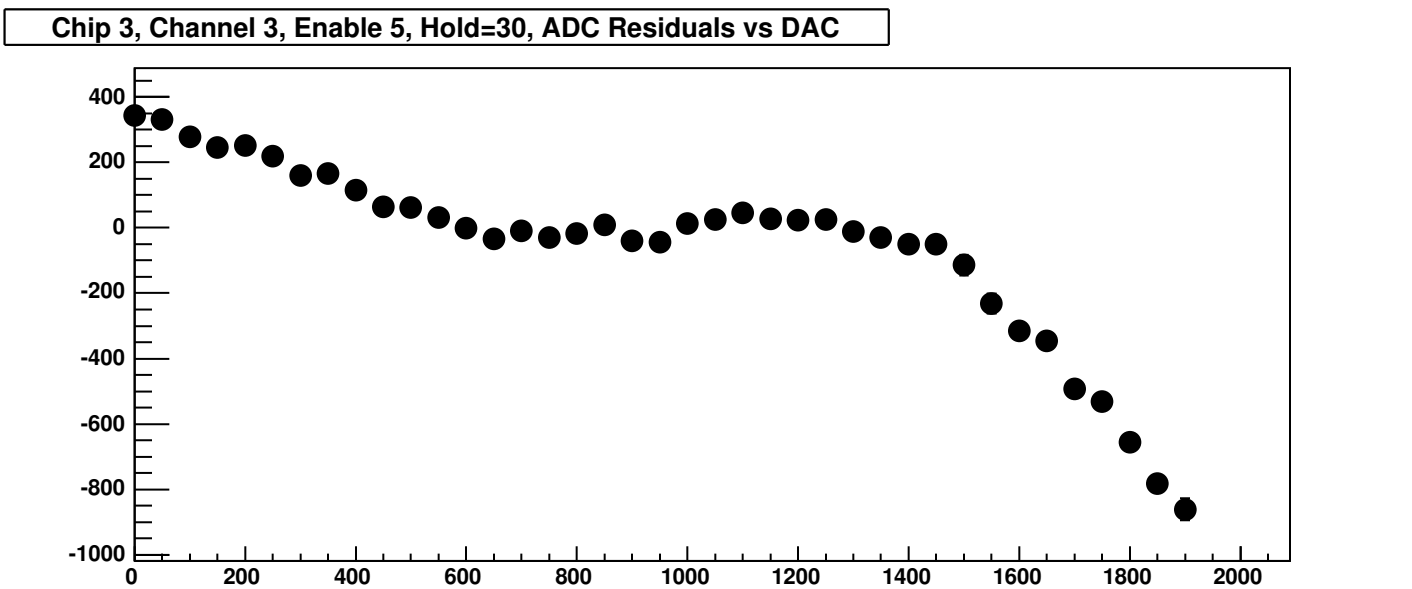

Chip 3, Channel 3, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 58.14 / 11
p0 -1630 ± 18.96
p1 18.06 ± 0.01649
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

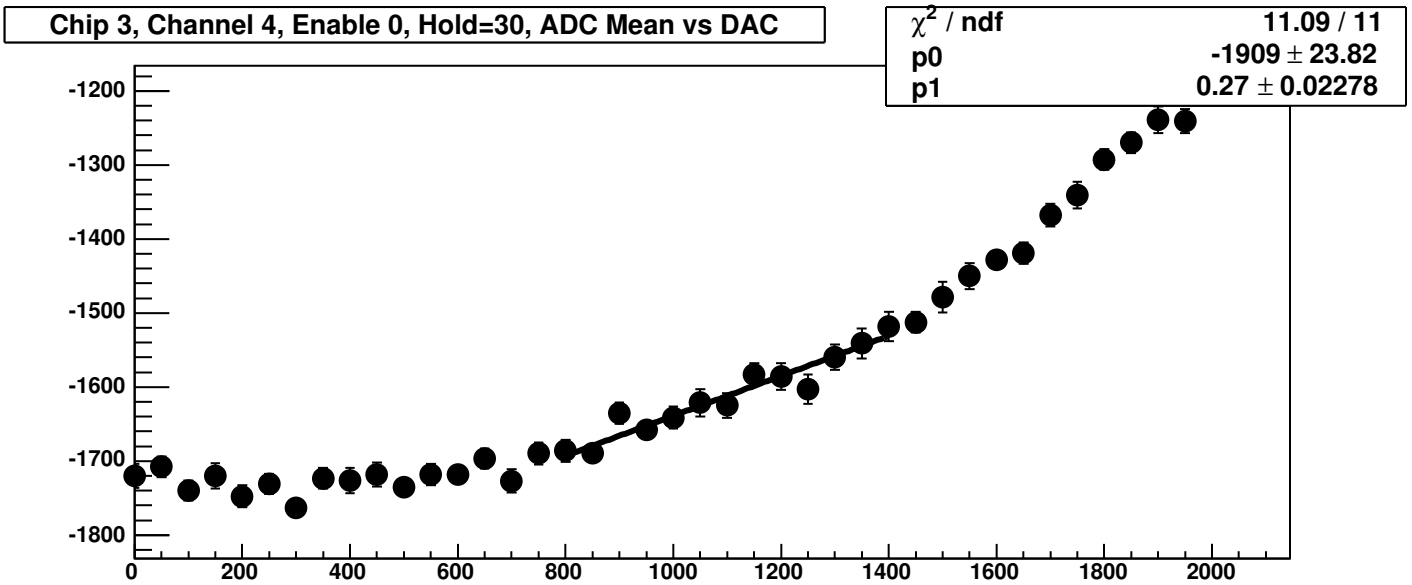

Chip 3, Channel 3, Enable 4!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

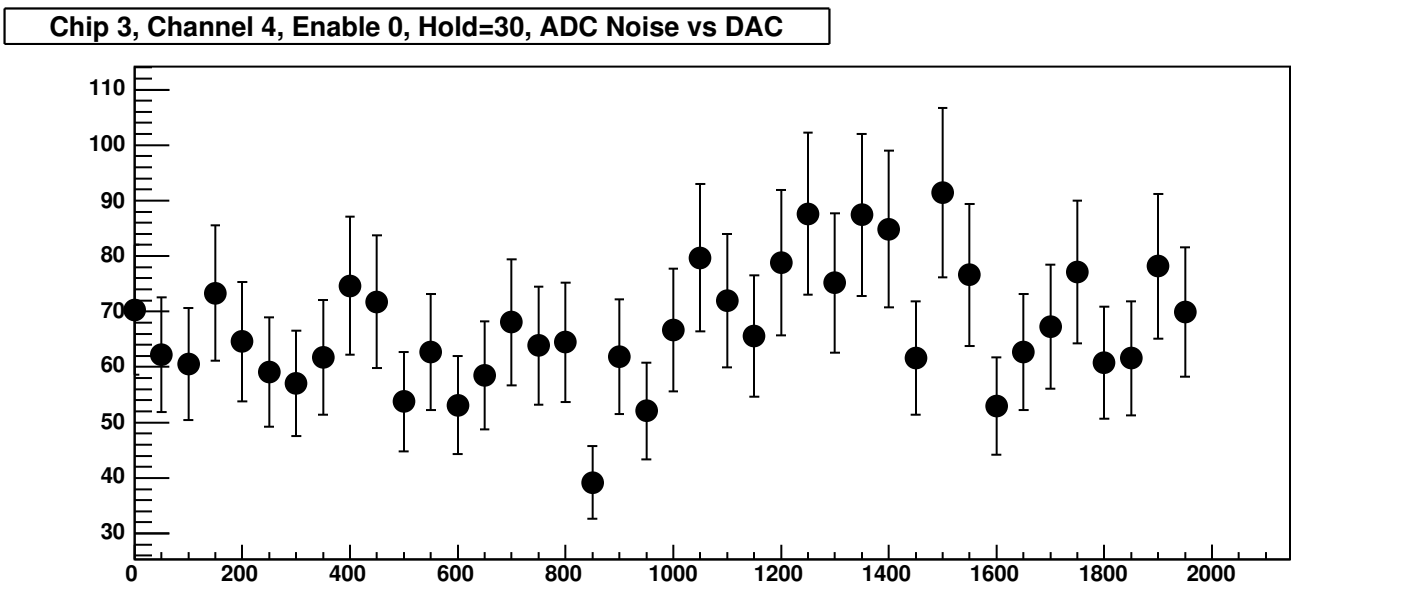

Chip 3, Channel 3, Enable 4!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

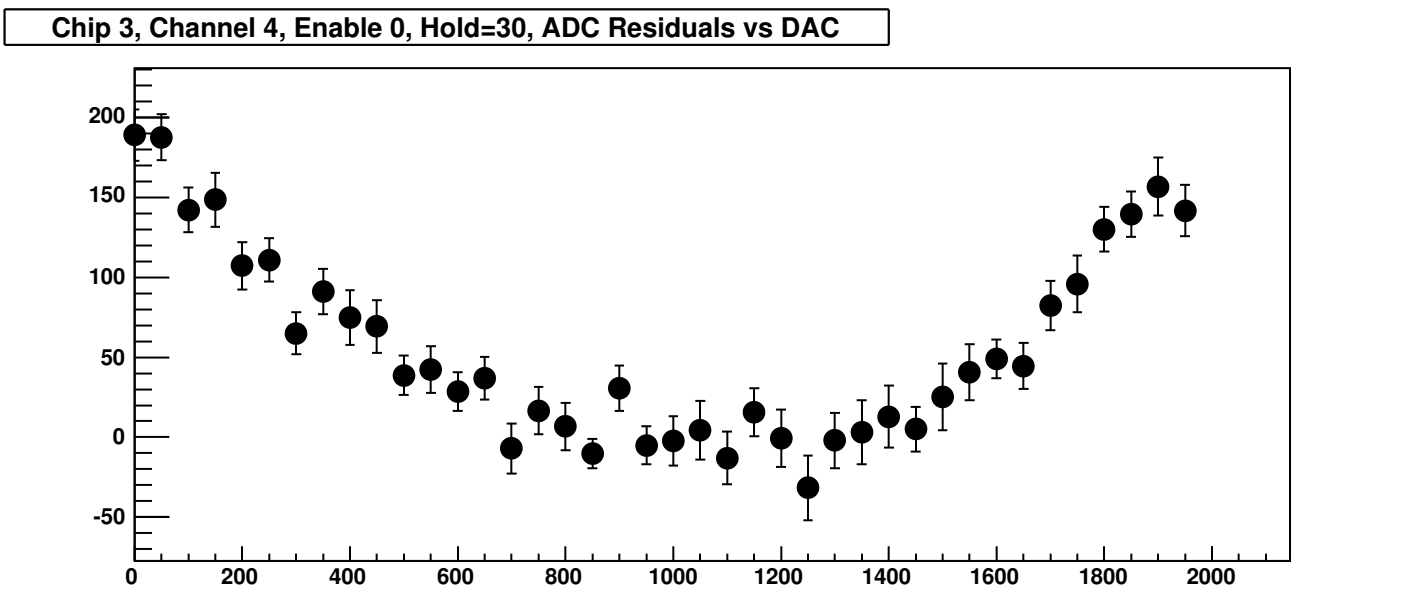

Chip 3, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 27.83 / 11
p0 -1723 ± 35.07
p1 0.6019 ± 0.03143
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

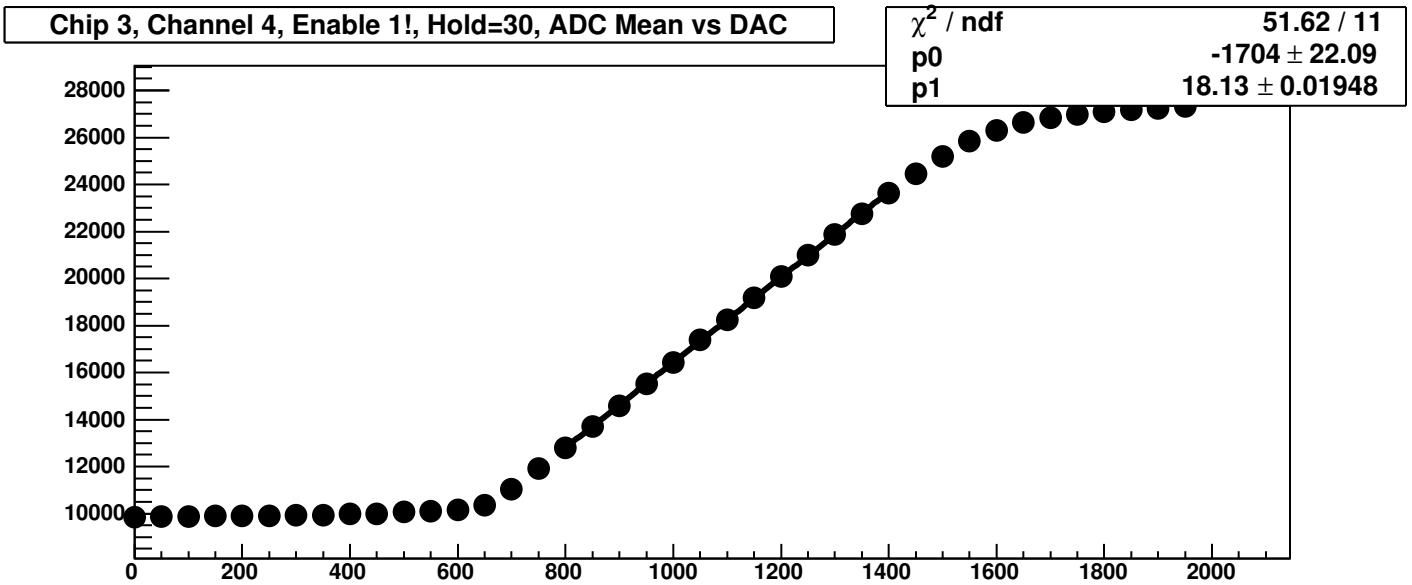

Chip 3, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

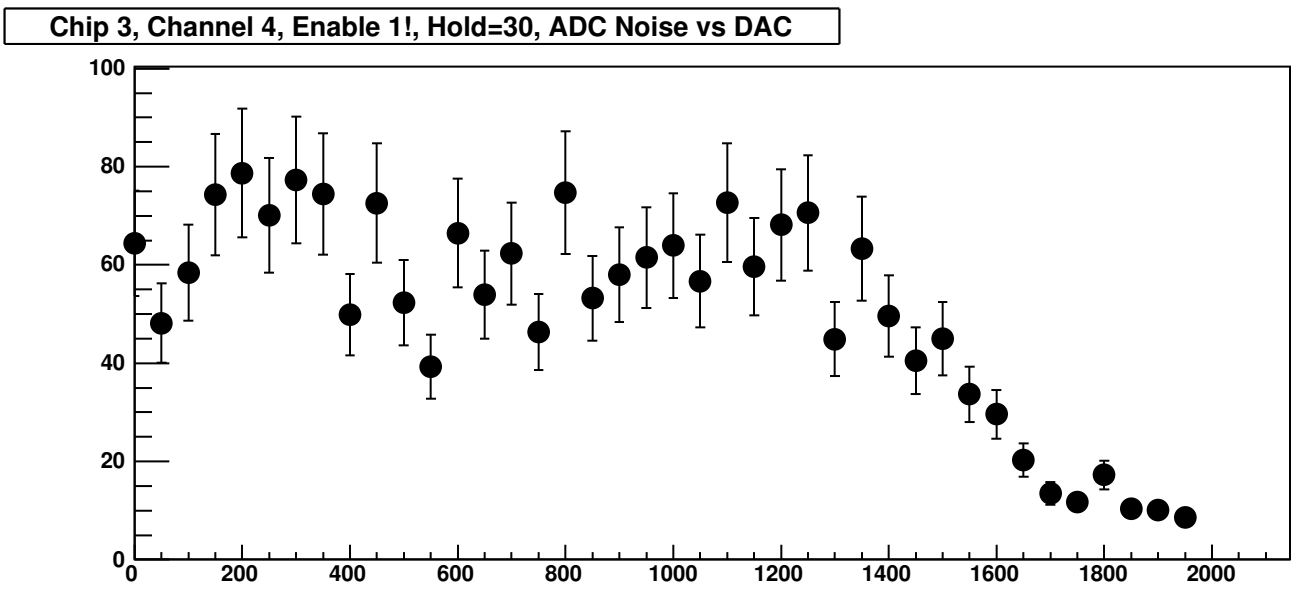

Chip 3, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

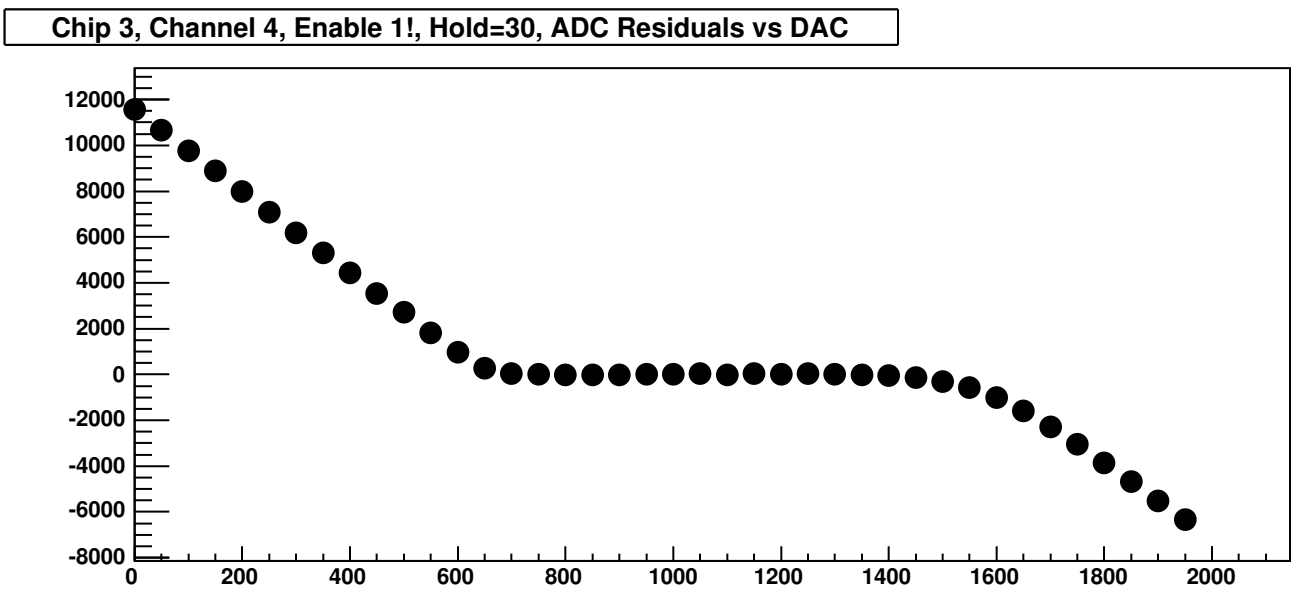

Chip 3, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 11.09 / 11
p0 -1909 ± 23.82
p1 0.27 ± 0.02278
-1200
-1300
-1400
-1500
-1600
-1700
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

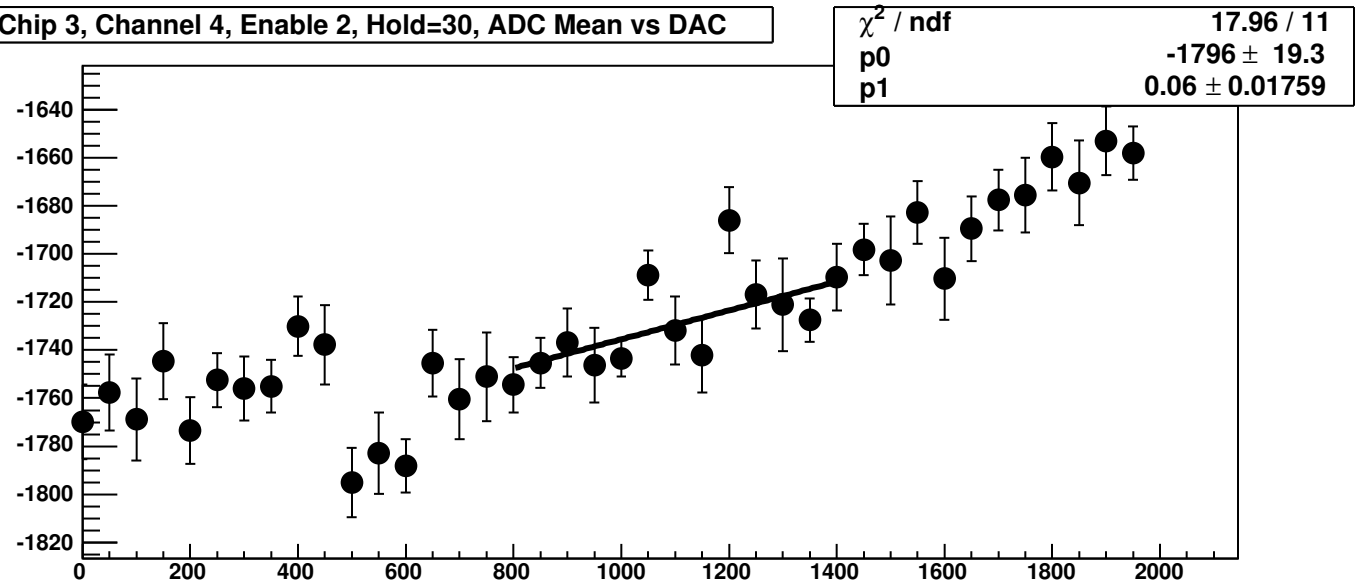

Chip 3, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 4, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 51.62 / 11
p0 -1704 ± 22.09
p1 18.13 ± 0.01948
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

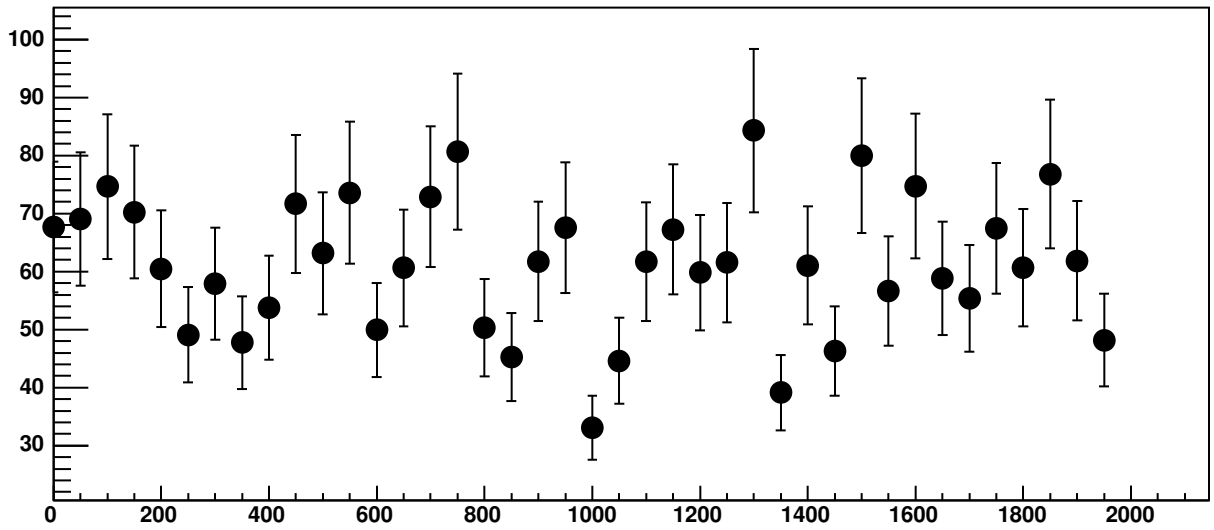

Chip 3, Channel 4, Enable 1!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 4, Enable 1!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

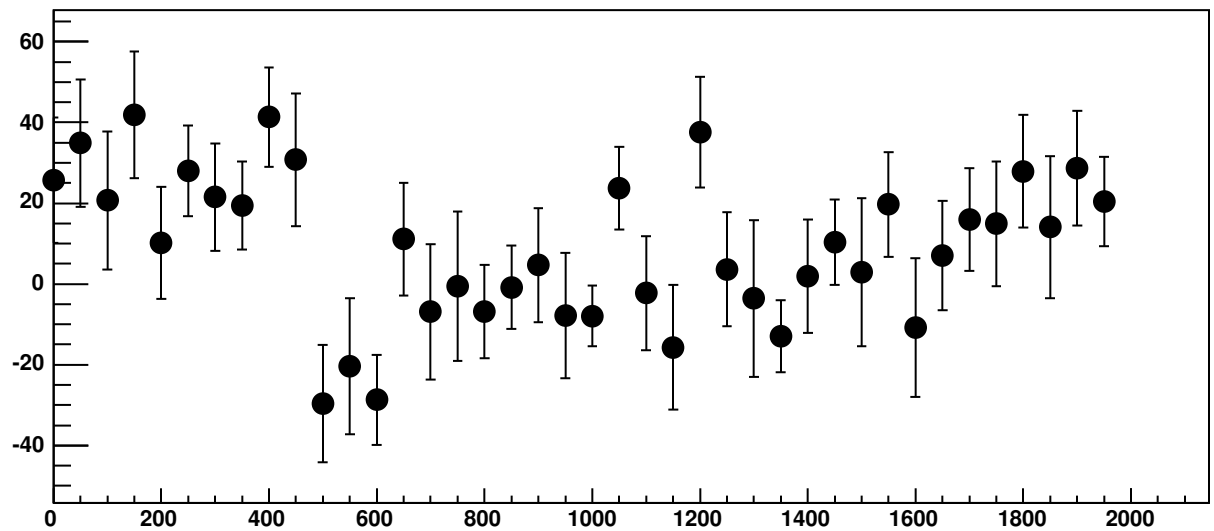
Chip 3, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC

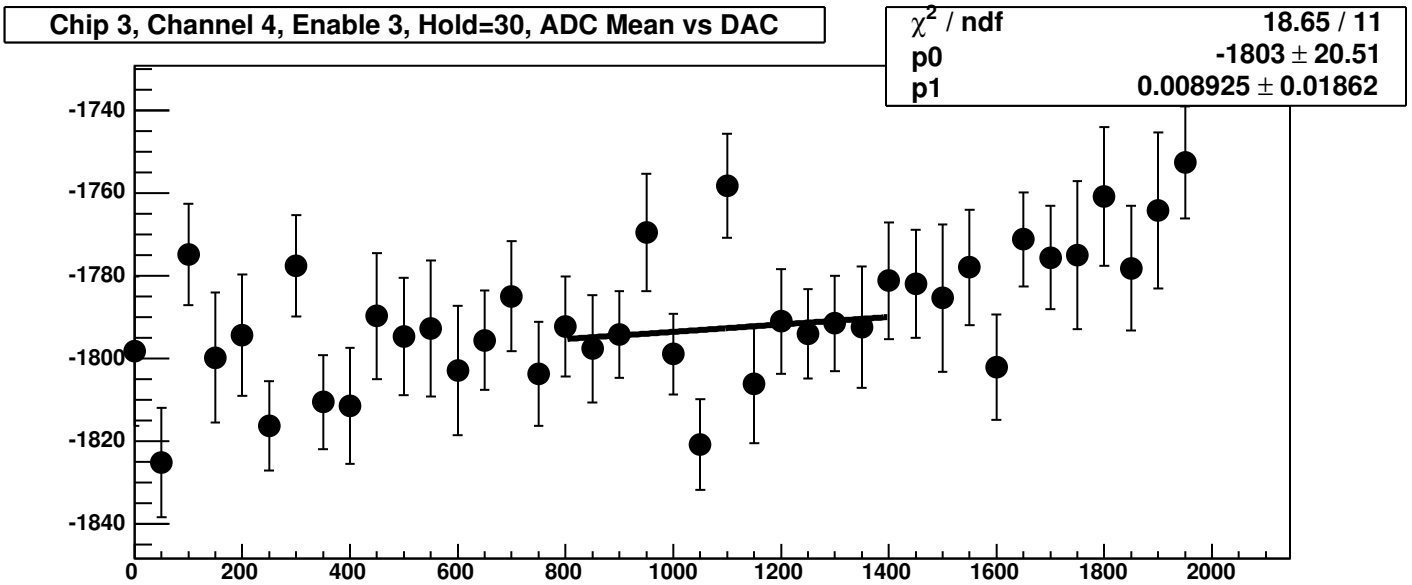


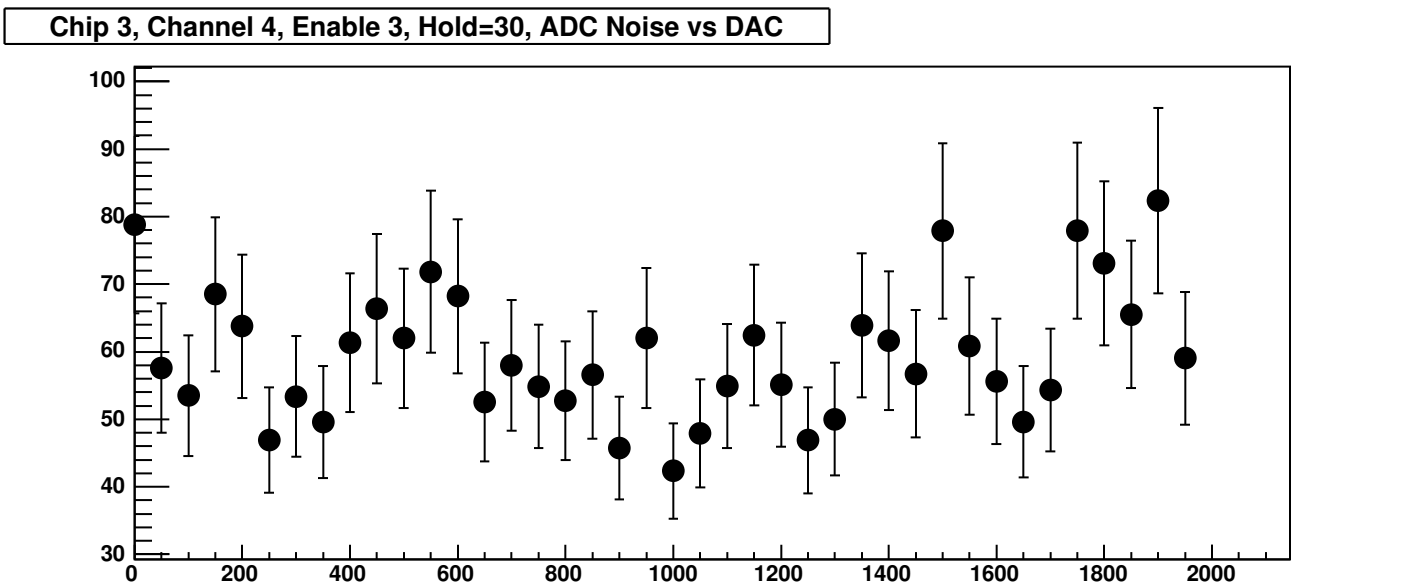
Chip 3, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC

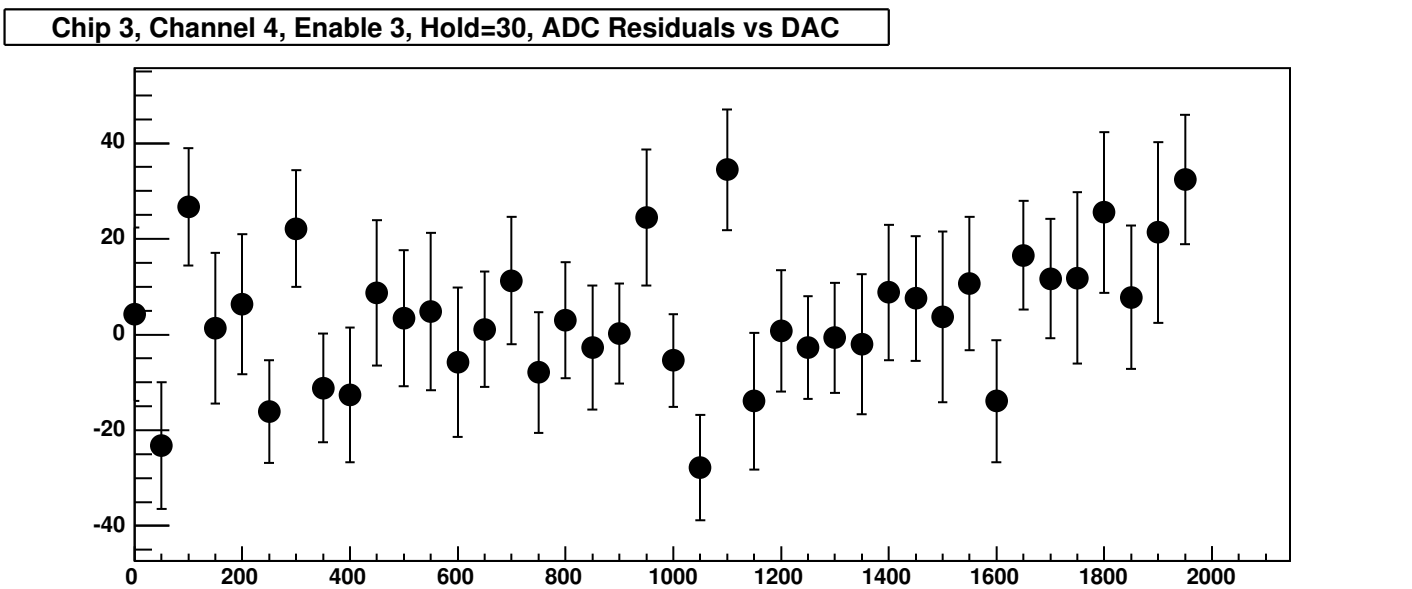


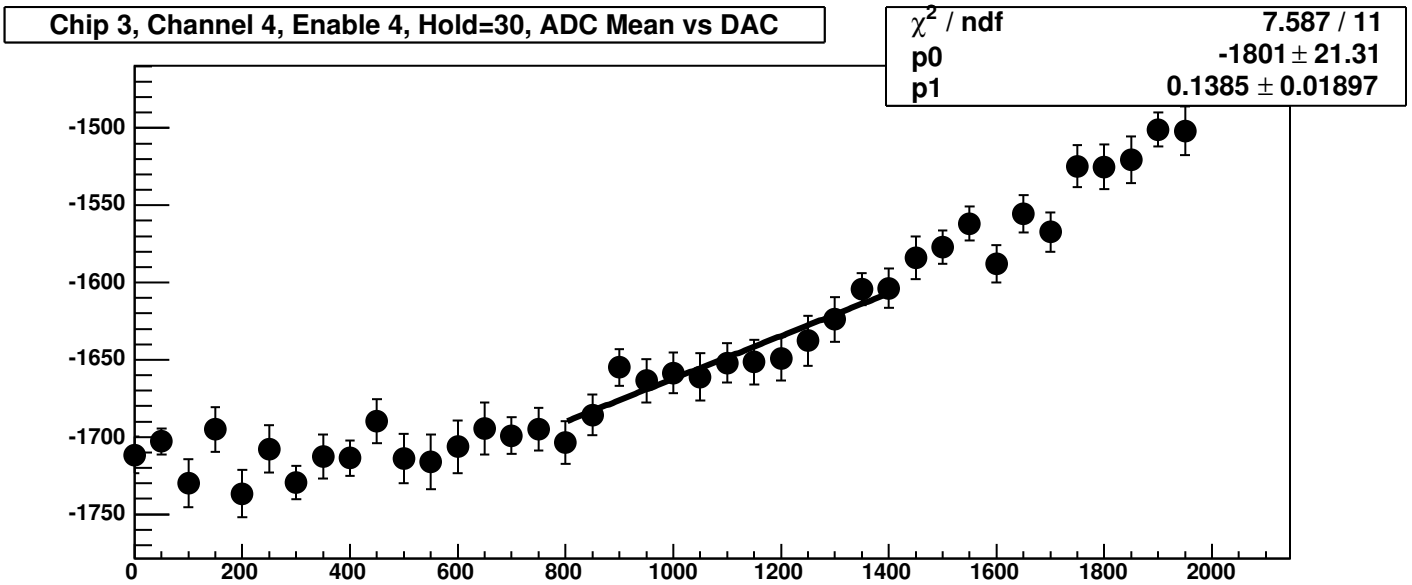
Chip 3, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC

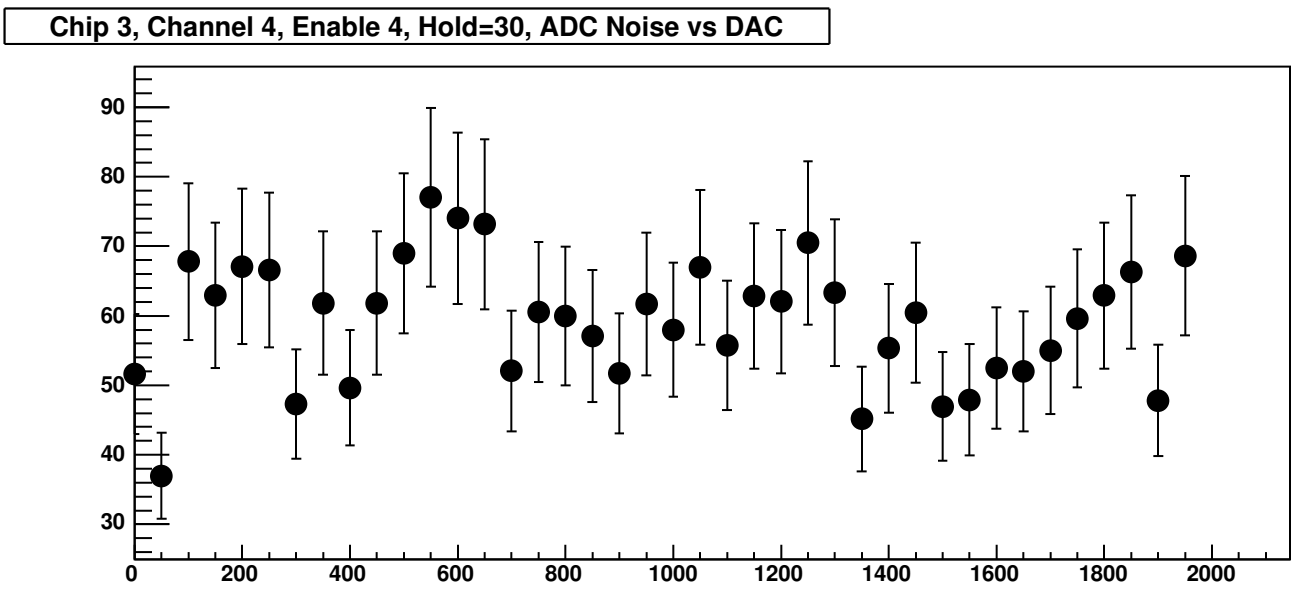


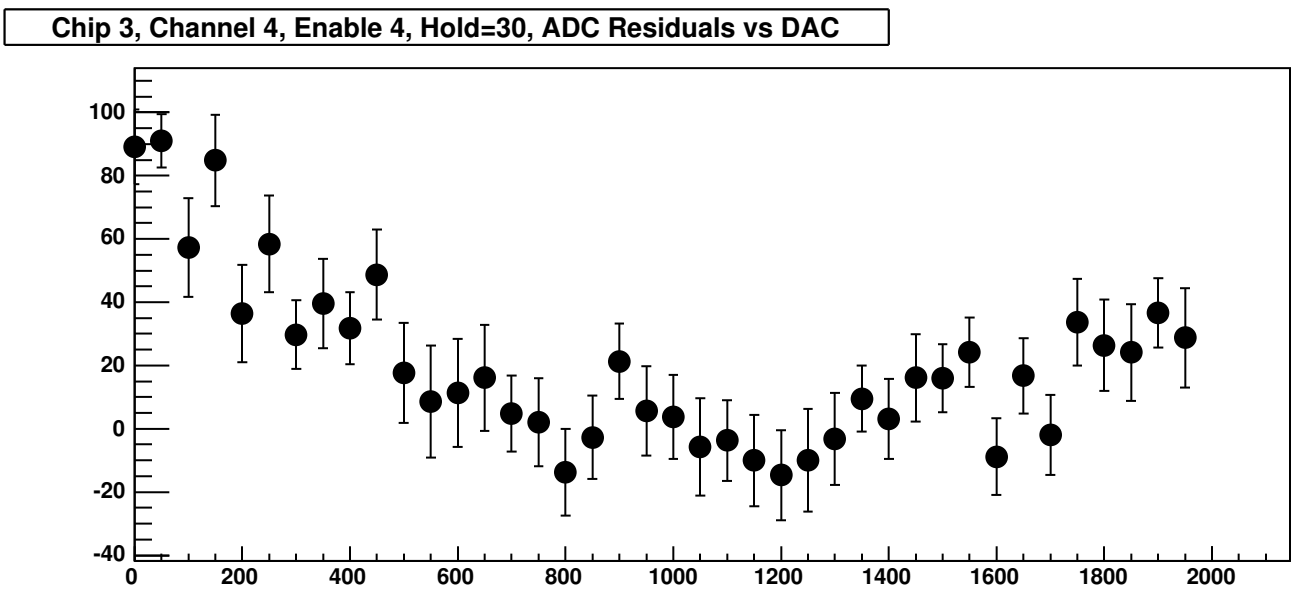

Chip 3, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 18.65 / 11
p0 -1803 ± 20.51
p1 0.008925 ± 0.01862
-1740
-1760
-1780
-1800
-1820
-1840
0
200
400
600
800
1000
1200
1400
1600
1800
2000

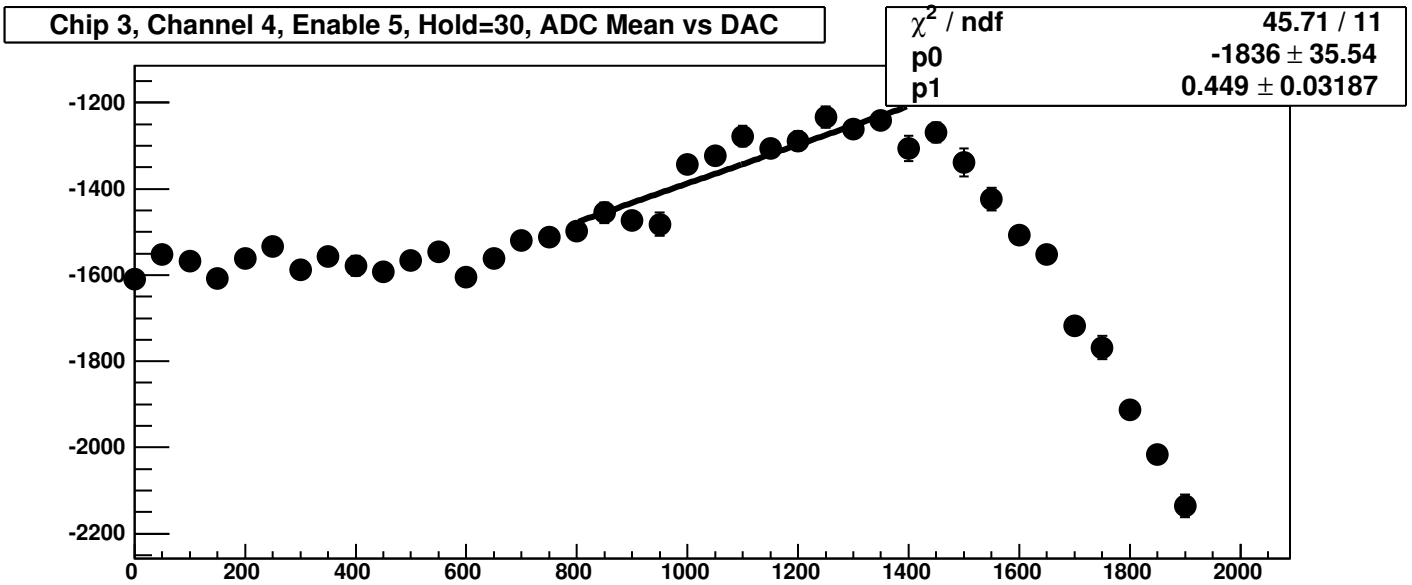

Chip 3, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

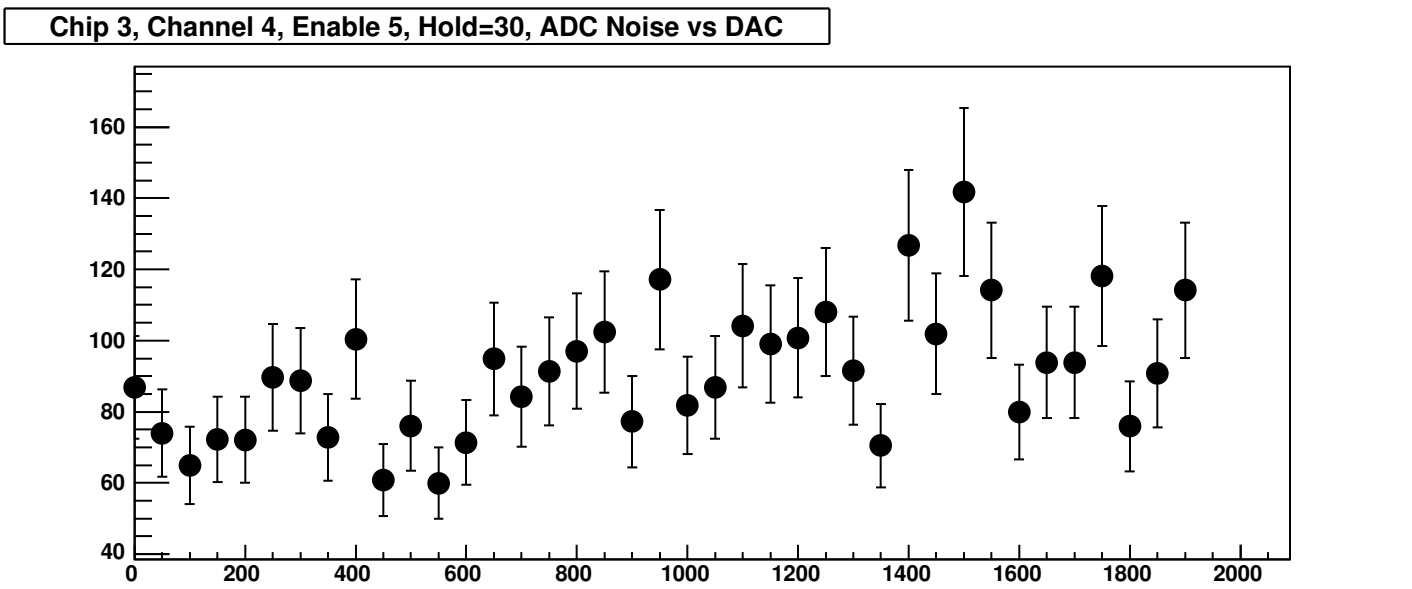

Chip 3, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

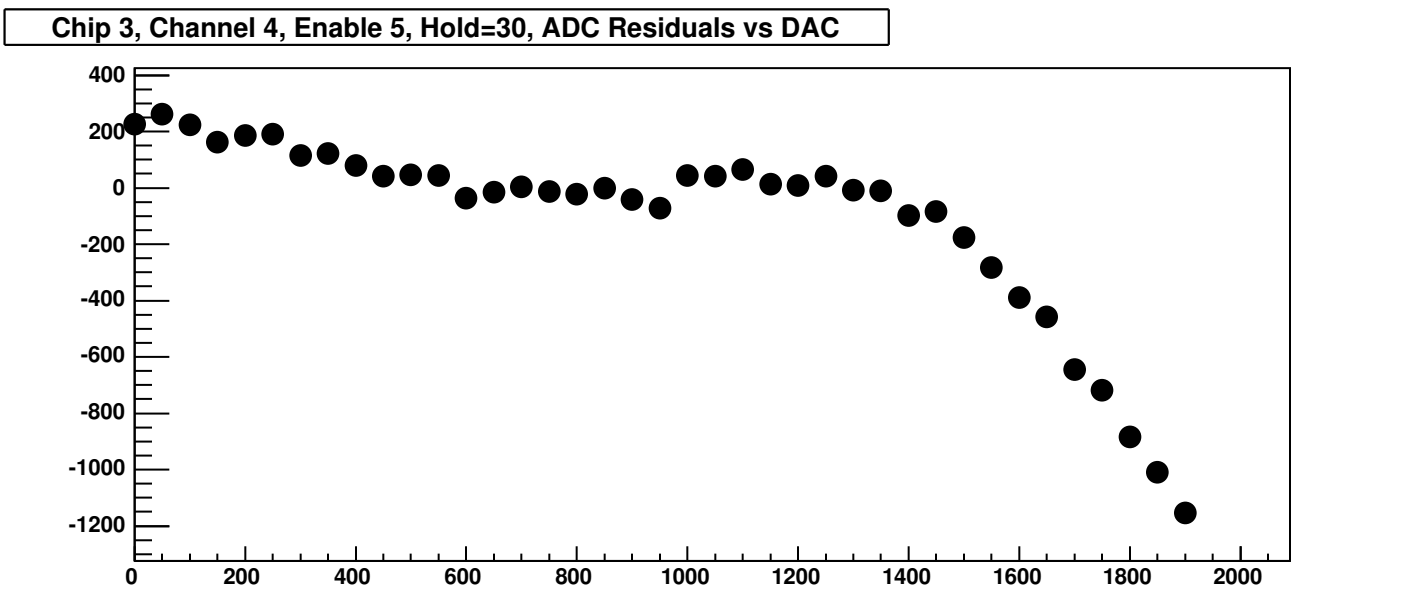

Chip 3, Channel 4, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7.587 / 11
p0 -1801 ± 21.31
p1 0.1385 ± 0.01897
-1500
-1550
-1600
-1650
-1700
-1750
0
200
400
600
800
1000
1200
1400
1600
1800
2000

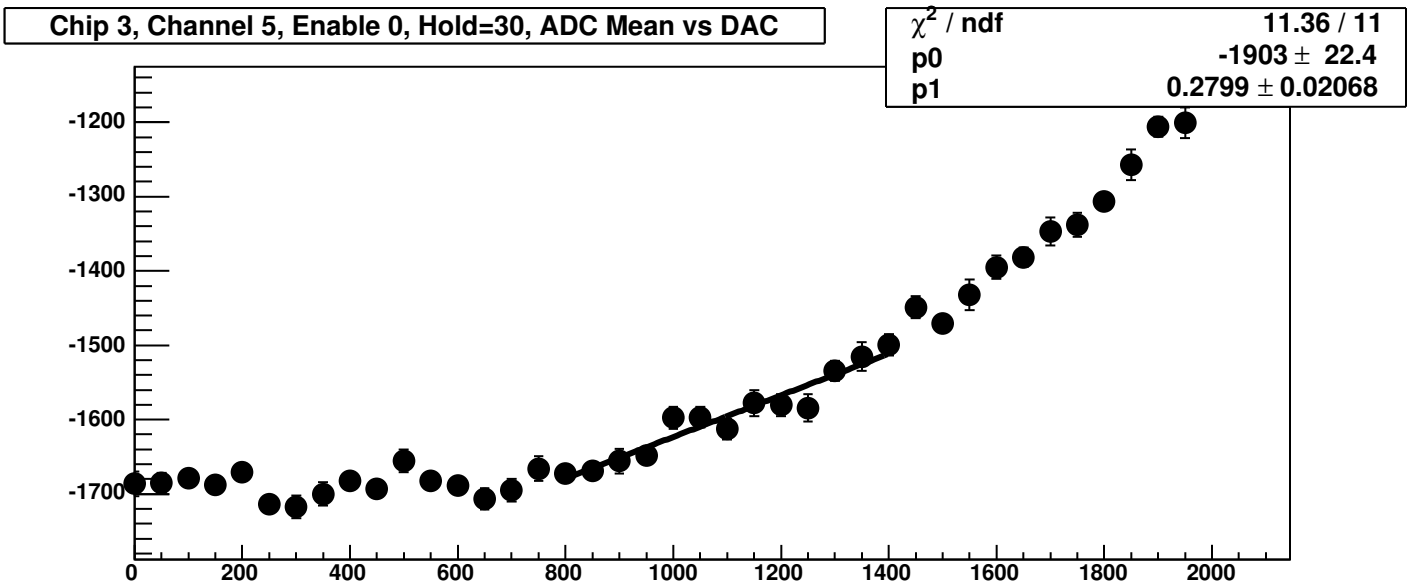

Chip 3, Channel 4, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

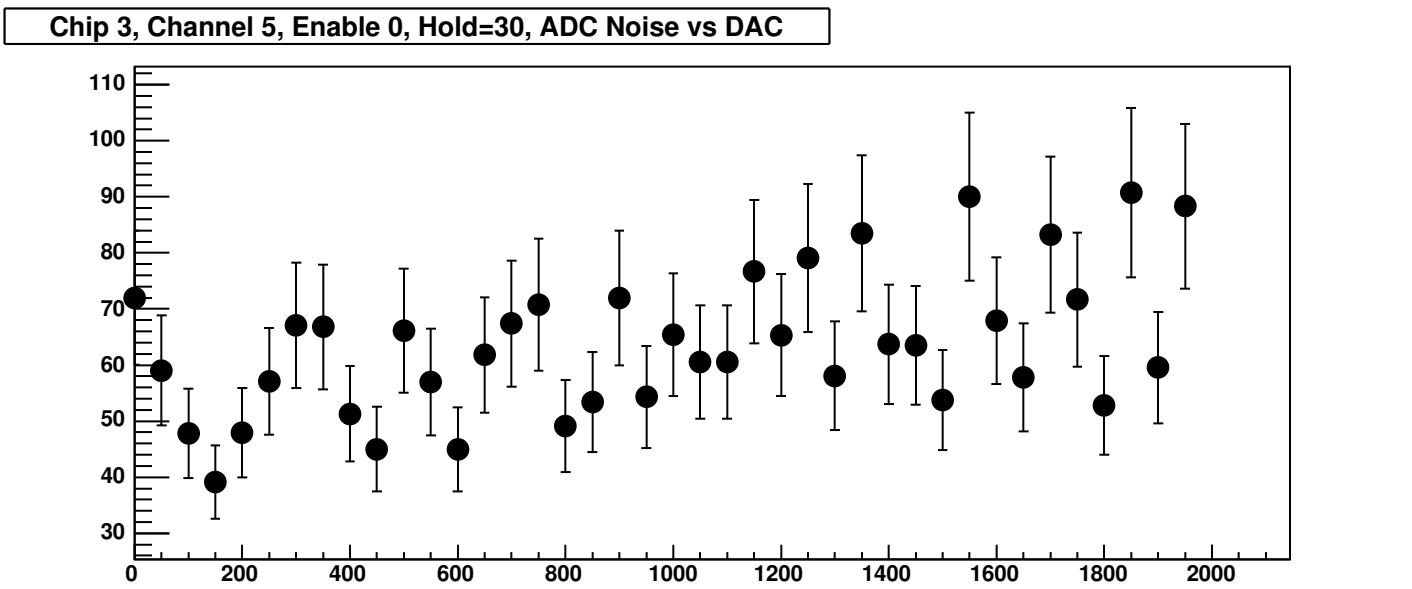

Chip 3, Channel 4, Enable 4, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

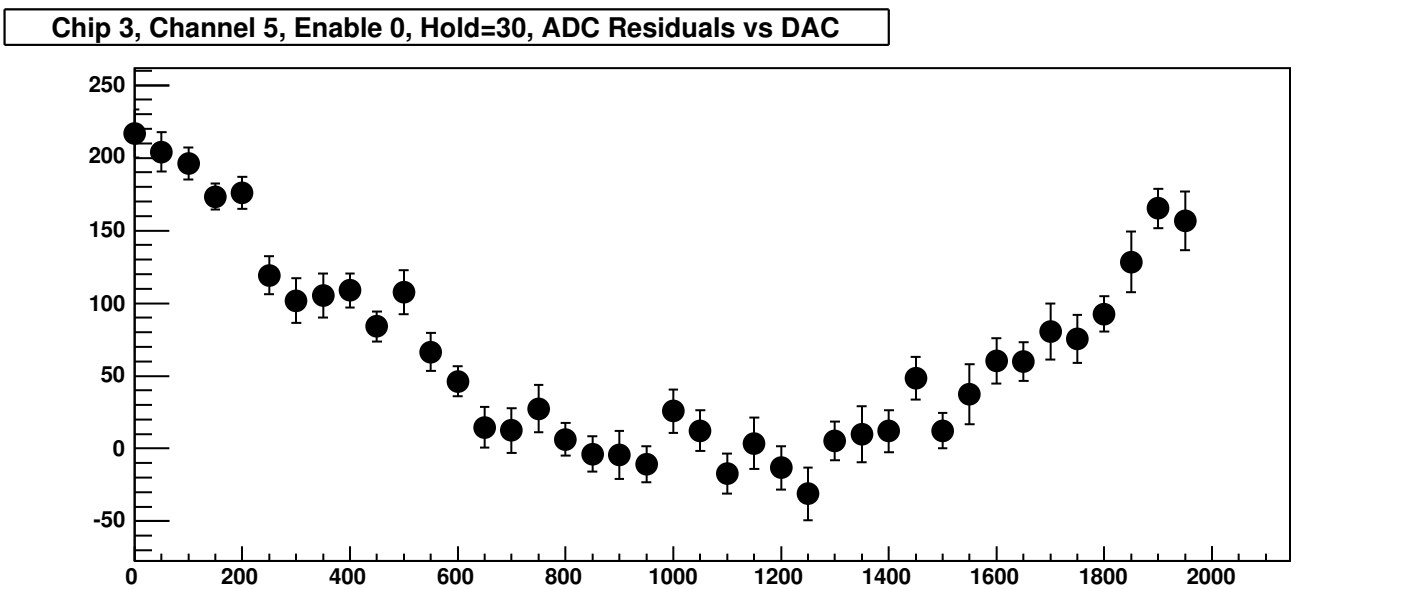

Chip 3, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 45.71 / 11
p0 -1836 ± 35.54
p1 0.449 ± 0.03187
-1200
-1400
-1600
-1800
-2000
-2200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

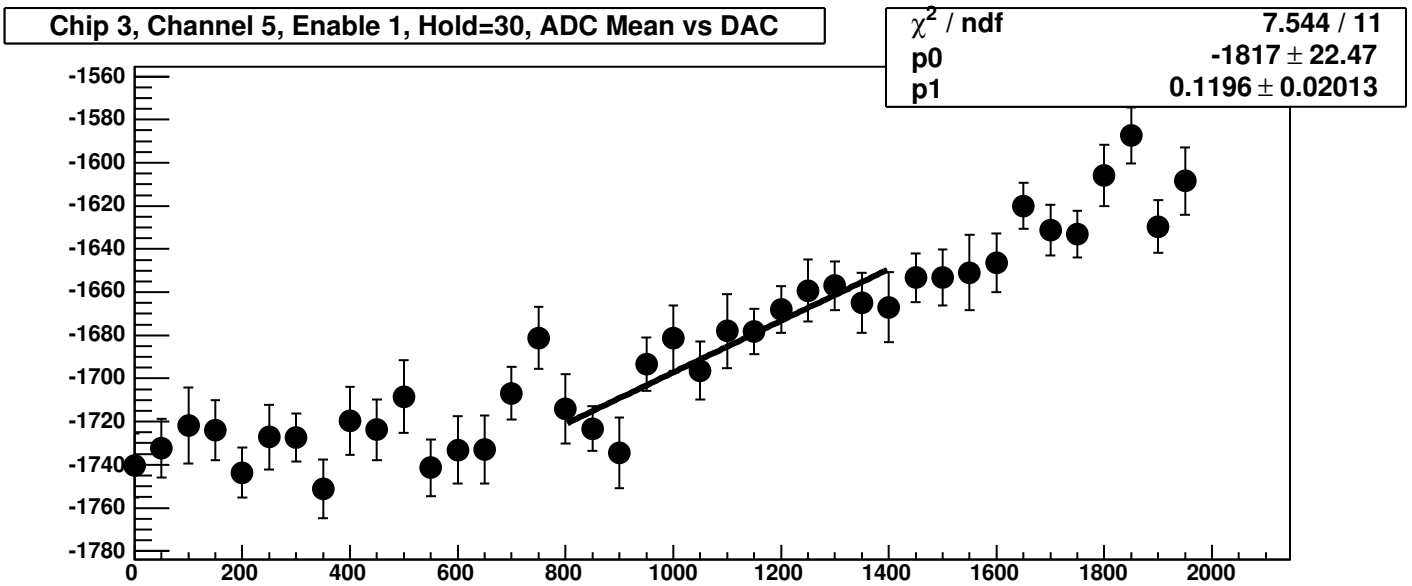

Chip 3, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

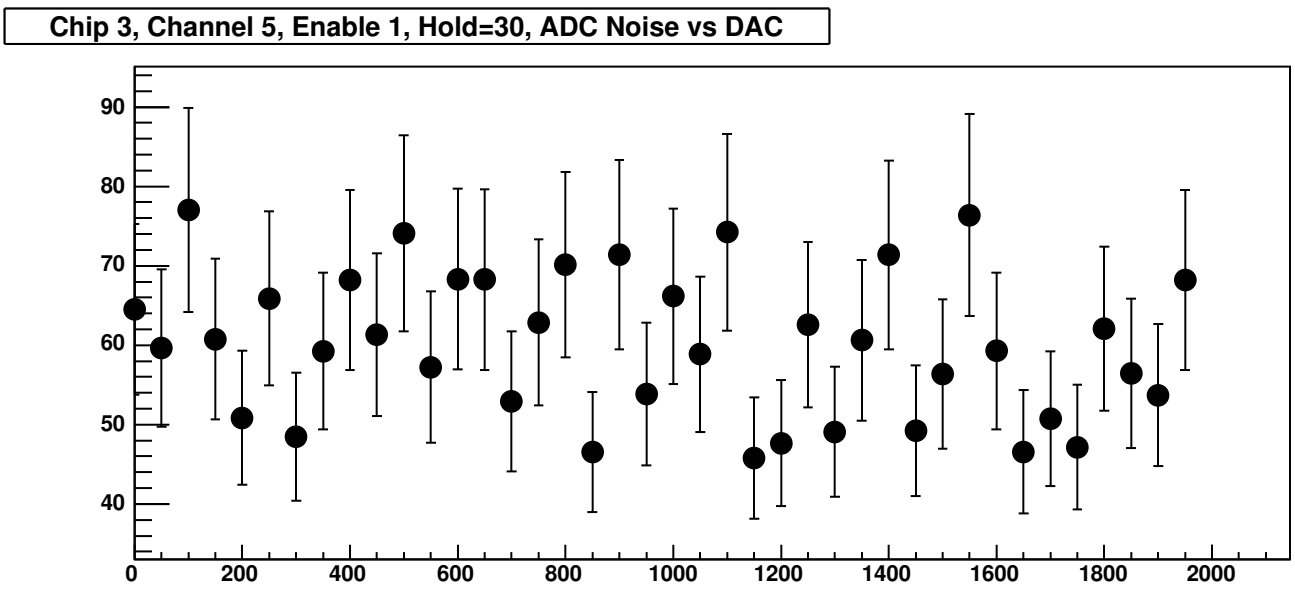

Chip 3, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

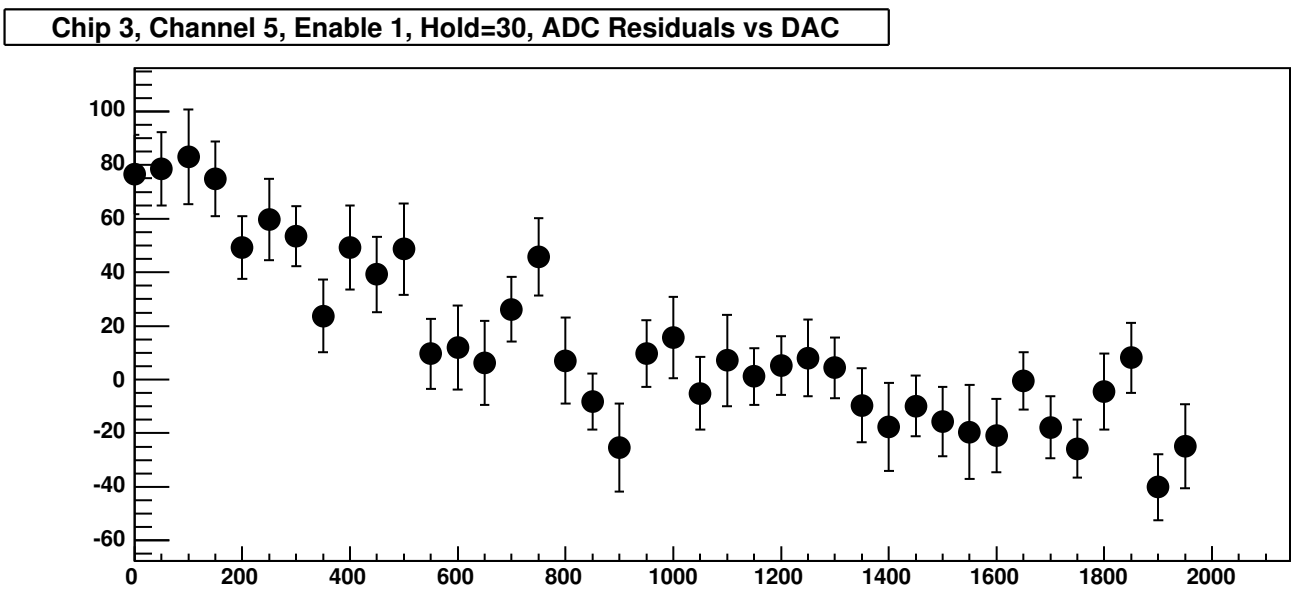

Chip 3, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 11.36 / 11
p0 -1903 ± 22.4
p1 0.2799 ± 0.02068
-1200
-1300
-1400
-1500
-1600
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 5, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7.544 / 11
p0 -1817 ± 22.47
p1 0.1196 ± 0.02013
-1560
-1580
-1600
-1620
-1640
-1660
-1680
-1700
-1720
-1740
-1760
-1780
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 5, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 5, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
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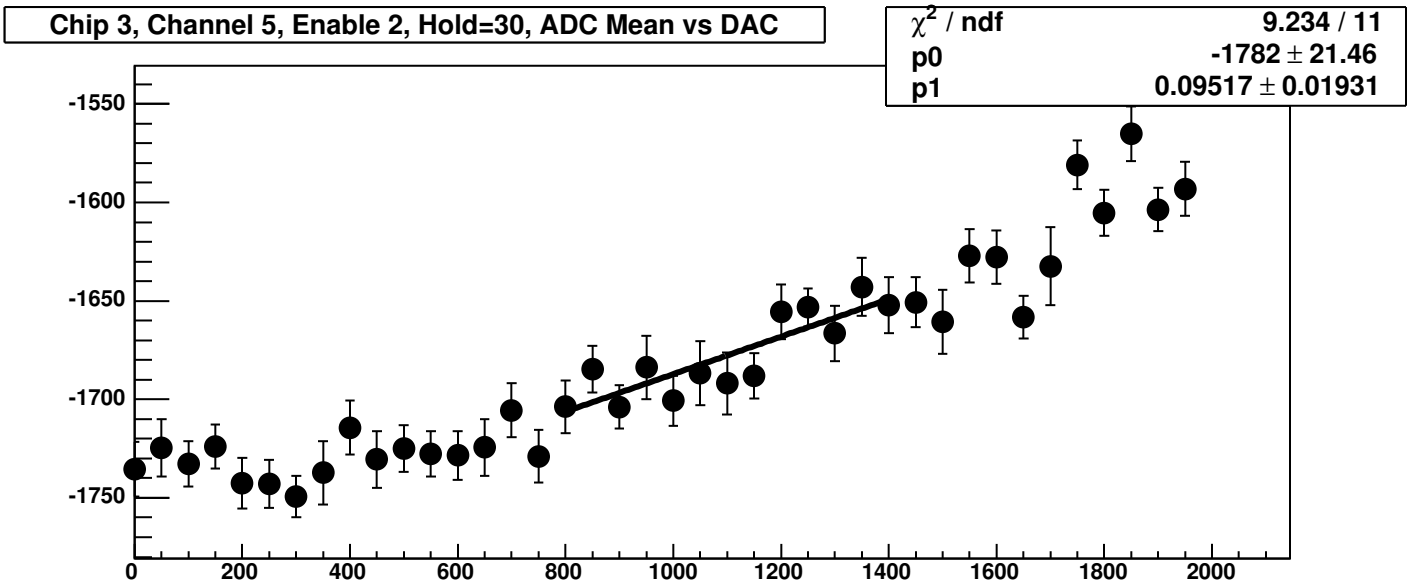
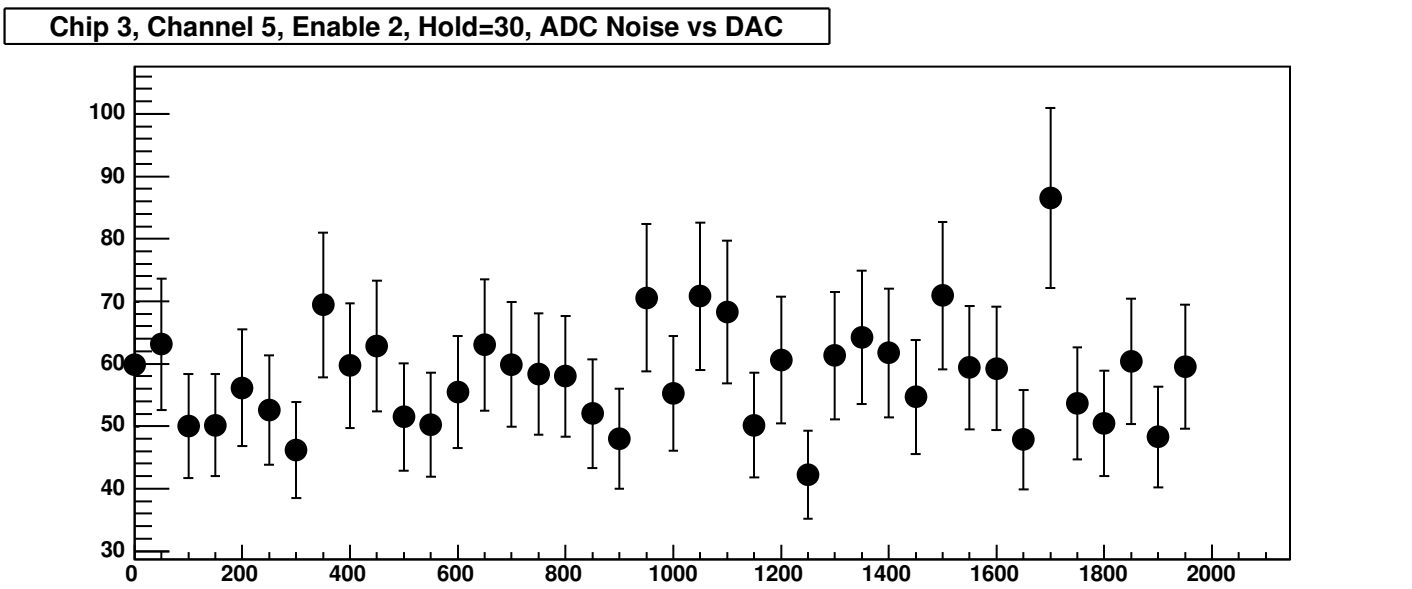
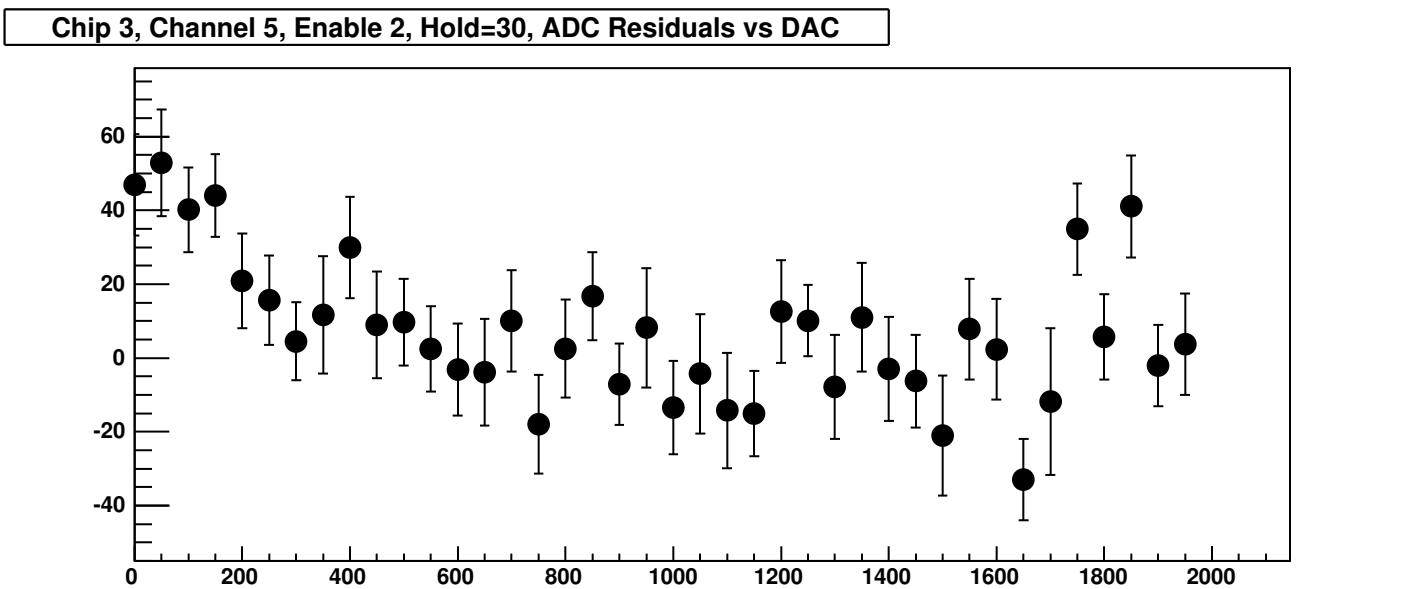
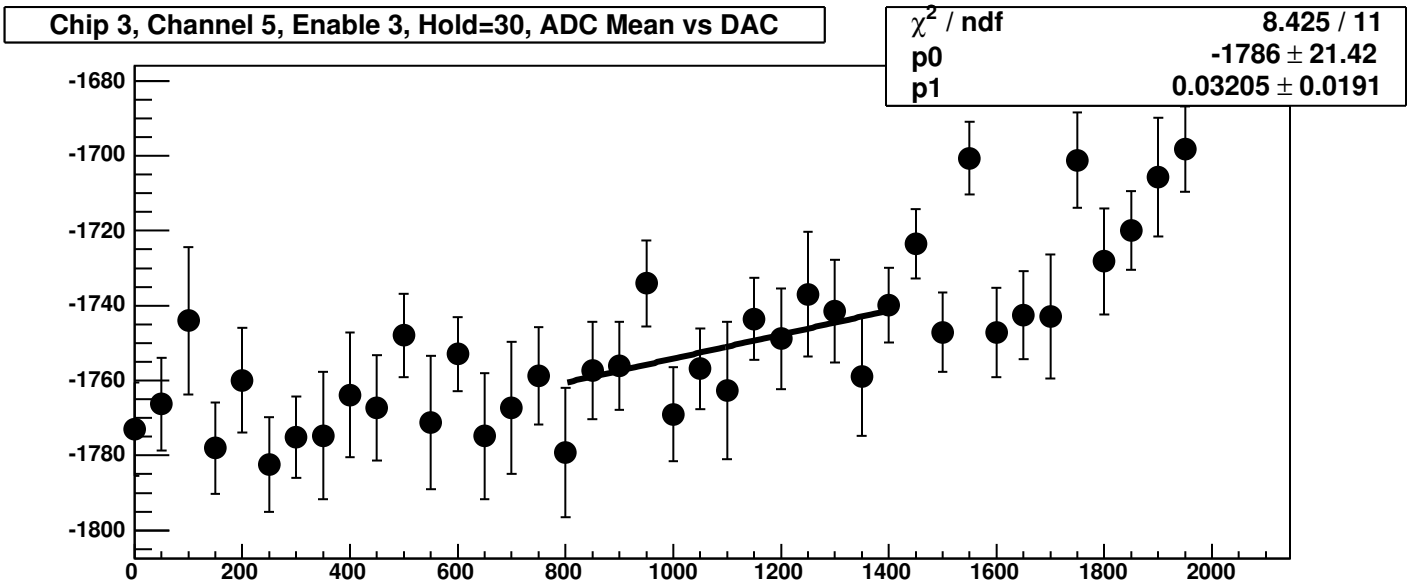
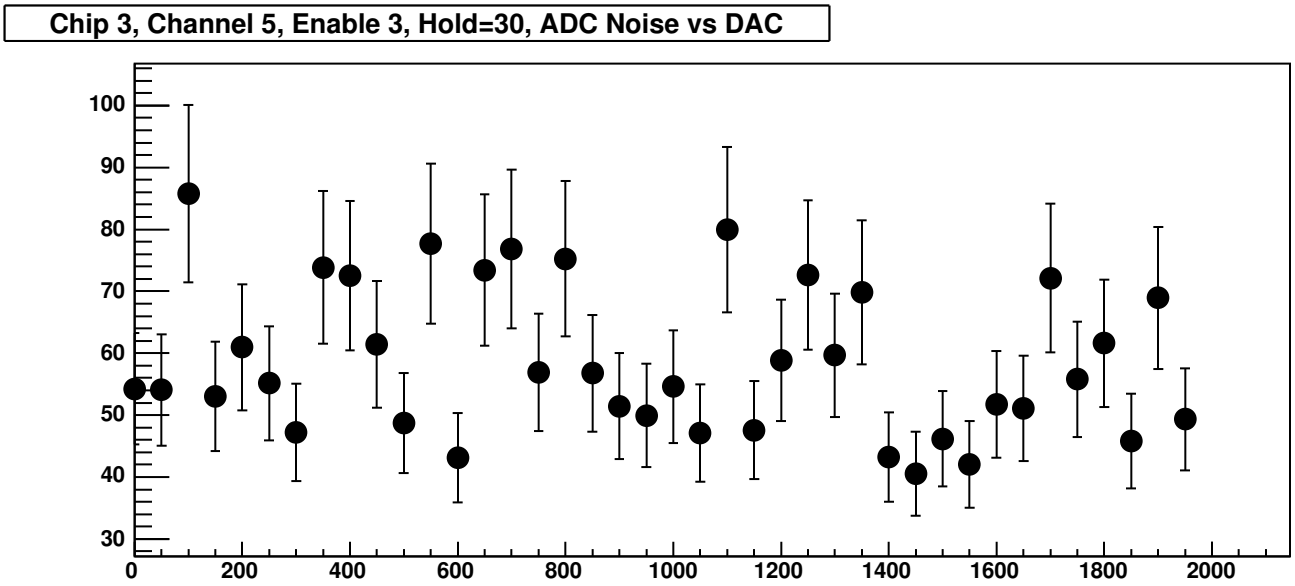
1000
1200
1400
1600
1800
2000

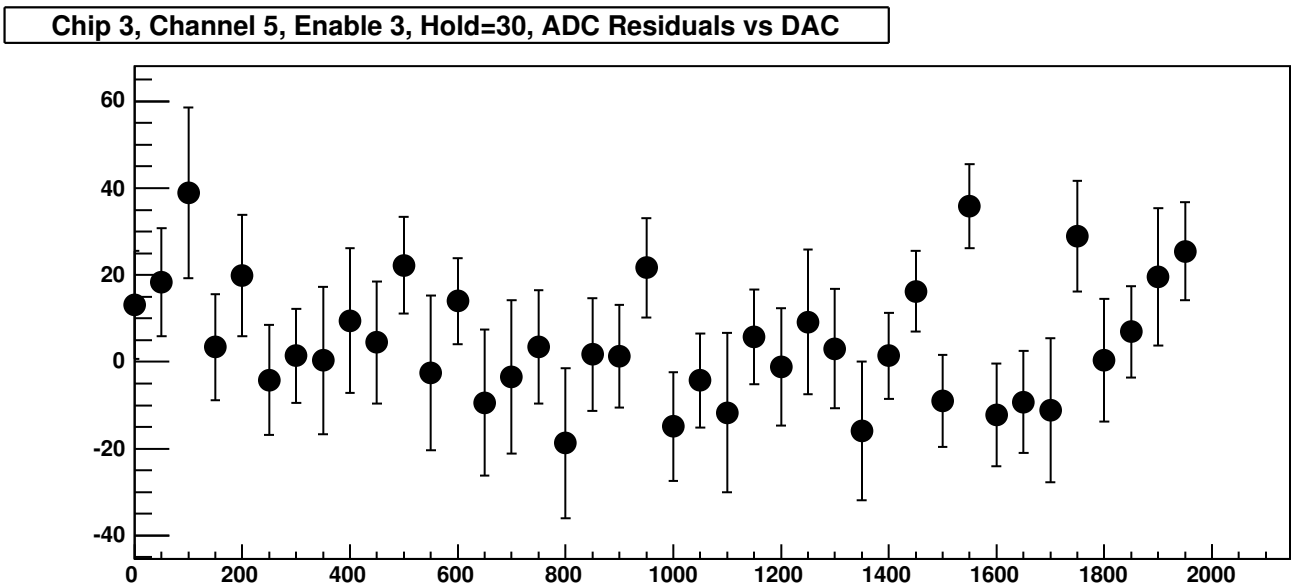

Chip 3, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 9.234 / 11
p0 -1782 ± 21.46
p1 0.09517 ± 0.01931
-1550
-1600
-1650
-1700
-1750
0
200
400
600
800
1000
1200
1400
1600
1800
2000

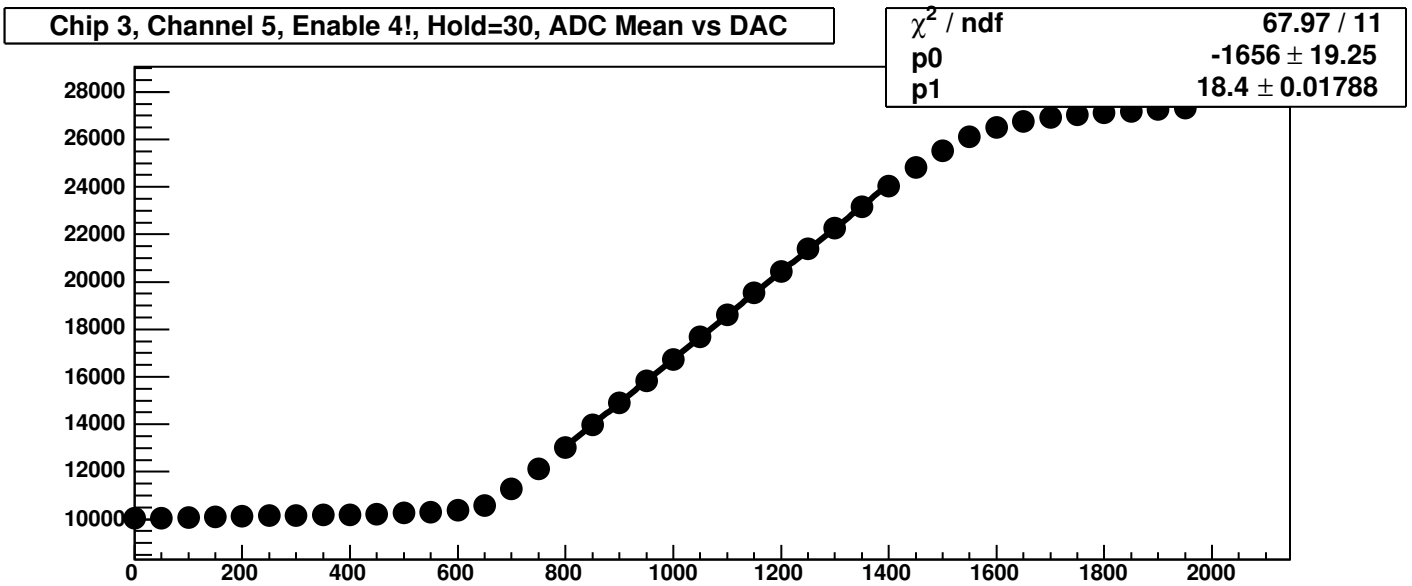

Chip 3, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

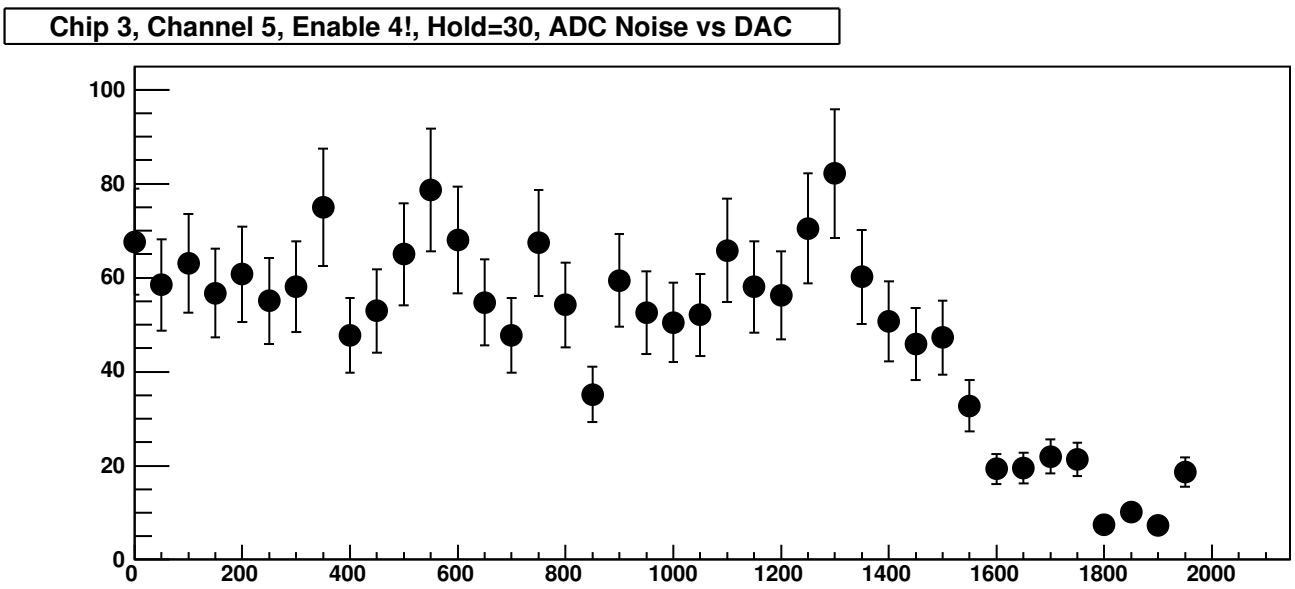

Chip 3, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

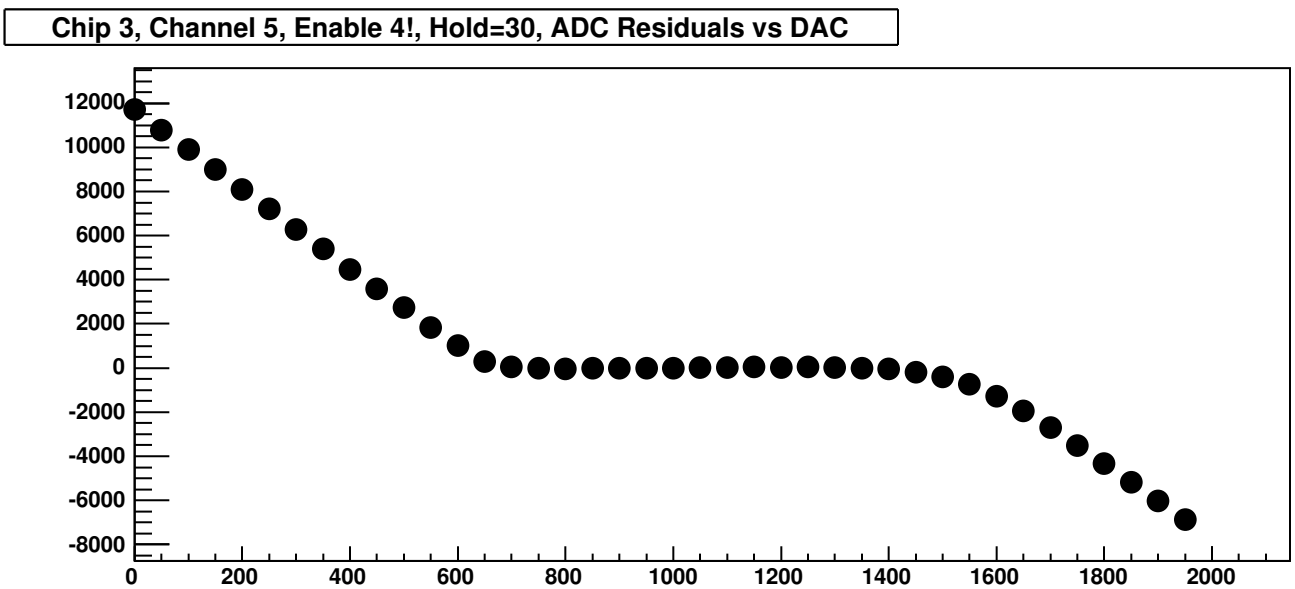

Chip 3, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 8.425 / 11
p0 -1786 ± 21.42
p1 0.03205 ± 0.0191
-1680
-1700
-1720
-1740
-1760
-1780
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

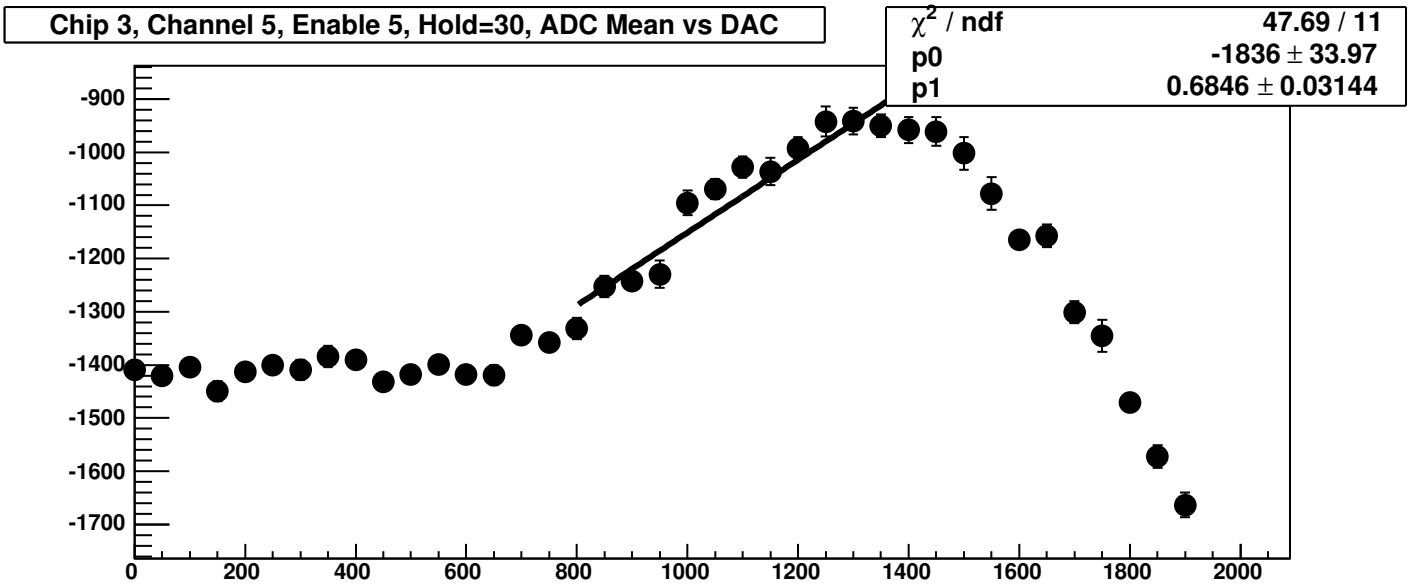

Chip 3, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

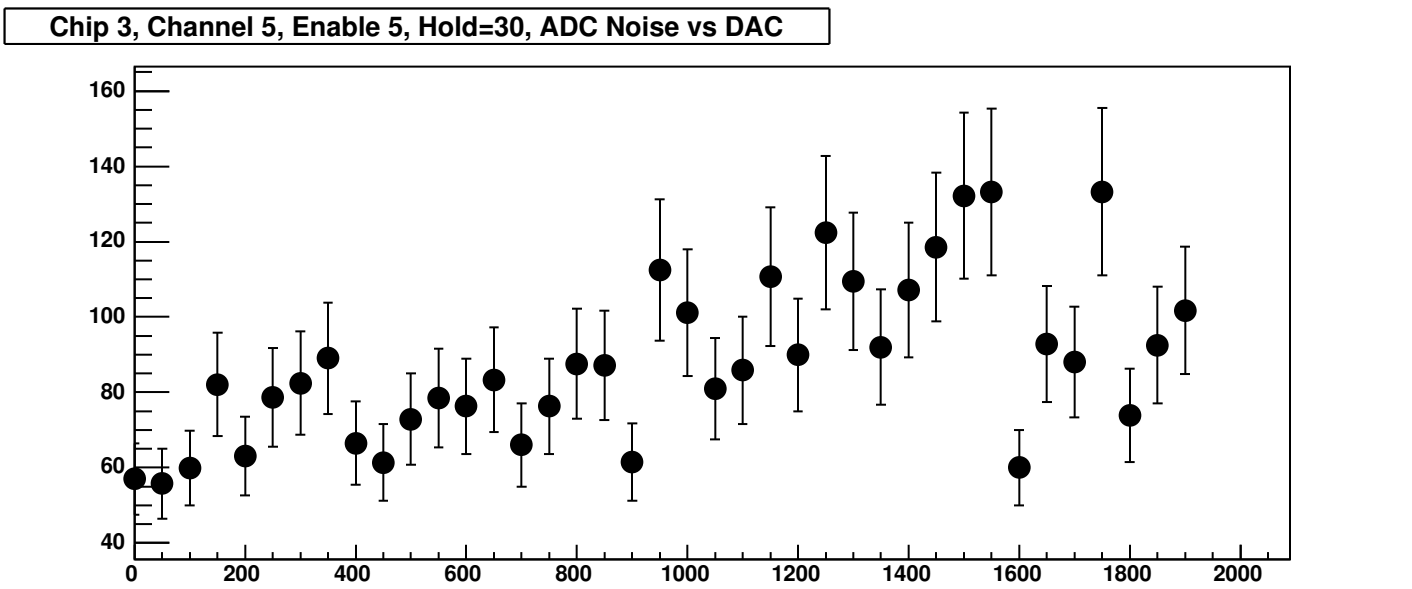

Chip 3, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

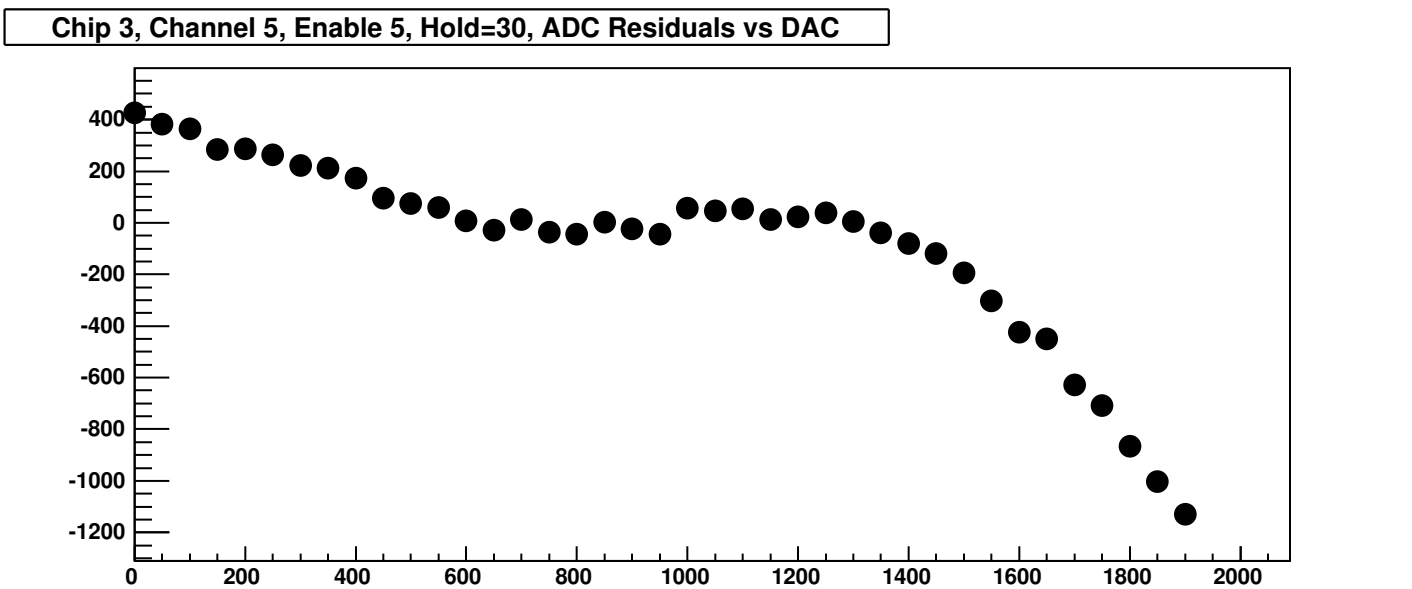

Chip 3, Channel 5, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 67.97 / 11
p0 -1656 ± 19.25
p1 18.4 ± 0.01788
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

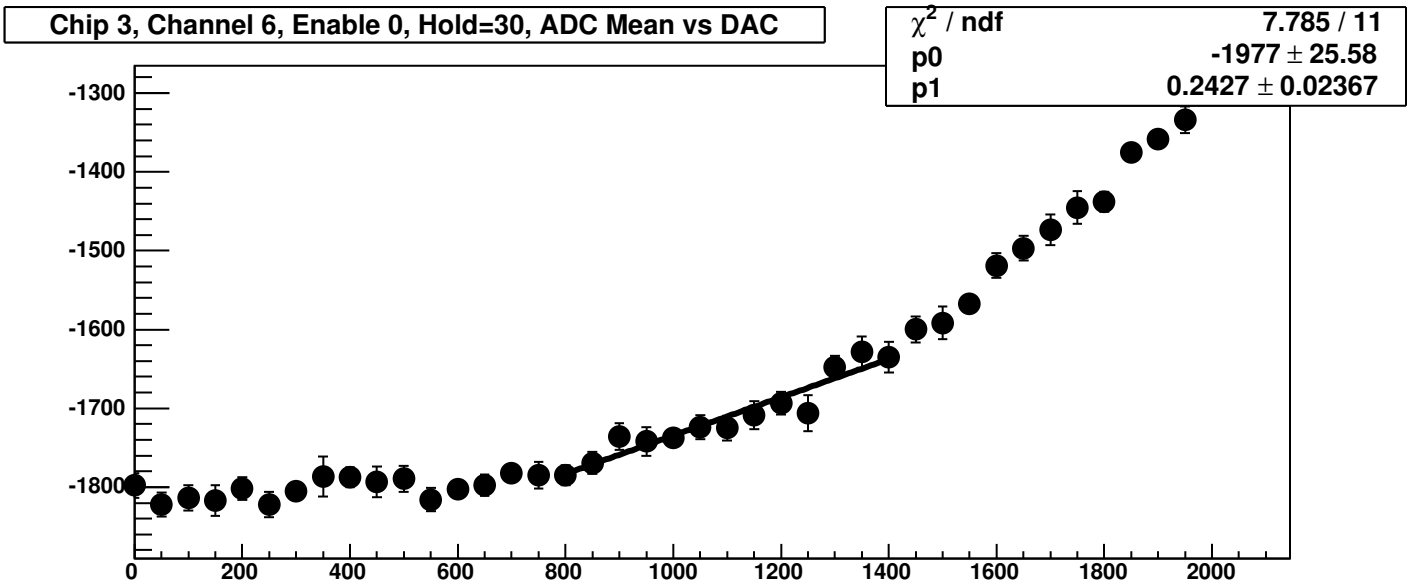

Chip 3, Channel 5, Enable 4!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

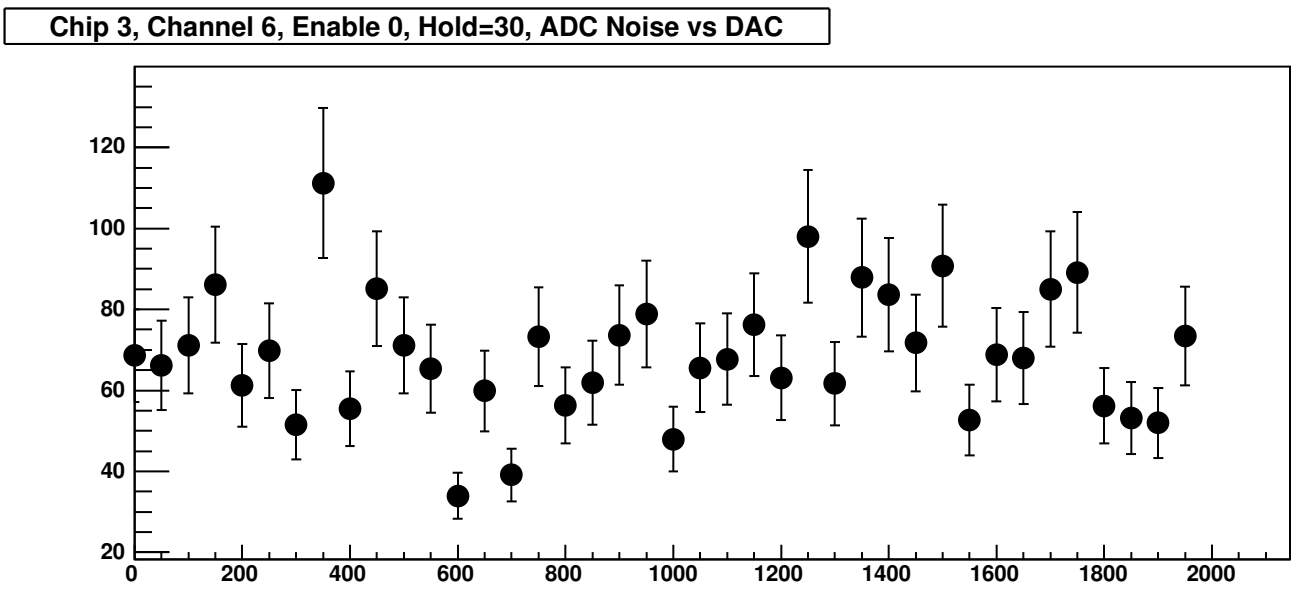

Chip 3, Channel 5, Enable 4!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

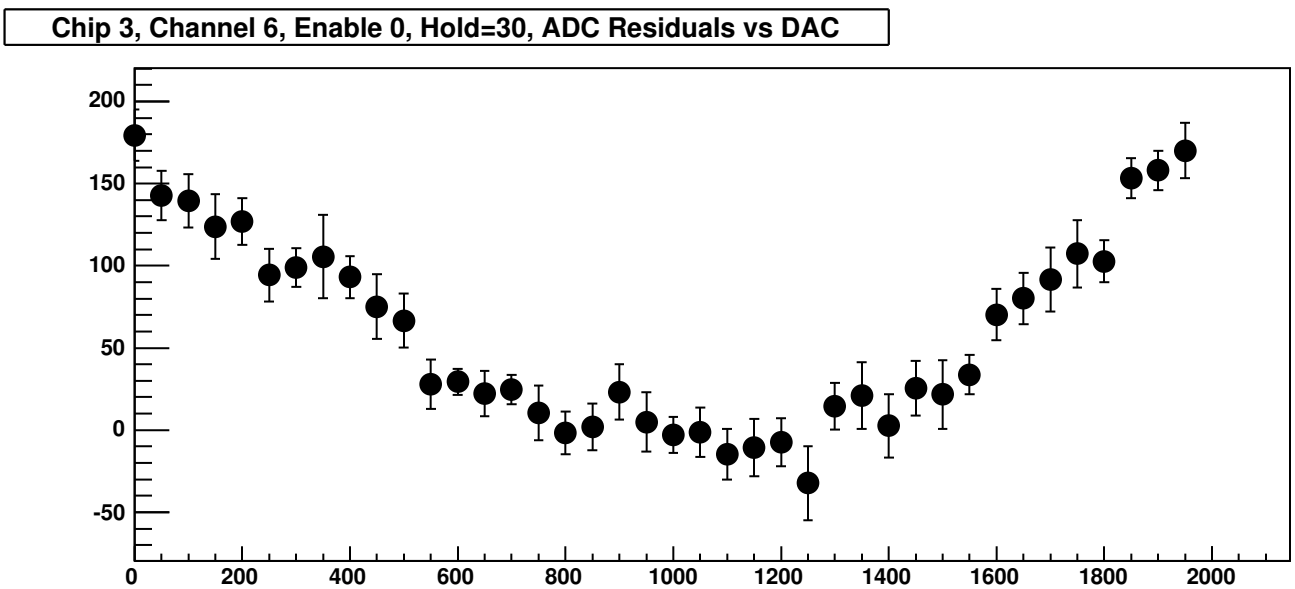

Chip 3, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 47.69 / 11
p0 -1836 ± 33.97
p1 0.6846 ± 0.03144
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

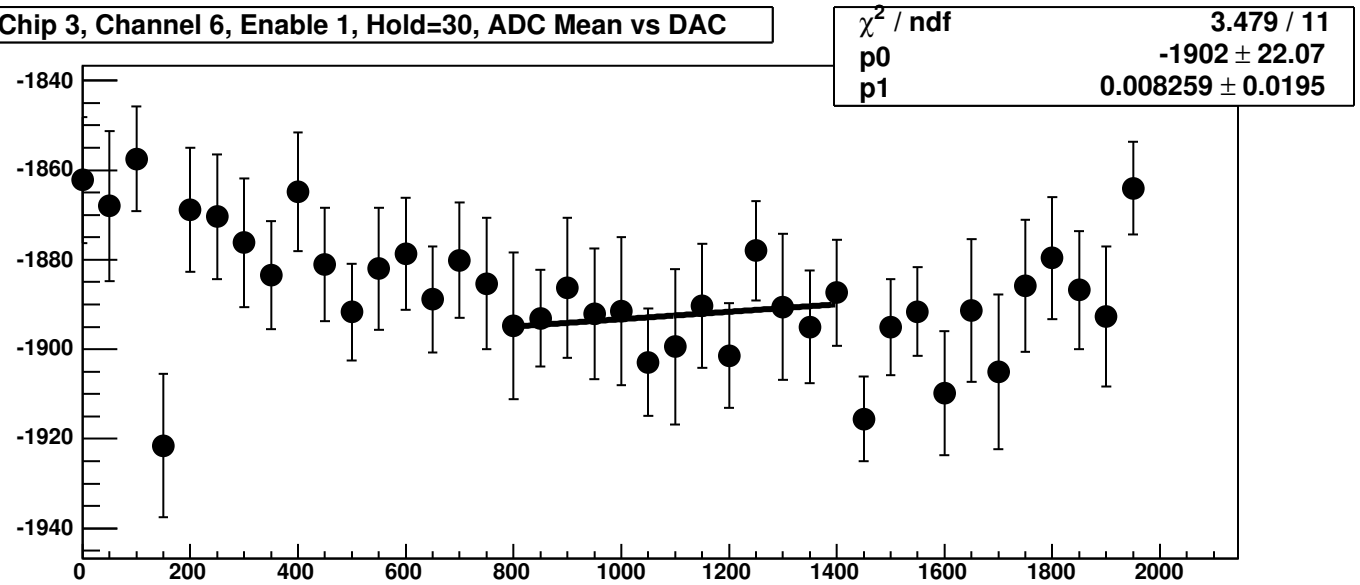

Chip 3, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 7.785 / 11
p0 -1977 ± 25.58
p1 0.2427 ± 0.02367
-1300
-1400
-1500
-1600
-1700
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

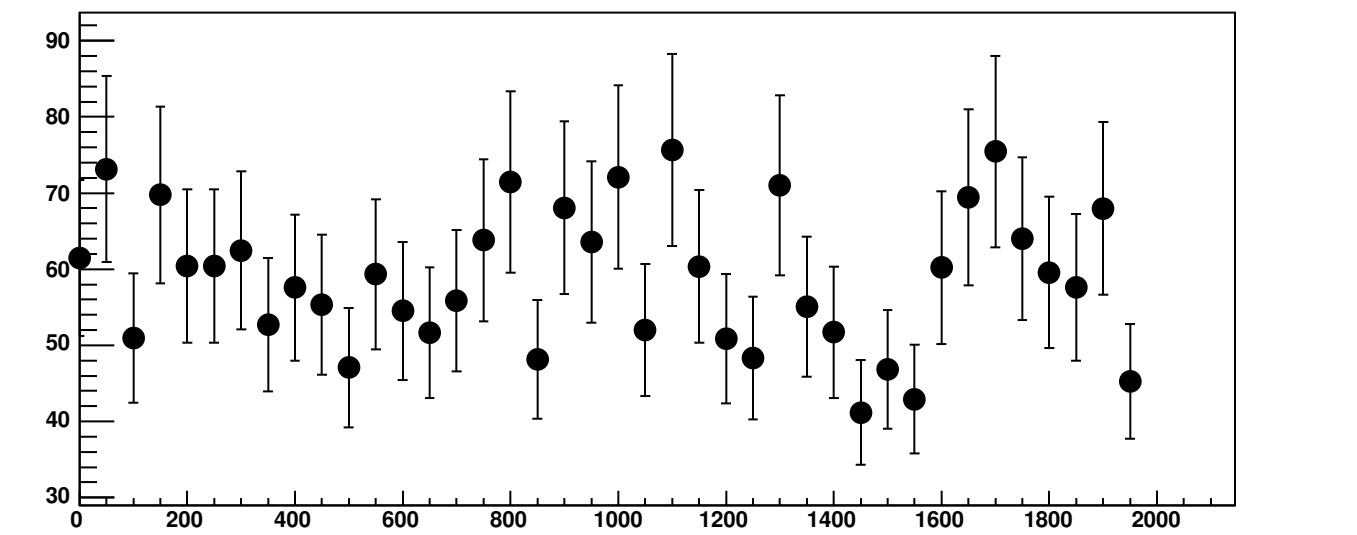

Chip 3, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

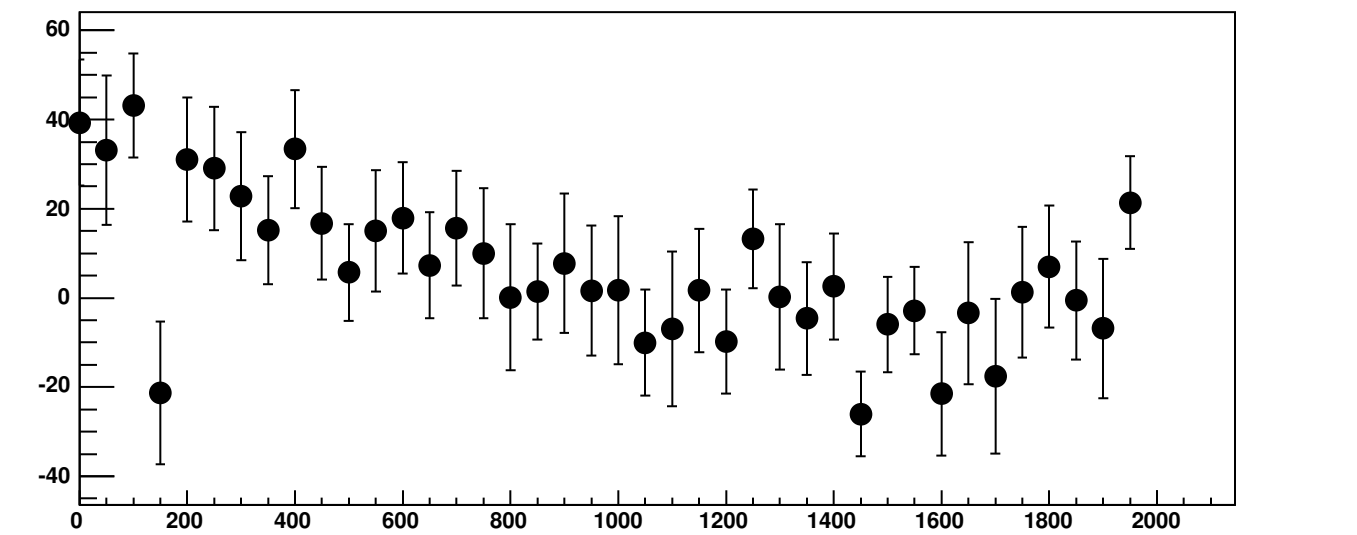
Chip 3, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC

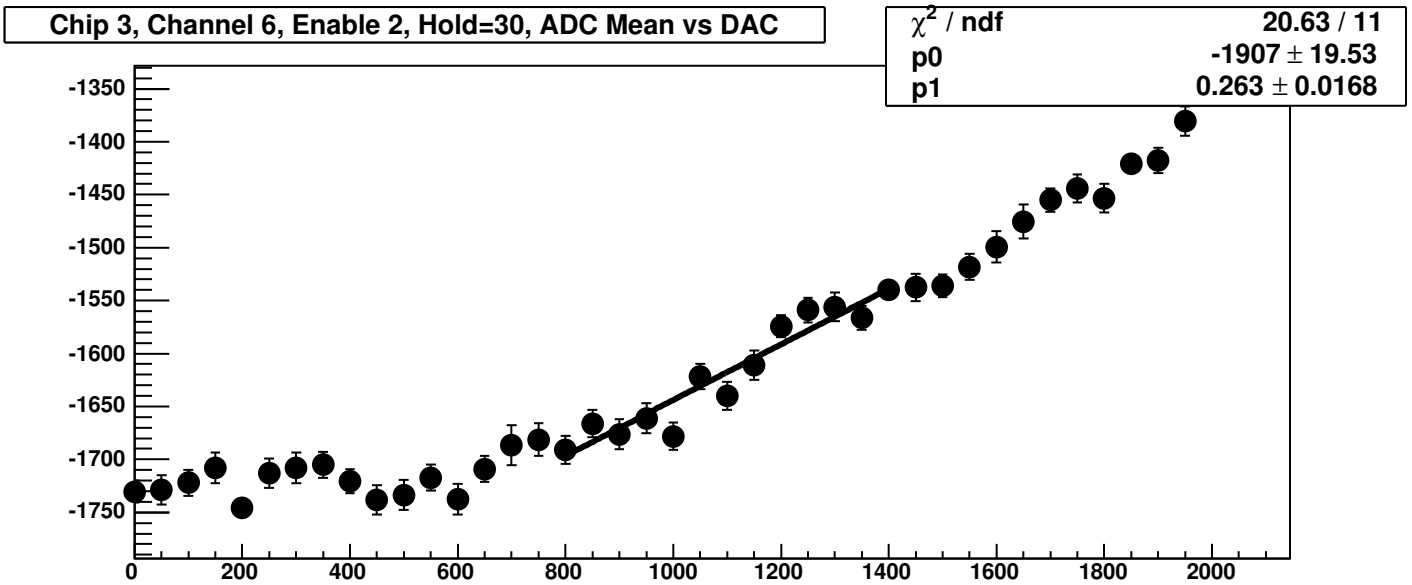


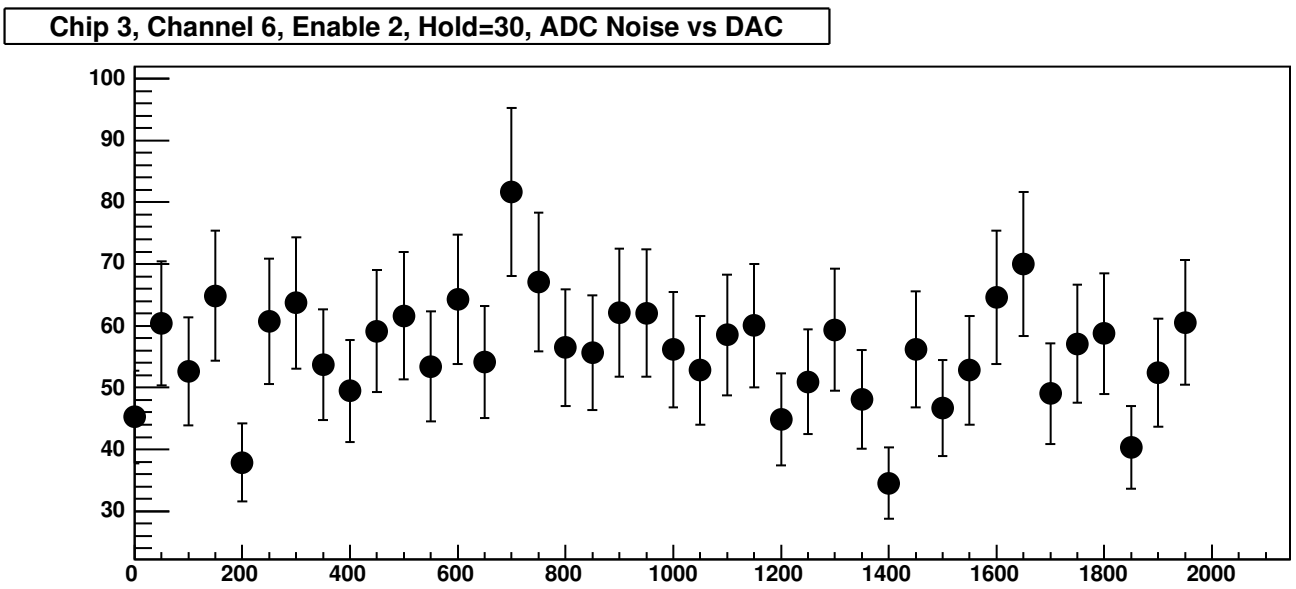
Chip 3, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC

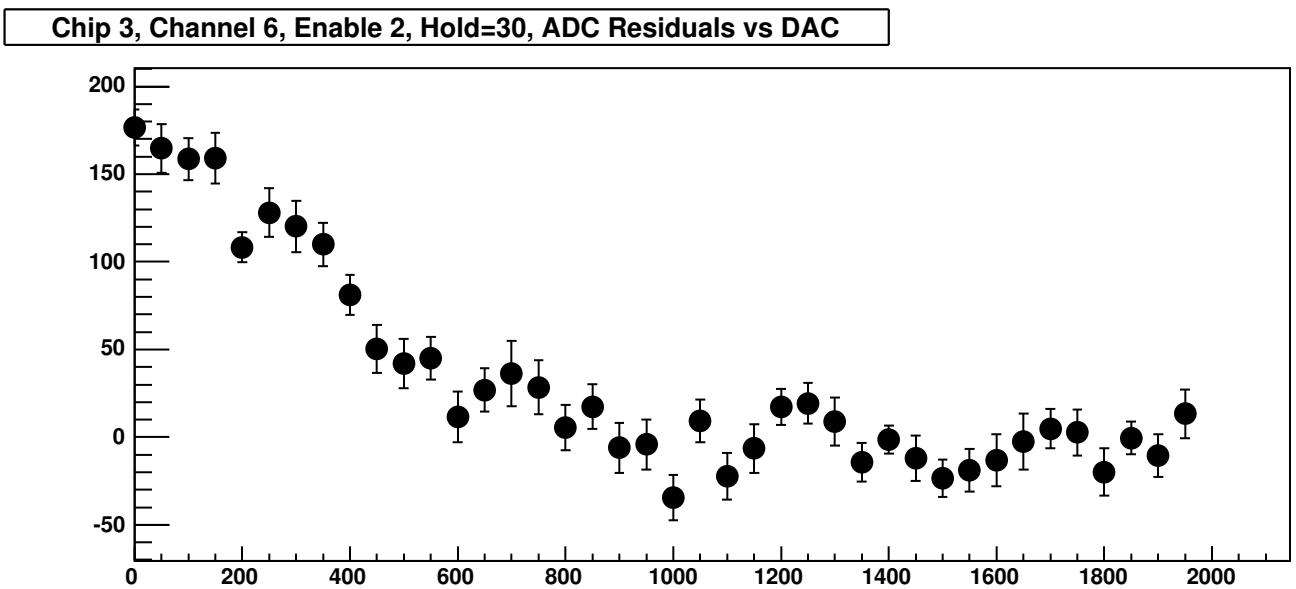


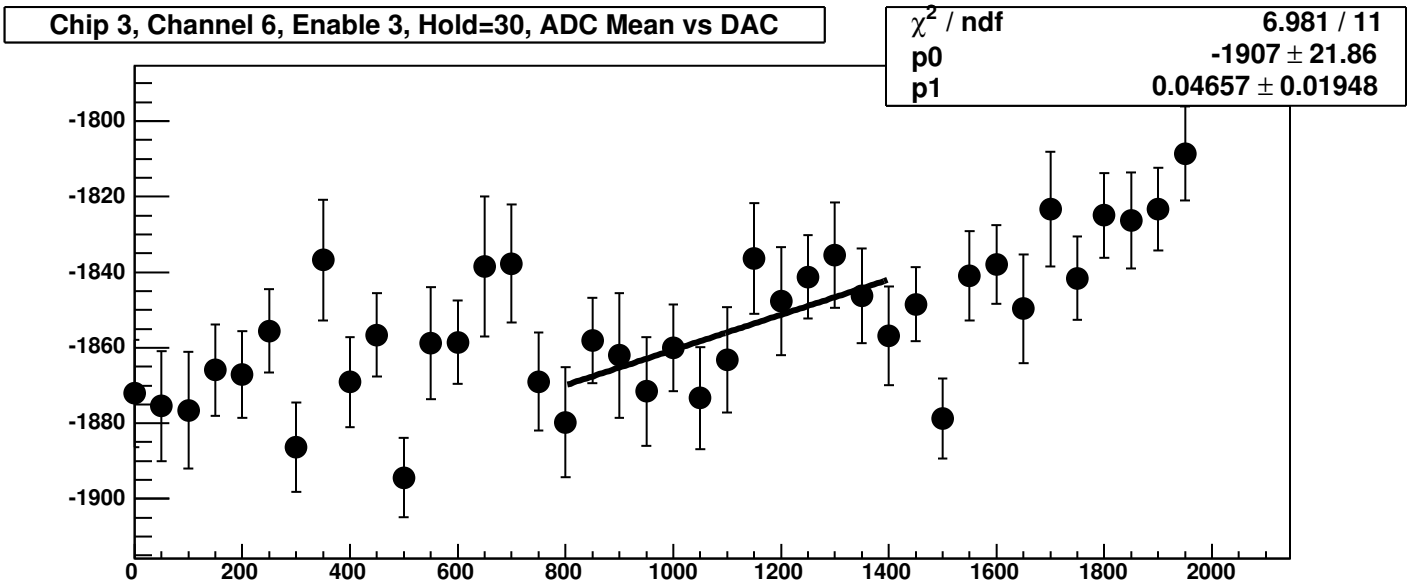
Chip 3, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC

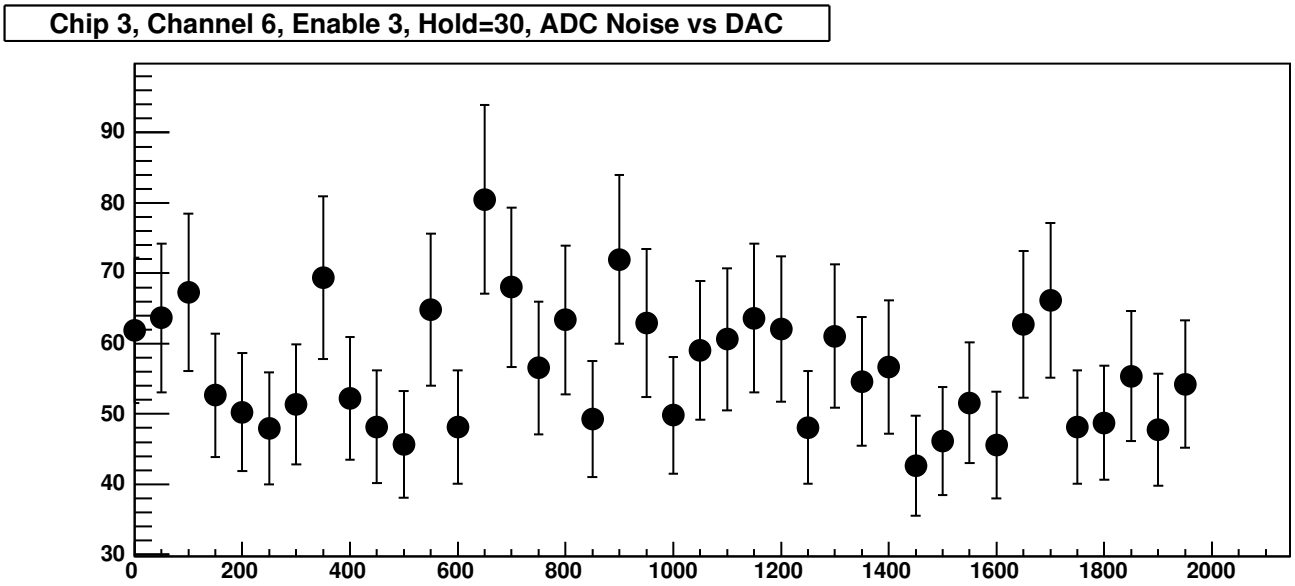


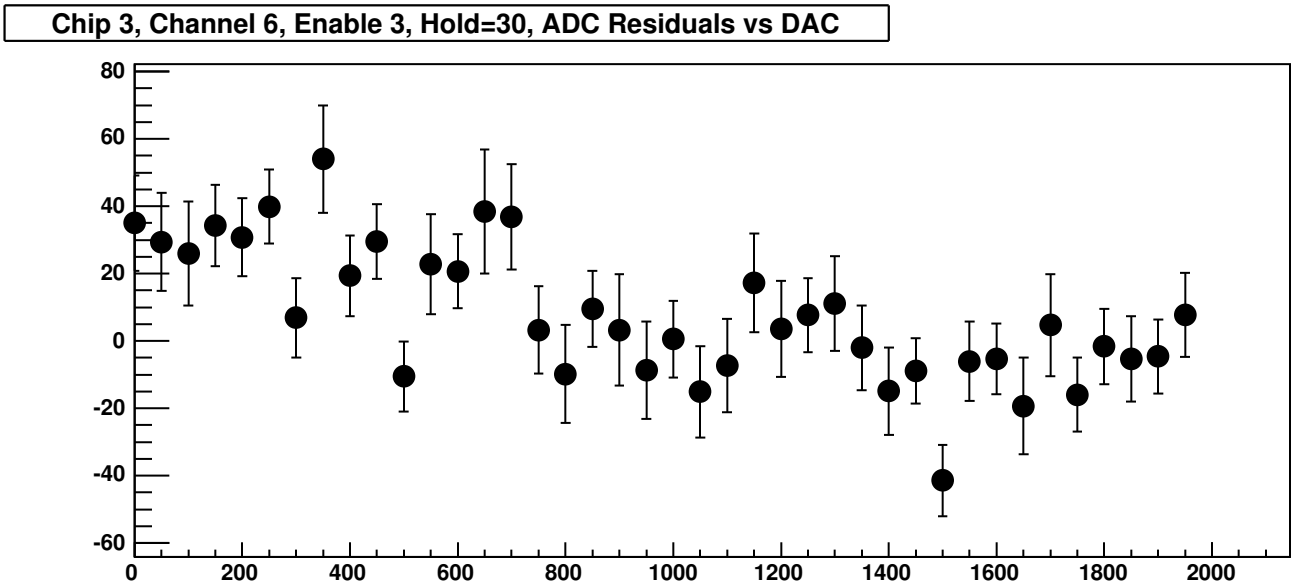

Chip 3, Channel 6, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 20.63 / 11
p0 -1907 ± 19.53
p1 0.263 ± 0.0168
-1350
-1400
-1450
-1500
-1550
-1600
-1650
-1700
-1750
0
200
400
600
800
1000
1200
1400
1600
1800
2000

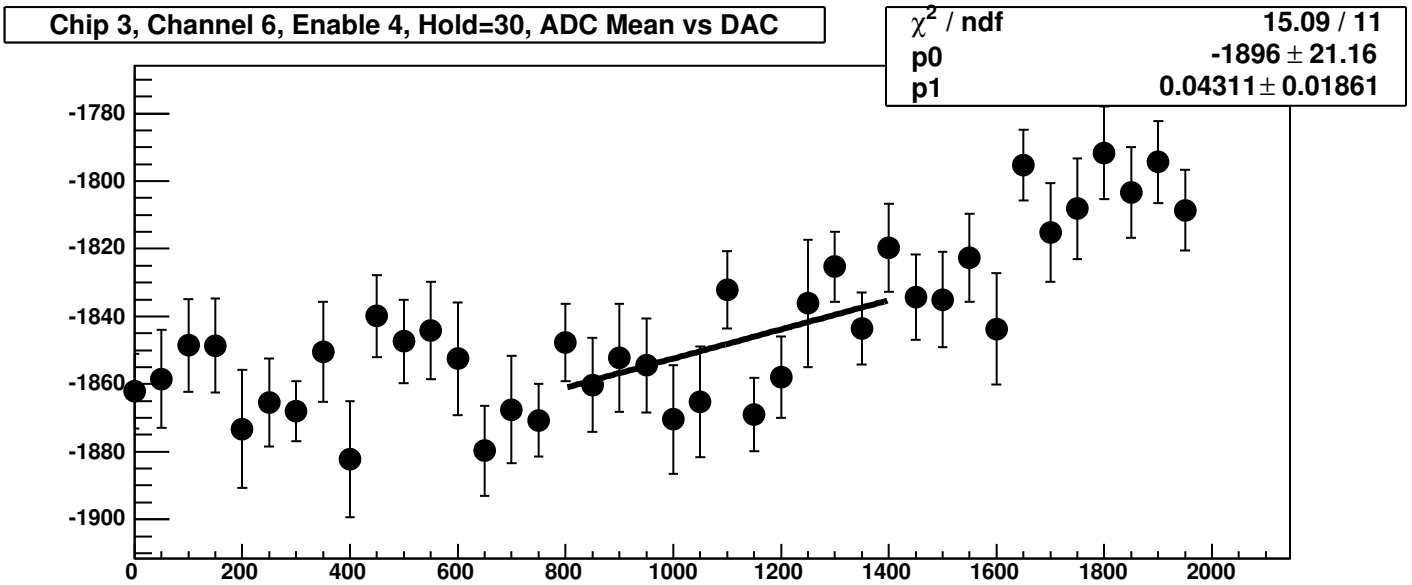

Chip 3, Channel 6, Enable 2, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

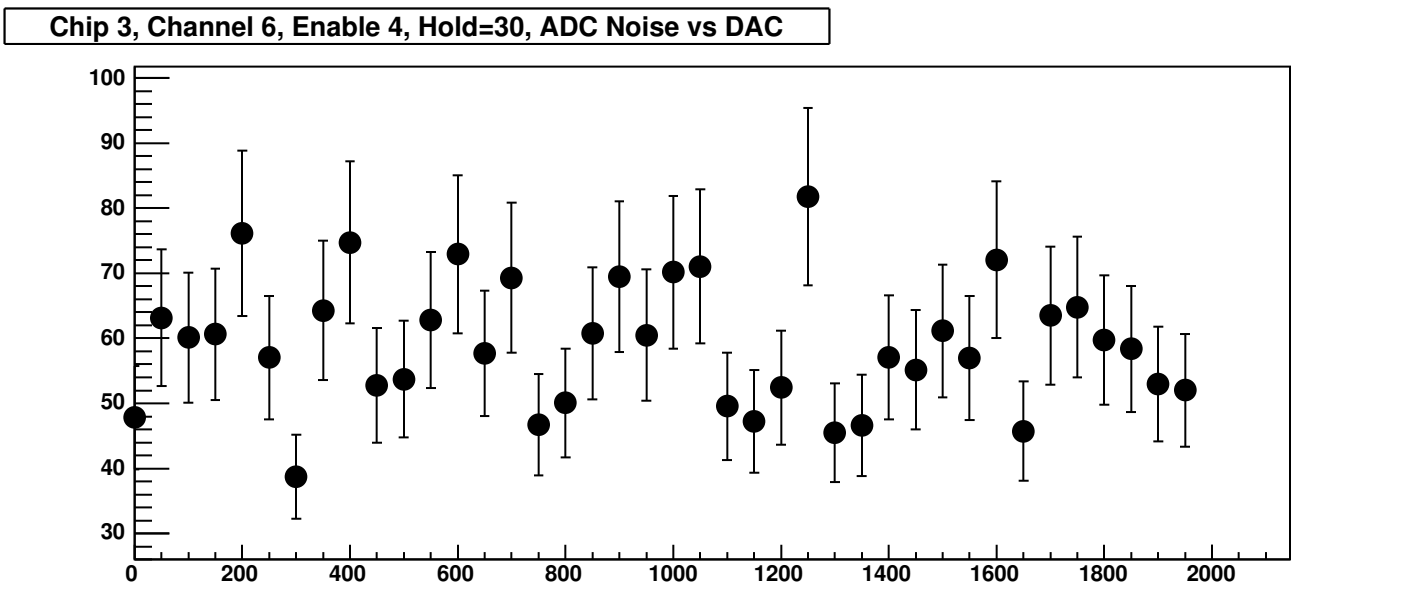

Chip 3, Channel 6, Enable 2, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

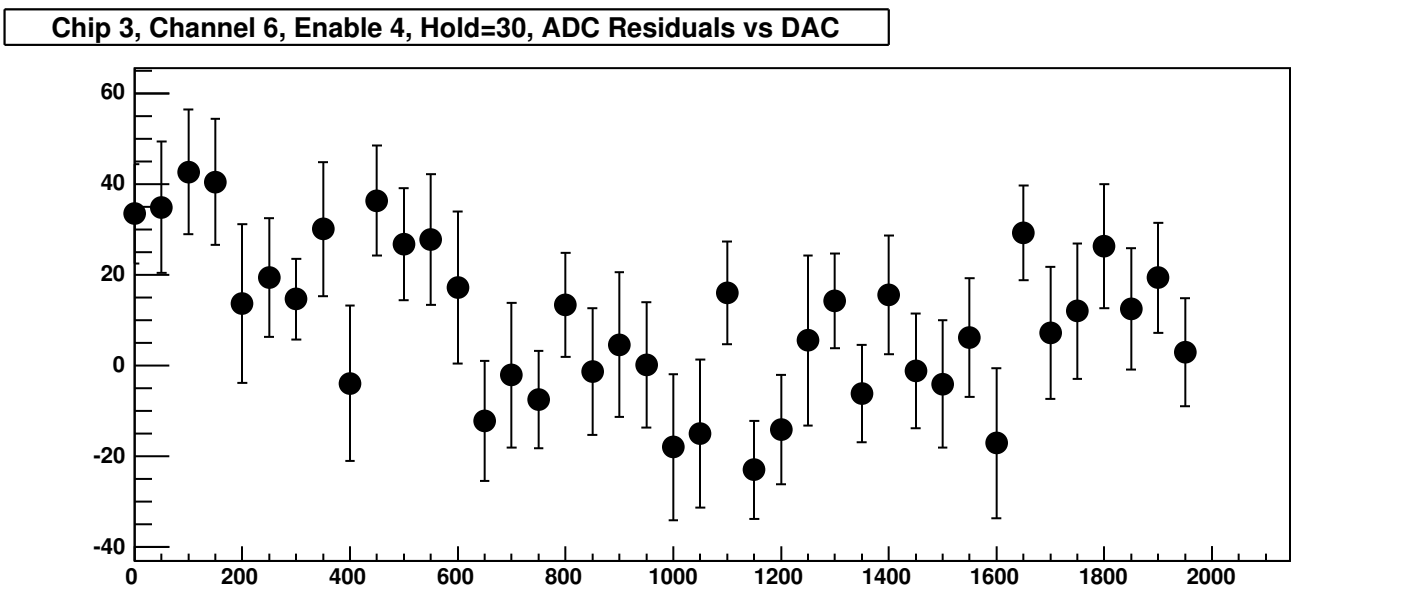

Chip 3, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 6.981 / 11
p0 -1907 ± 21.86
p1 0.04657 ± 0.01948
-1800
-1820
-1840
-1860
-1880
-1900
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

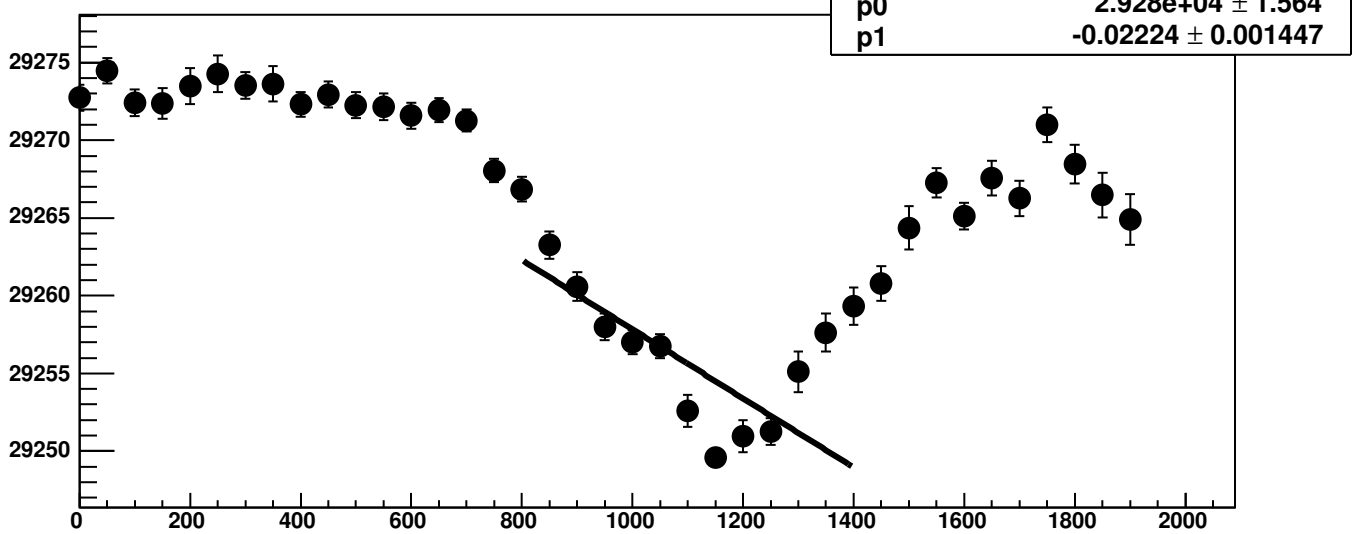

Chip 3, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 15.09 / 11
p0 -1896 ± 21.16
p1 0.04311 ± 0.01861
-1780
-1800
-1820
-1840
-1860
-1880
-1900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

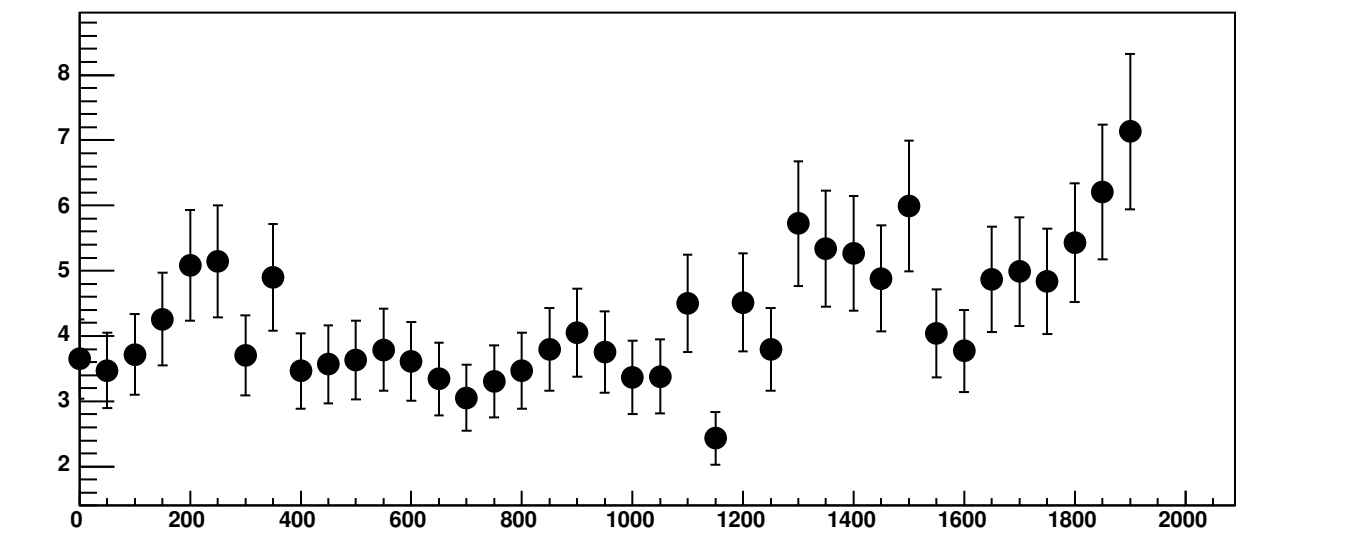

Chip 3, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

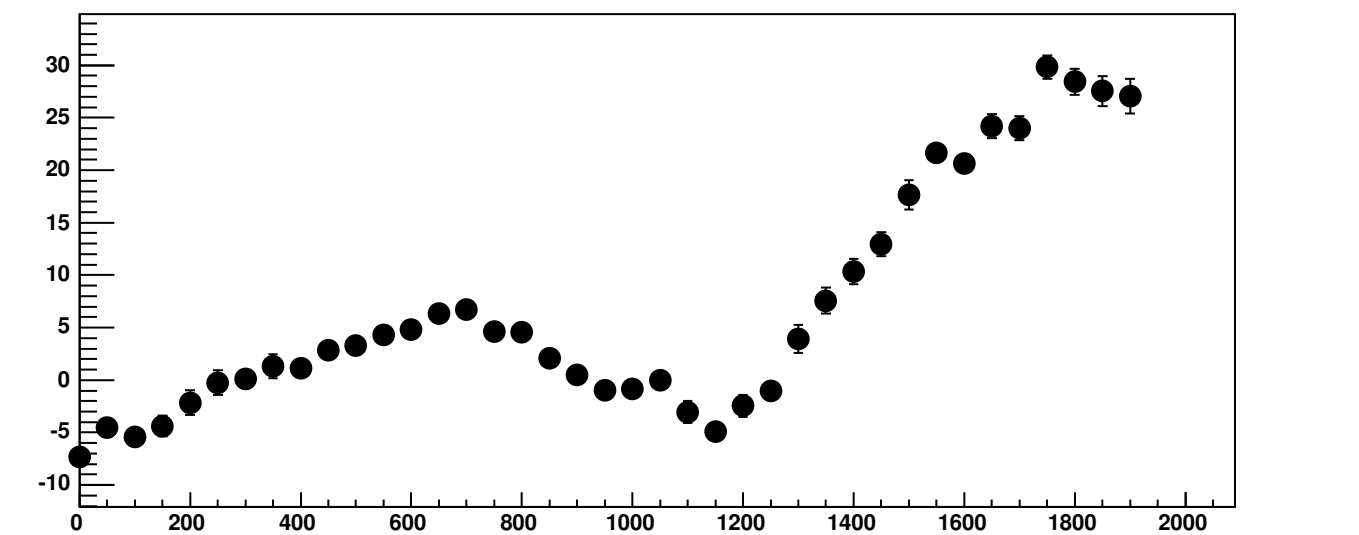
Chip 3, Channel 6, Enable 5!, Hold=30, ADC Mean vs DAC

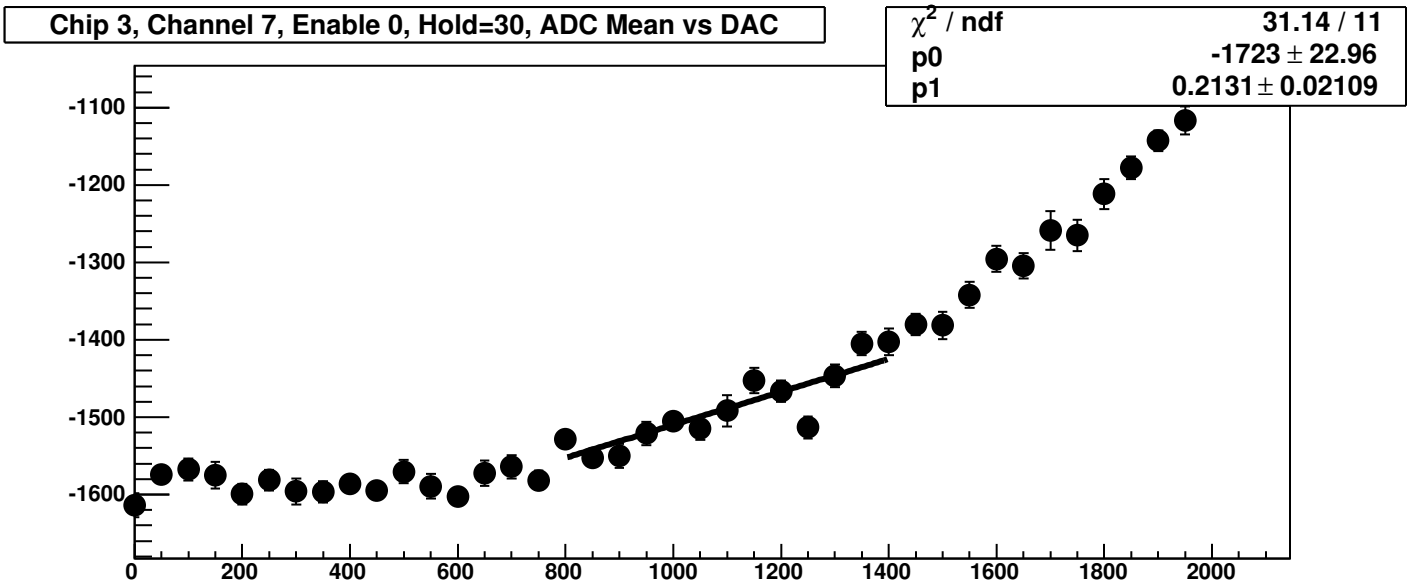


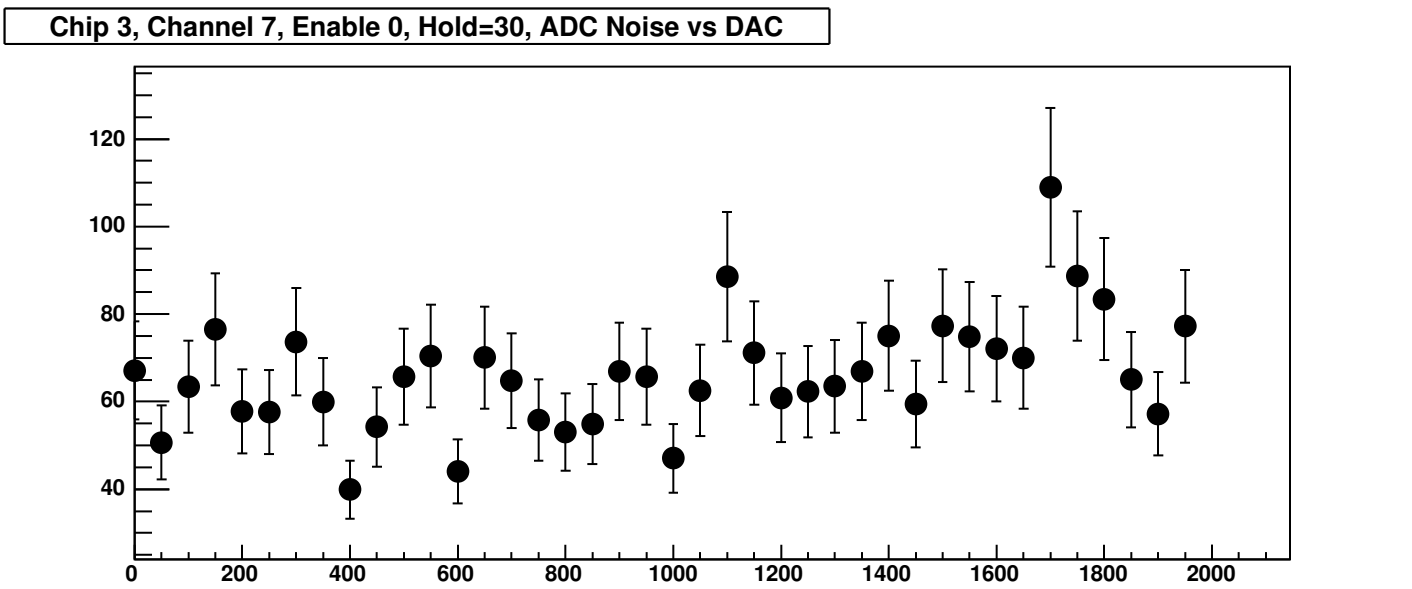
Chip 3, Channel 6, Enable 5!, Hold=30, ADC Noise vs DAC

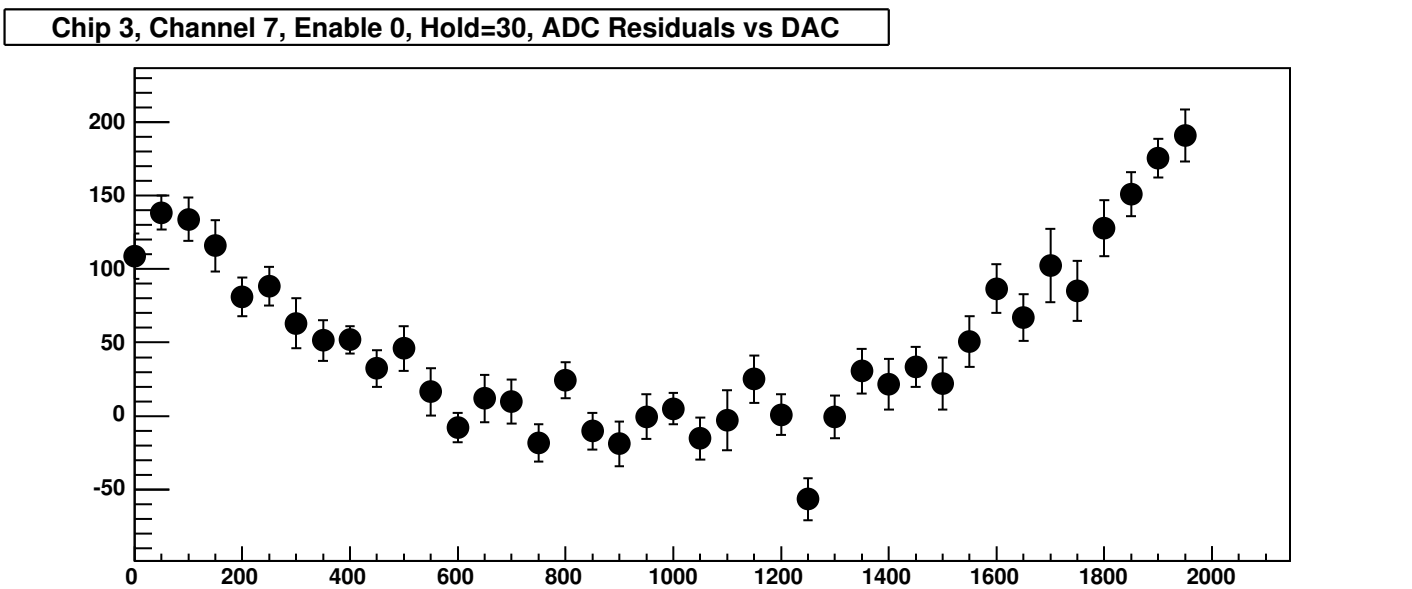


Chip 3, Channel 6, Enable 5!, Hold=30, ADC Residuals vs DAC

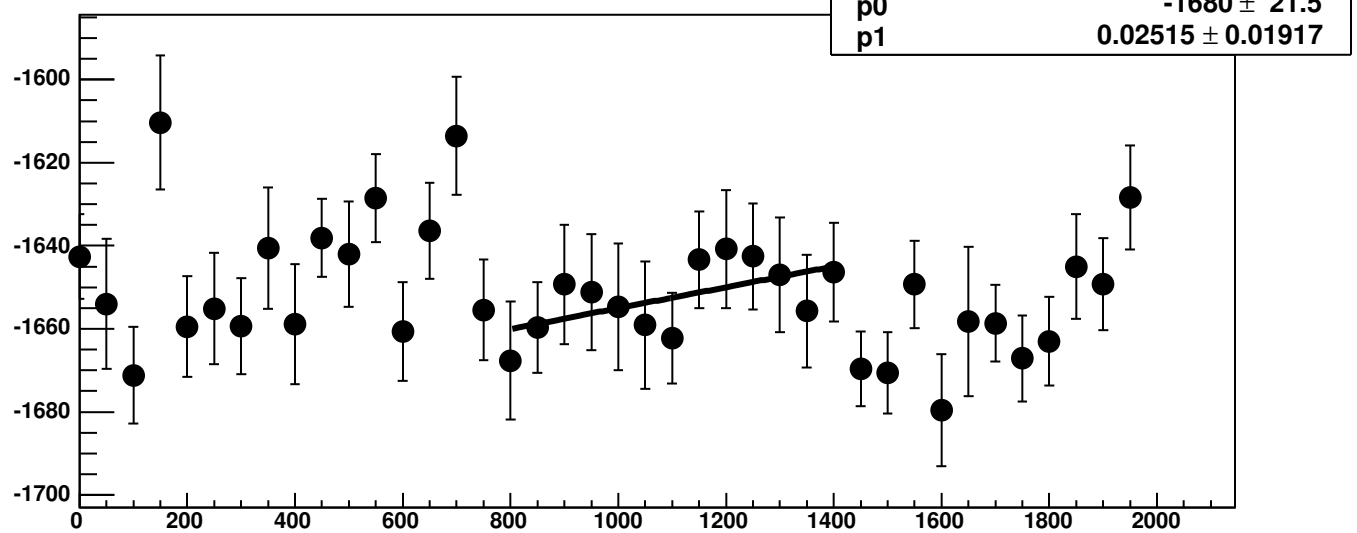



Chip 3, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 31.14 / 11
p0 -1723 ± 22.96
p1 0.2131 ± 0.02109
-1100
-1200
-1300
-1400
-1500
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

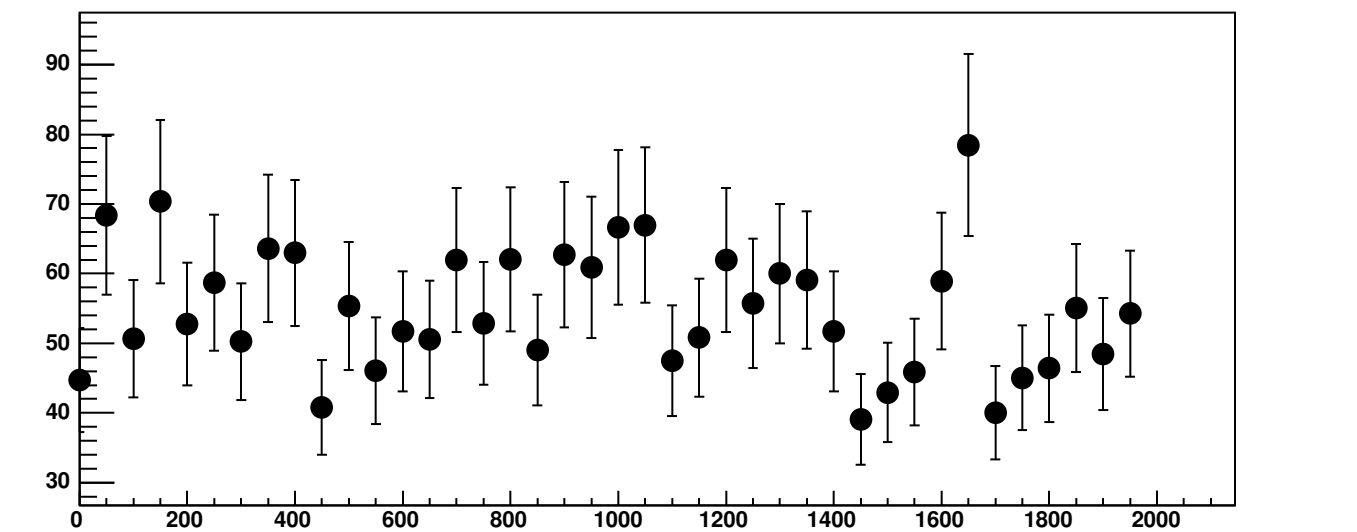

Chip 3, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

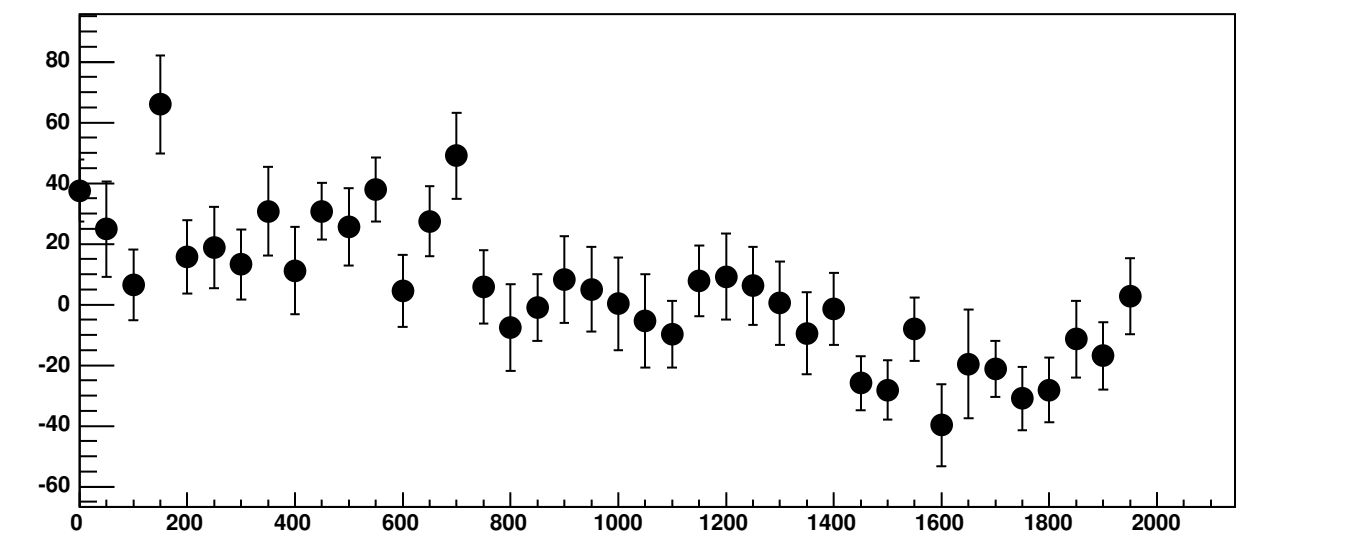
Chip 3, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC

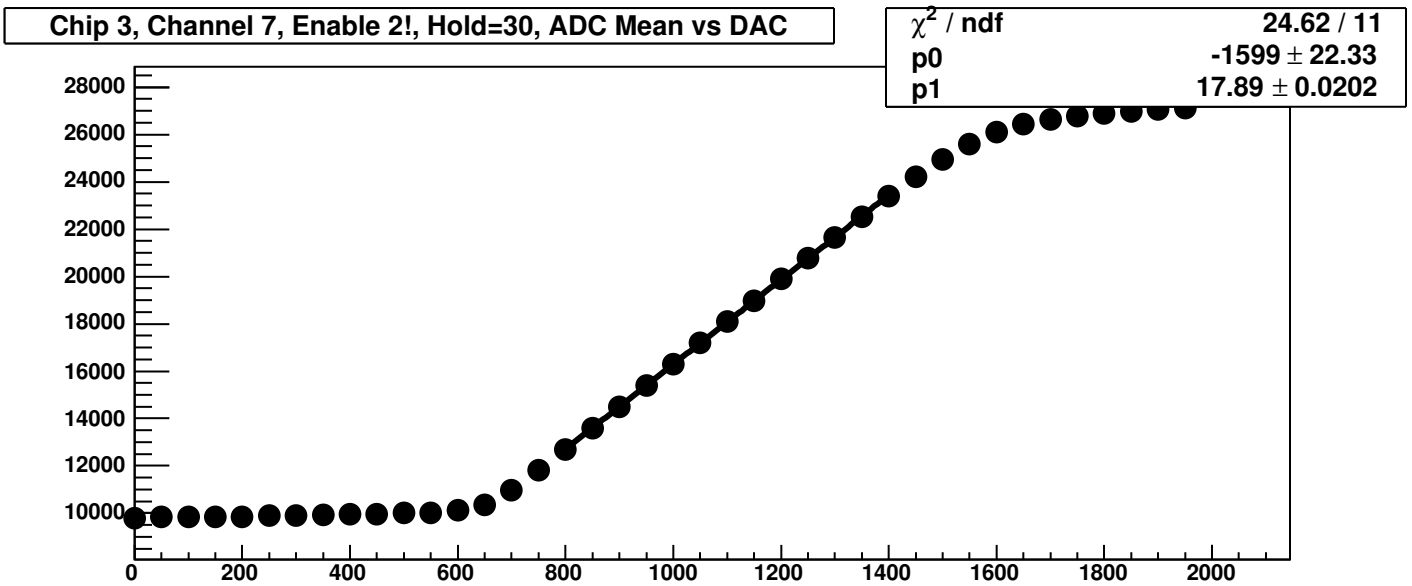


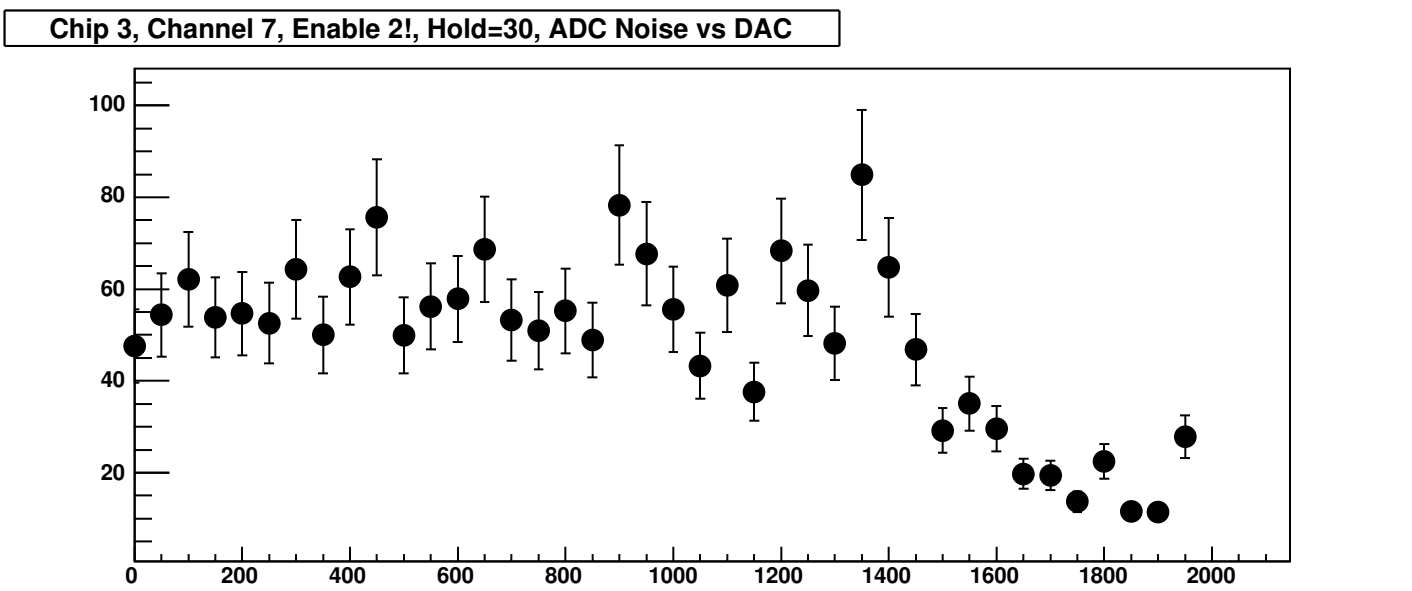
Chip 3, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC

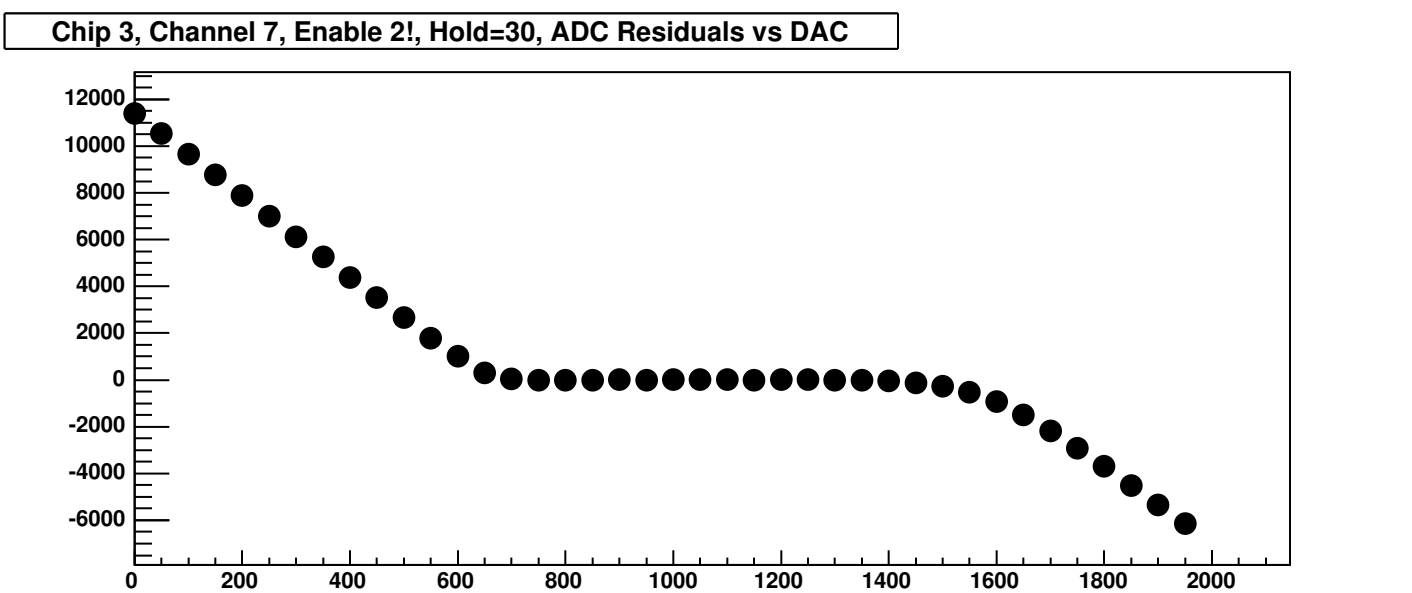


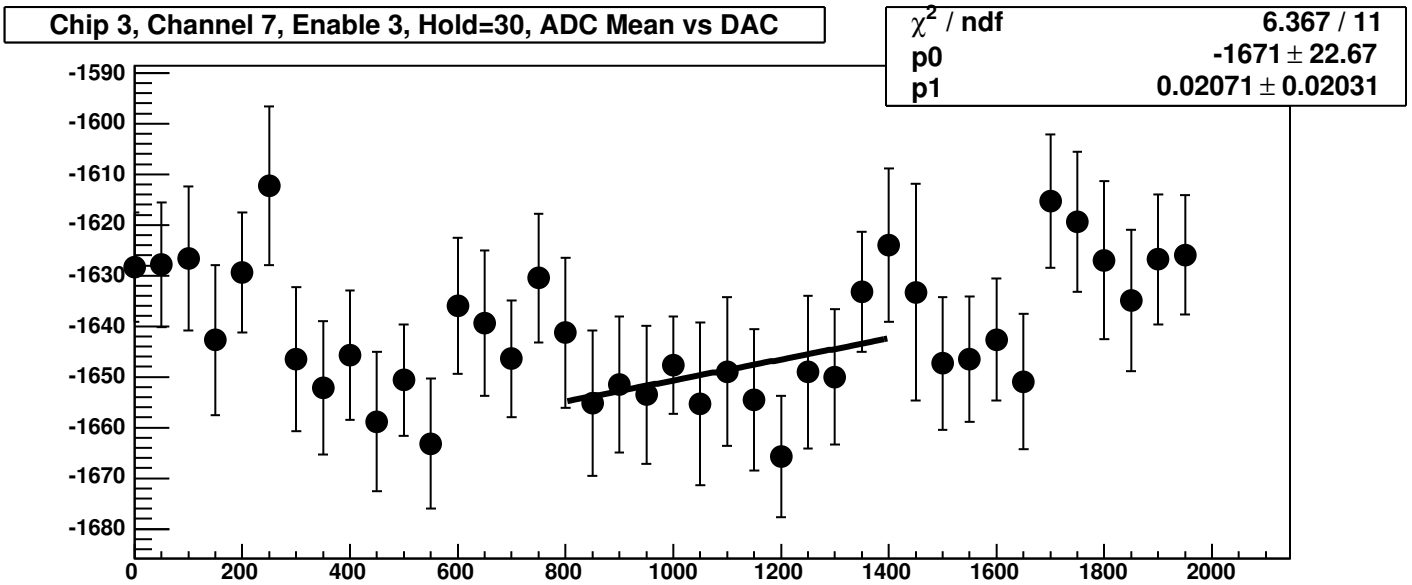
Chip 3, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC

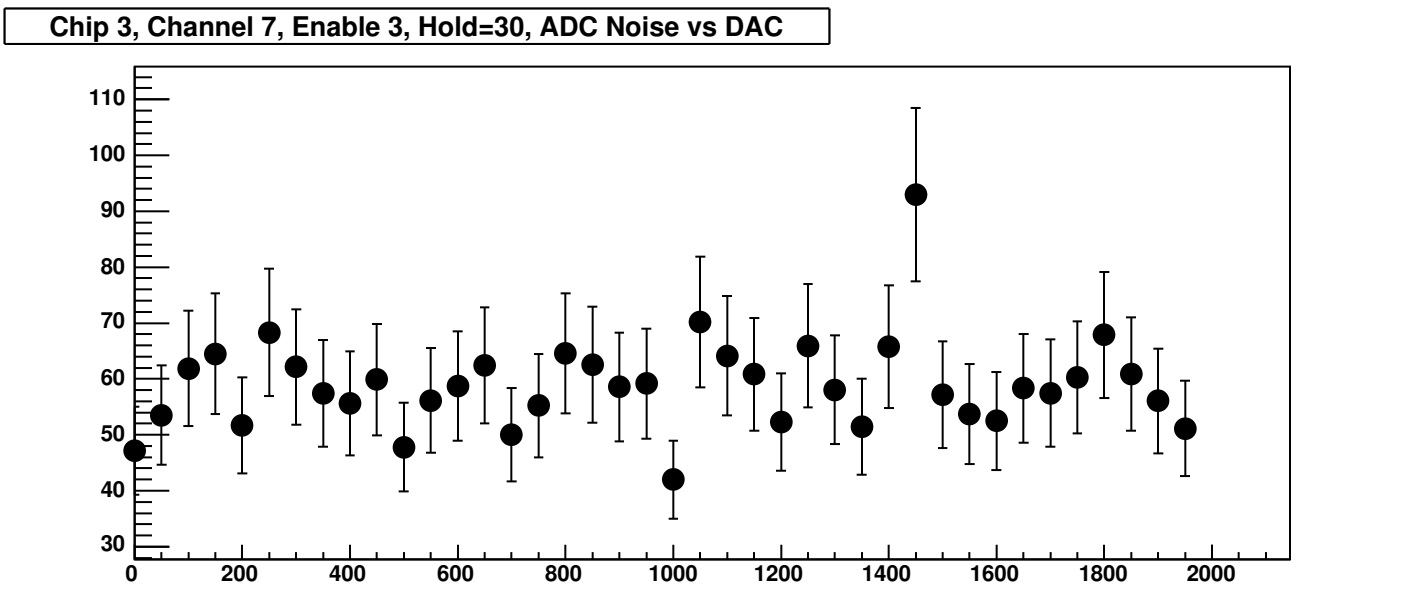


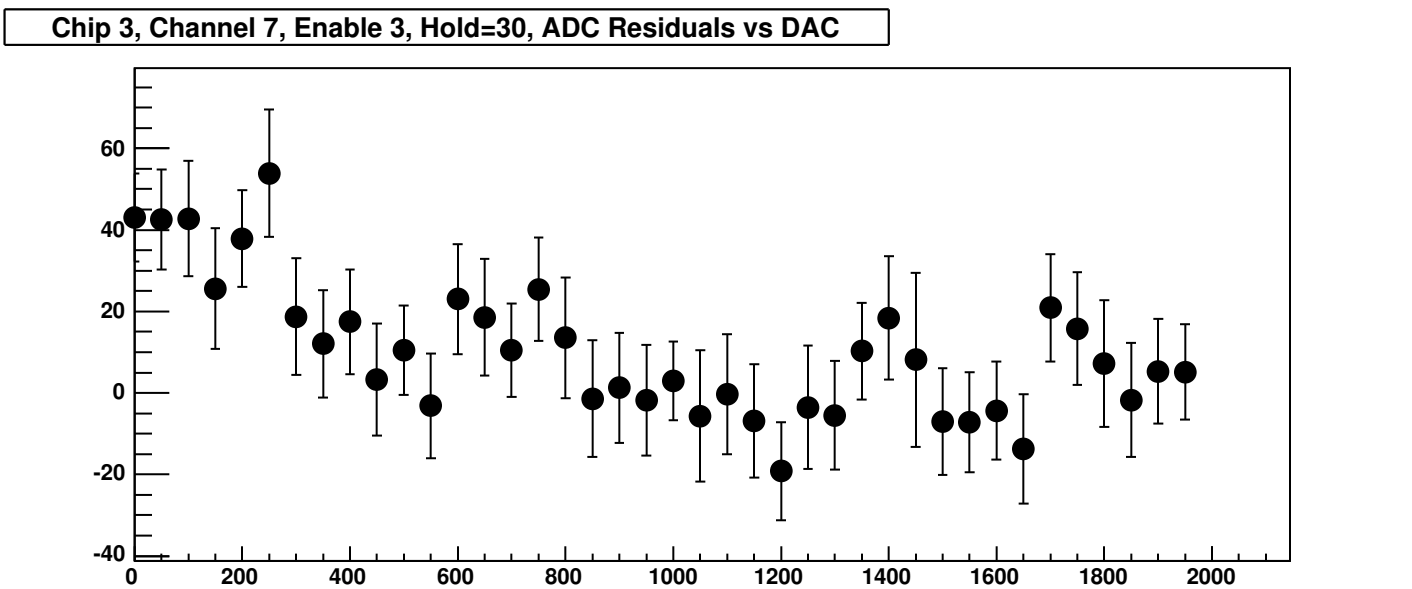

Chip 3, Channel 7, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 24.62 / 11
p0 -1599 ± 22.33
p1 17.89 ± 0.0202
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

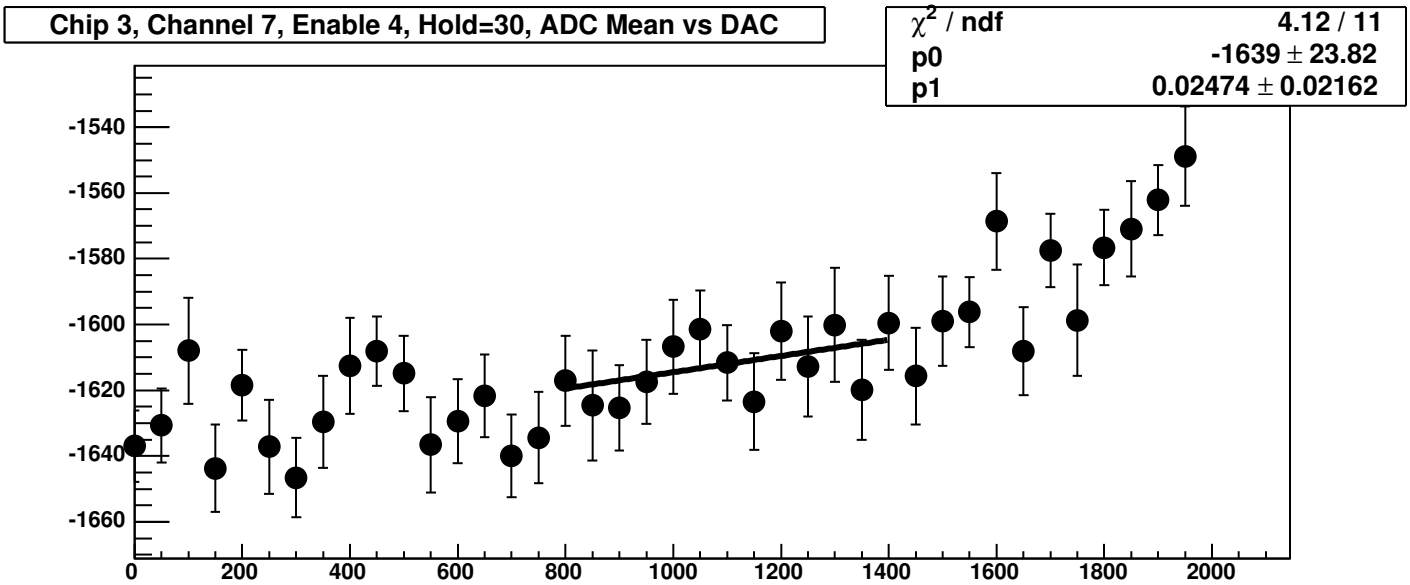

Chip 3, Channel 7, Enable 2!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

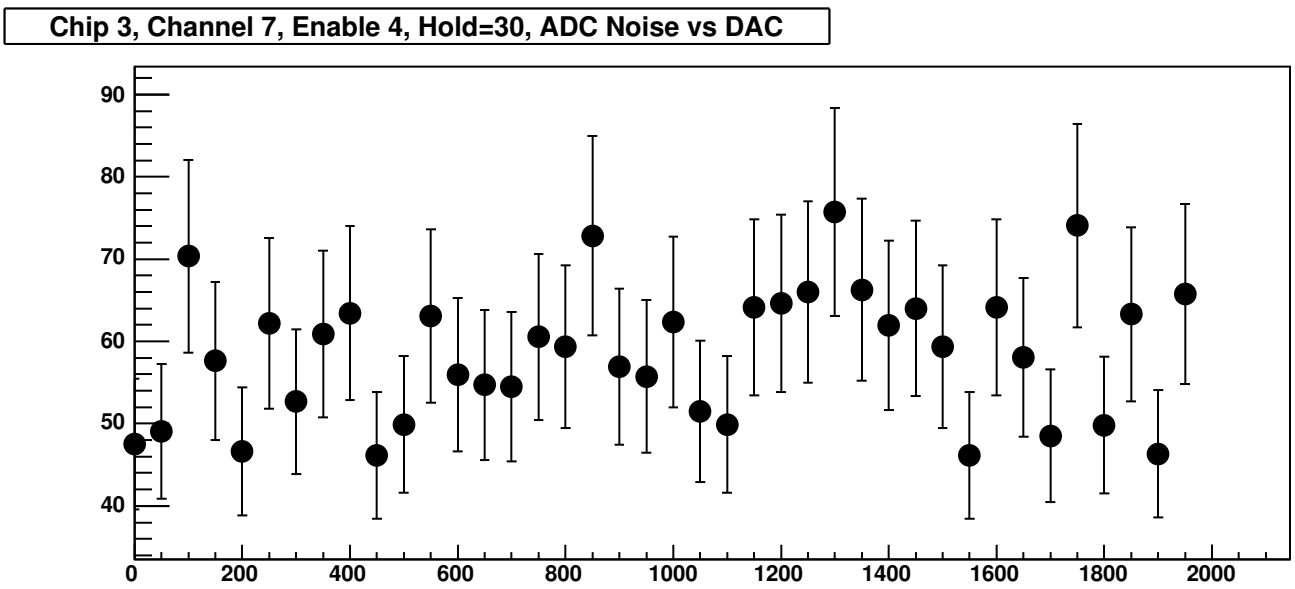

Chip 3, Channel 7, Enable 2!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

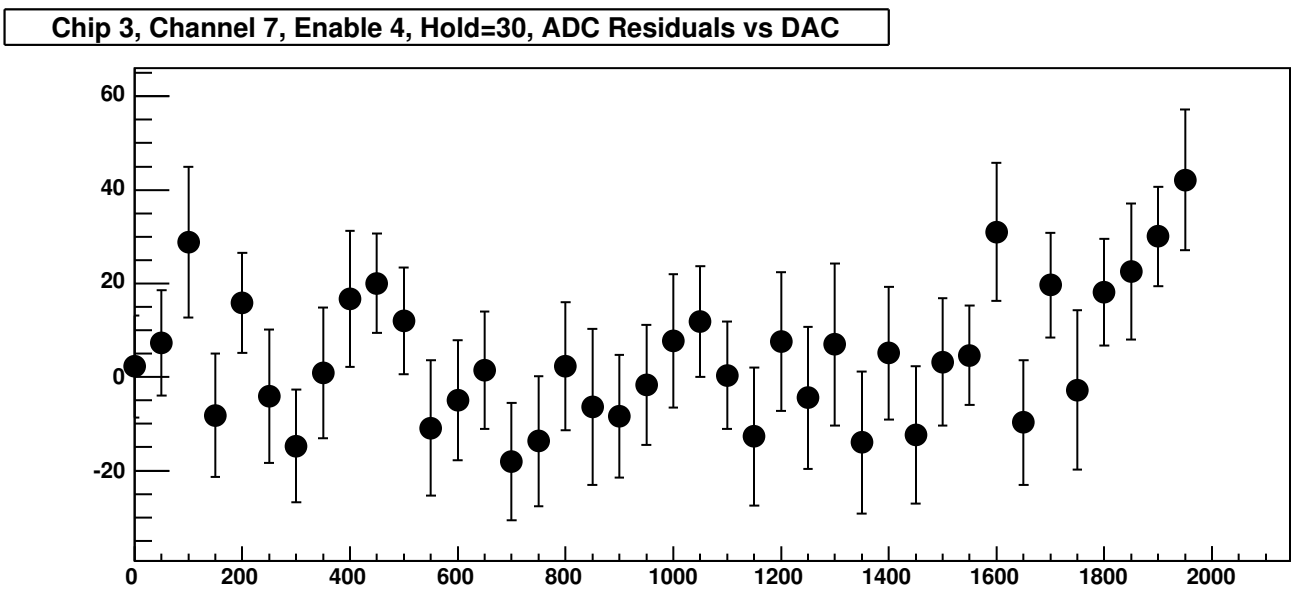

Chip 3, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 6.367 / 11
p0 -1671 ± 22.67
p1 0.02071 ± 0.02031
-1590
-1600
-1610
-1620
-1630
-1640
-1650
-1660
-1670
-1680
0
200
400
600
800
1000
1200
1400
1600
1800
2000

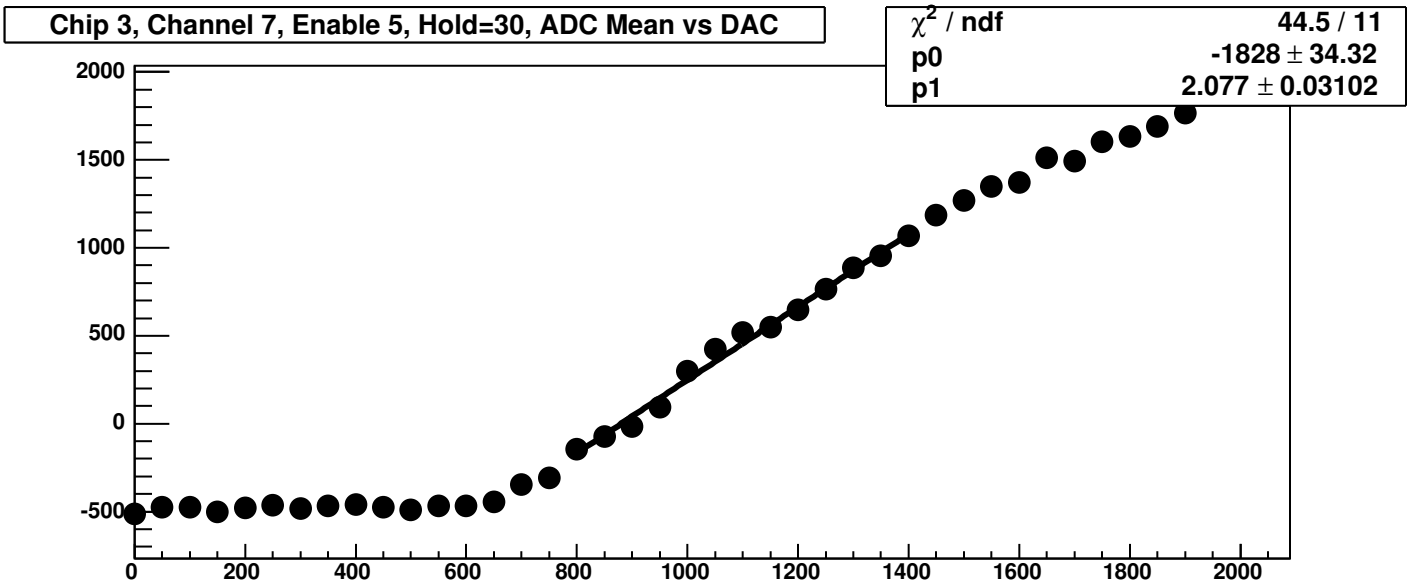

Chip 3, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

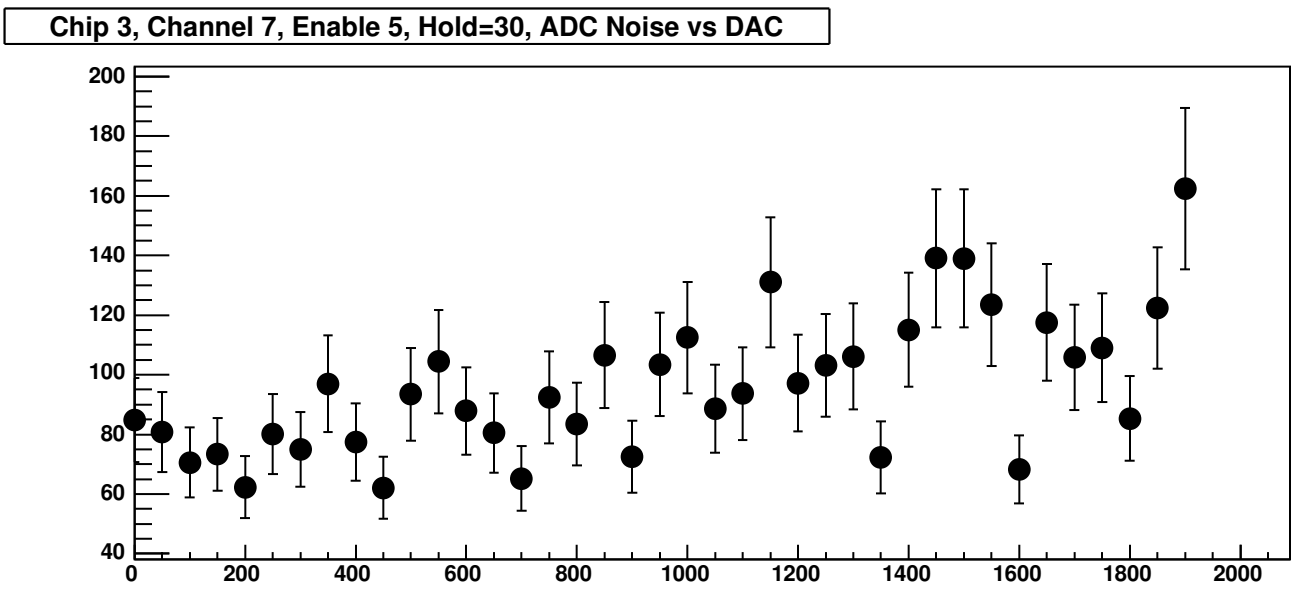

Chip 3, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

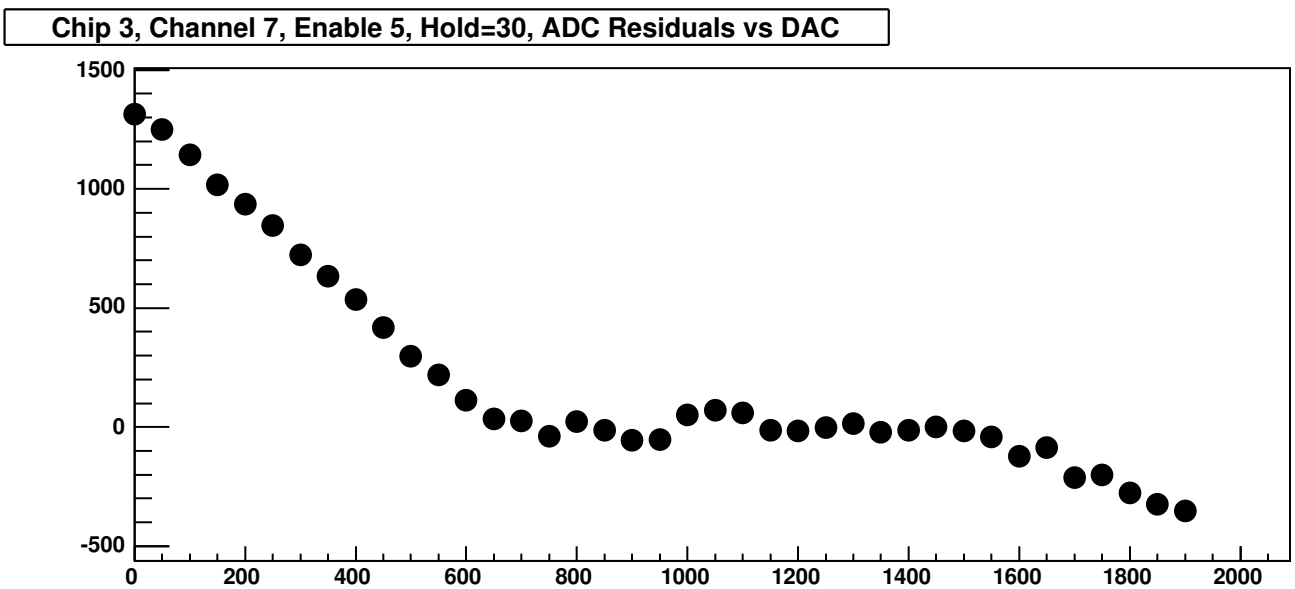

Chip 3, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4.12 / 11
p0 -1639 ± 23.82
p1 0.02474 ± 0.02162
-1540
-1560
-1580
-1600
-1620
-1640
-1660
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

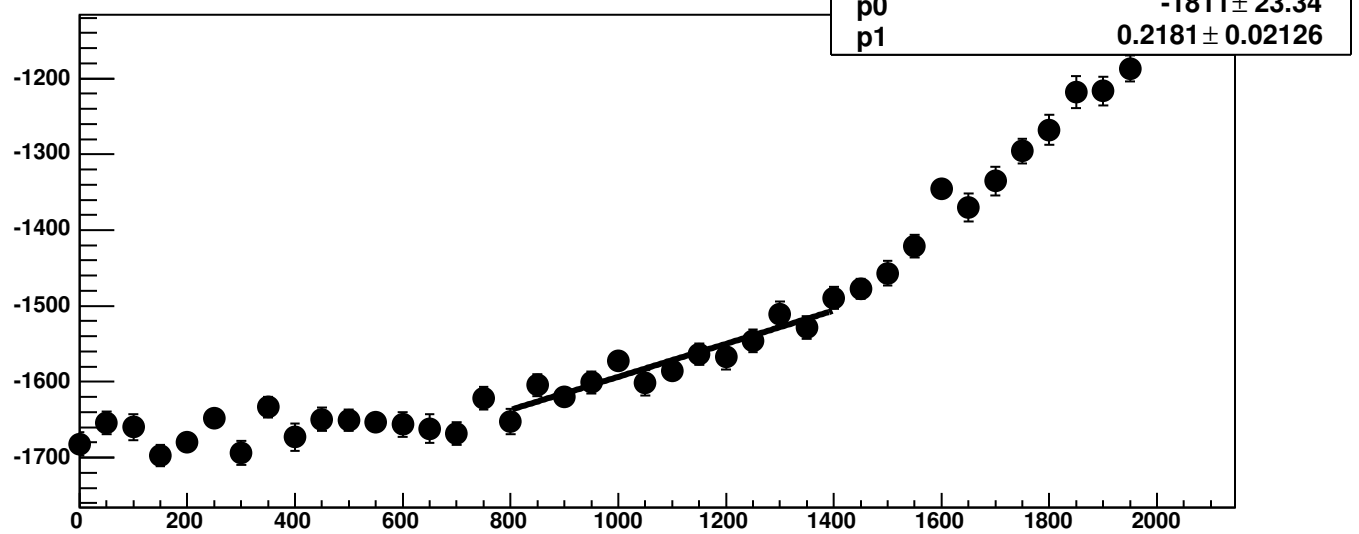

Chip 3, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 7, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 44.5 / 11
p0 -1828 ± 34.32
p1 2.077 ± 0.03102
2000
1500
1000
500
0
-500
0 200 400 600 800 1000 1200 1400 1600 1800 2000

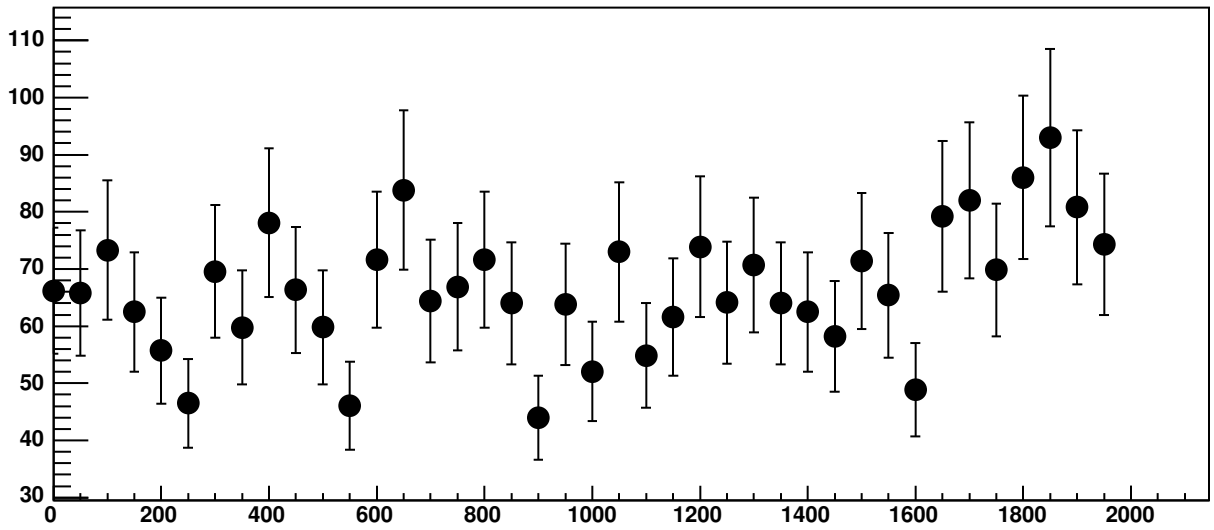

Chip 3, Channel 7, Enable 5, Hold=30, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
0 200 400 600 800 1000 1200 1400 1600 1800 2000


Chip 3, Channel 7, Enable 5, Hold=30, ADC Residuals vs DAC
1500
1000
500
0
-500
0 200 400 600 800 1000 1200 1400 1600 1800 2000

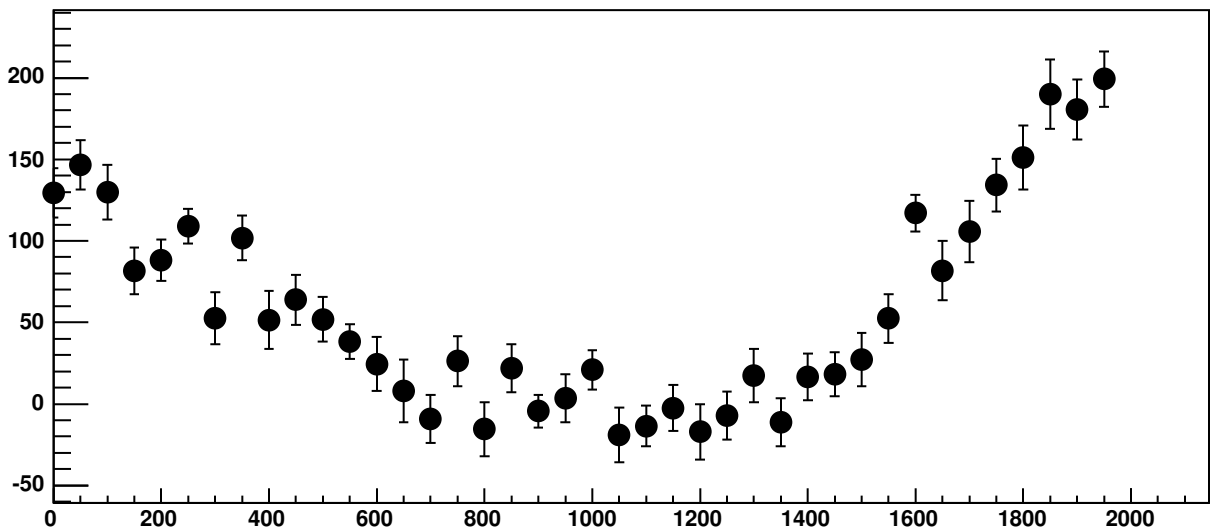
Chip 3, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC



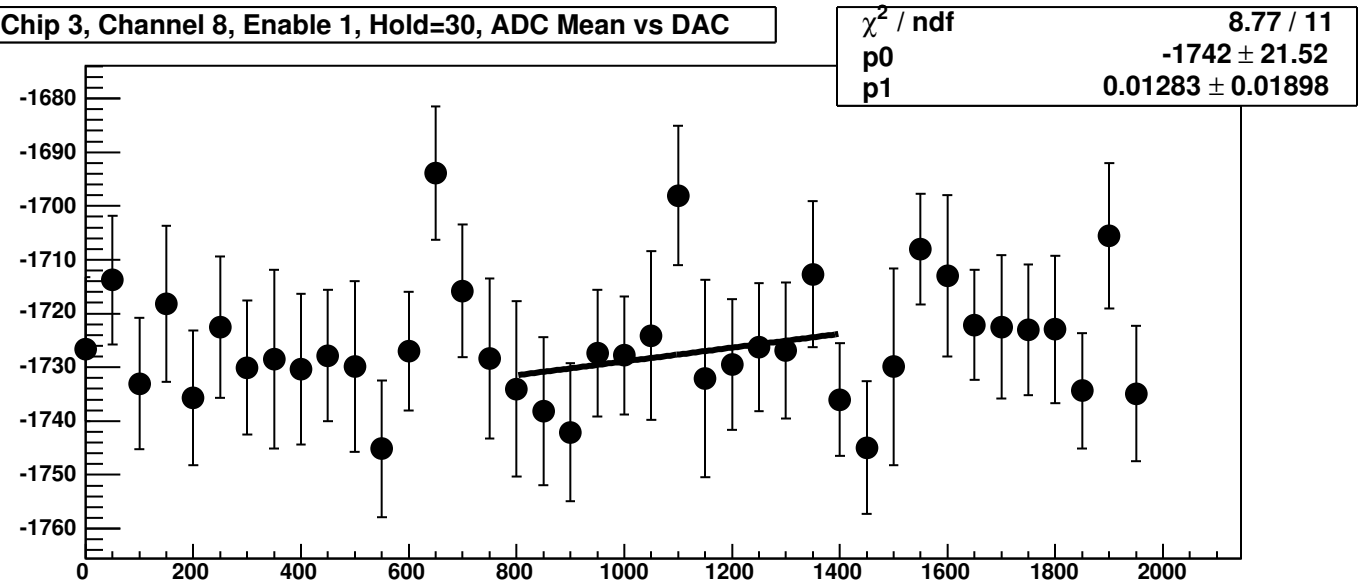
Chip 3, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC



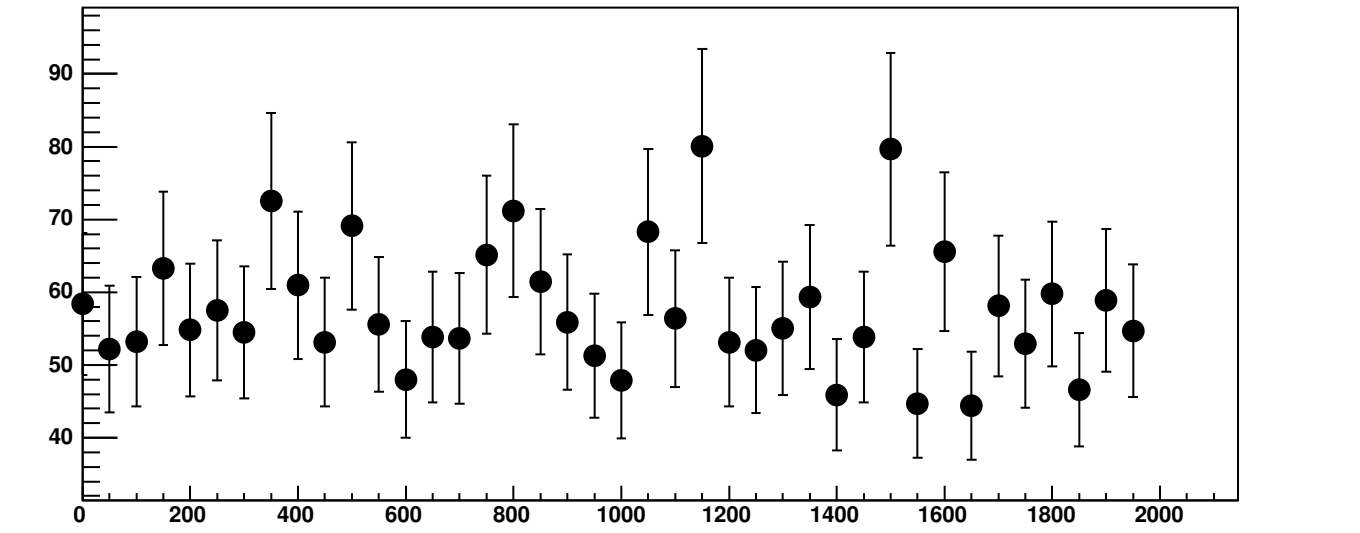
Chip 3, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC



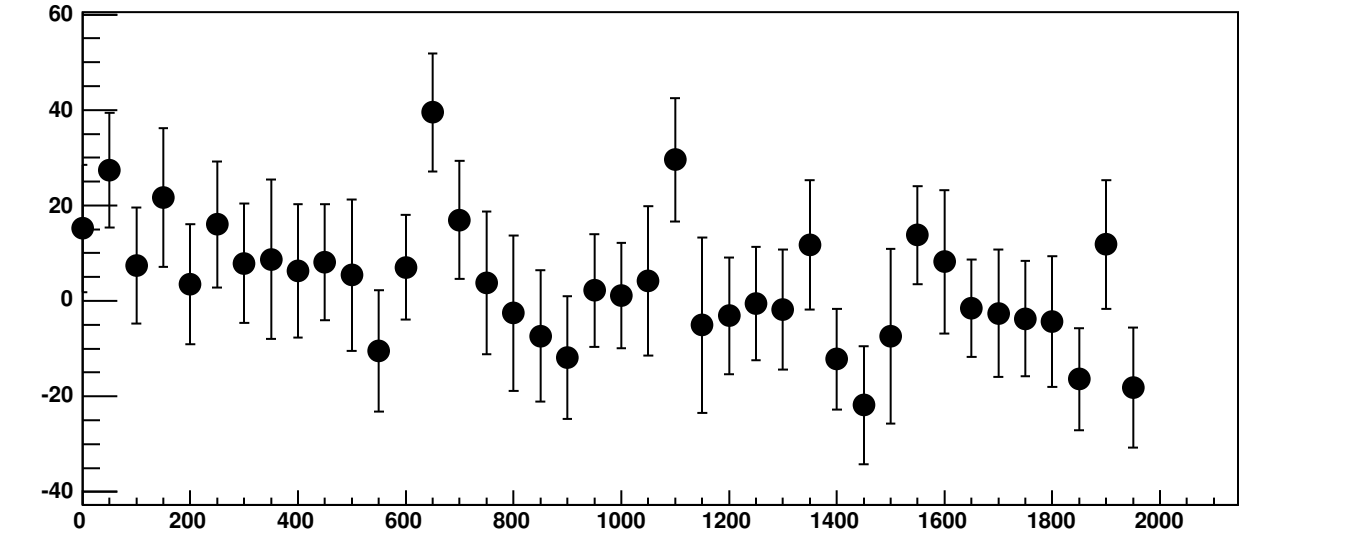
Chip 3, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC

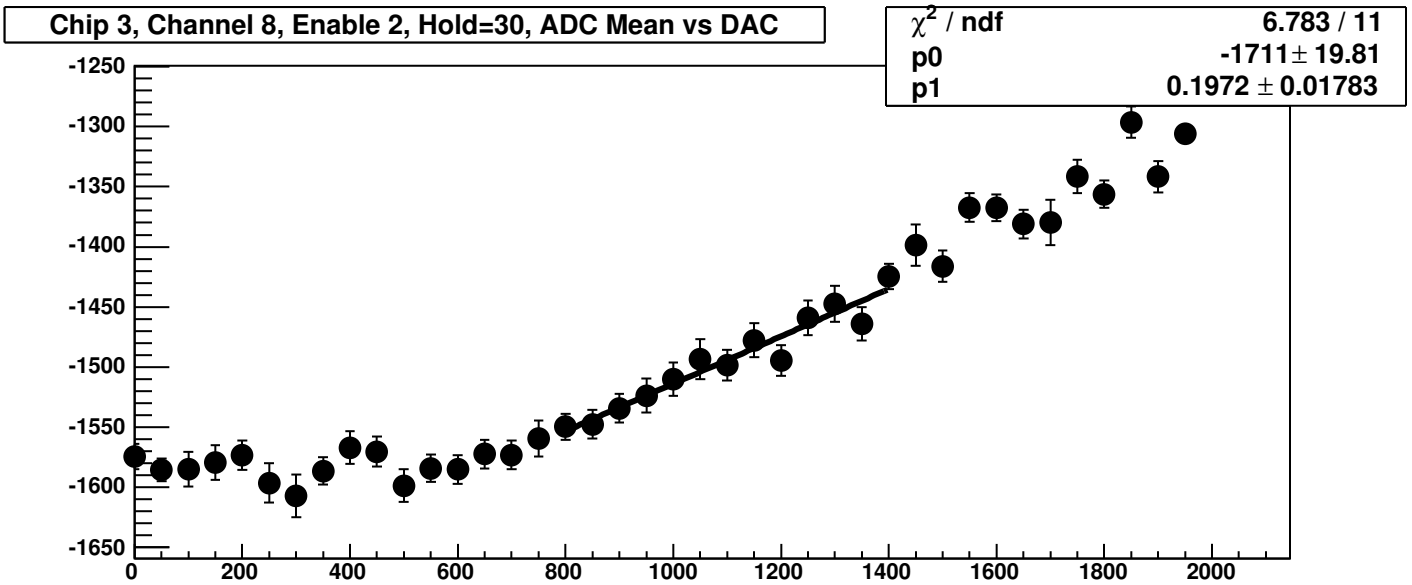


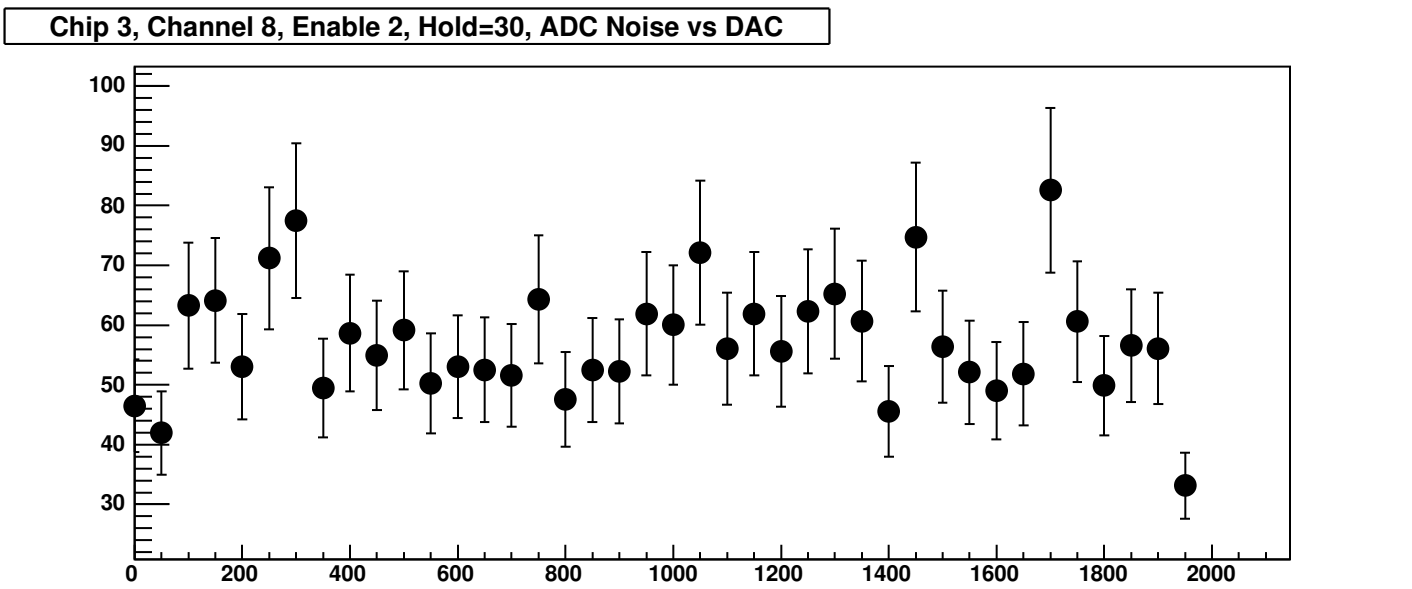
Chip 3, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC

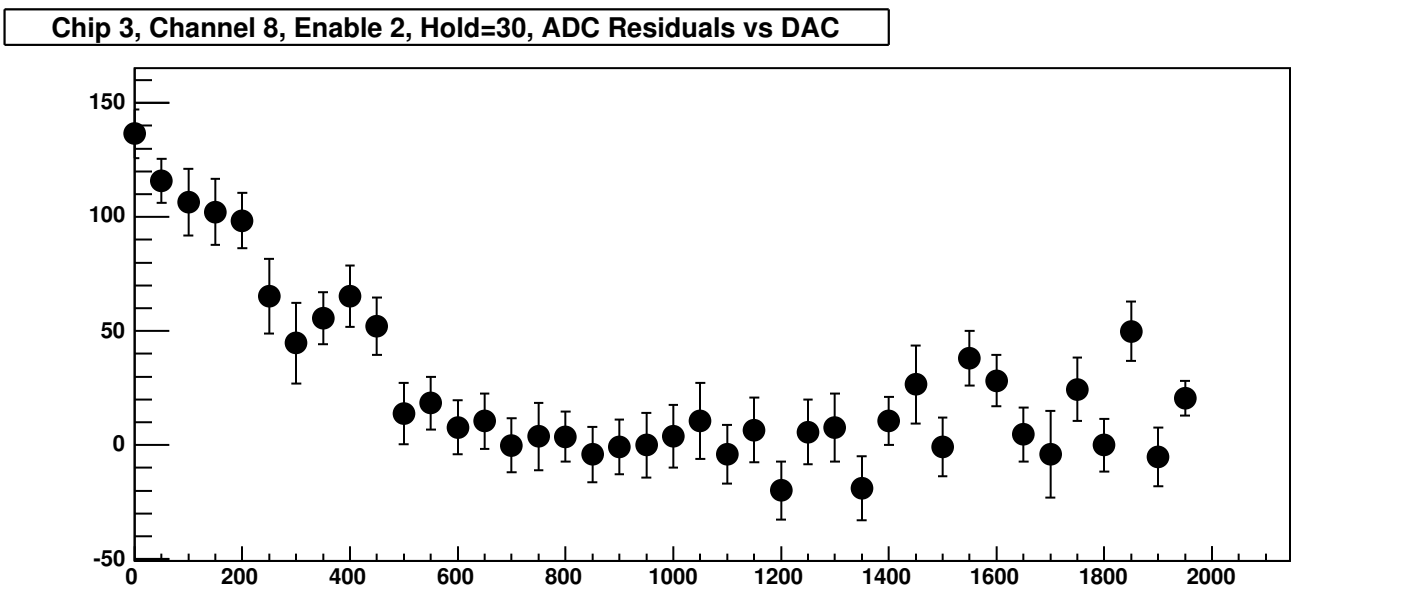


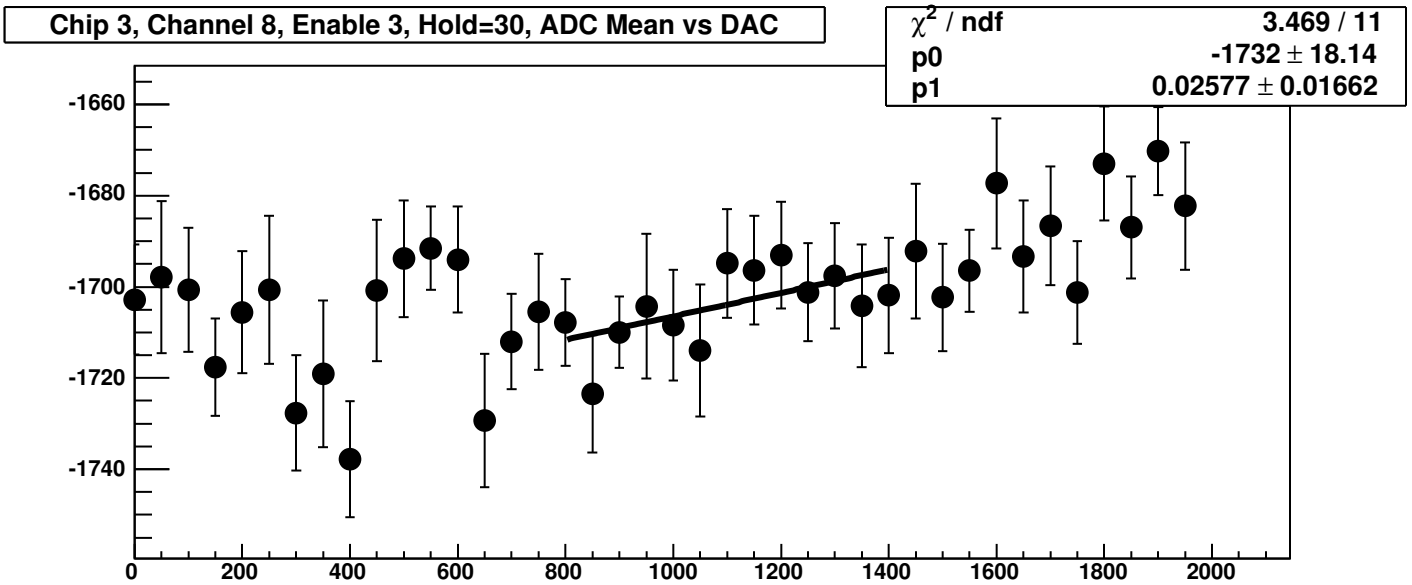
Chip 3, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC

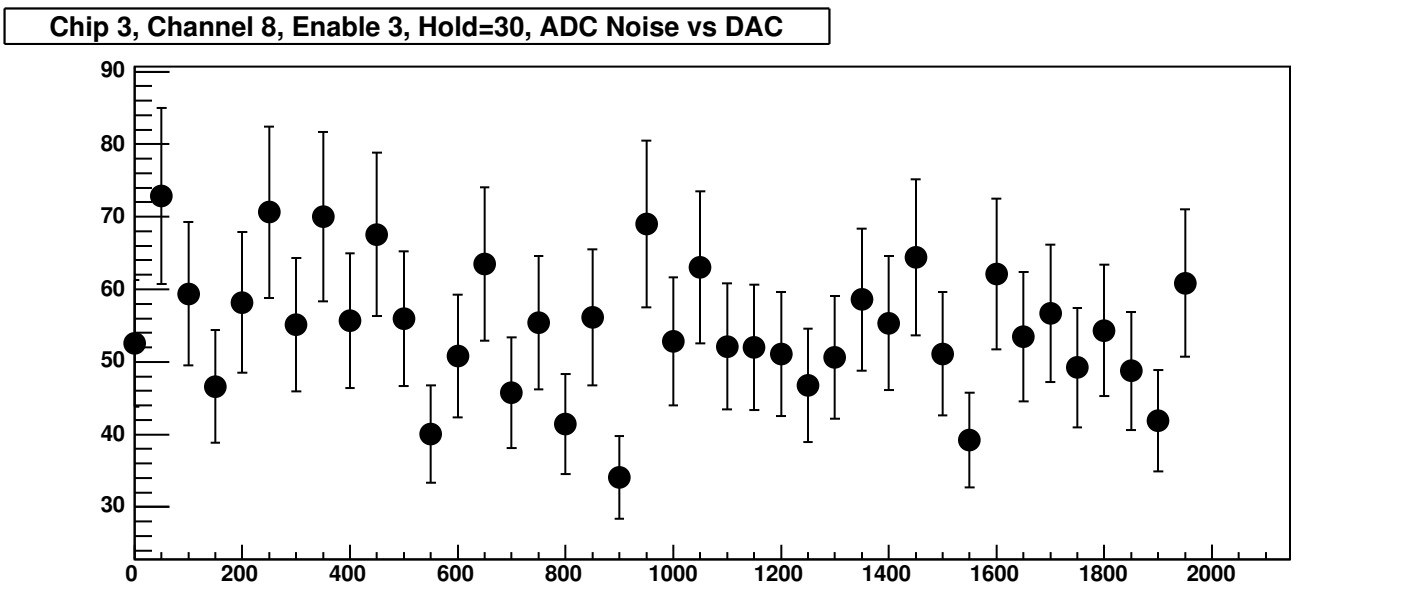


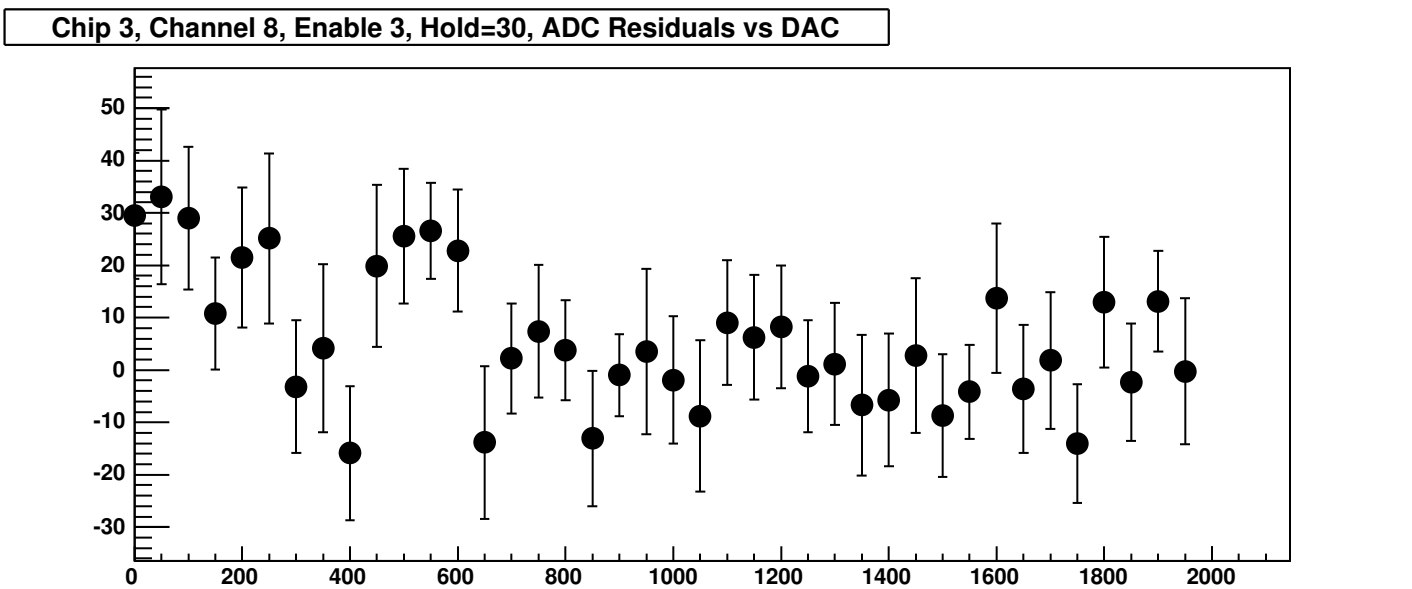

Chip 3, Channel 8, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 6.783 / 11
p0 -1711 ± 19.81
p1 0.1972 ± 0.01783
-1250
-1300
-1350
-1400
-1450
-1500
-1550
-1600
-1650
0
200
400
600
800
1000
1200
1400
1600
1800
2000

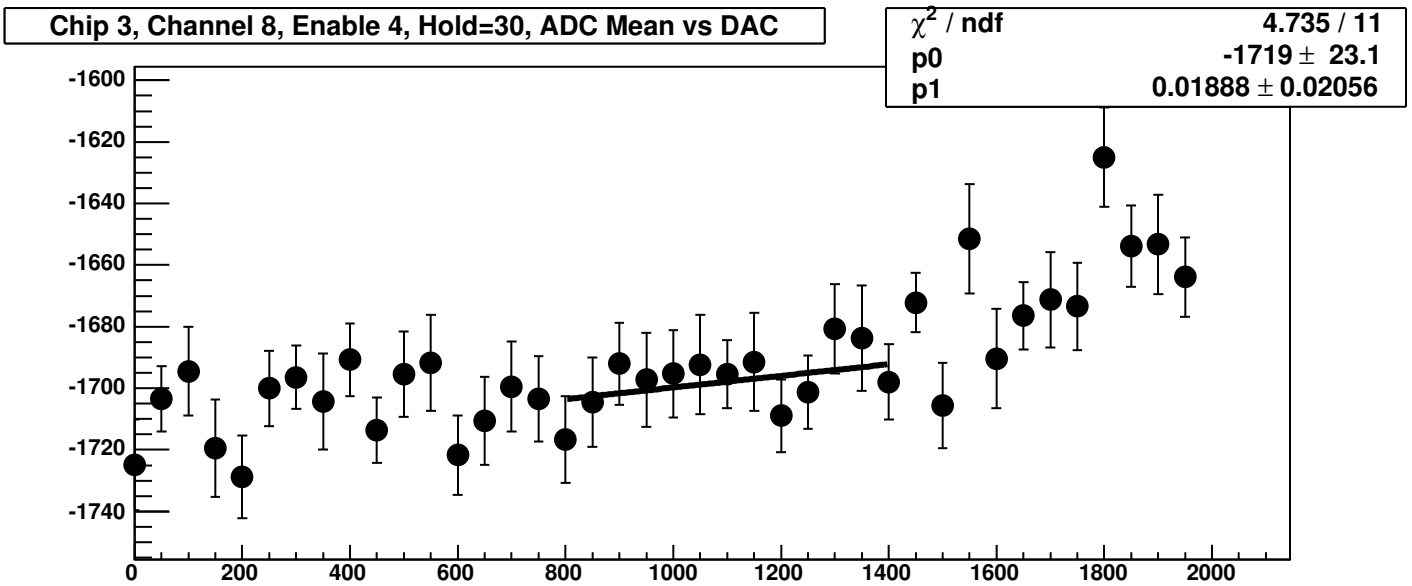

Chip 3, Channel 8, Enable 2, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

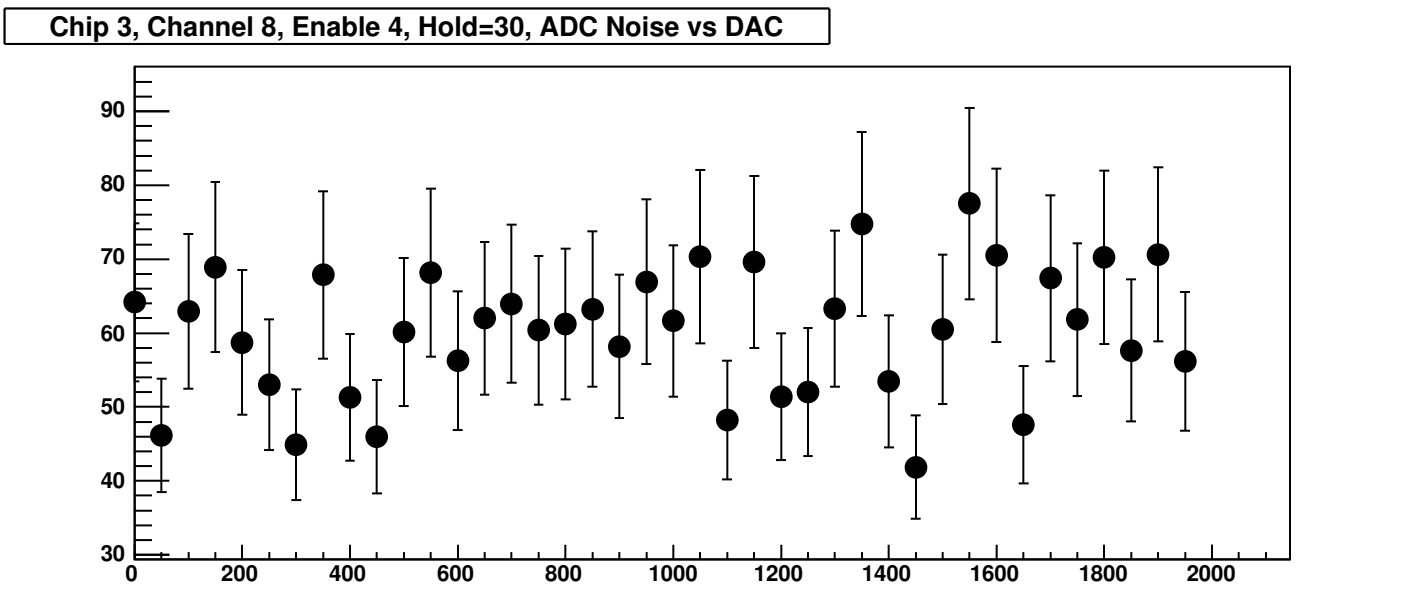

Chip 3, Channel 8, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

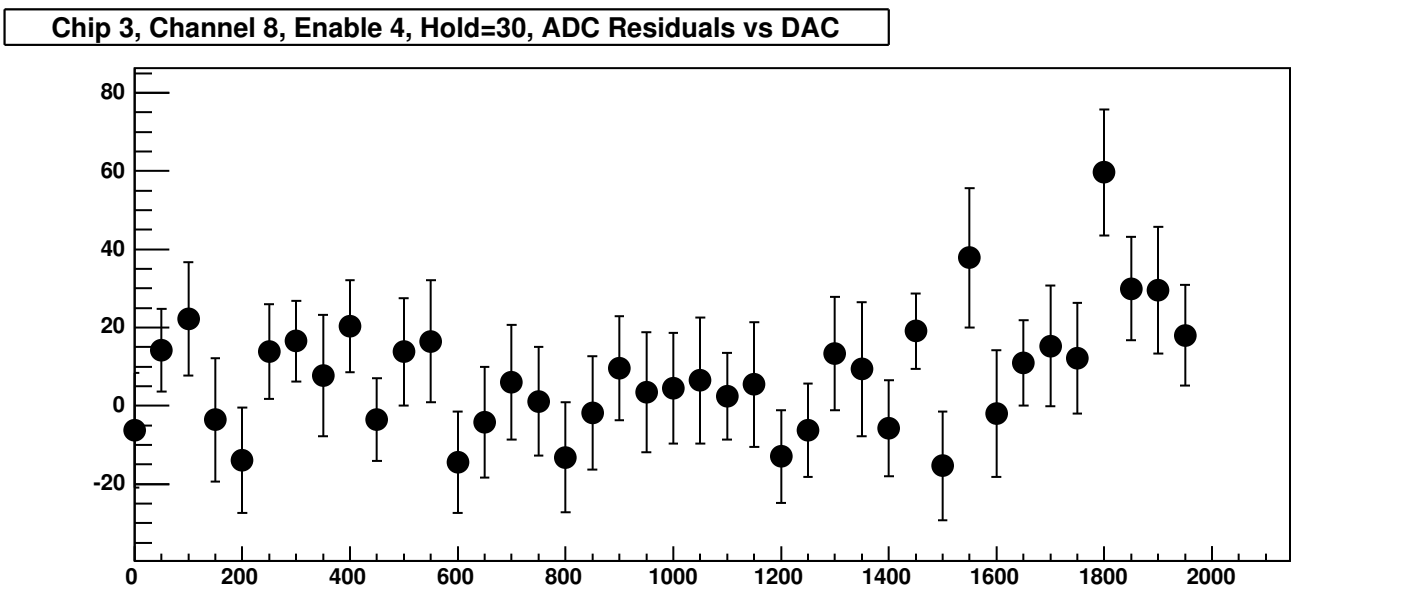

Chip 3, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3.469 / 11
p0 -1732 ± 18.14
p1 0.02577 ± 0.01662
-1660
-1680
-1700
-1720
-1740
0
200
400
600
800
1000
1200
1400
1600
1800
2000

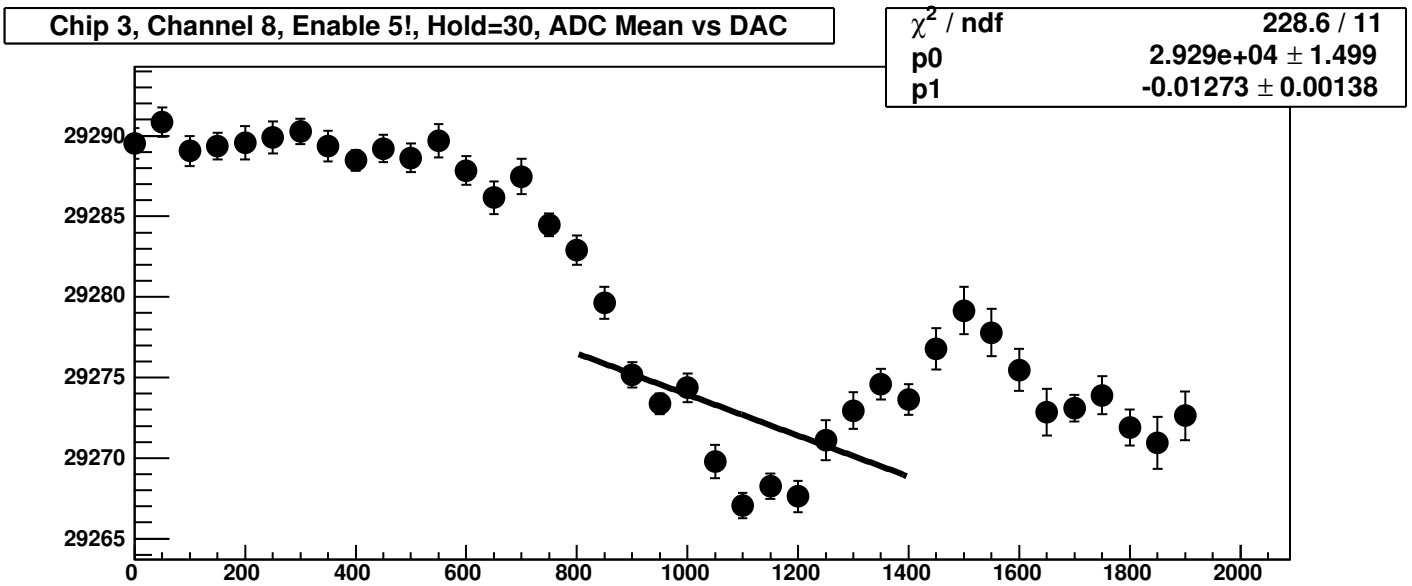

Chip 3, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

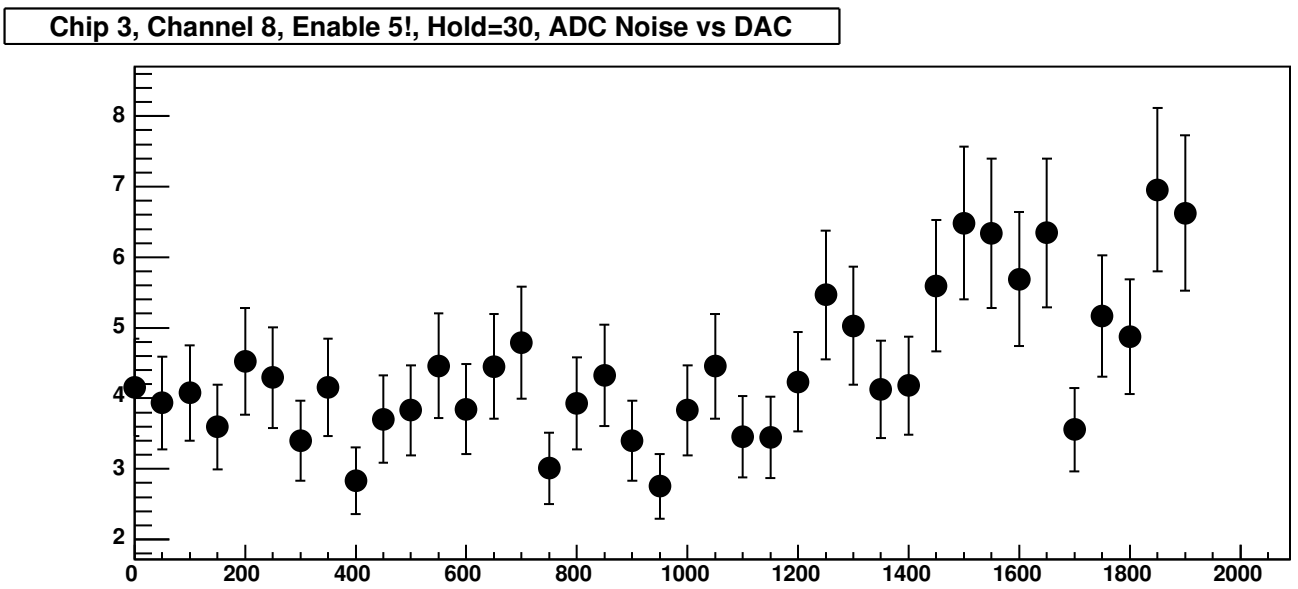

Chip 3, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
50
40
30
20
10
0
-10
-20
-30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

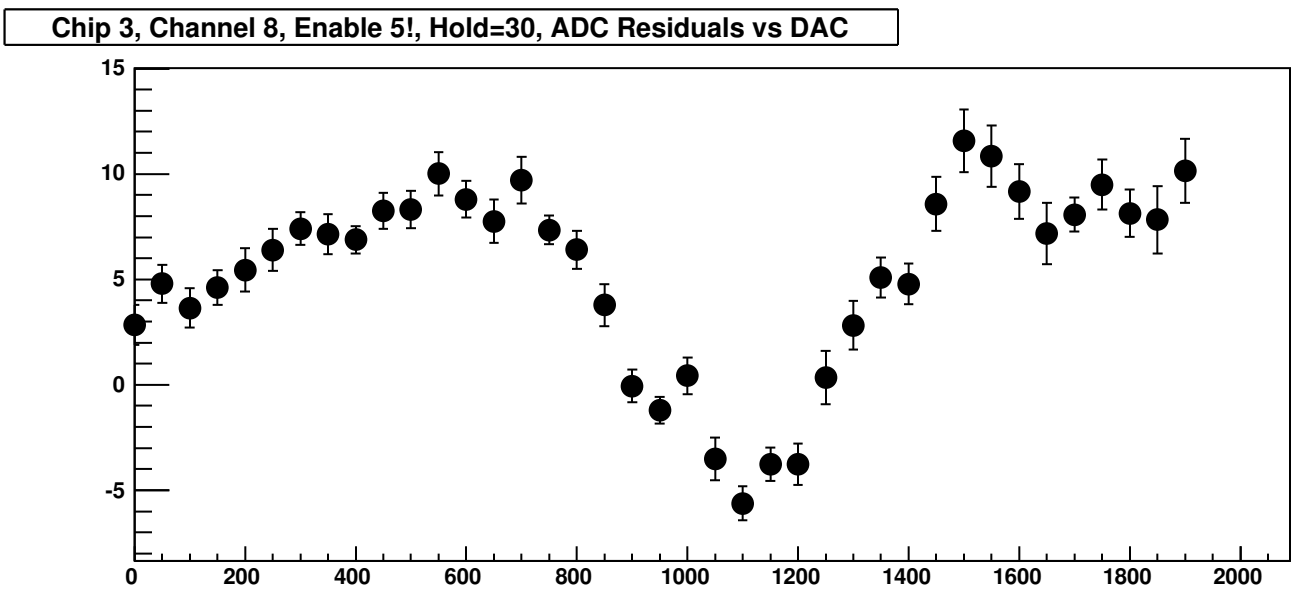

Chip 3, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4.735 / 11
p0 -1719 ± 23.1
p1 0.01888 ± 0.02056
-1600
-1620
-1640
-1660
-1680
-1700
-1720
-1740
0
200
400
600
800
1000
1200
1400
1600
1800
2000

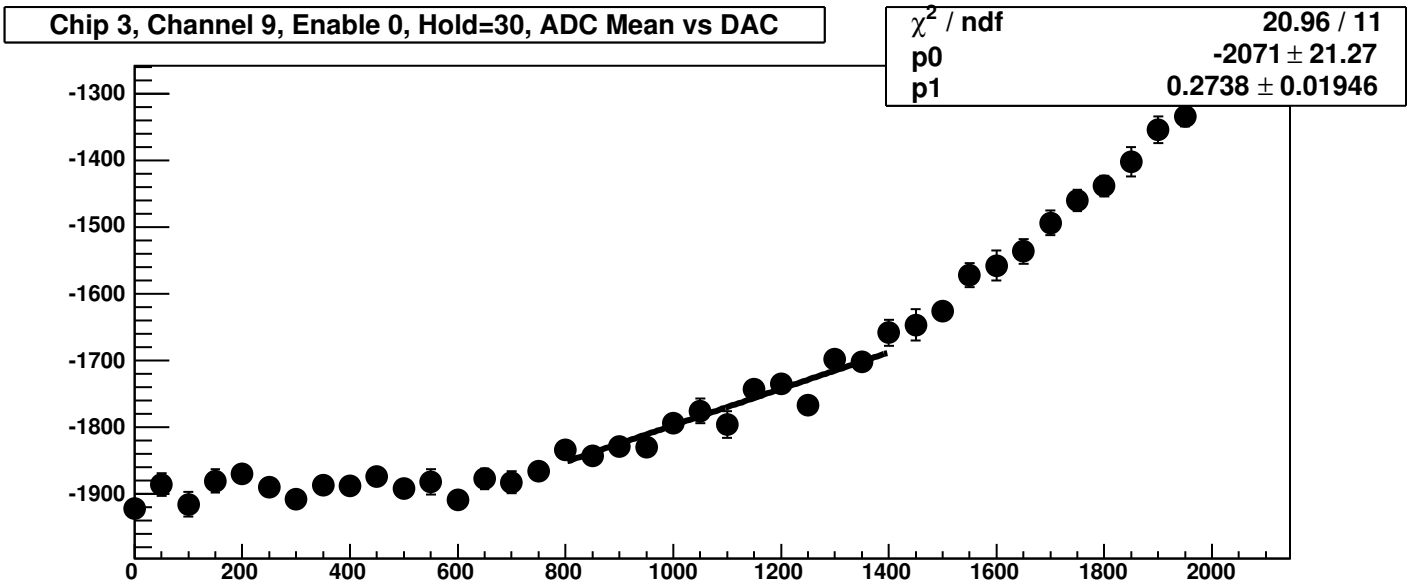

Chip 3, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

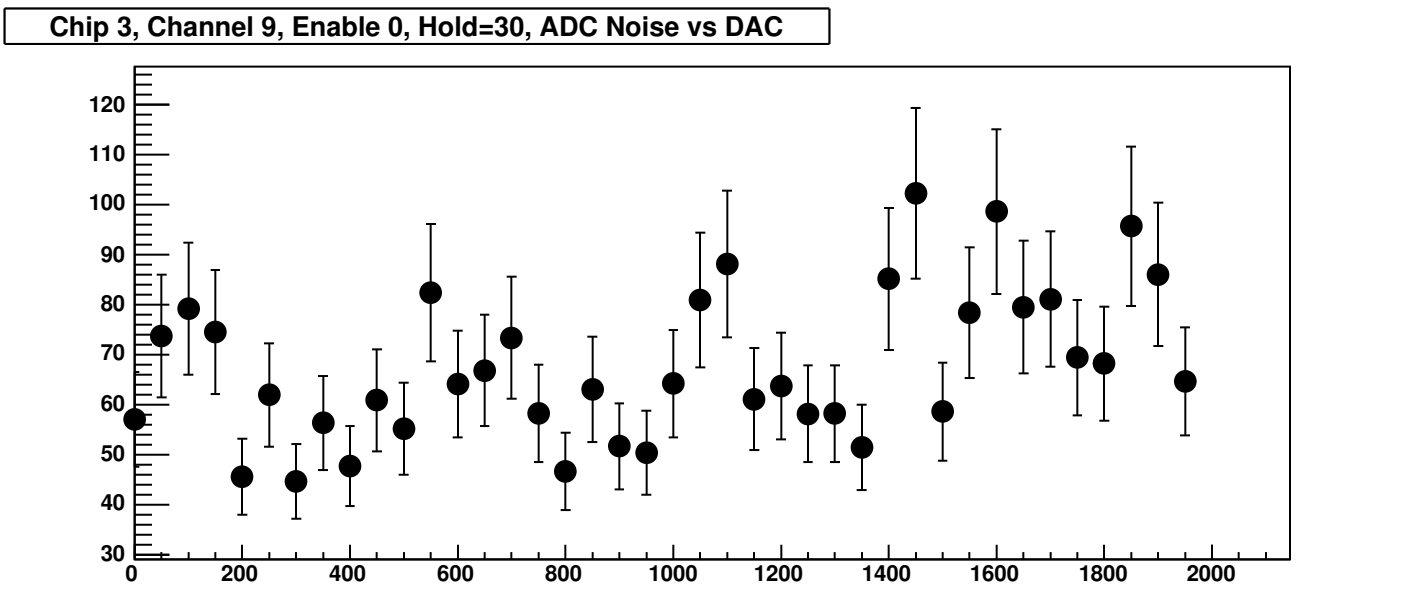

Chip 3, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

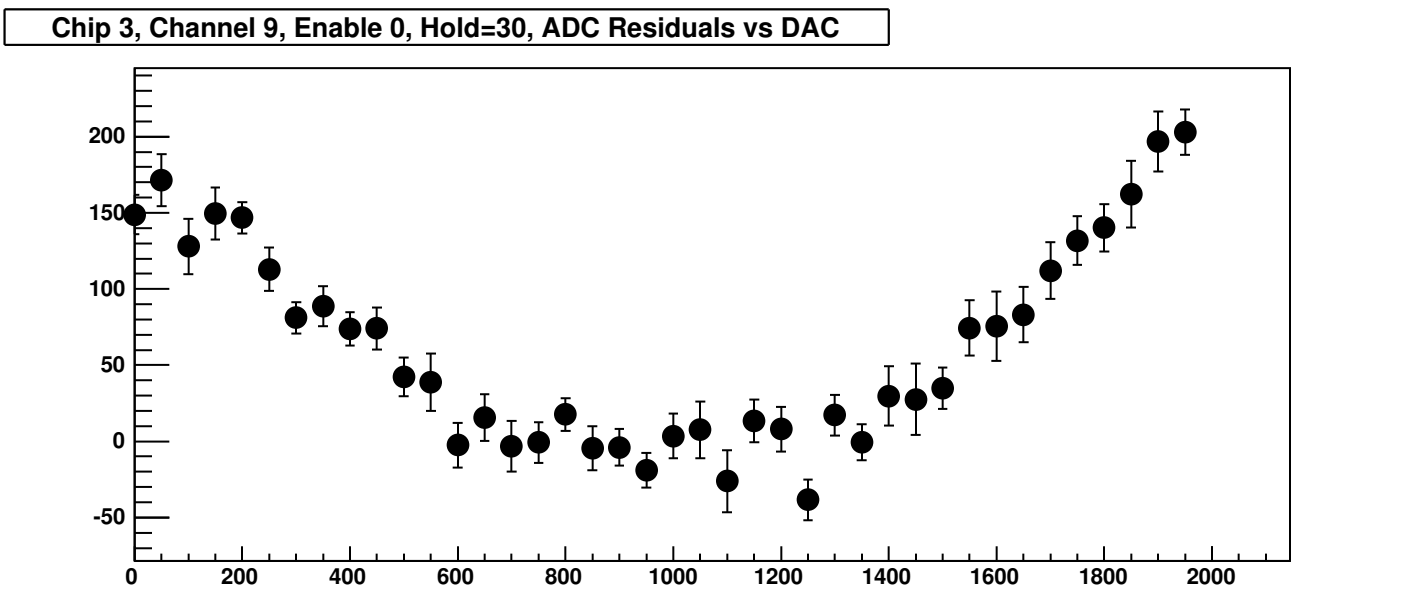

Chip 3, Channel 8, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 228.6 / 11
p0 2.929e+04 ± 1.499
p1 -0.01273 ± 0.00138
29290
29285
29280
29275
29270
29265
0
200
400
600
800
1000
1200
1400
1600
1800
2000

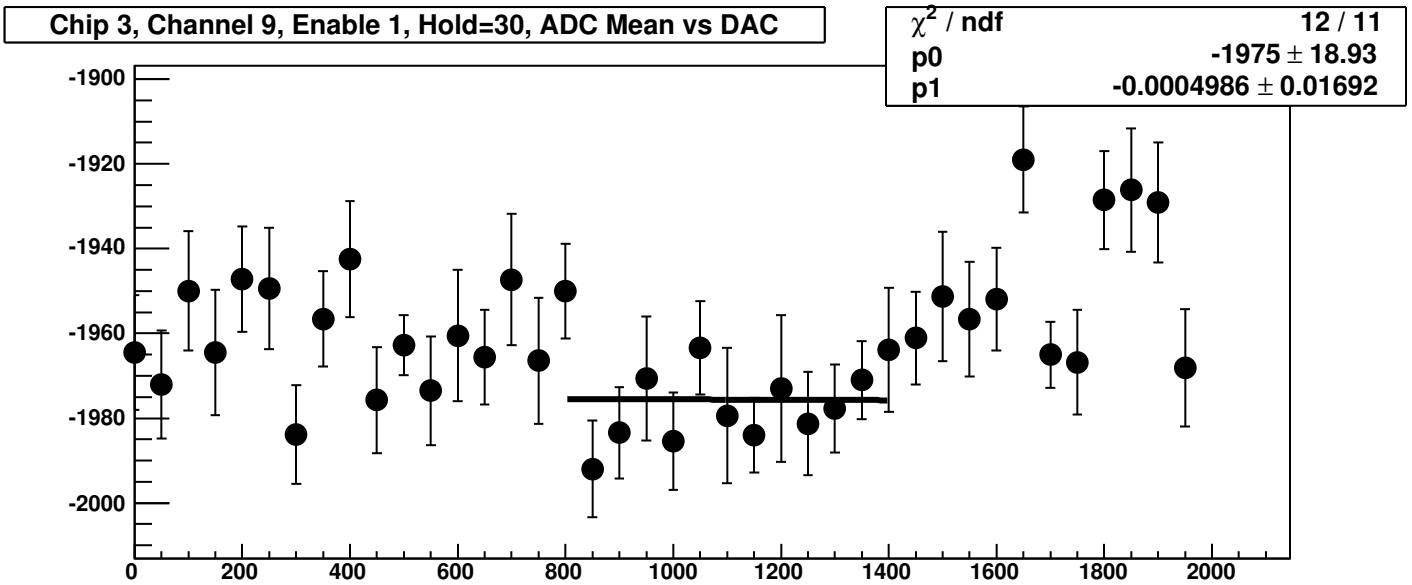

Chip 3, Channel 8, Enable 5!, Hold=30, ADC Noise vs DAC
8
7
6
5
4
3
2
0
200
400
600
800
1000
1200
1400
1600
1800
2000

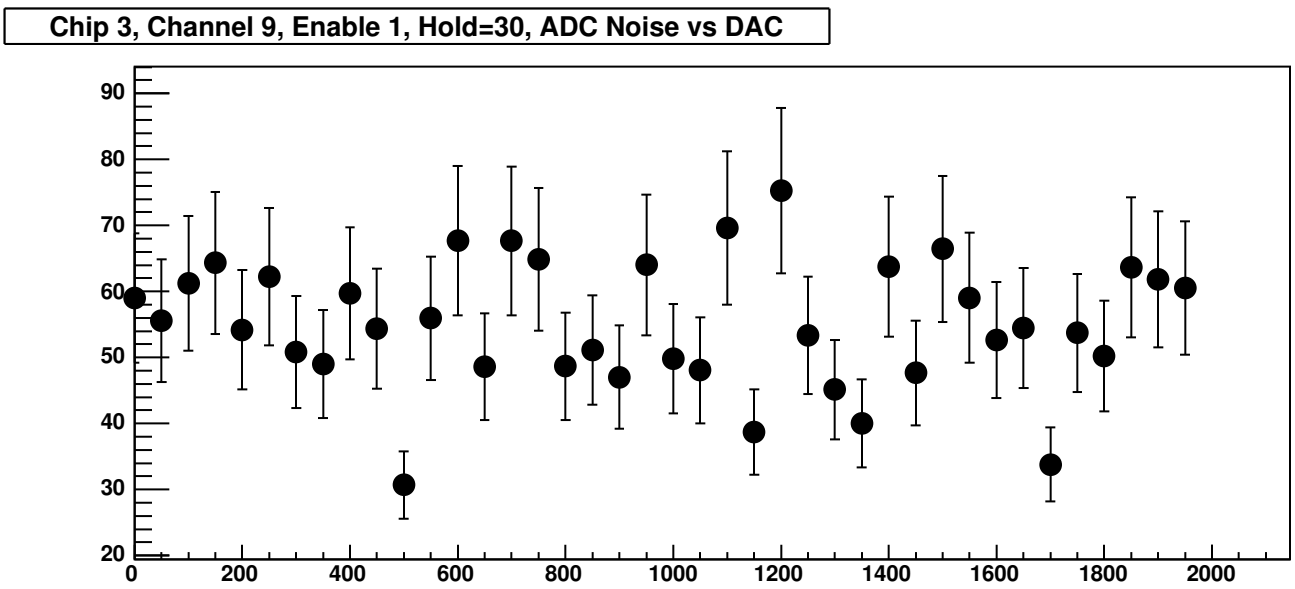

Chip 3, Channel 8, Enable 5!, Hold=30, ADC Residuals vs DAC
15
10
5
0
-5
0
200
400
600
800
1000
1200
1400
1600
1800
2000

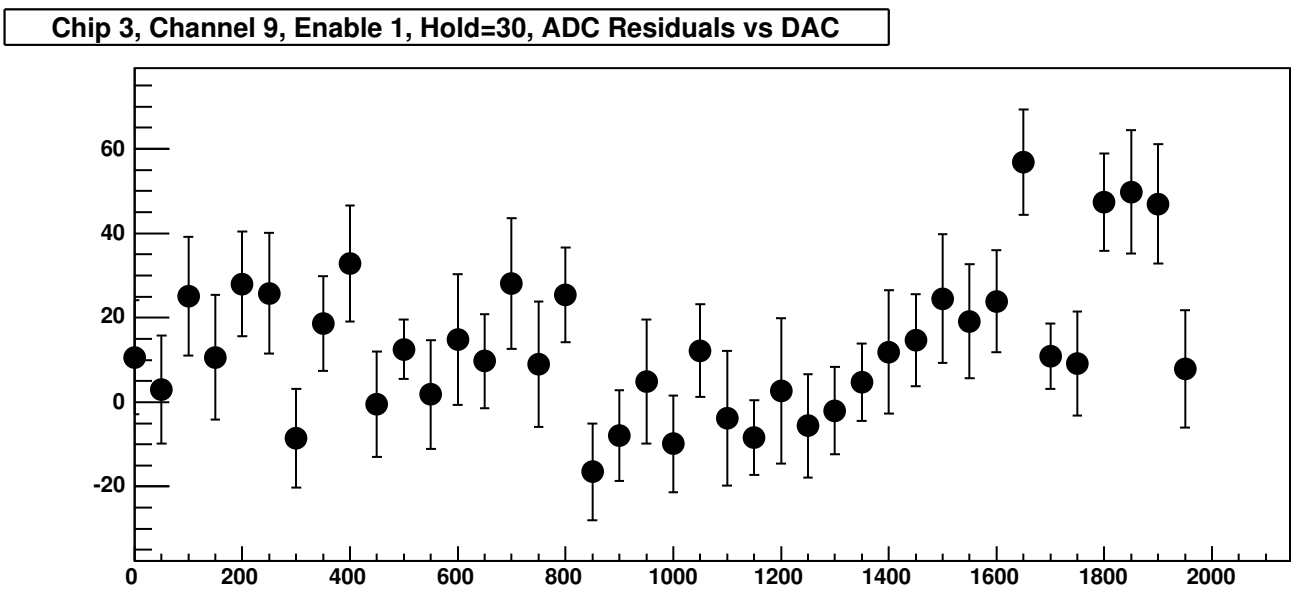

Chip 3, Channel 9, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 20.96 / 11
p0 -2071 ± 21.27
p1 0.2738 ± 0.01946
-1300
-1400
-1500
-1600
-1700
-1800
-1900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

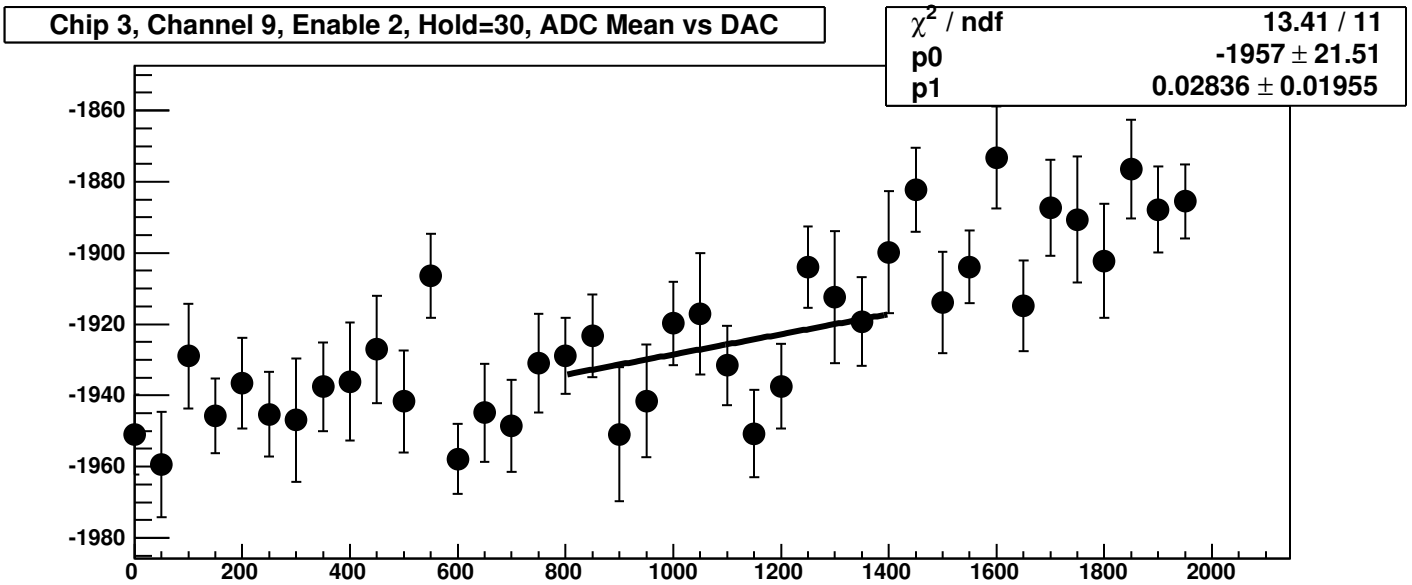

Chip 3, Channel 9, Enable 0, Hold=30, ADC Noise vs DAC
120
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

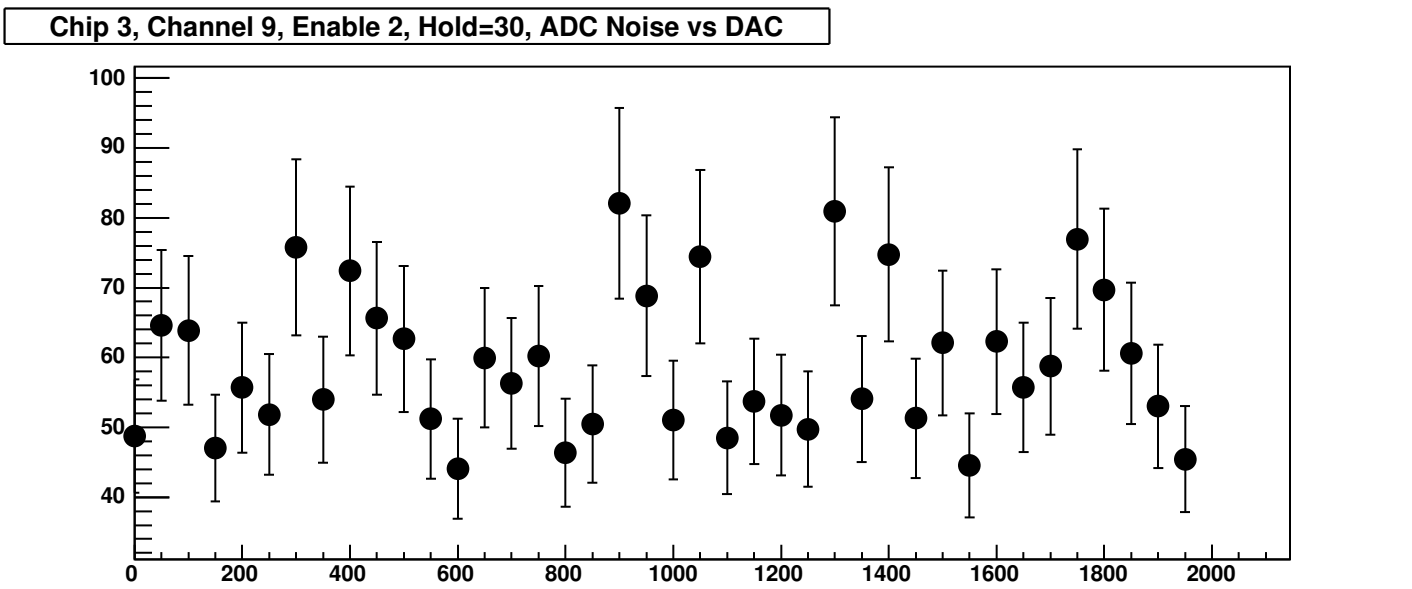

Chip 3, Channel 9, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

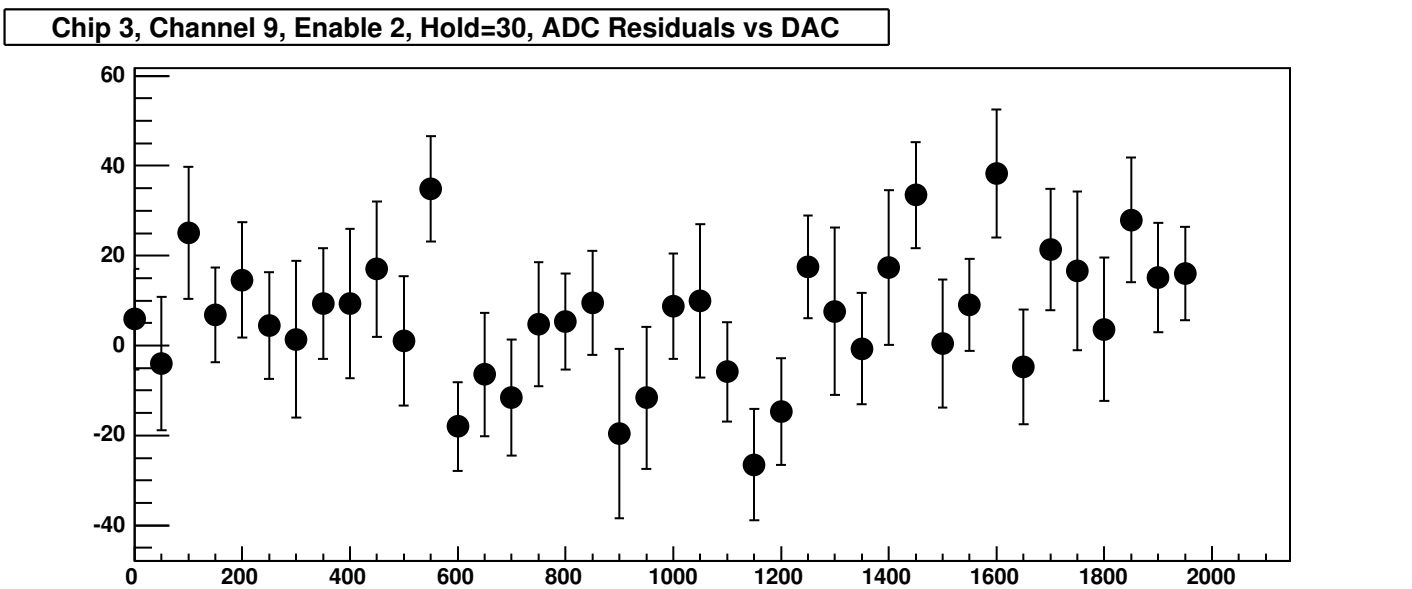

Chip 3, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 12 / 11
p0 -1975 ± 18.93
p1 -0.0004986 ± 0.01692
-1900
-1920
-1940
-1960
-1980
-2000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

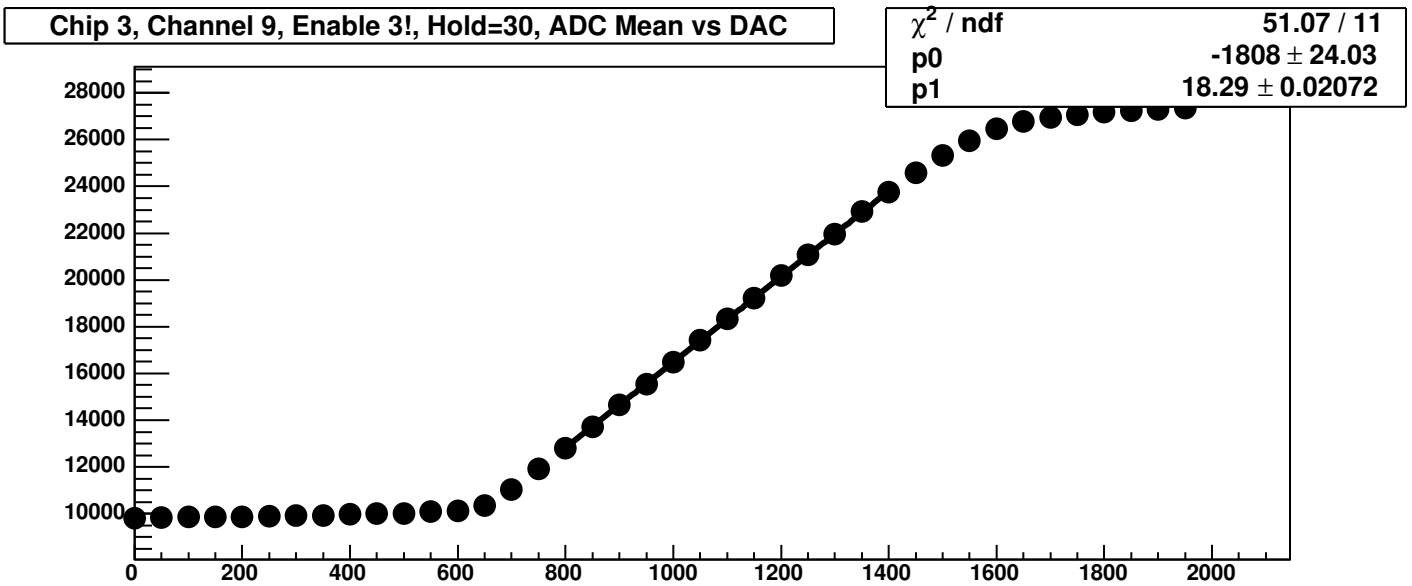

Chip 3, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

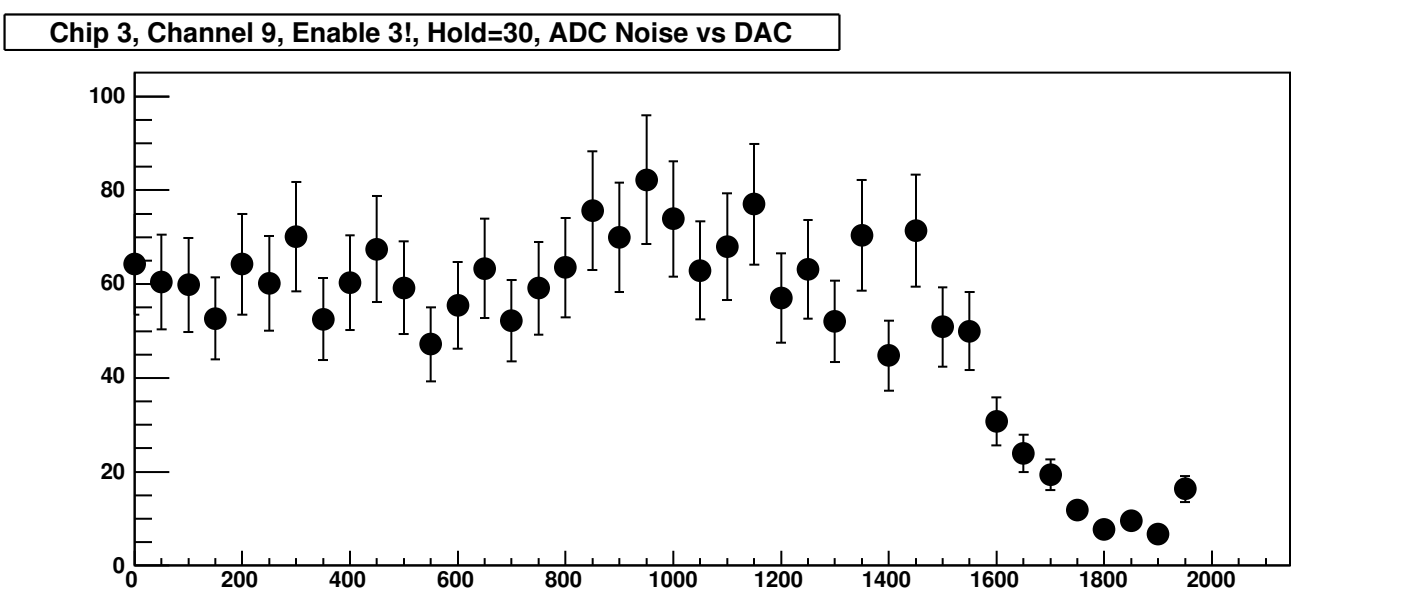

Chip 3, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

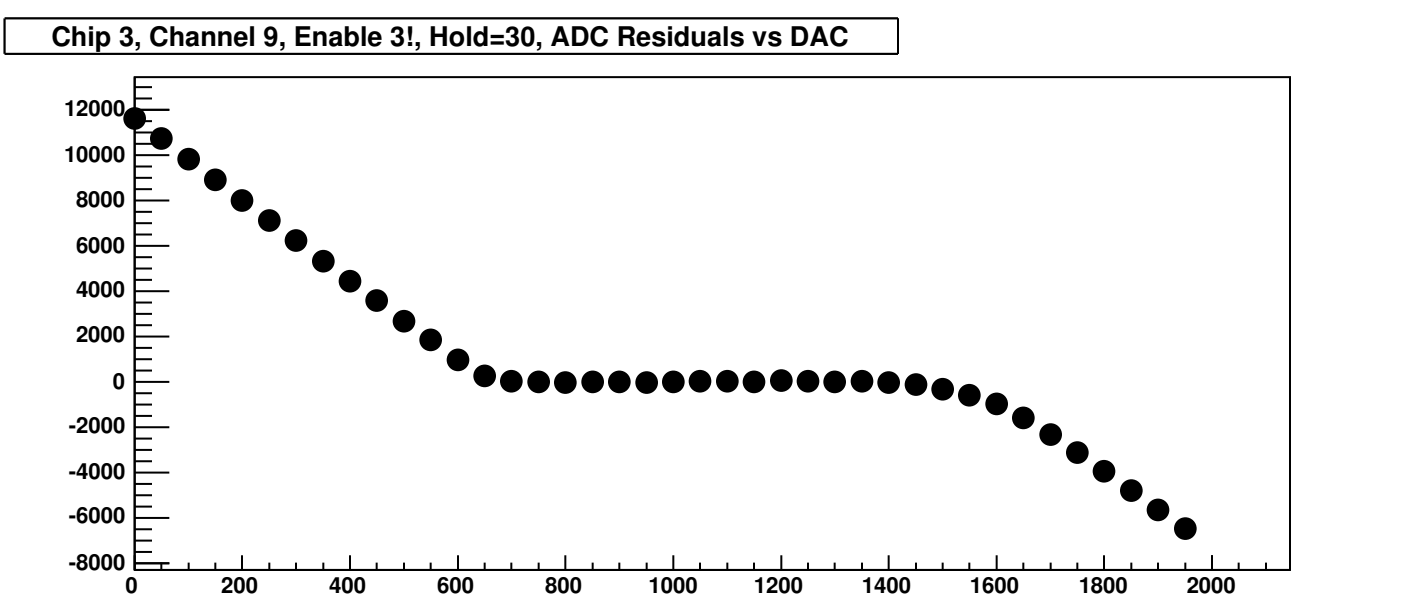

Chip 3, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 13.41 / 11
p0 -1957 ± 21.51
p1 0.02836 ± 0.01955
-1860
-1880
-1900
-1920
-1940
-1960
-1980
0
200
400
600
800
1000
1200
1400
1600
1800
2000

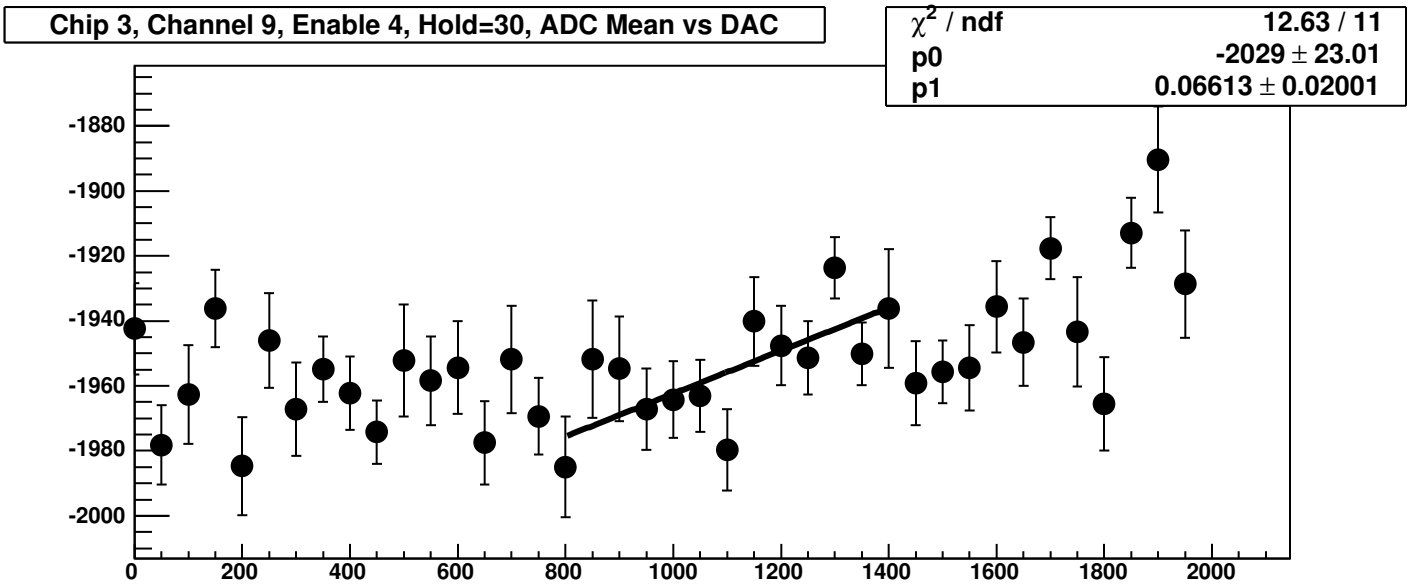

Chip 3, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

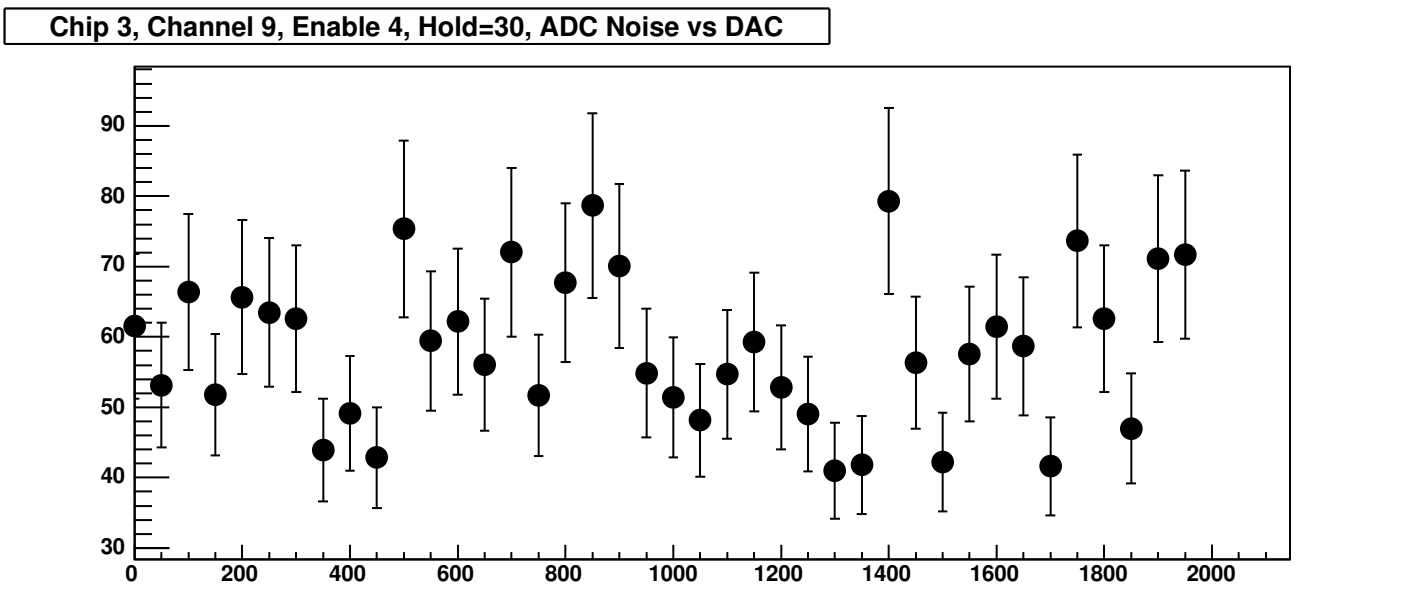

Chip 3, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

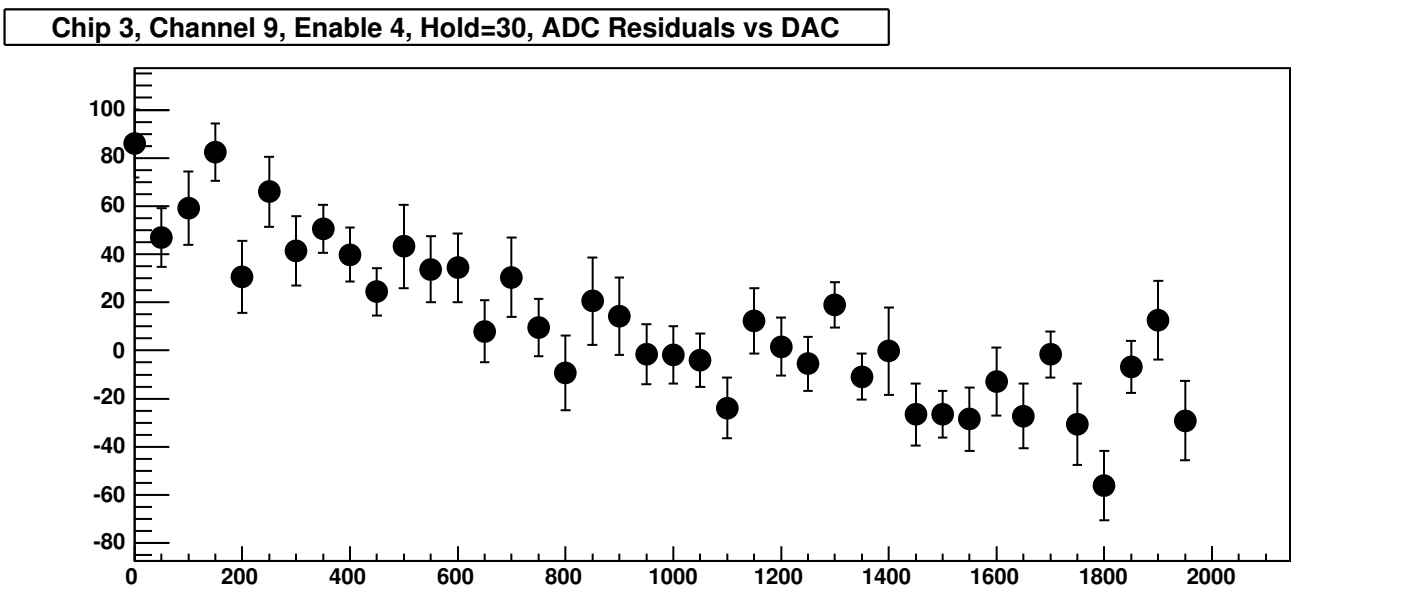

Chip 3, Channel 9, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 51.07 / 11
p0 -1808 ± 24.03
p1 18.29 ± 0.02072
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

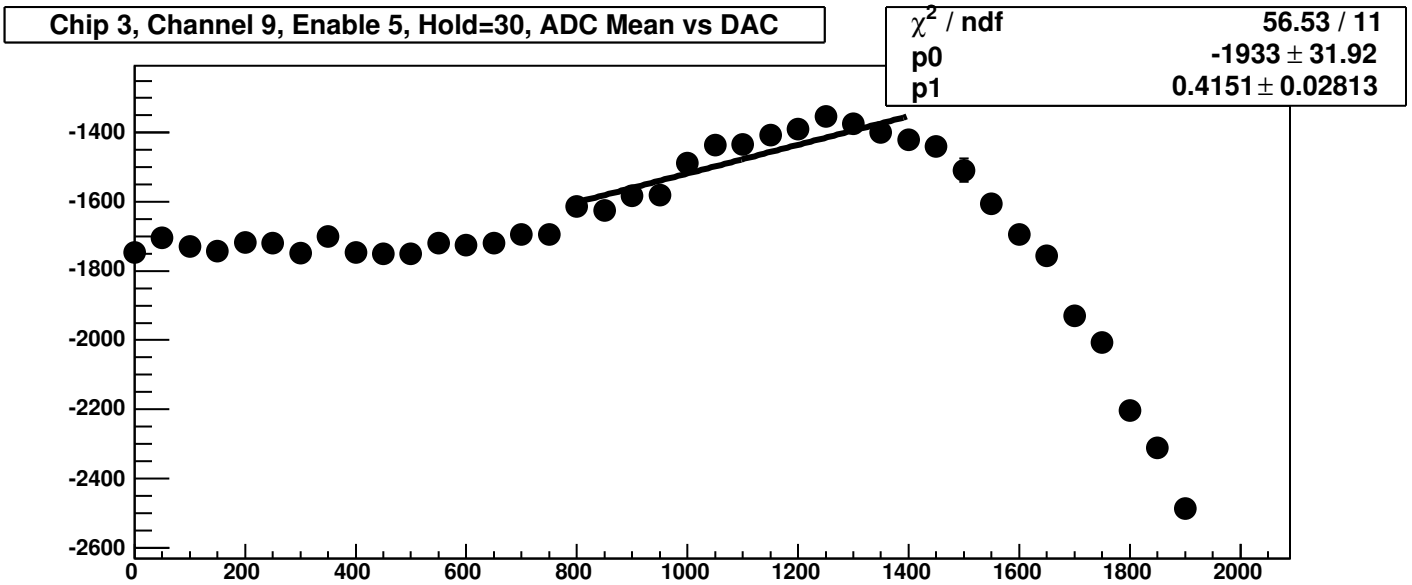

Chip 3, Channel 9, Enable 3!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

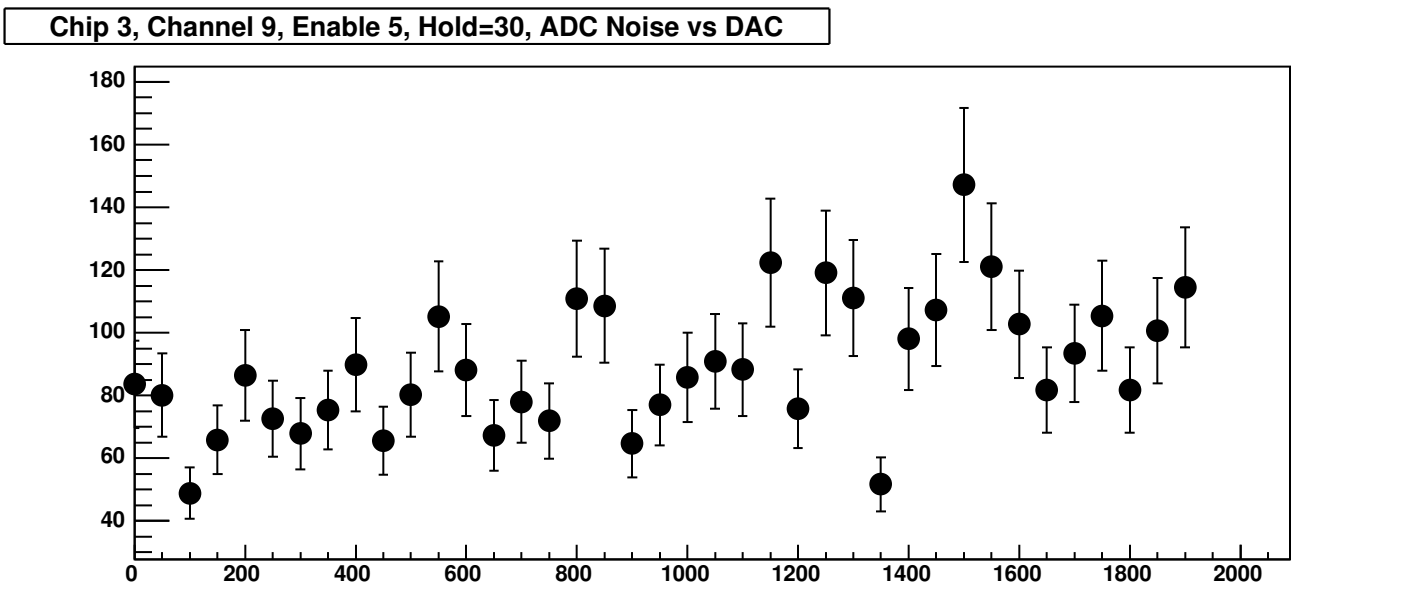

Chip 3, Channel 9, Enable 3!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

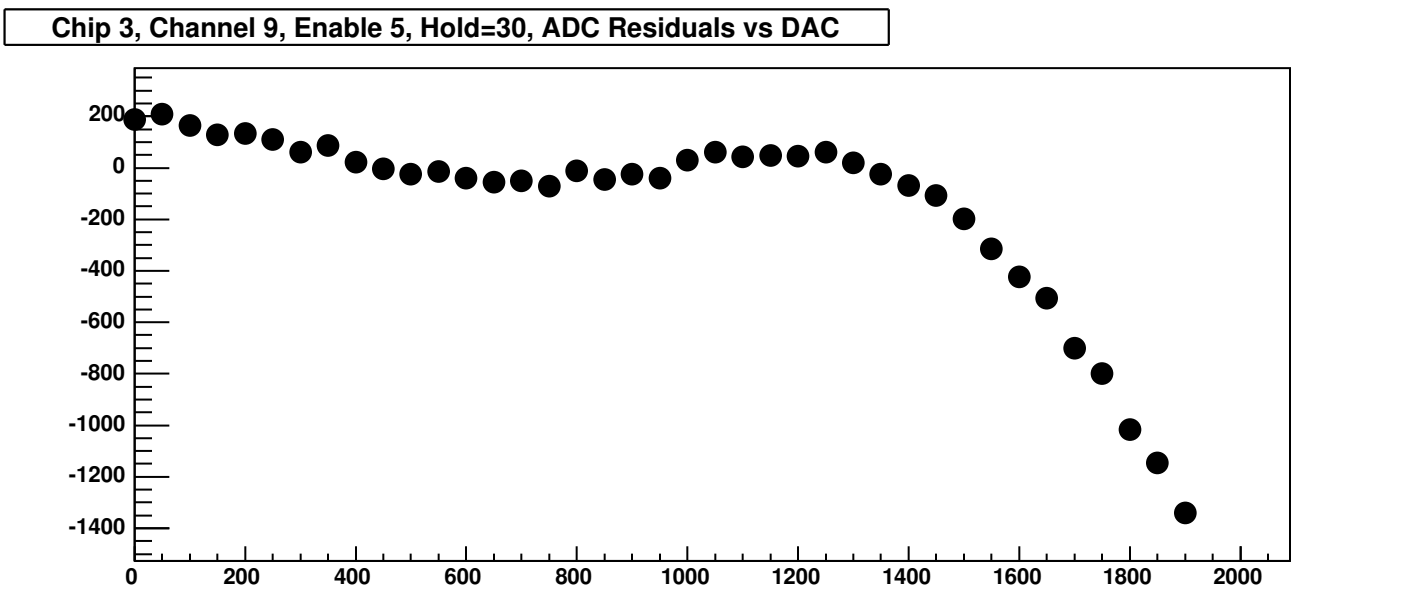

Chip 3, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 12.63 / 11
p0 -2029 ± 23.01
p1 0.06613 ± 0.02001
-1880
-1900
-1920
-1940
-1960
-1980
-2000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

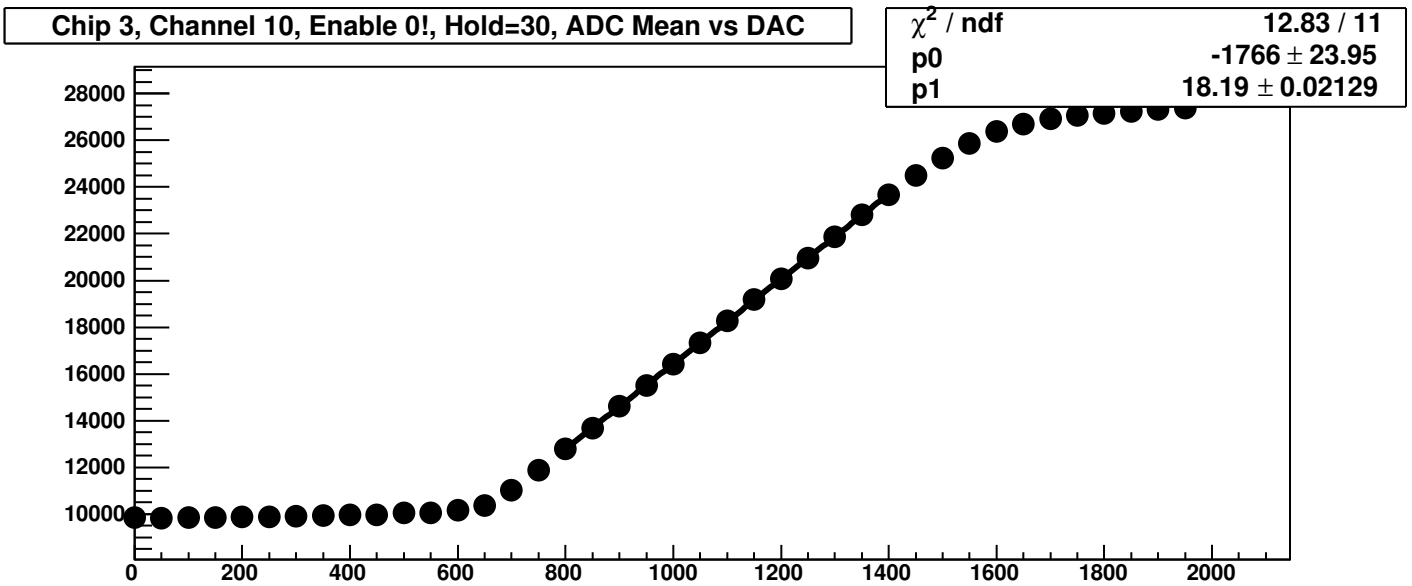

Chip 3, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

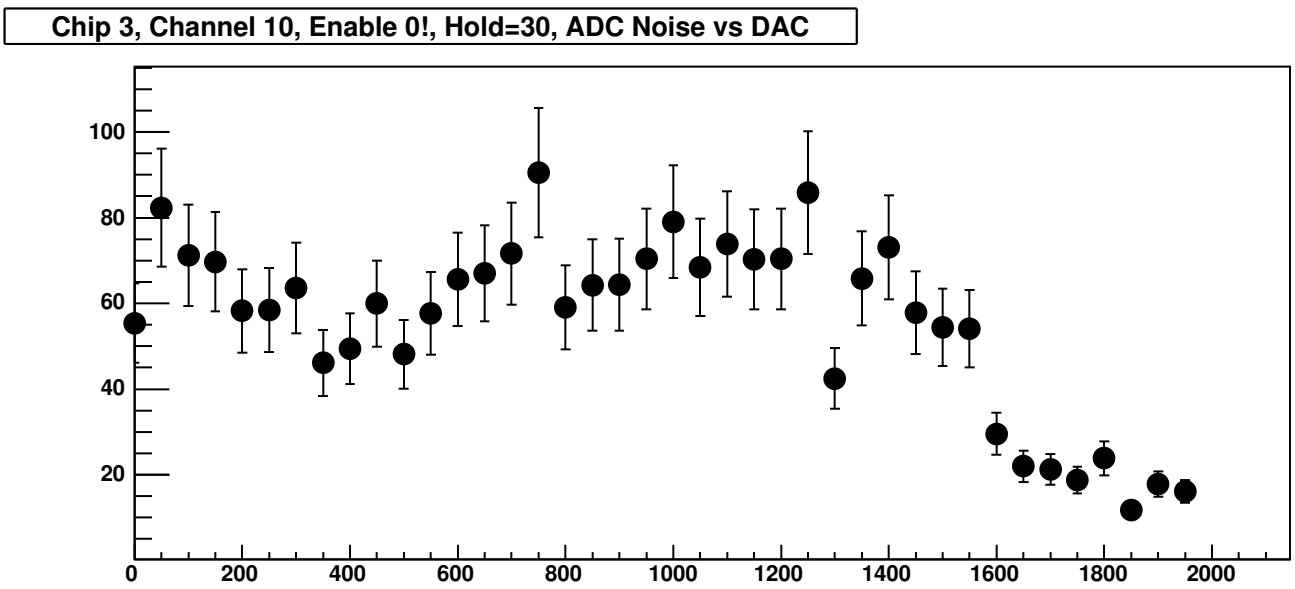

Chip 3, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
-60
-80
0
200
400
600
800
1000
1200
1400
1600
1800
2000

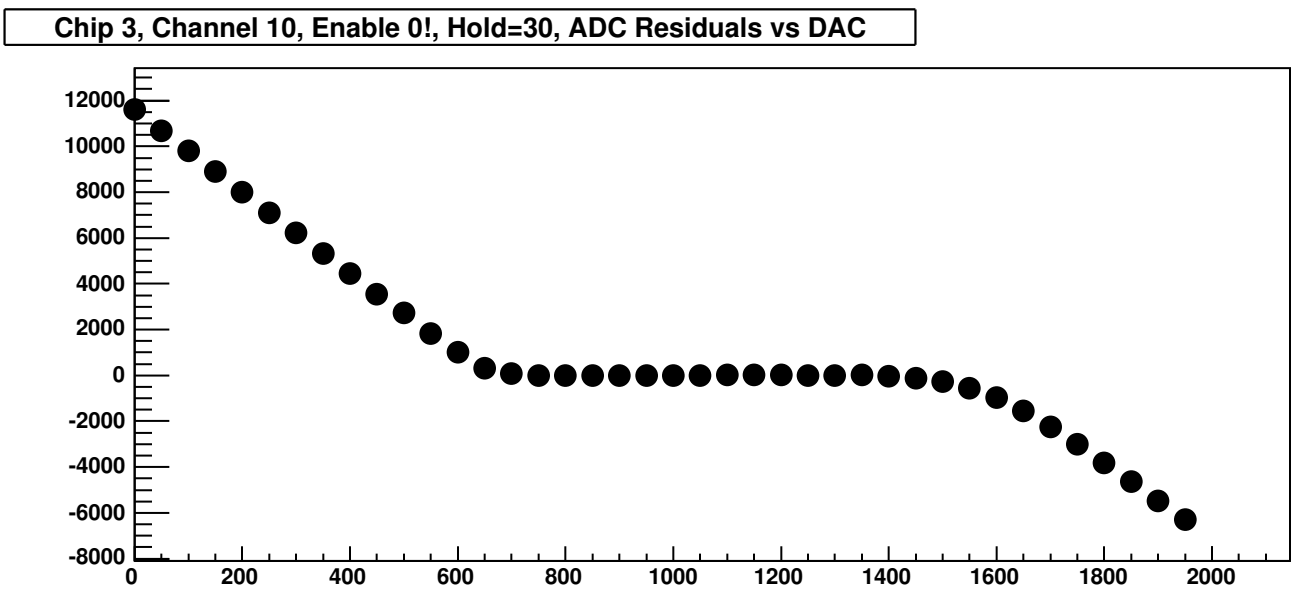

Chip 3, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 56.53 / 11
p0 -1933 ± 31.92
p1 0.4151 ± 0.02813
-1400
-1600
-1800
-2000
-2200
-2400
-2600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

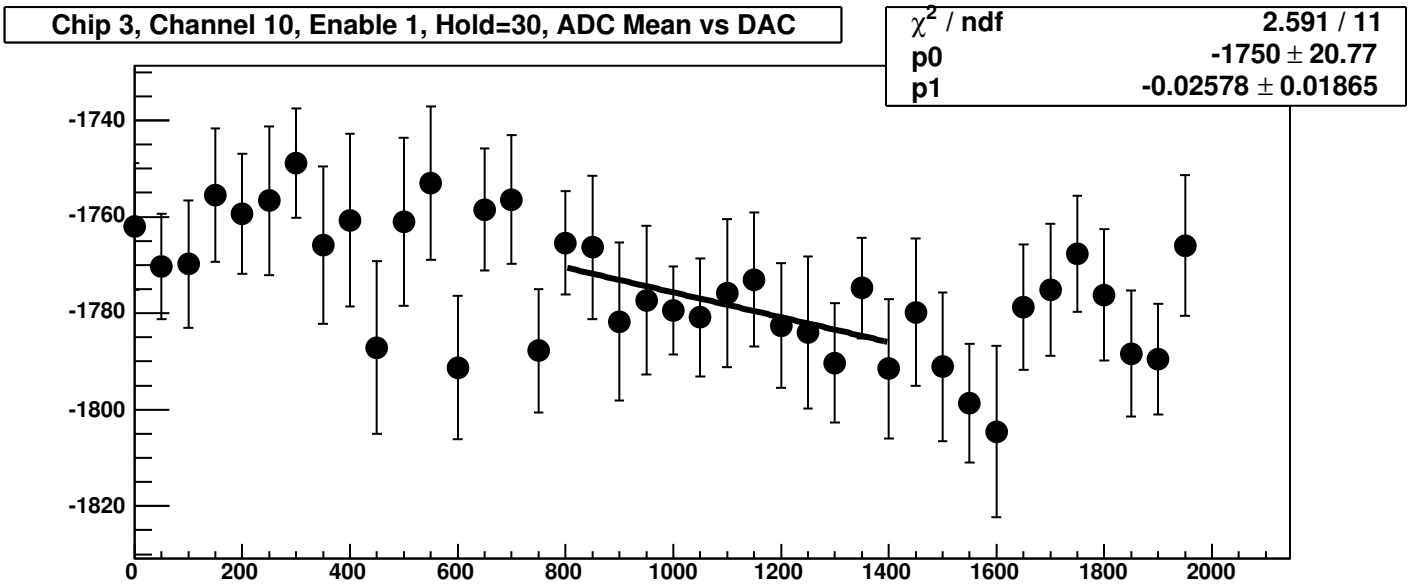

Chip 3, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

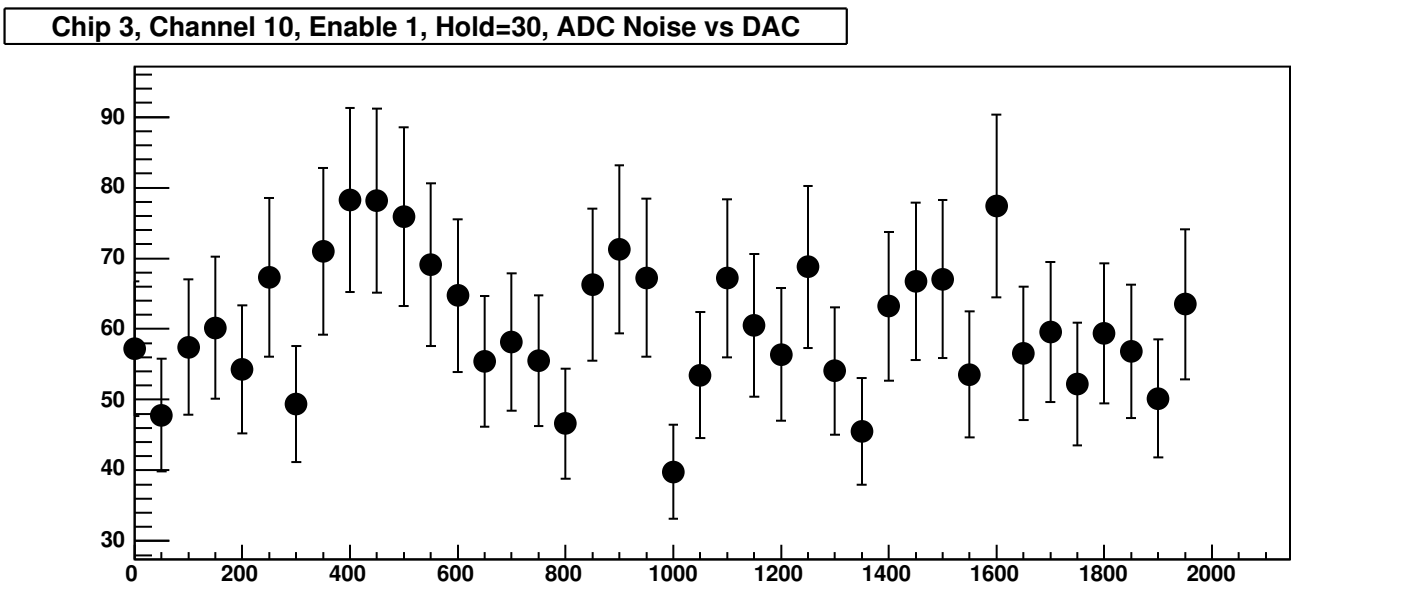

Chip 3, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

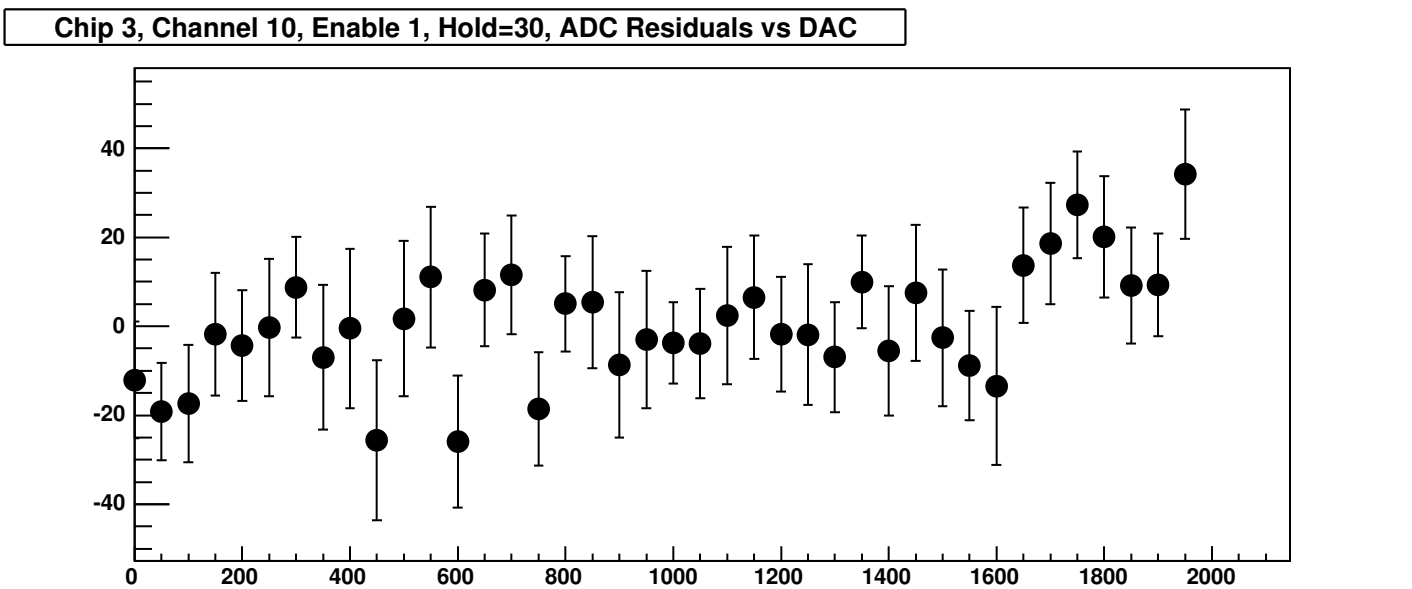

Chip 3, Channel 10, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 12.83 / 11
p0 -1766 ± 23.95
p1 18.19 ± 0.02129
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

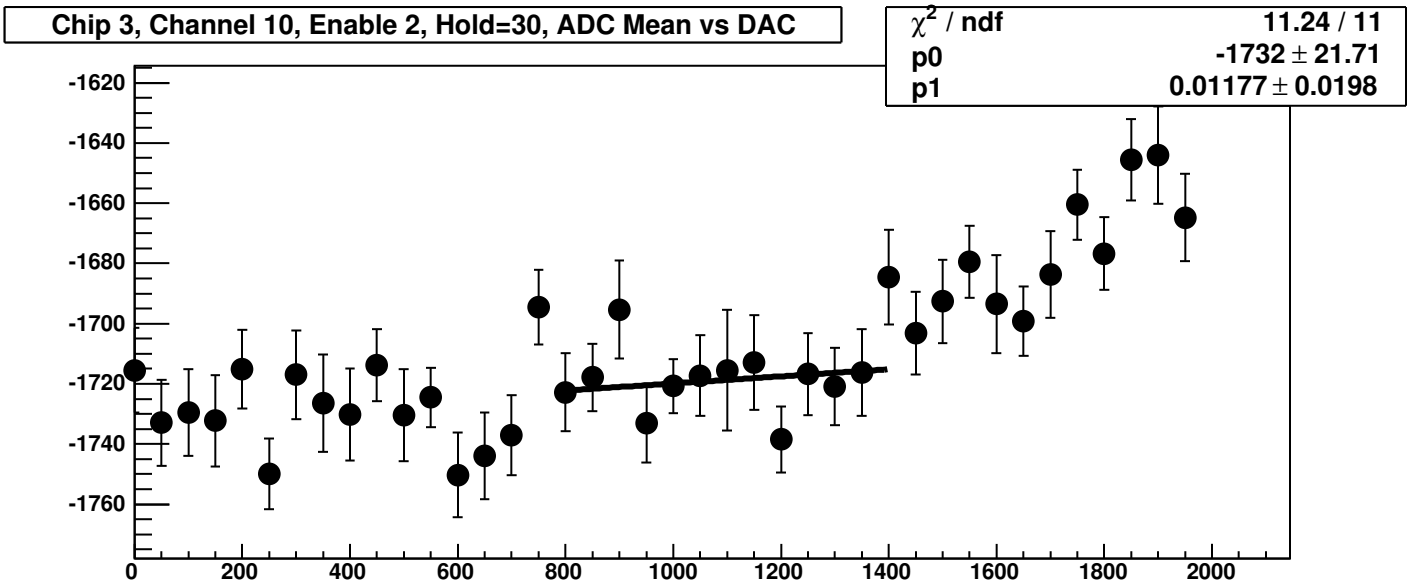

Chip 3, Channel 10, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

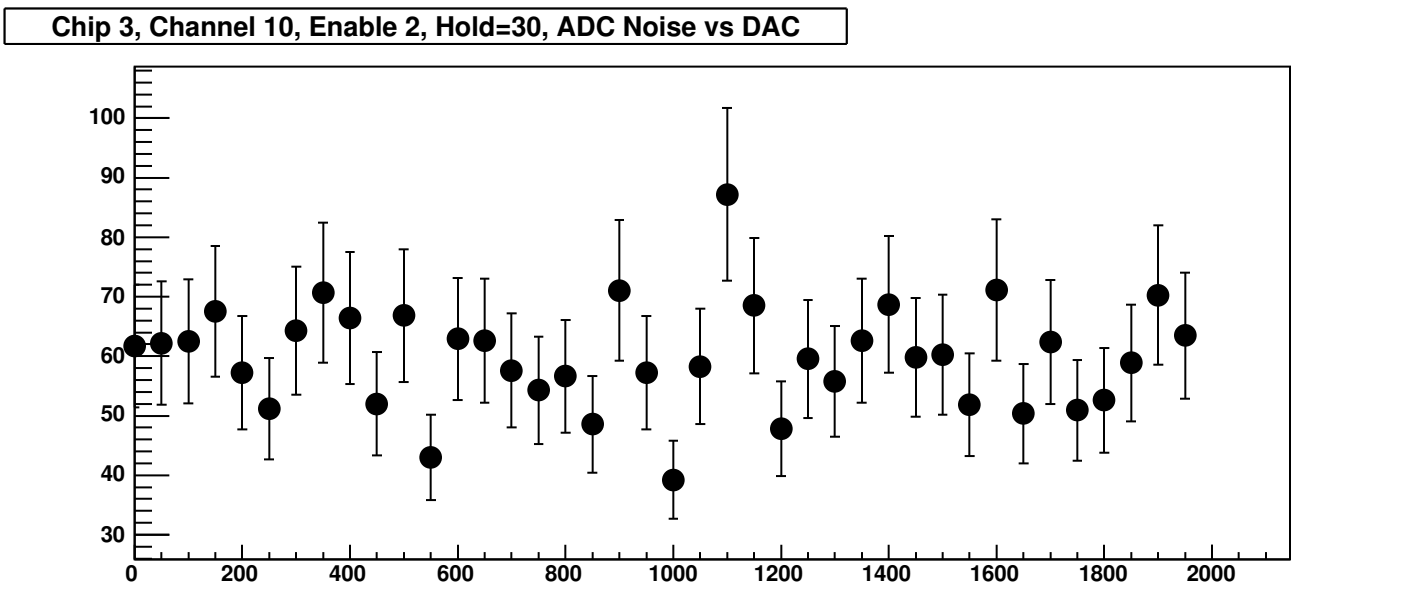

Chip 3, Channel 10, Enable 0!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

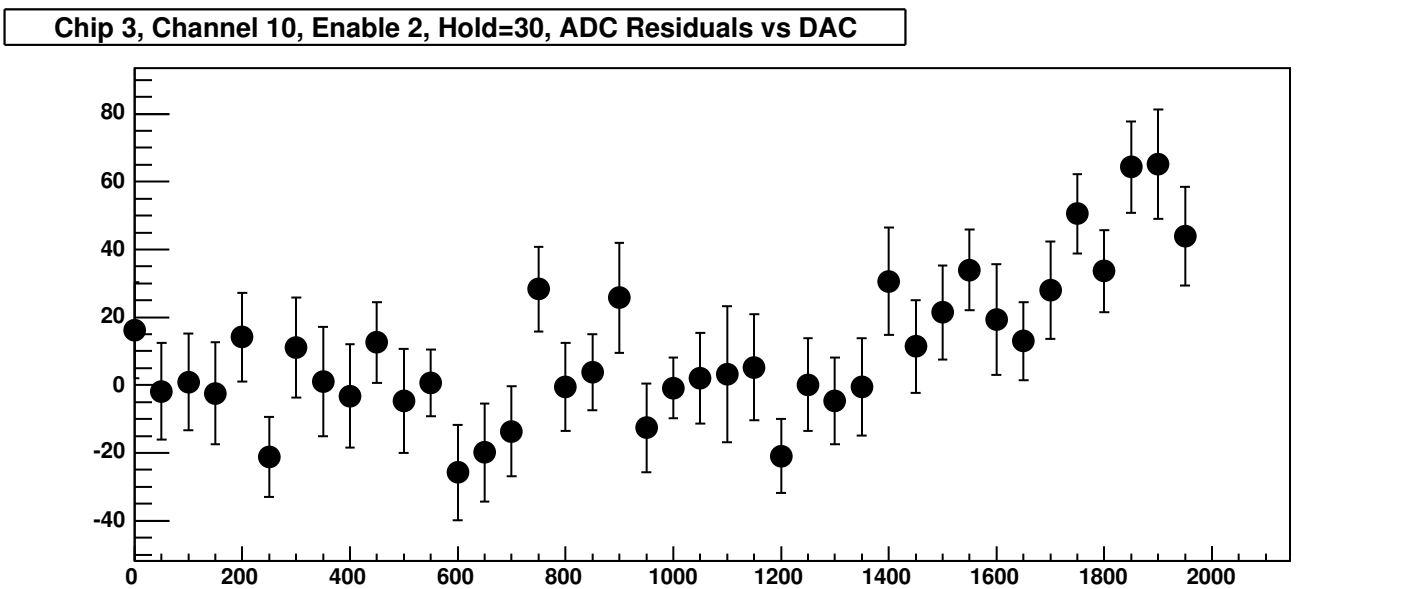

Chip 3, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 2.591 / 11
p0 -1750 ± 20.77
p1 -0.02578 ± 0.01865
-1740
-1760
-1780
-1800
-1820
0
200
400
600
800
1000
1200
1400
1600
1800
2000

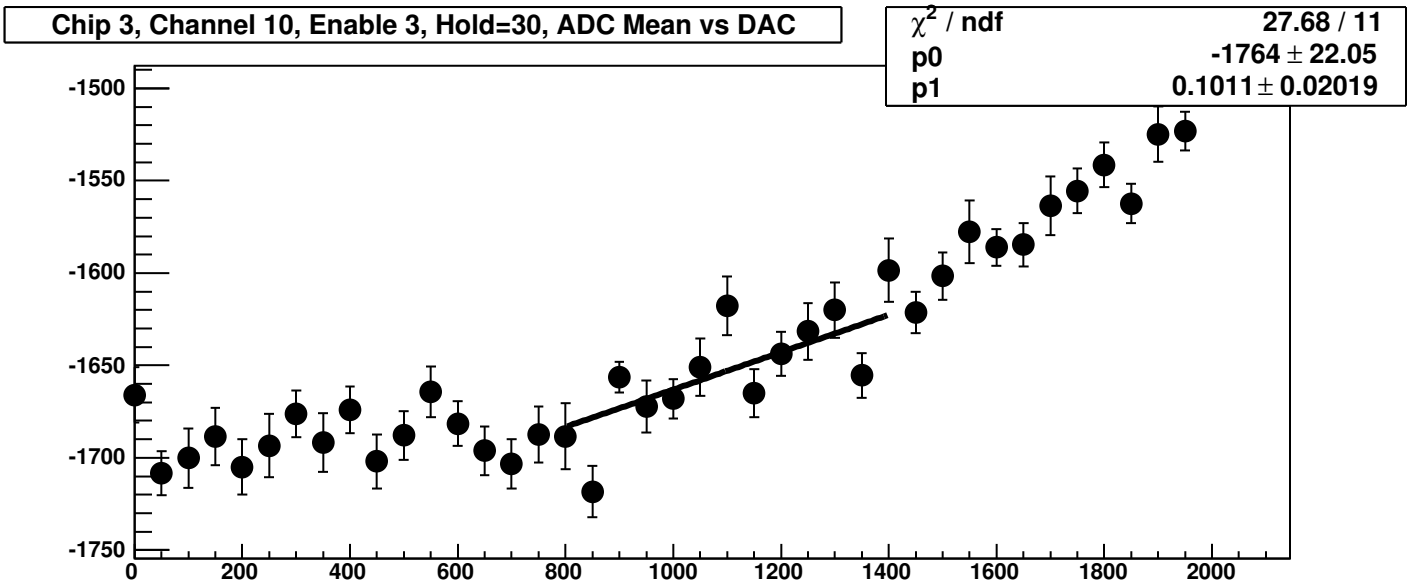

Chip 3, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

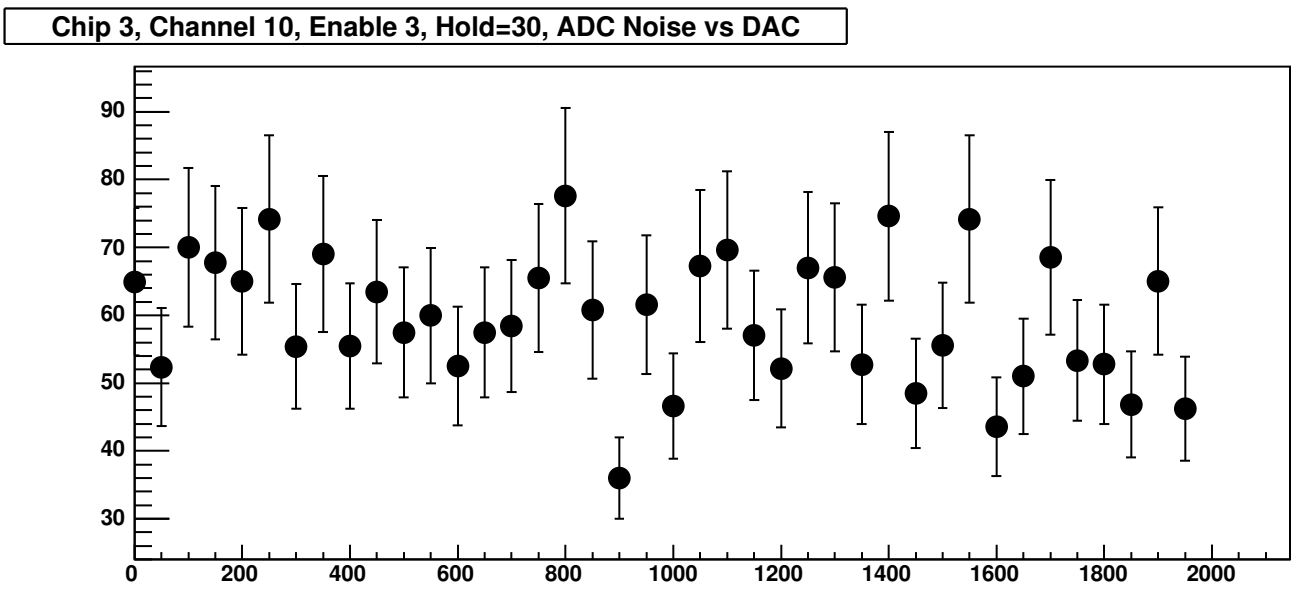

Chip 3, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

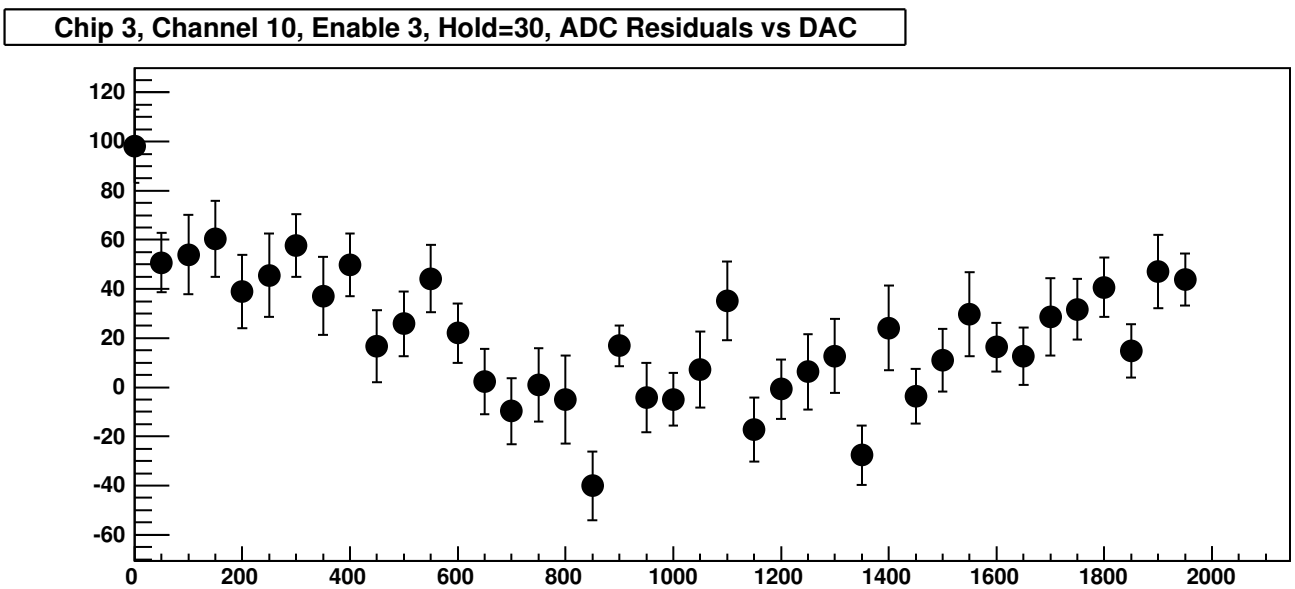

Chip 3, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 11.24 / 11
p0 -1732 ± 21.71
p1 0.01177 ± 0.0198
-1620
-1640
-1660
-1680
-1700
-1720
-1740
-1760
0
200
400
600
800
1000
1200
1400
1600
1800
2000

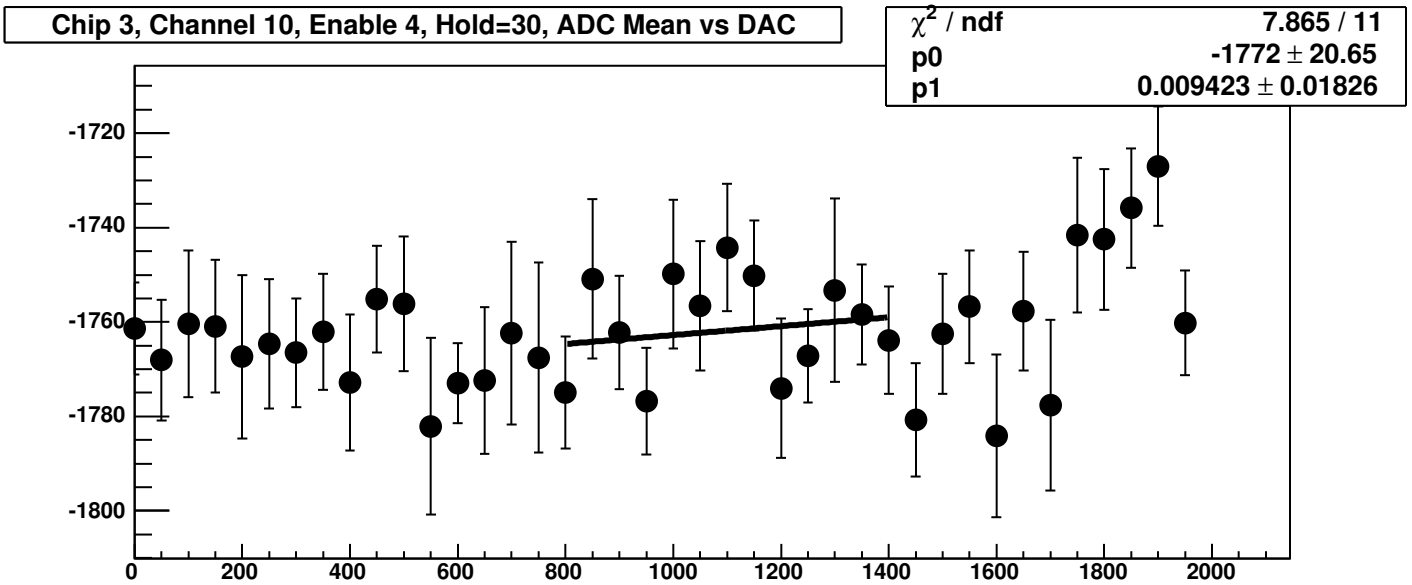

Chip 3, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

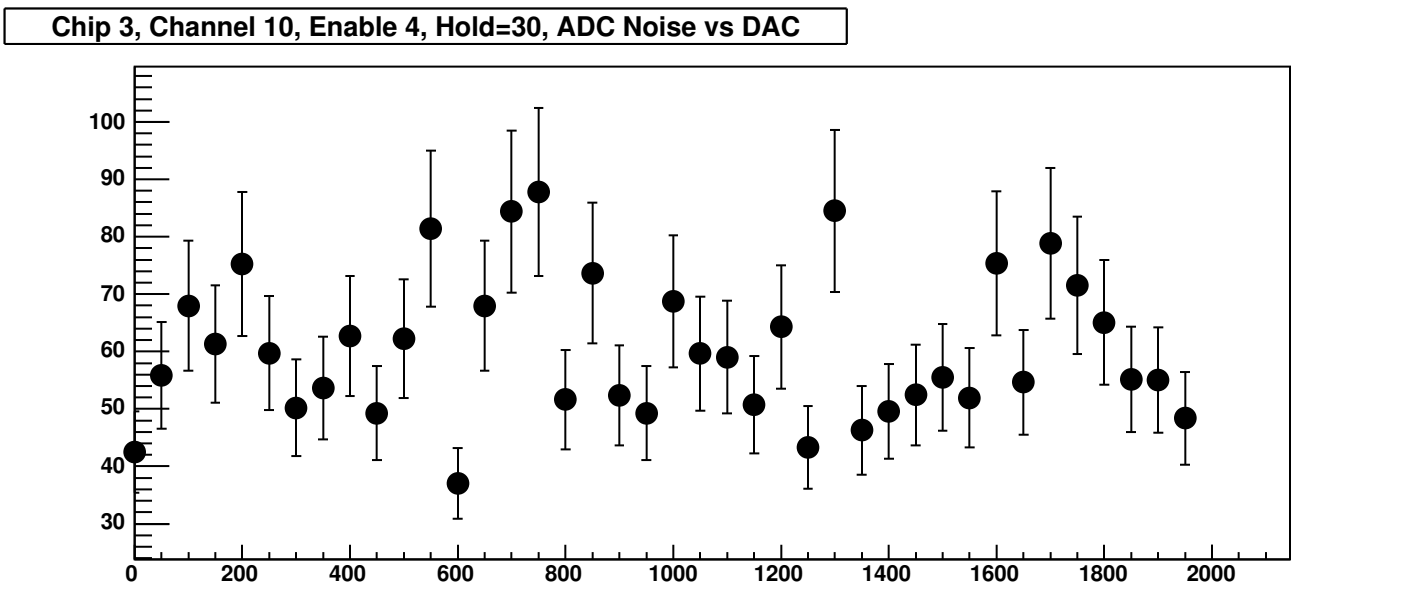

Chip 3, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

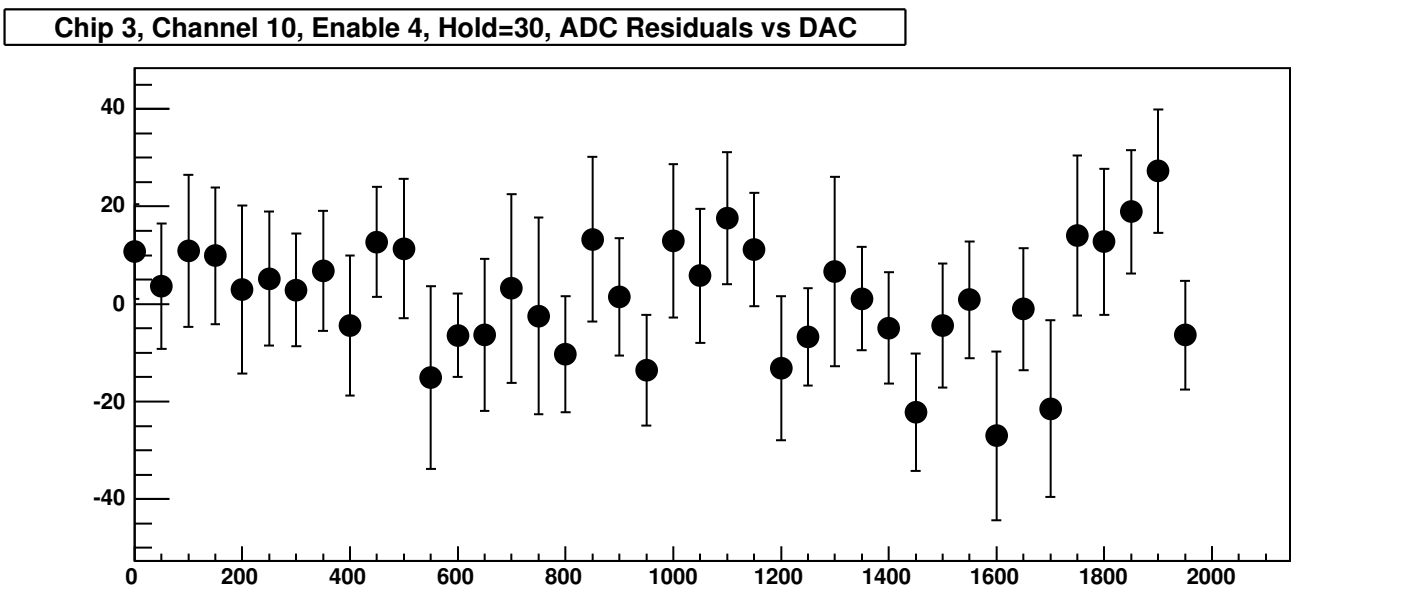

Chip 3, Channel 10, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 27.68 / 11
p0 -1764 ± 22.05
p1 0.1011 ± 0.02019
-1500
-1550
-1600
-1650
-1700
-1750
0
200
400
600
800
1000
1200
1400
1600
1800
2000

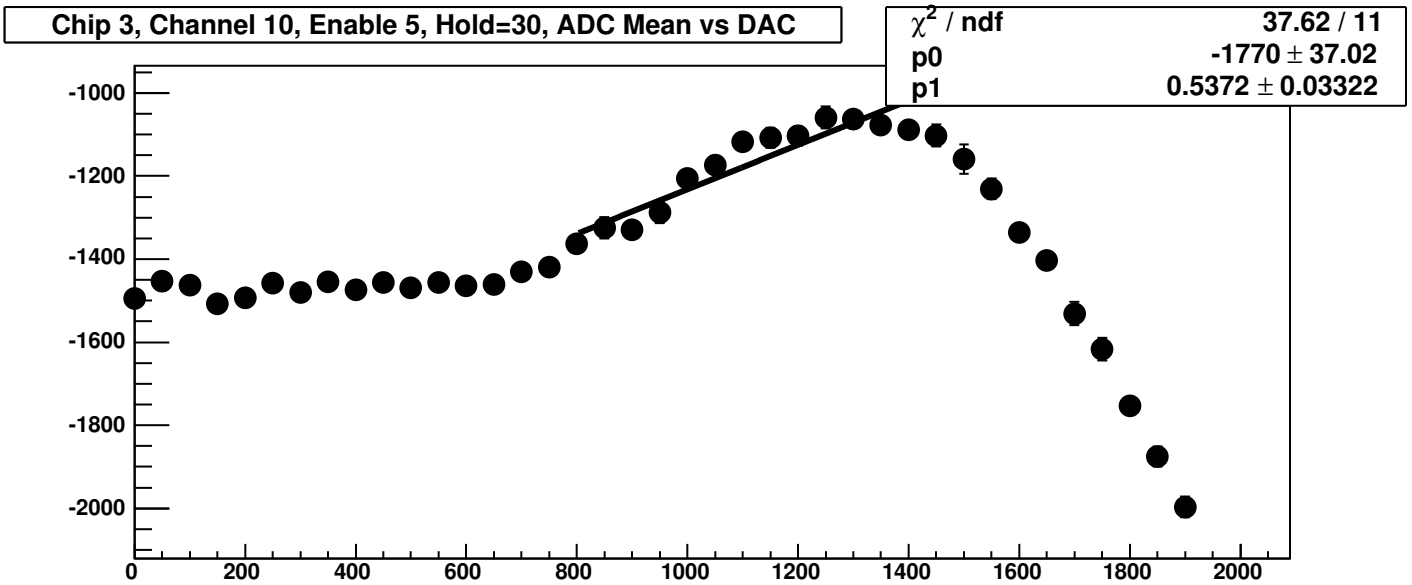

Chip 3, Channel 10, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

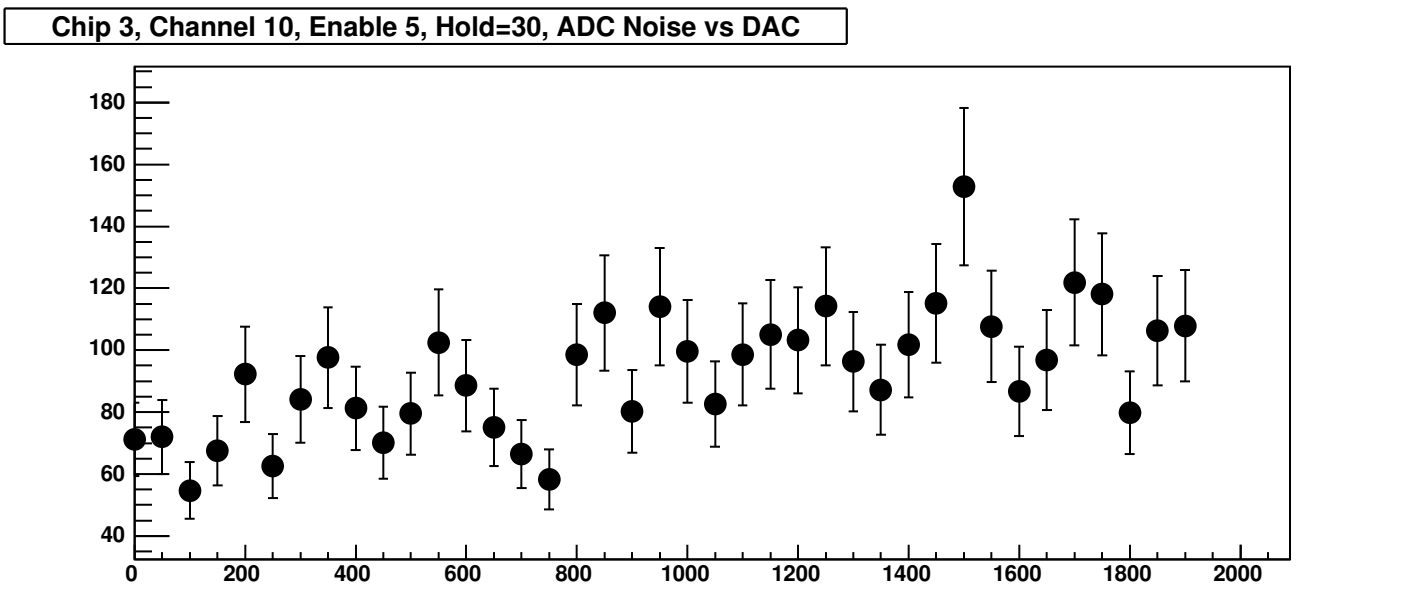

Chip 3, Channel 10, Enable 3, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

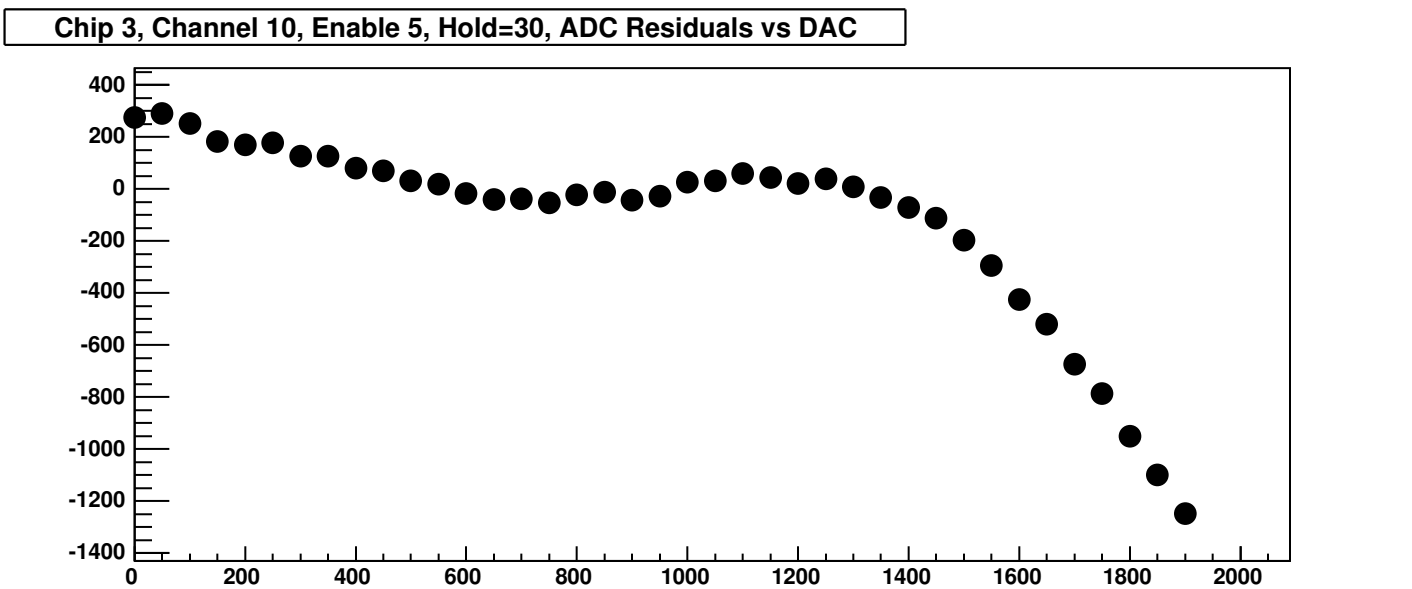

Chip 3, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7.865 / 11
p0 -1772 ± 20.65
p1 0.009423 ± 0.01826
-1720
-1740
-1760
-1780
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

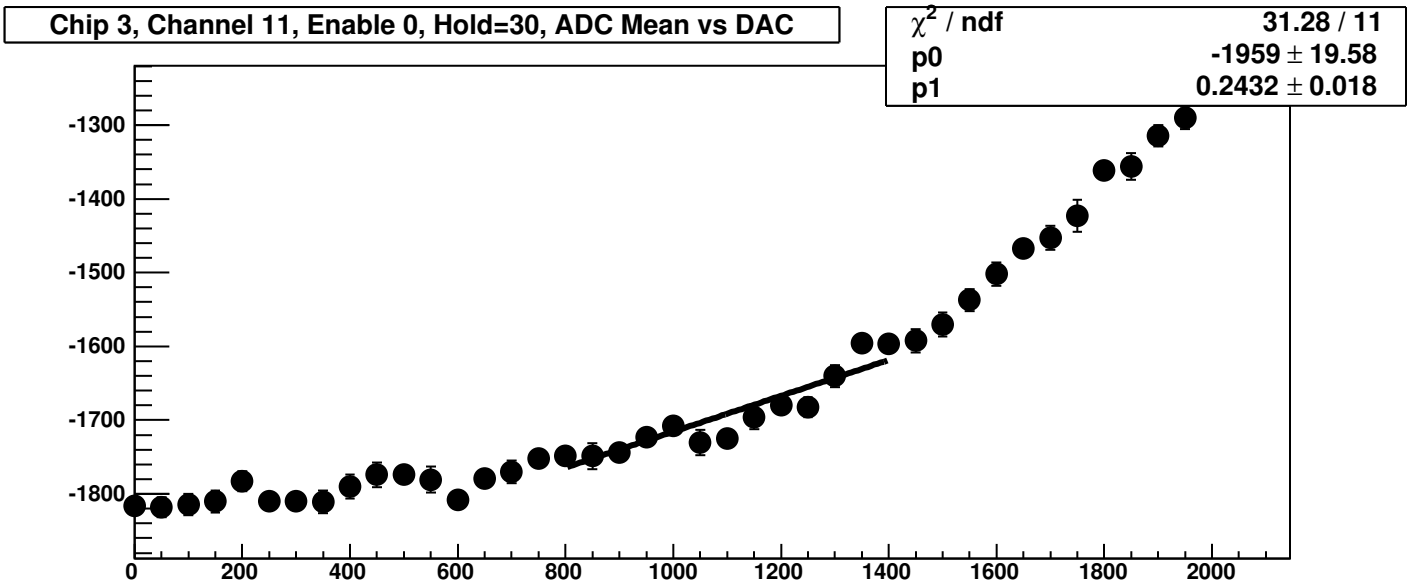

Chip 3, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

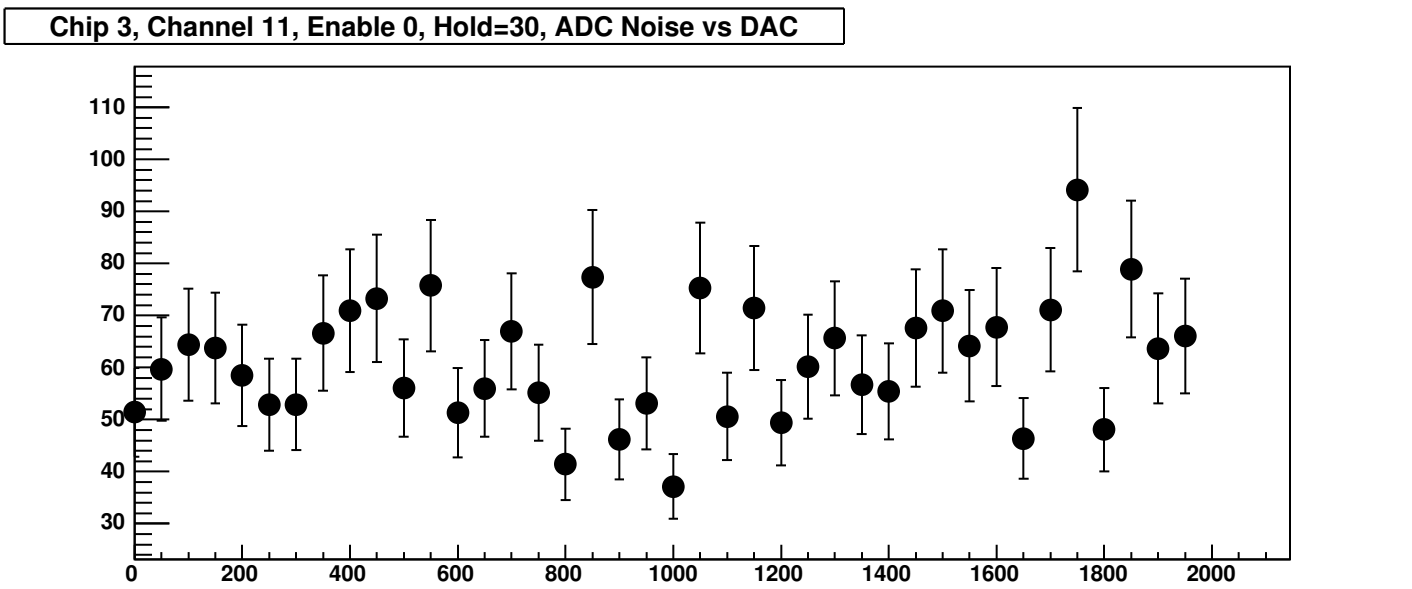

Chip 3, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

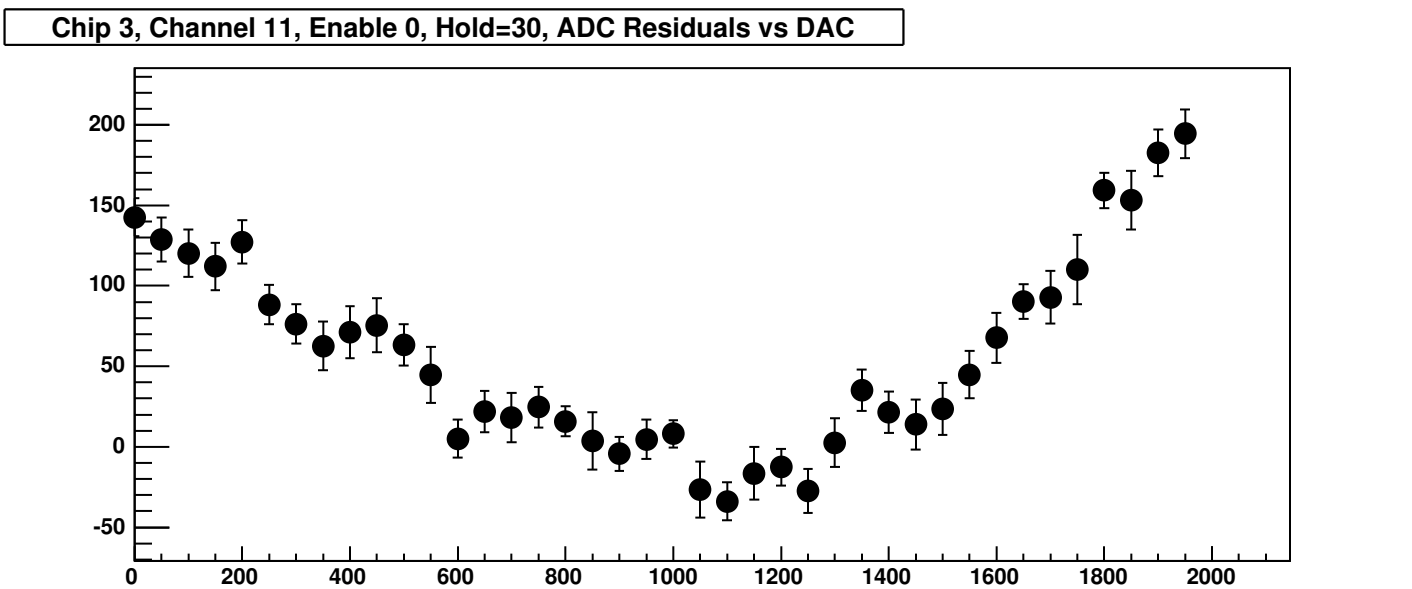

Chip 3, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 37.62 / 11
p0 -1770 ± 37.02
p1 0.5372 ± 0.03322
-1000
-1200
-1400
-1600
-1800
-2000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

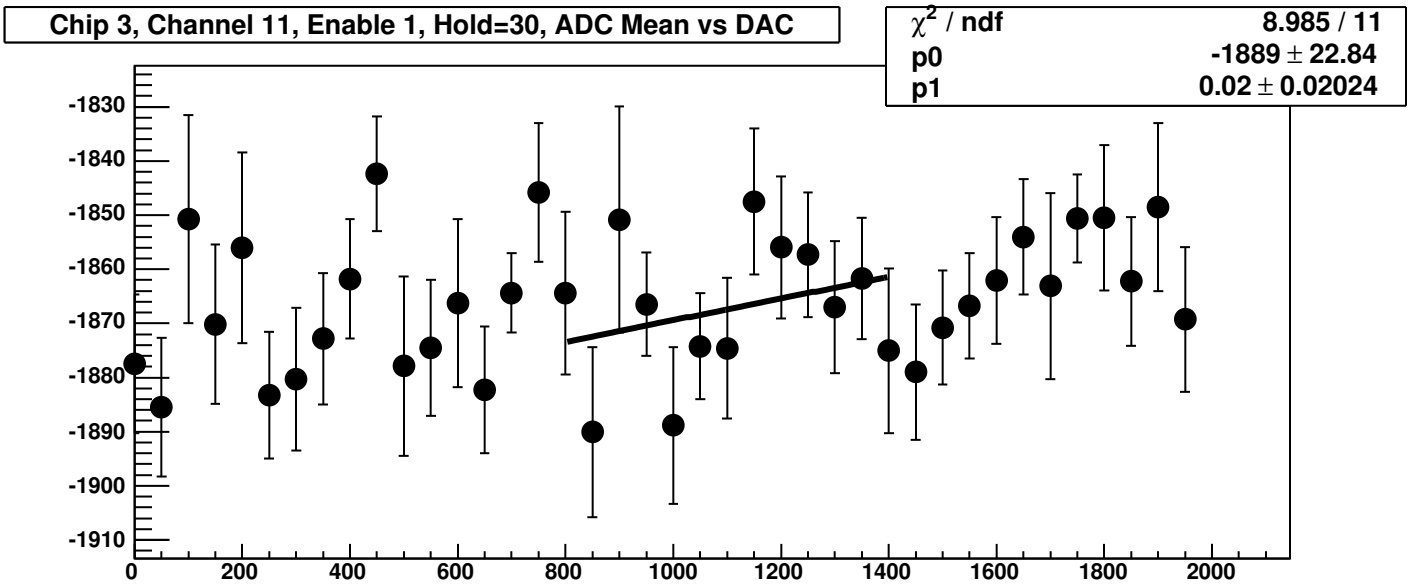

Chip 3, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

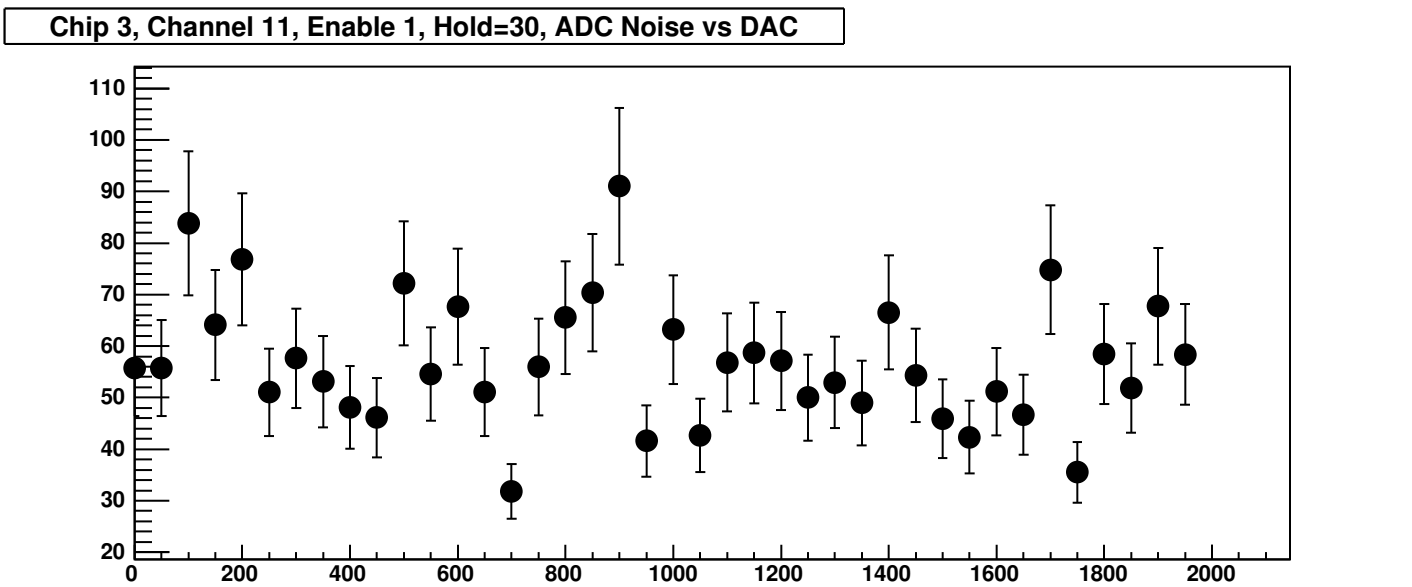

Chip 3, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

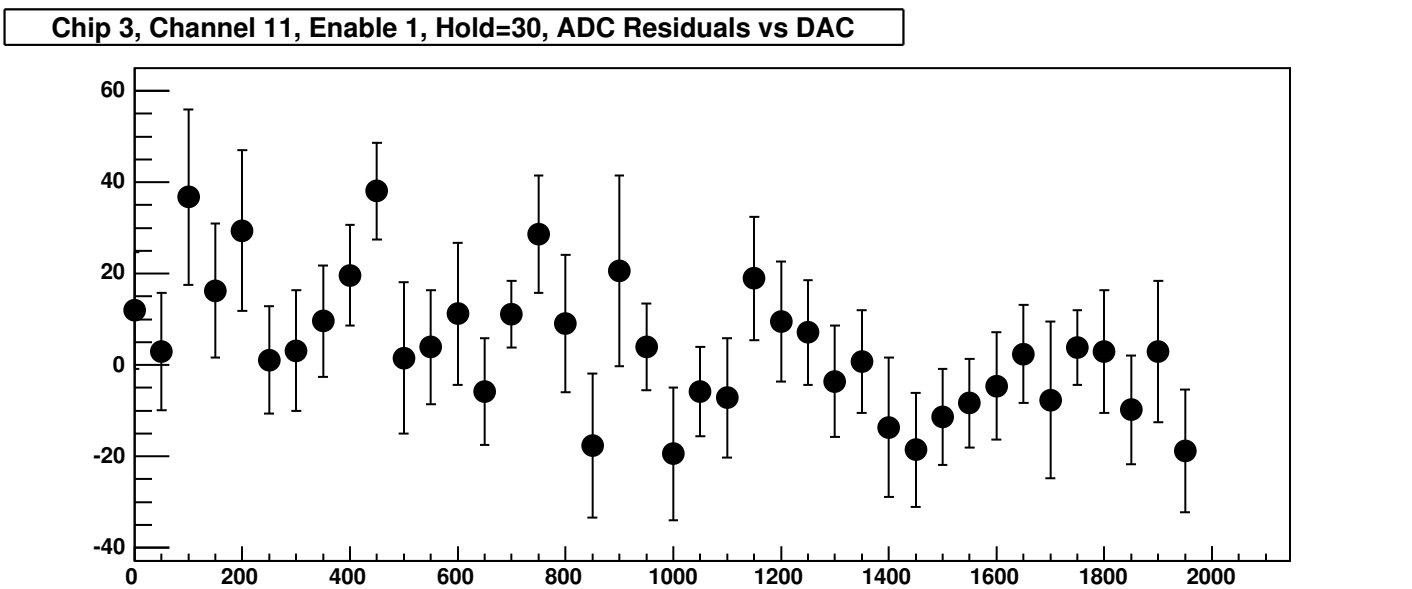

Chip 3, Channel 11, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 31.28 / 11
p0 -1959 ± 19.58
p1 0.2432 ± 0.018
-1300
-1400
-1500
-1600
-1700
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

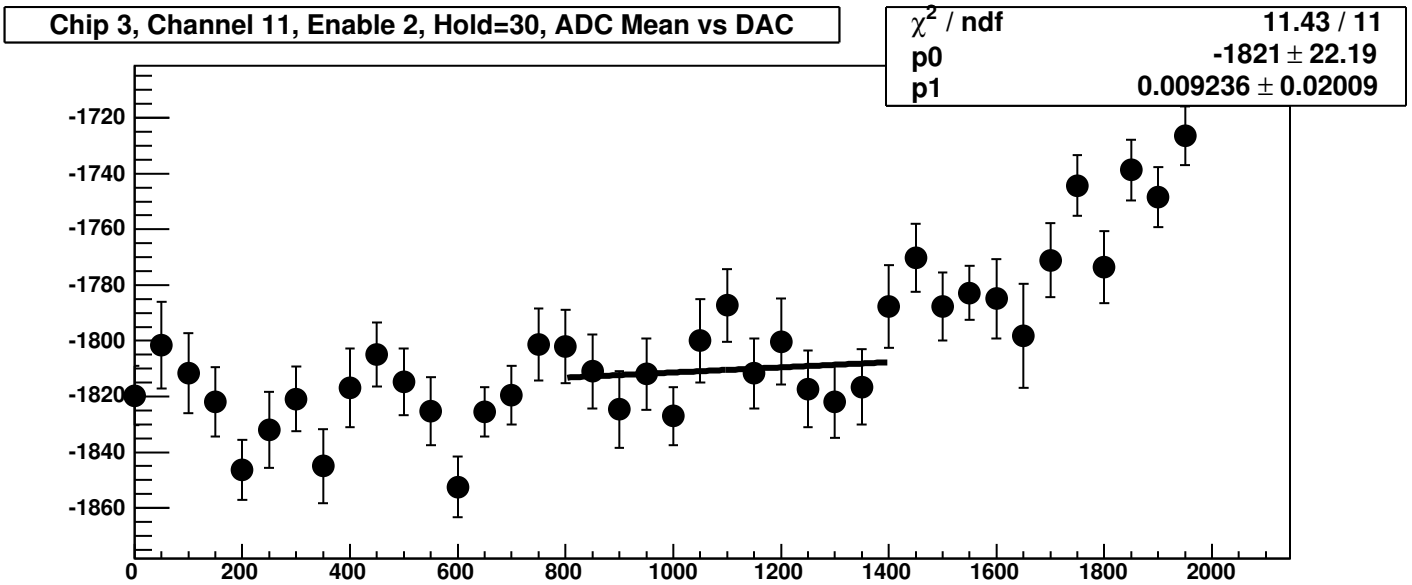

Chip 3, Channel 11, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

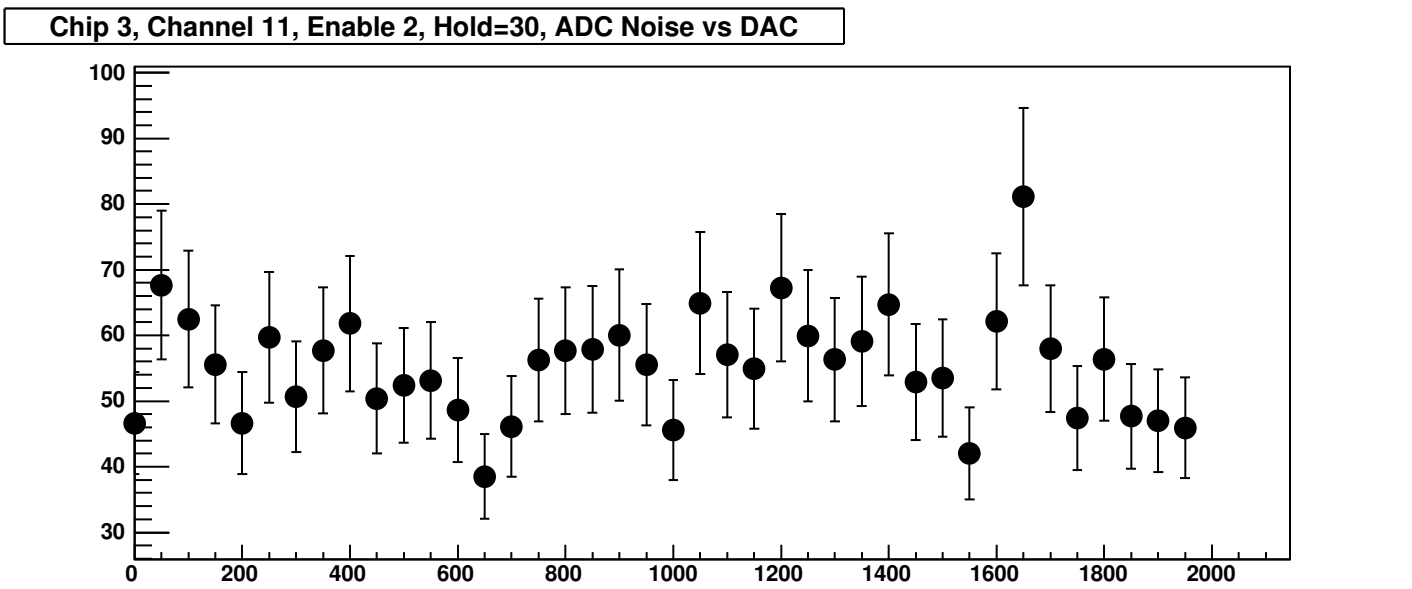

Chip 3, Channel 11, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

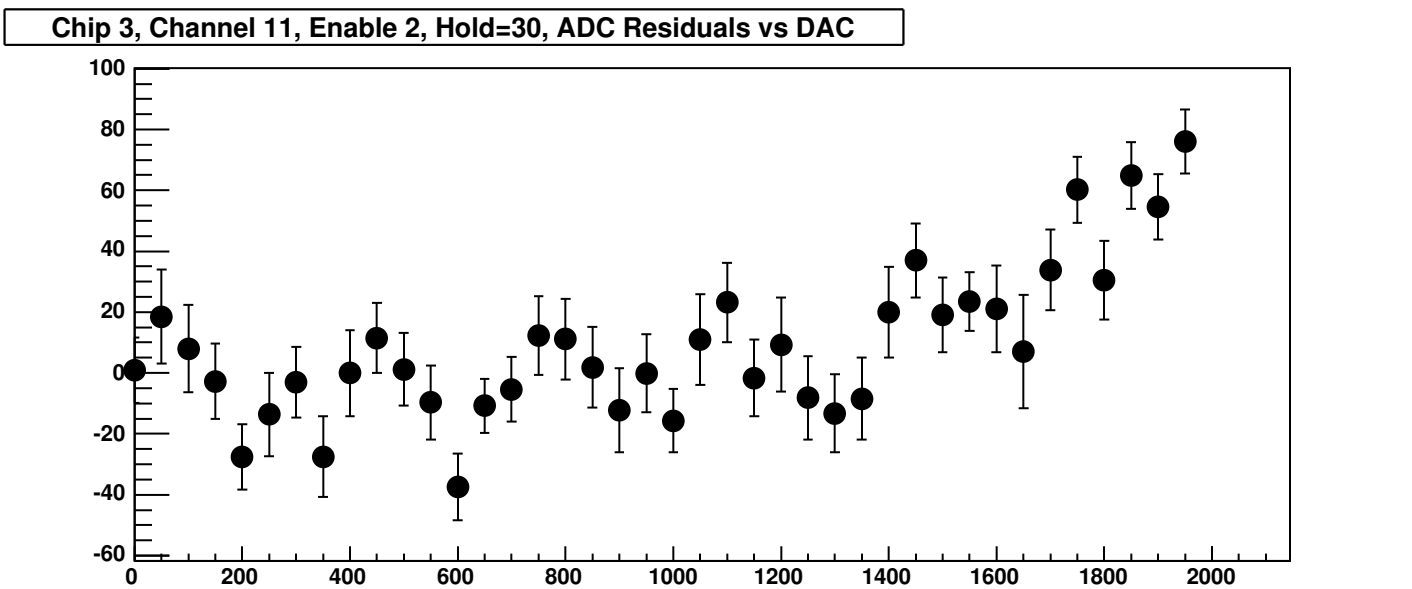

Chip 3, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 8.985 / 11
p0 -1889 ± 22.84
p1 0.02 ± 0.02024
-1830
-1840
-1850
-1860
-1870
-1880
-1890
-1900
-1910
0
200
400
600
800
1000
1200
1400
1600
1800
2000

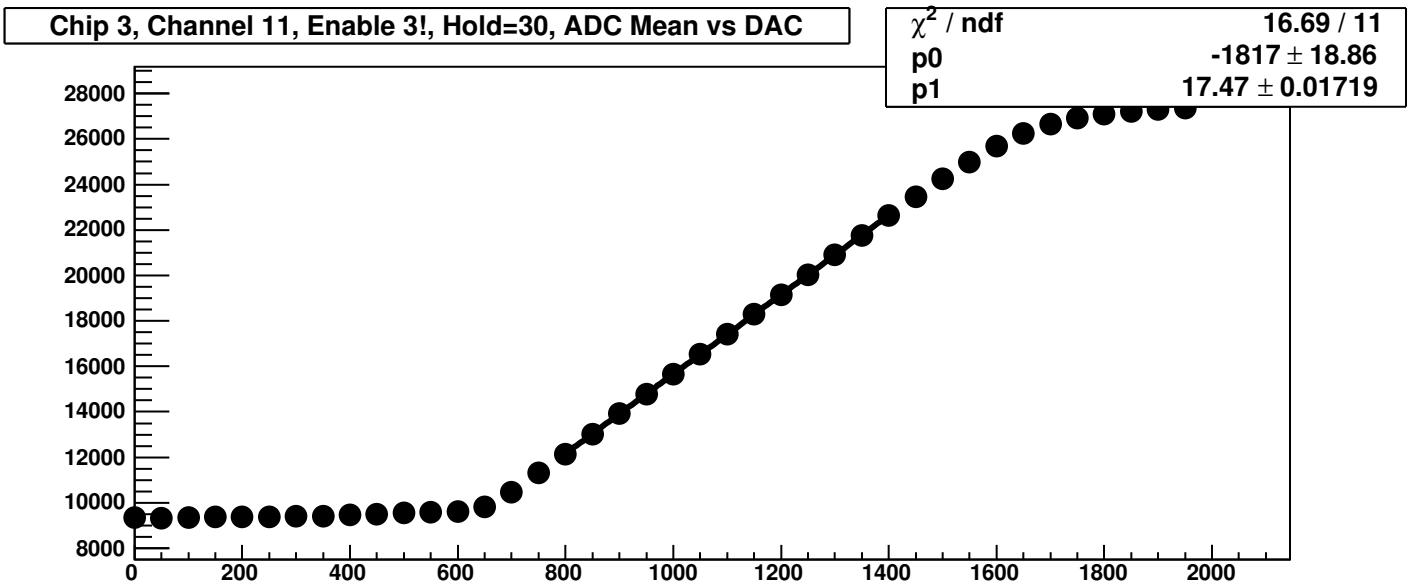

Chip 3, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

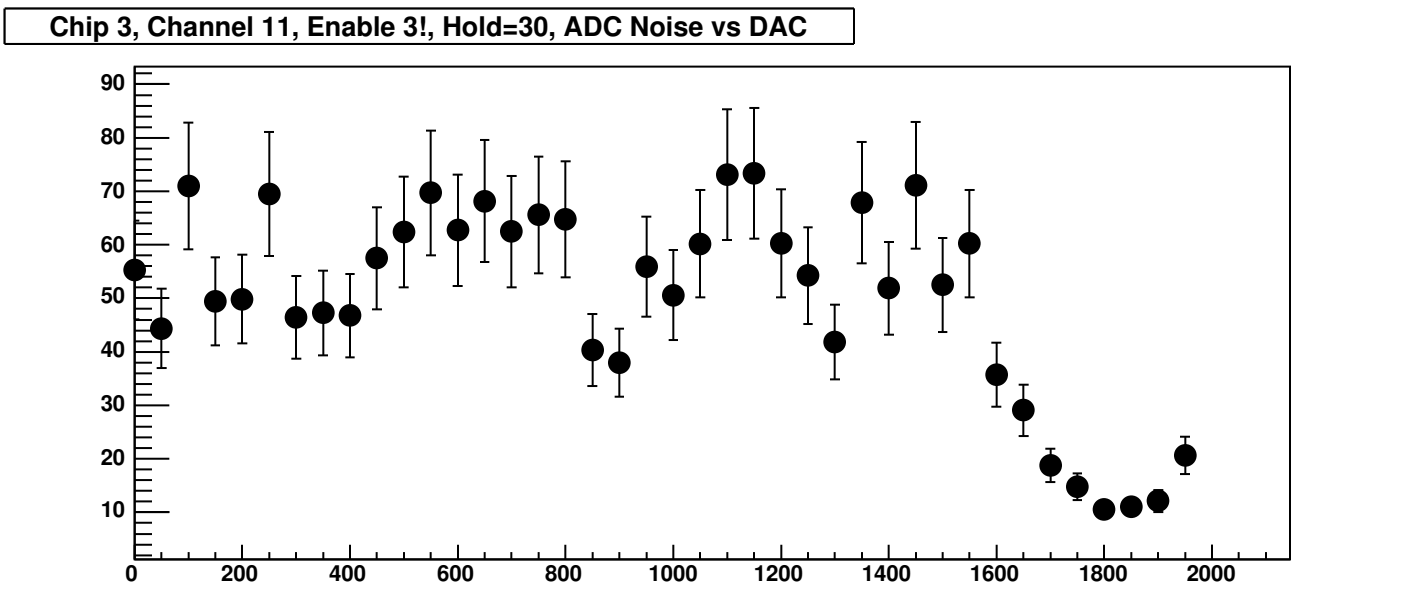

Chip 3, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

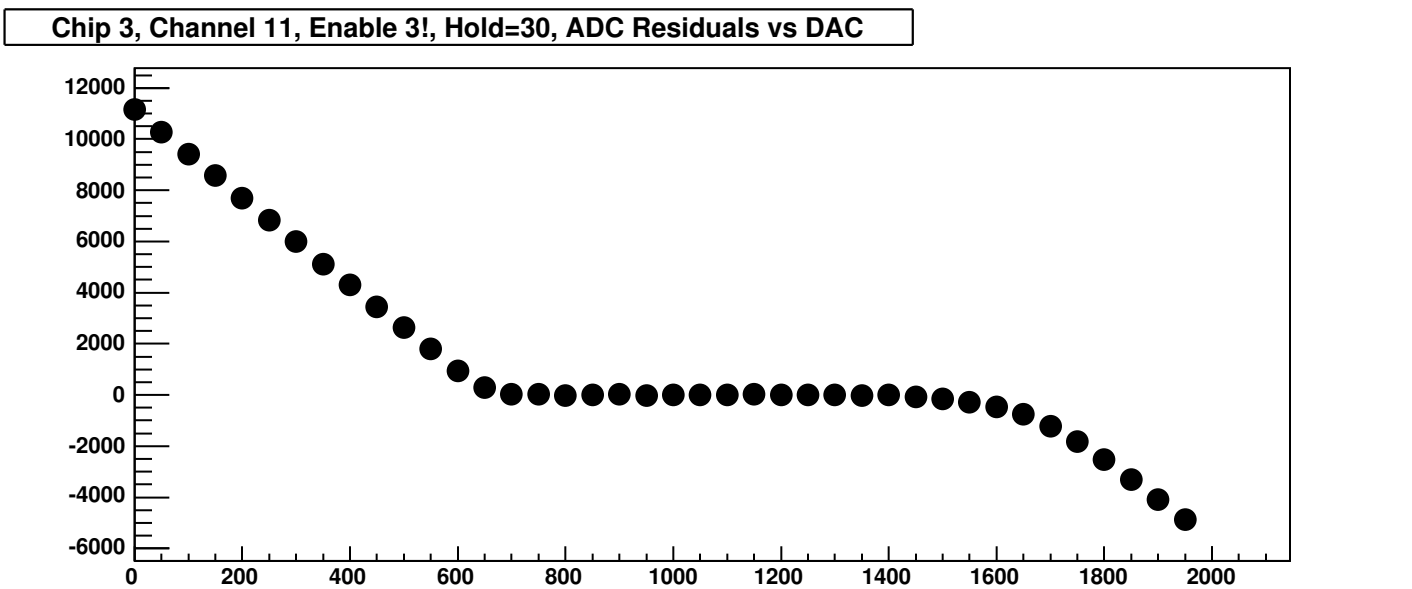

Chip 3, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 11.43 / 11
p0 -1821 ± 22.19
p1 0.009236 ± 0.02009
-1720
-1740
-1760
-1780
-1800
-1820
-1840
-1860
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

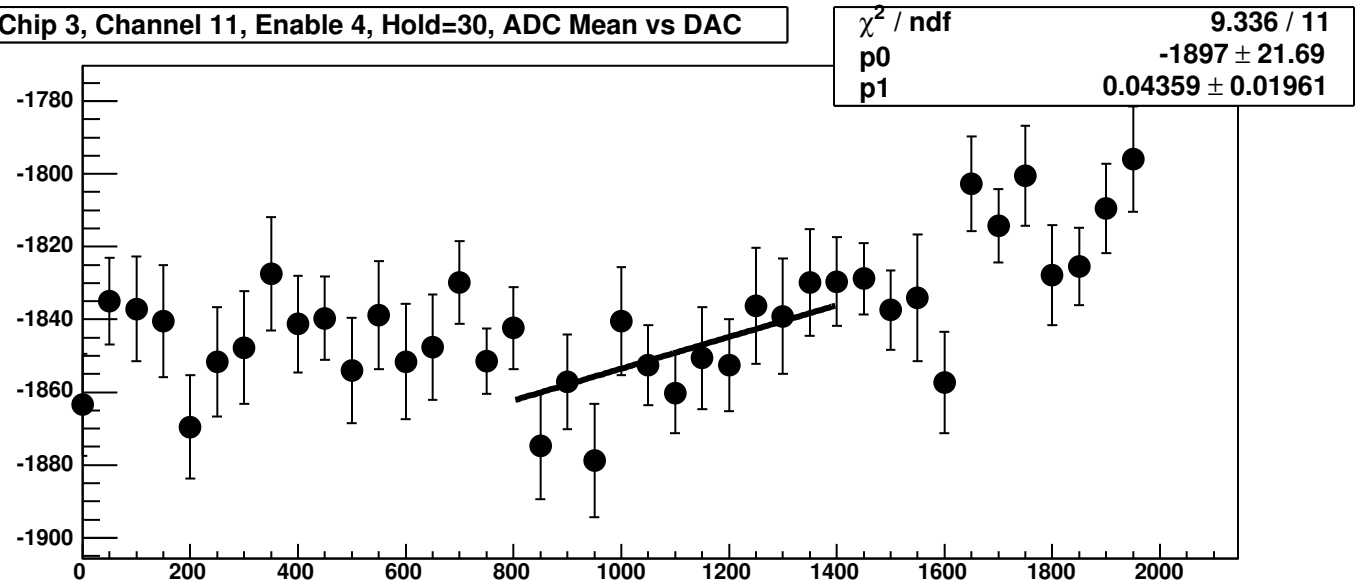

Chip 3, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 11, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 16.69 / 11
p0 -1817 ± 18.86
p1 17.47 ± 0.01719
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

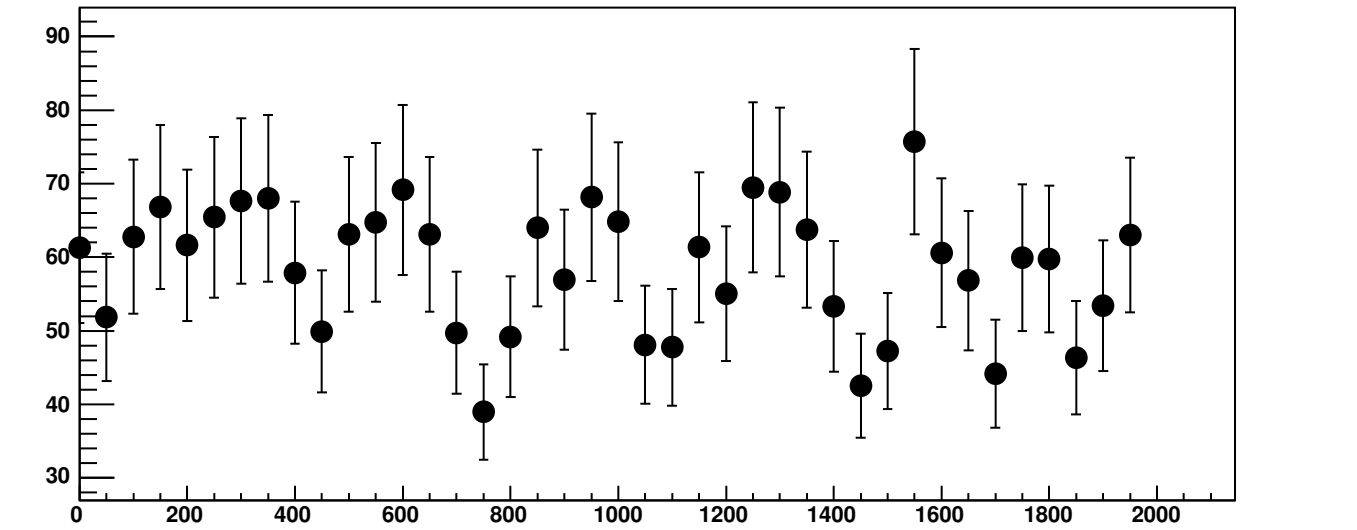

Chip 3, Channel 11, Enable 3!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 11, Enable 3!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

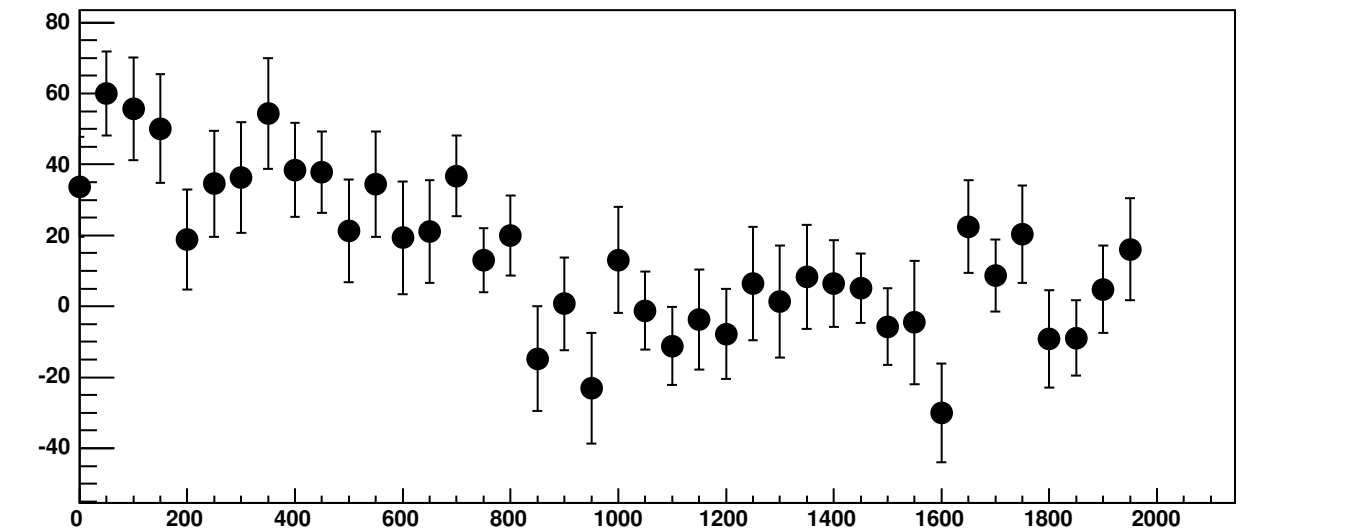
Chip 3, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC

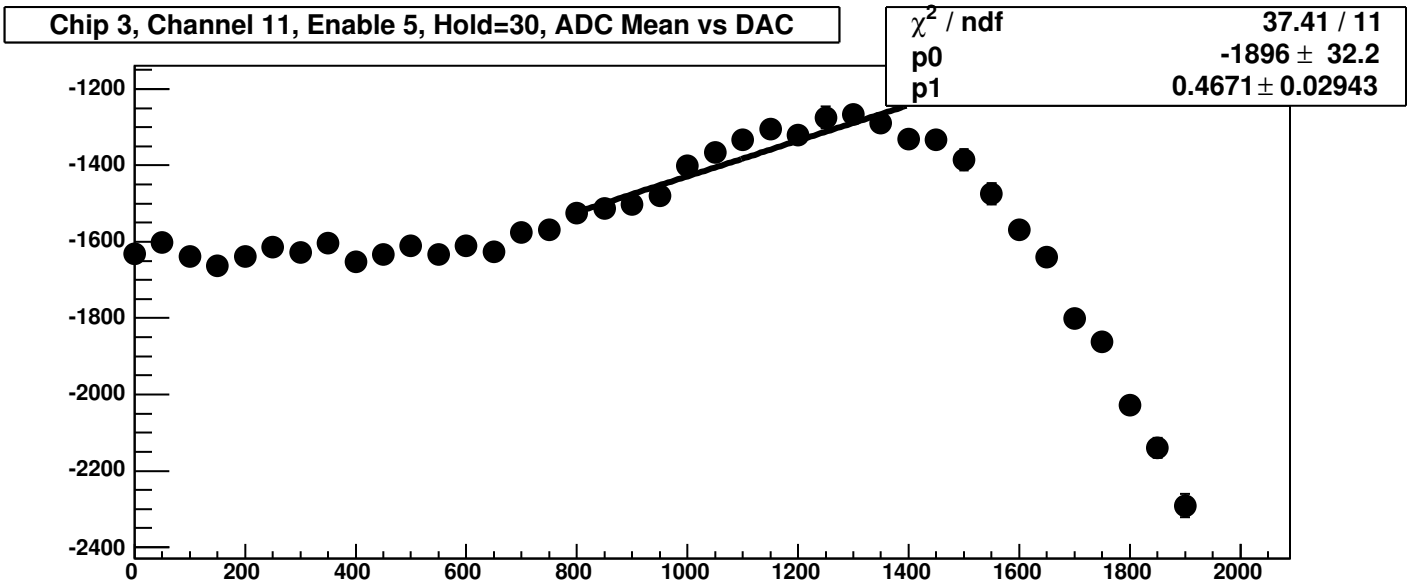


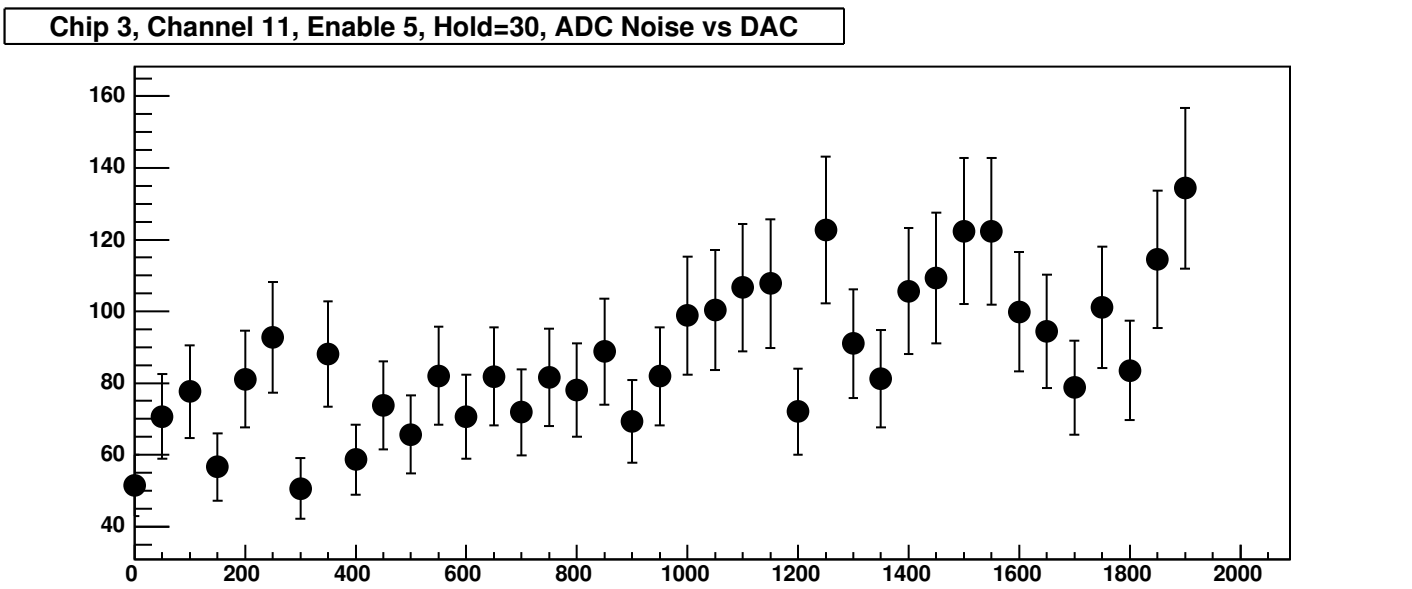
Chip 3, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC

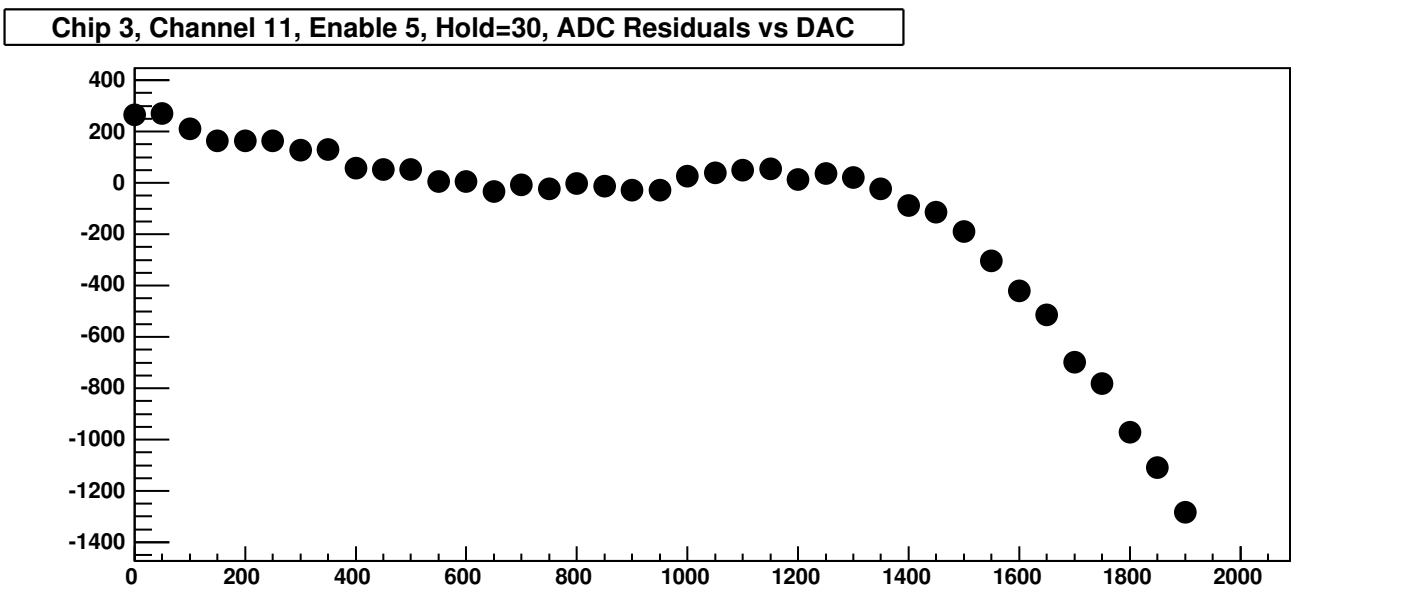


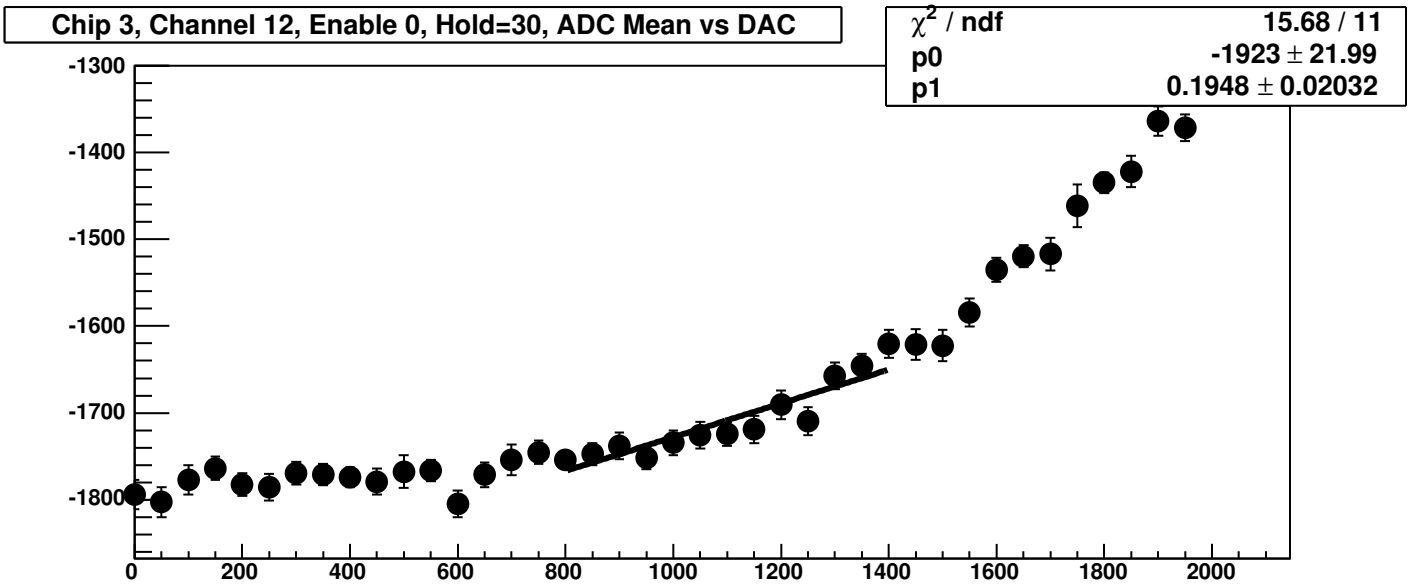
Chip 3, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC

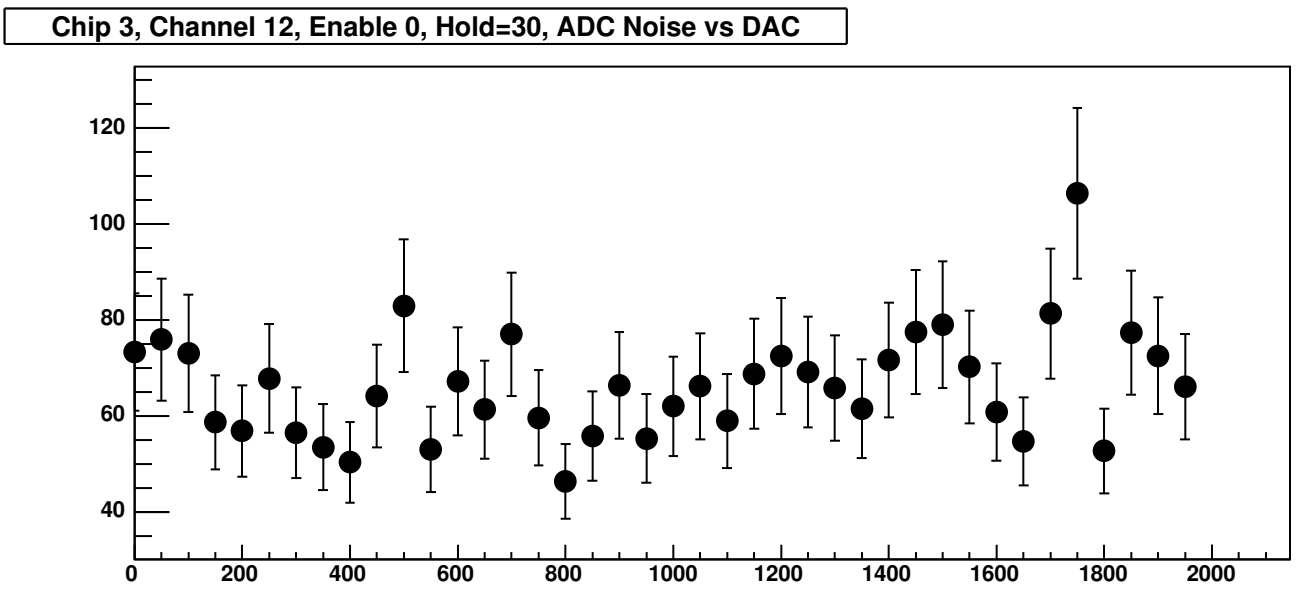


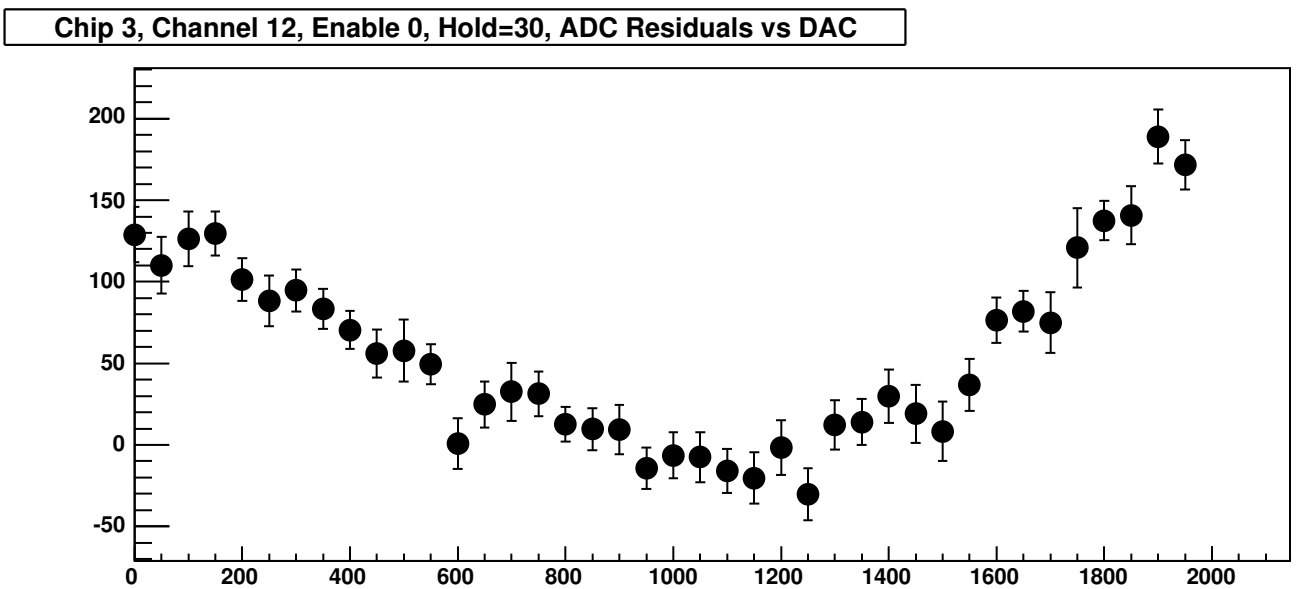

Chip 3, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 37.41 / 11
p0 -1896 ± 32.2
p1 0.4671 ± 0.02943
-1200
-1400
-1600
-1800
-2000
-2200
-2400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

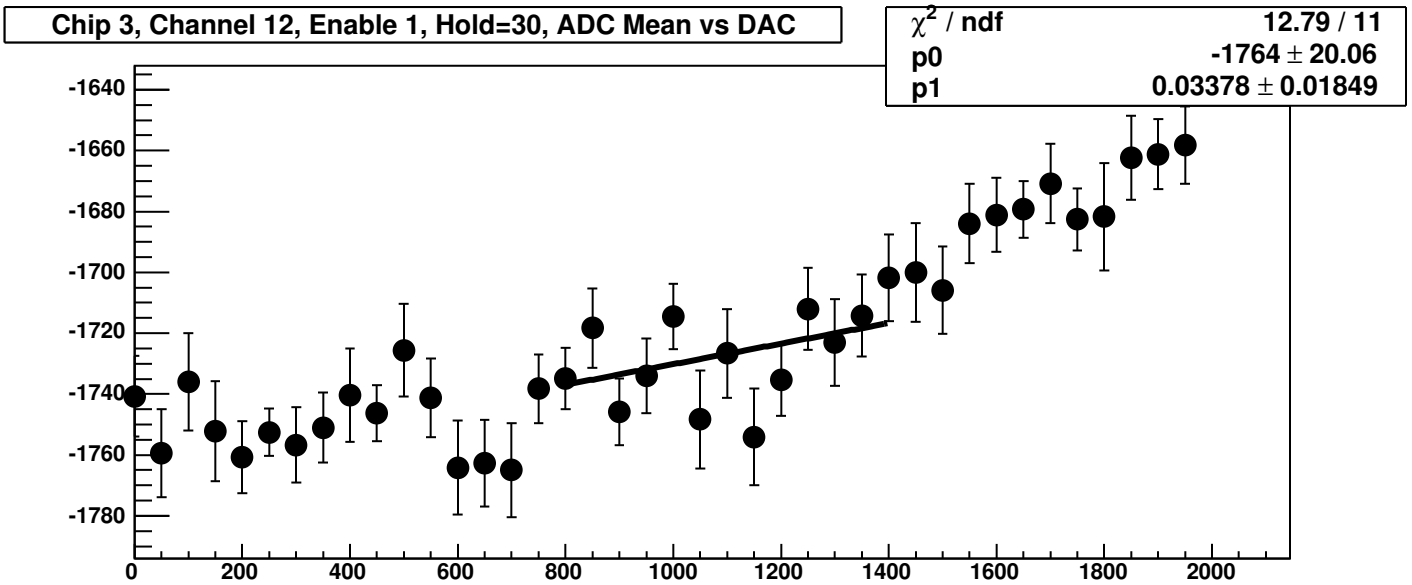

Chip 3, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

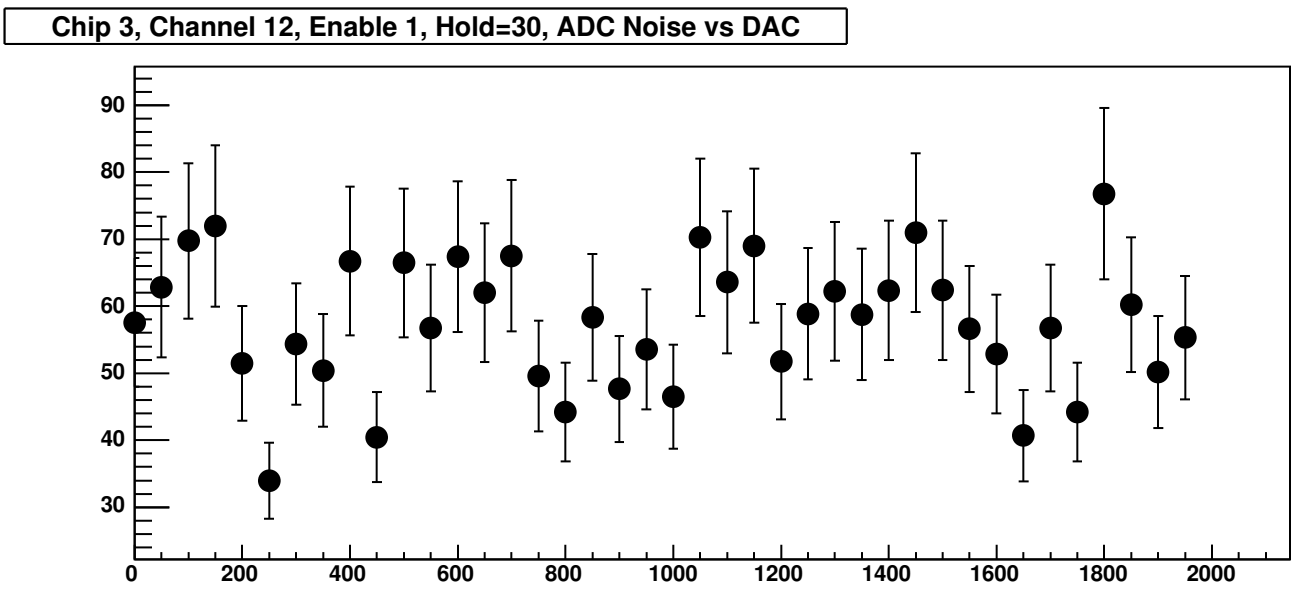

Chip 3, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

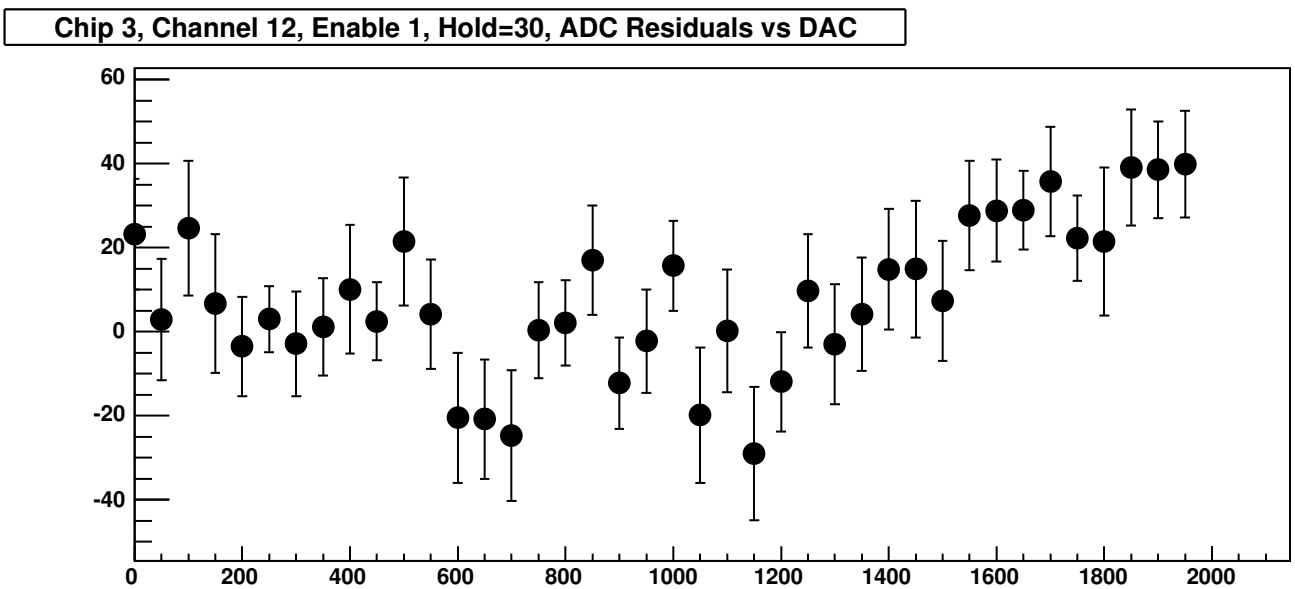

Chip 3, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 15.68 / 11
p0 -1923 ± 21.99
p1 0.1948 ± 0.02032
-1300
-1400
-1500
-1600
-1700
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

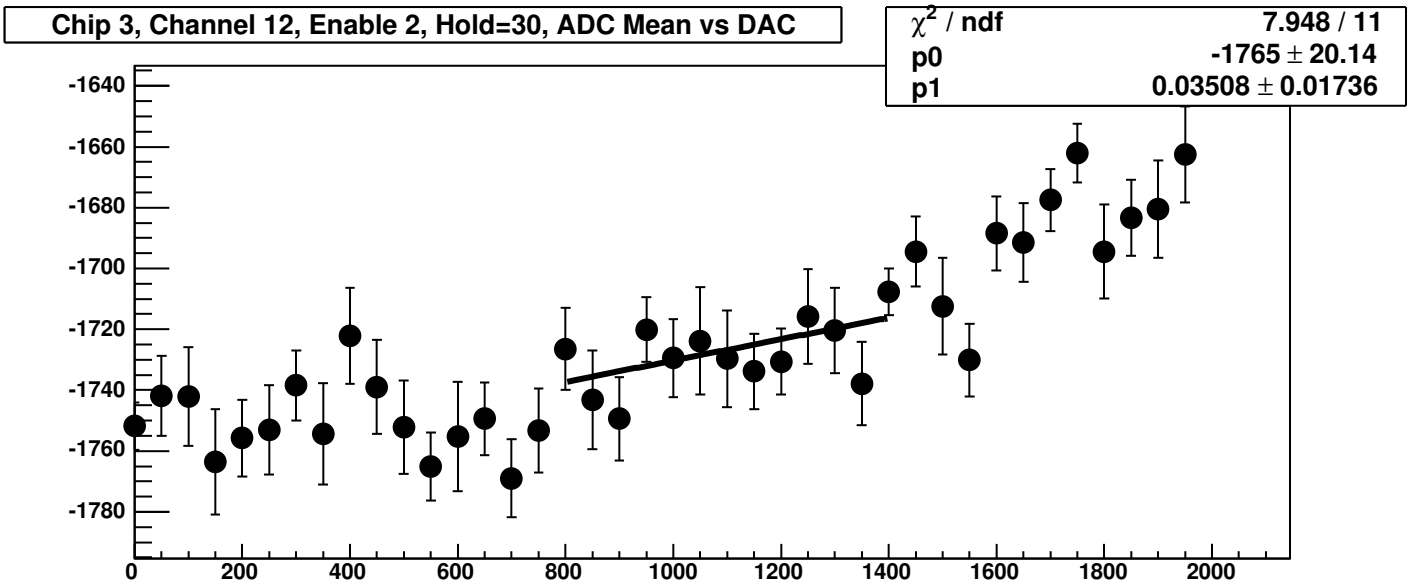

Chip 3, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

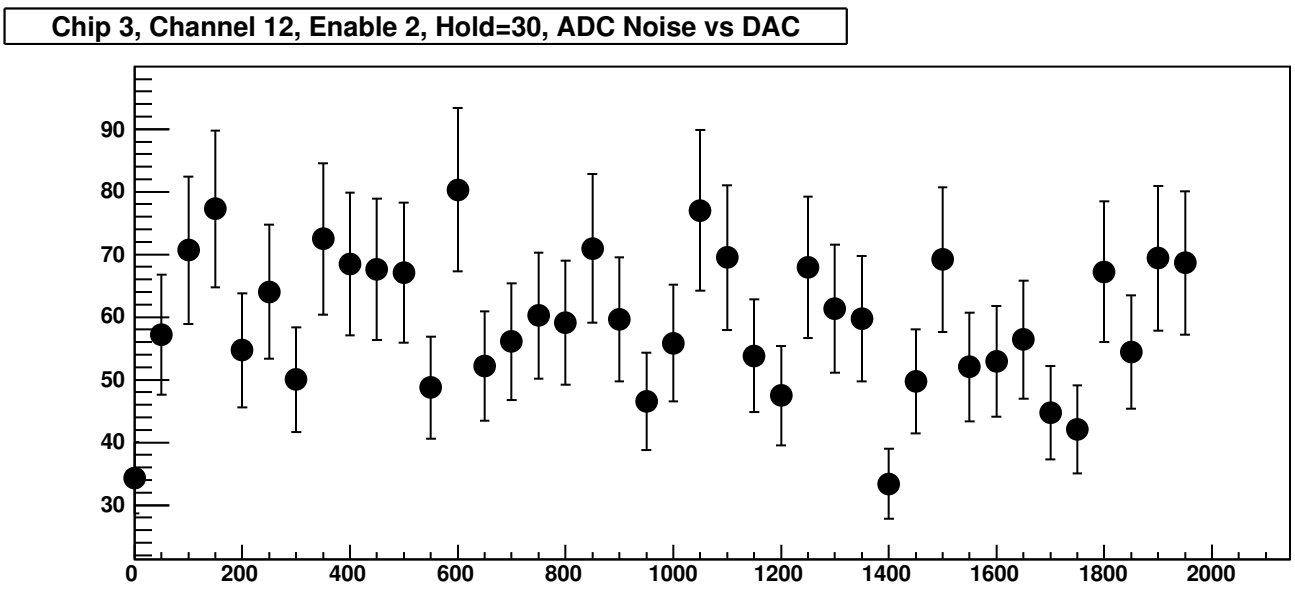

Chip 3, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

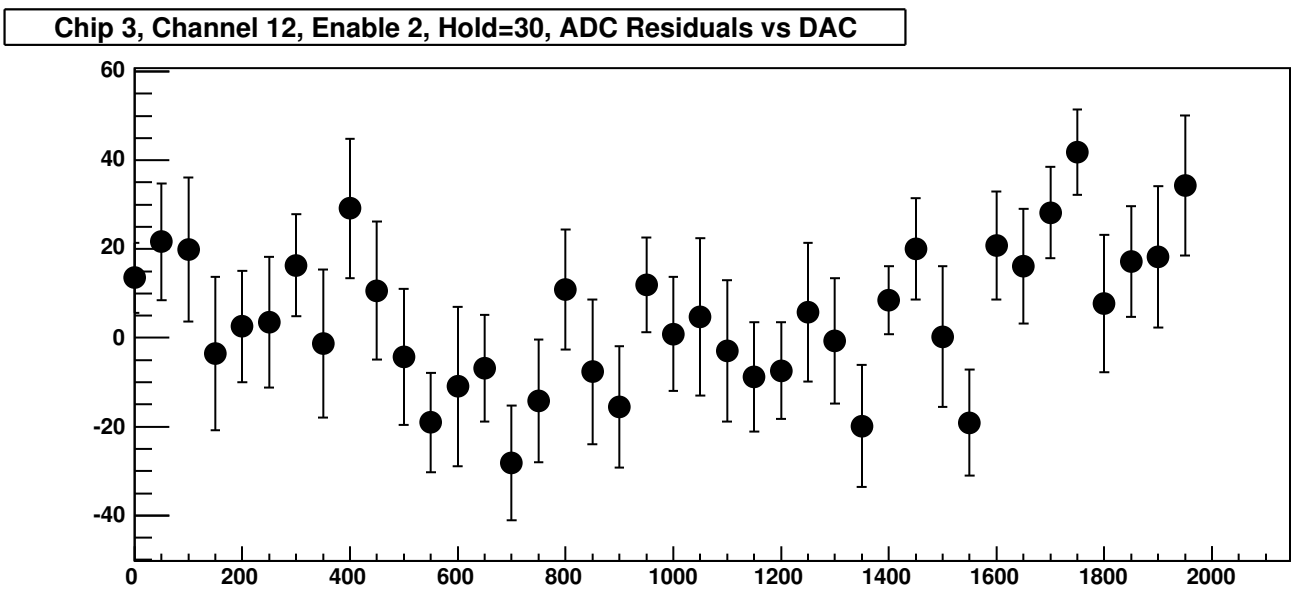

Chip 3, Channel 12, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 12.79 / 11
p0 -1764 ± 20.06
p1 0.03378 ± 0.01849
-1640
-1660
-1680
-1700
-1720
-1740
-1760
-1780
0
200
400
600
800
1000
1200
1400
1600
1800
2000

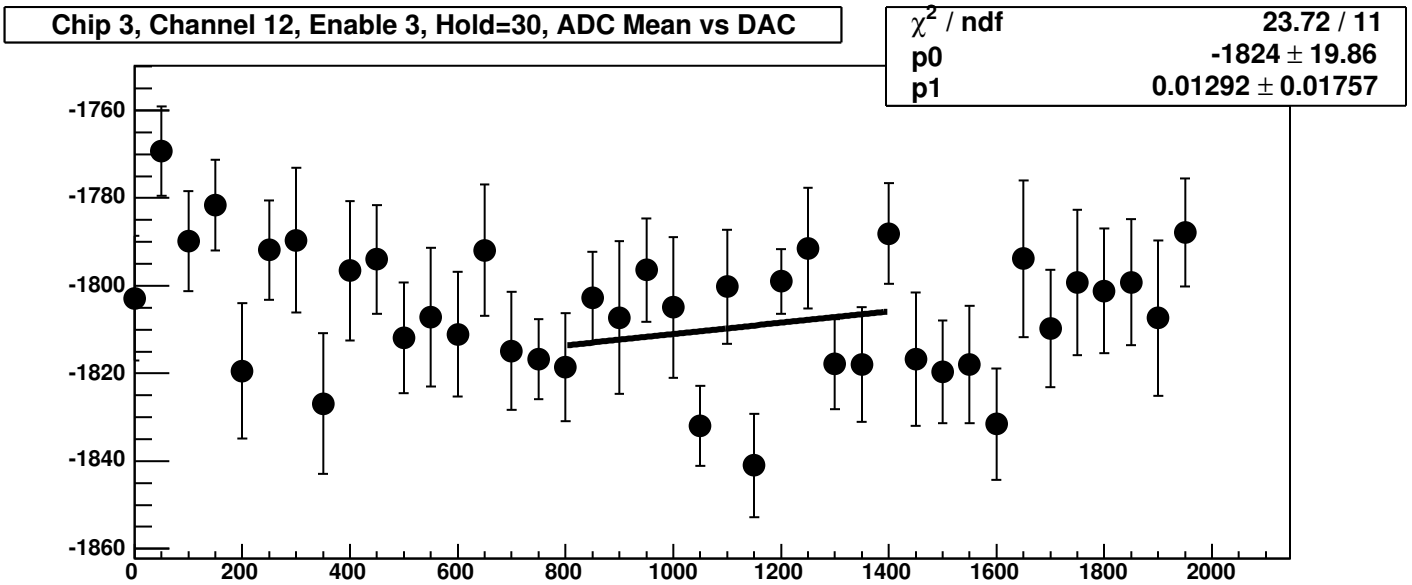

Chip 3, Channel 12, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

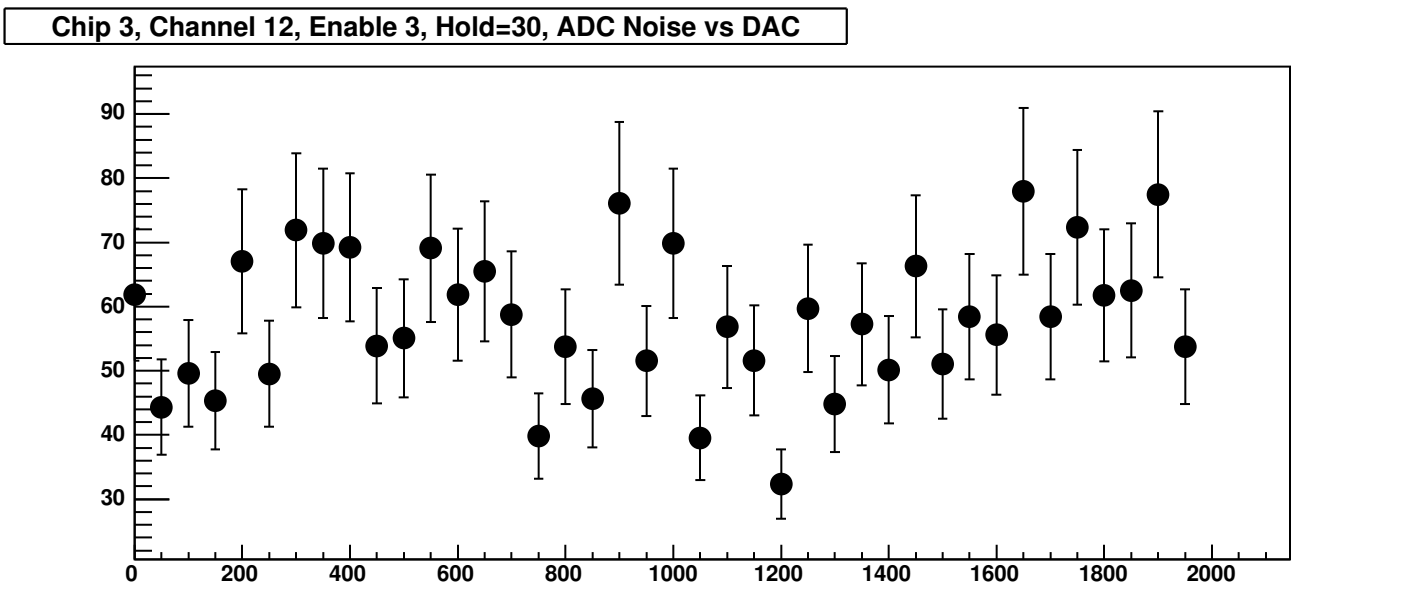

Chip 3, Channel 12, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

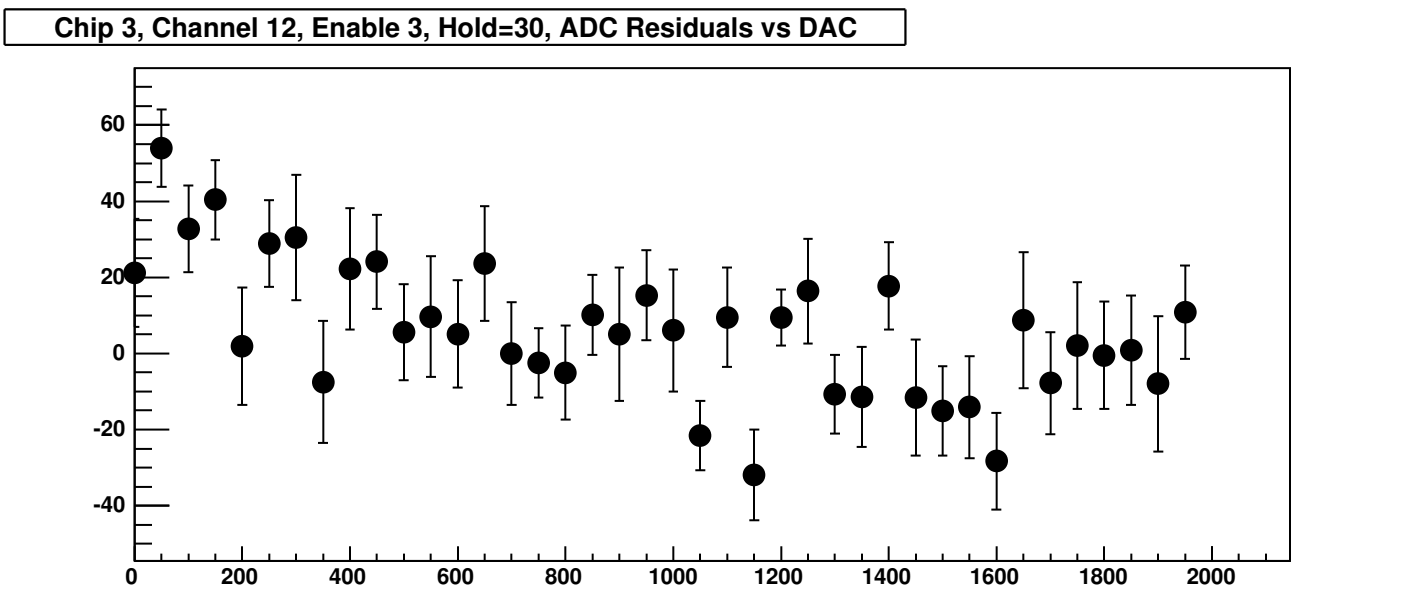

Chip 3, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 7.948 / 11
p0 -1765 ± 20.14
p1 0.03508 ± 0.01736
-1640
-1660
-1680
-1700
-1720
-1740
-1760
-1780
0
200
400
600
800
1000
1200
1400
1600
1800
2000

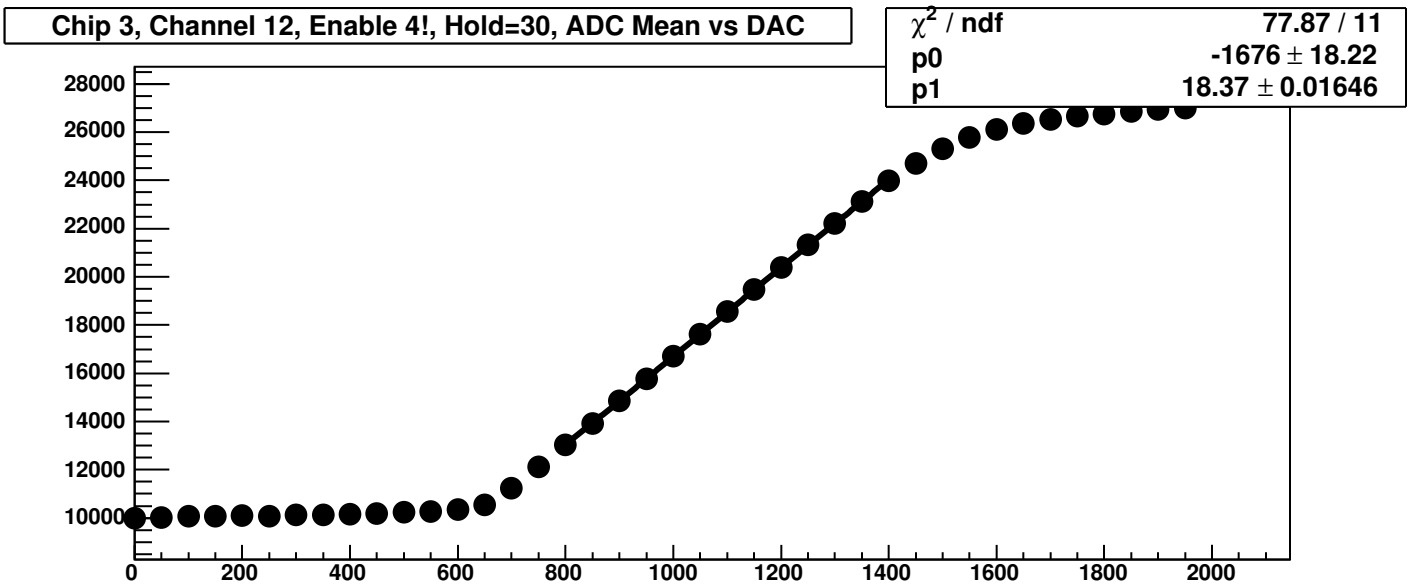

Chip 3, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

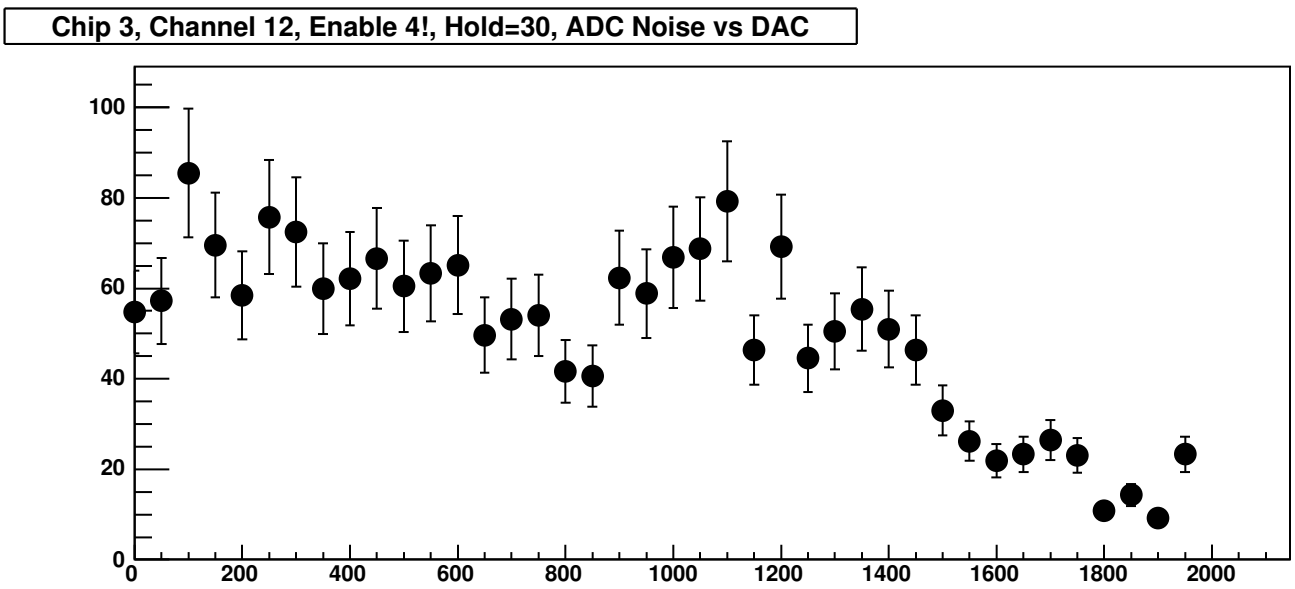

Chip 3, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

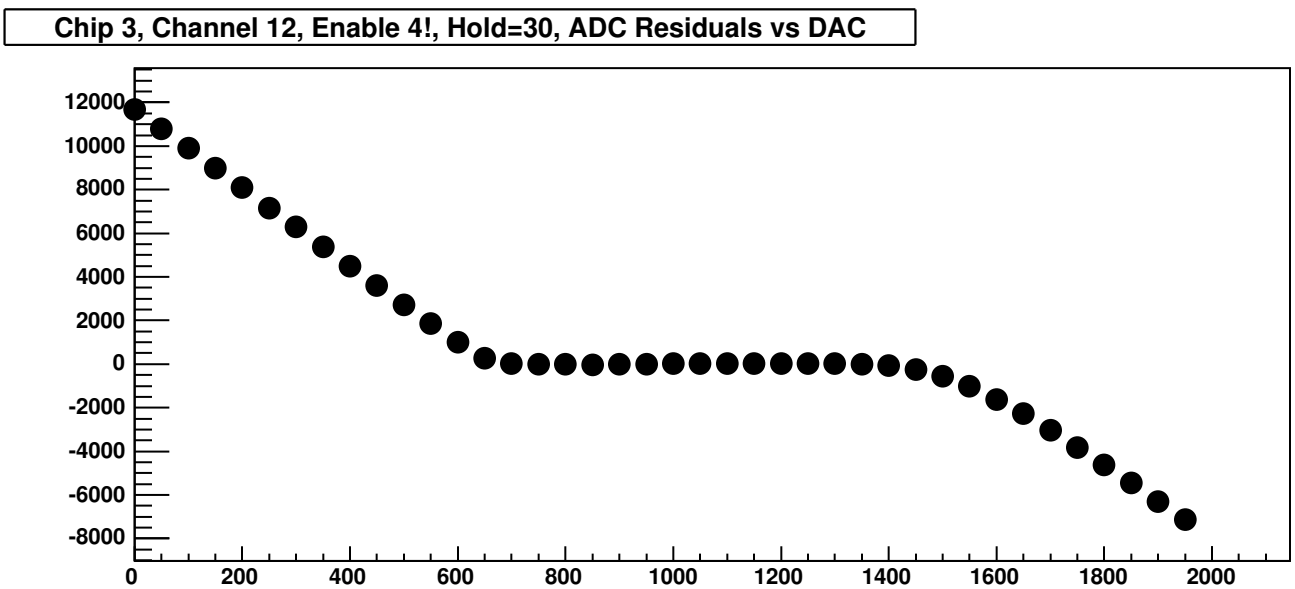

Chip 3, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 23.72 / 11
p0 -1824 ± 19.86
p1 0.01292 ± 0.01757
-1760
-1780
-1800
-1820
-1840
-1860
0
200
400
600
800
1000
1200
1400
1600
1800
2000

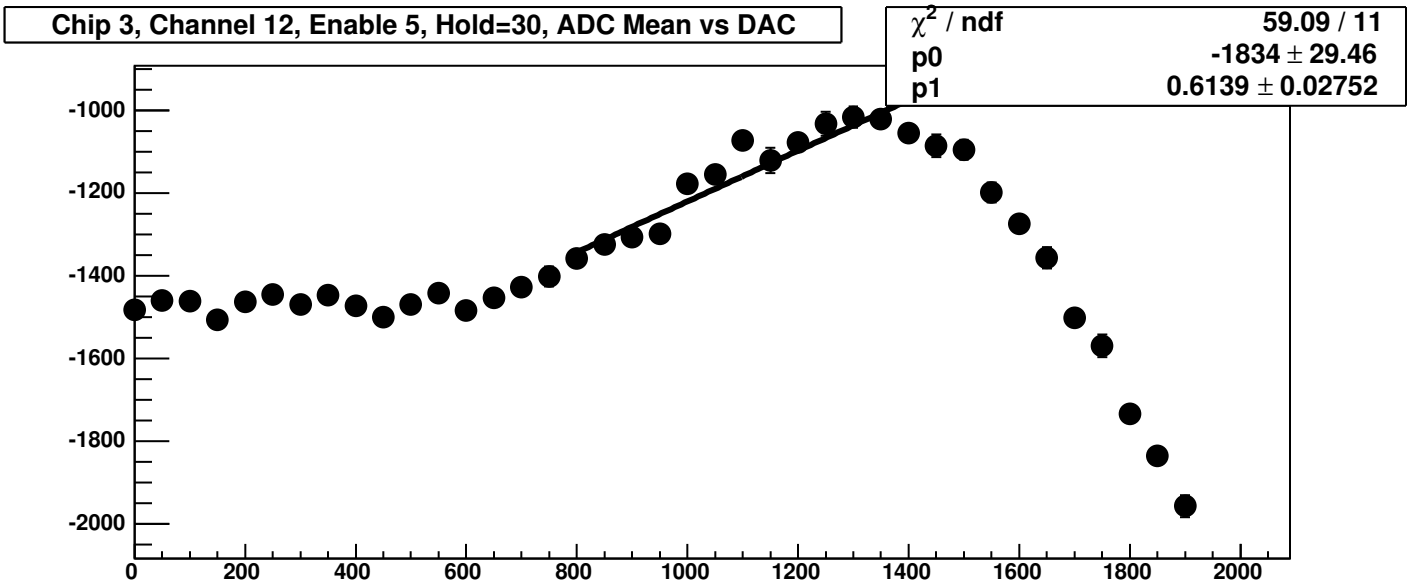

Chip 3, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

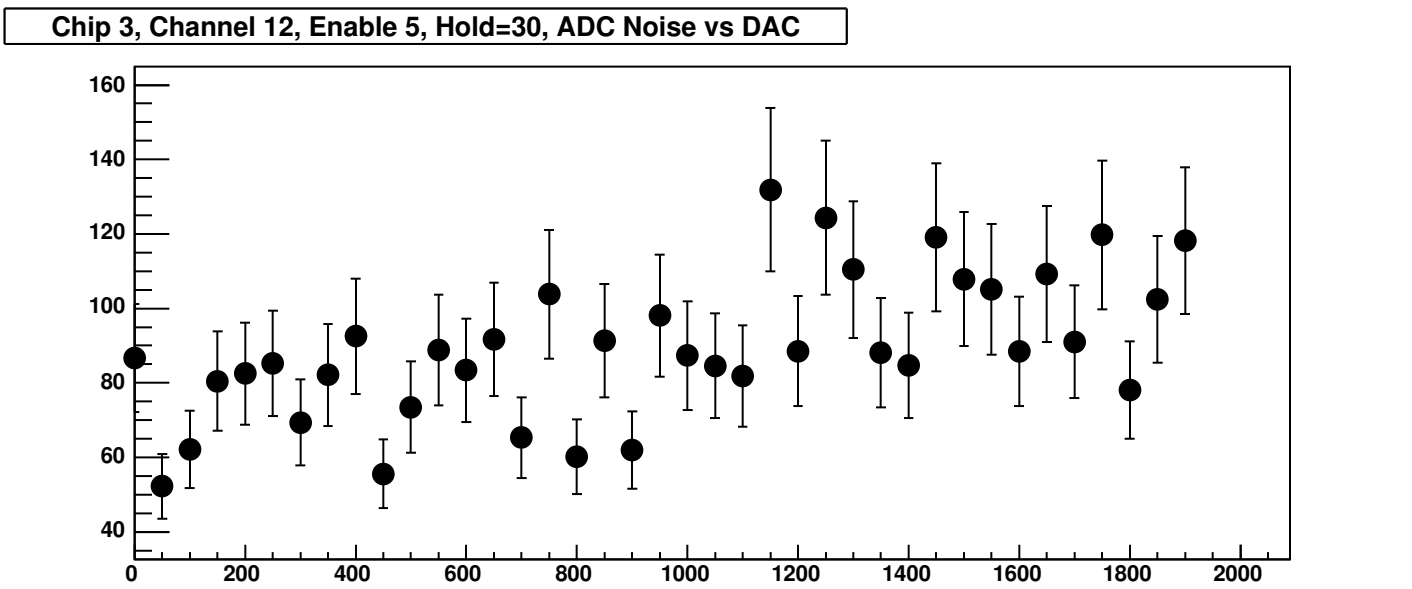

Chip 3, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

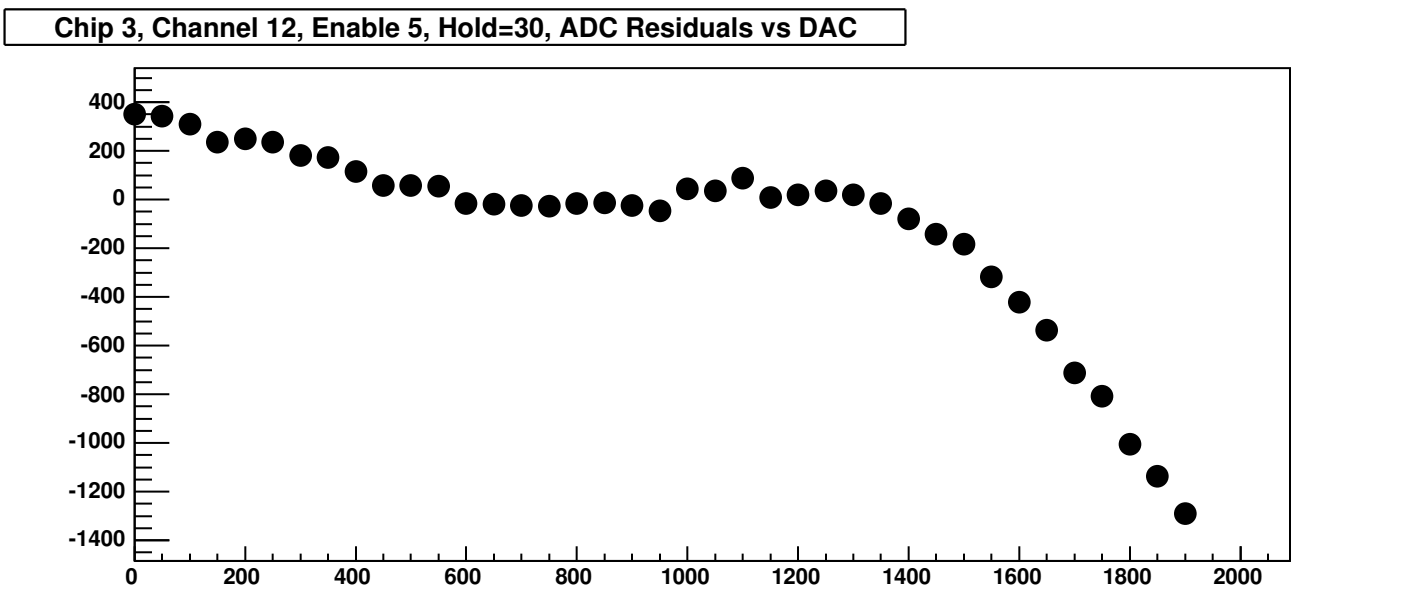

Chip 3, Channel 12, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 77.87 / 11
p0 -1676 ± 18.22
p1 18.37 ± 0.01646
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

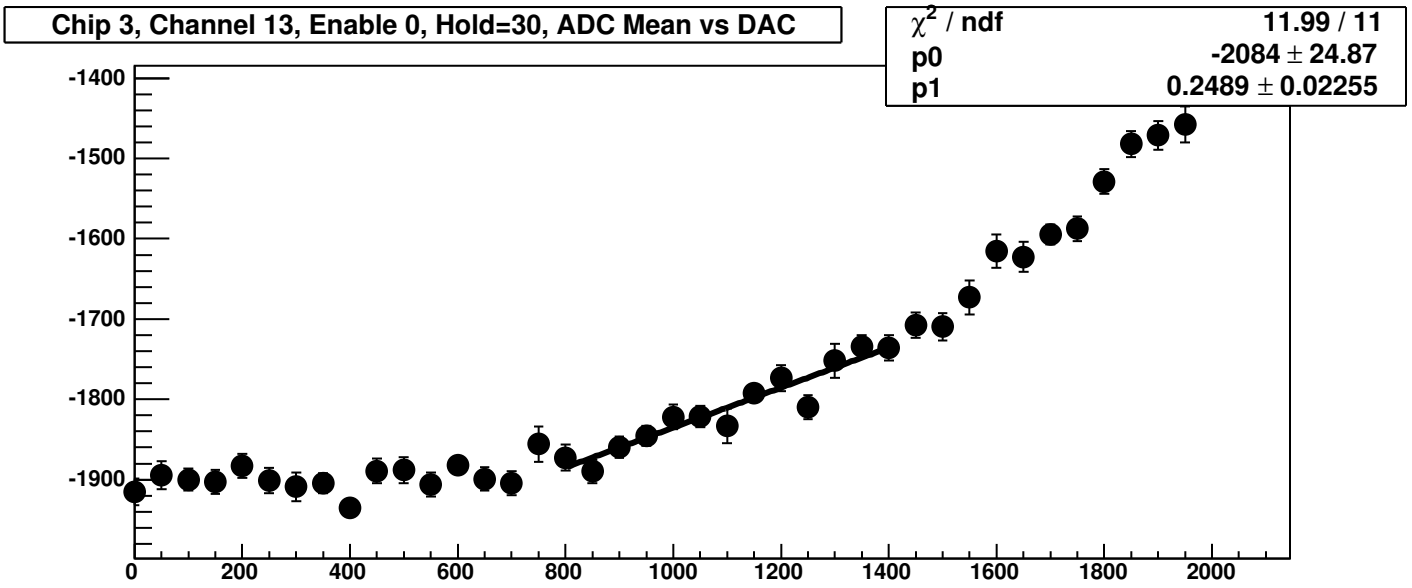

Chip 3, Channel 12, Enable 4!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

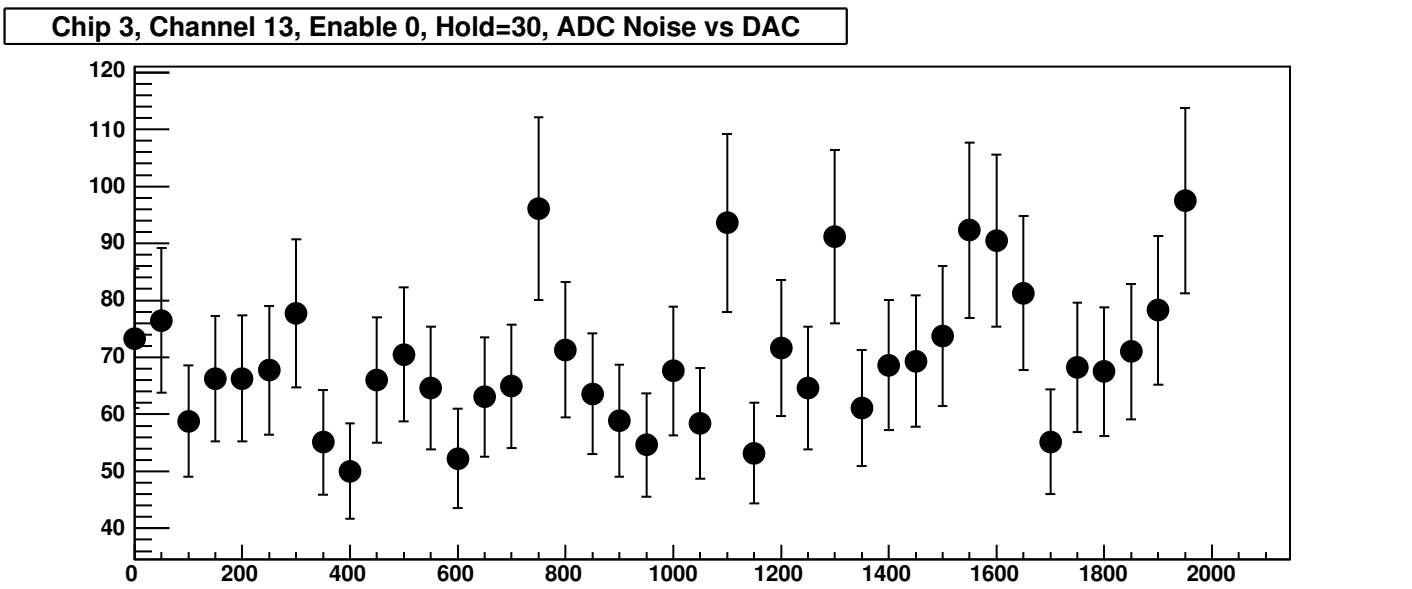

Chip 3, Channel 12, Enable 4!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

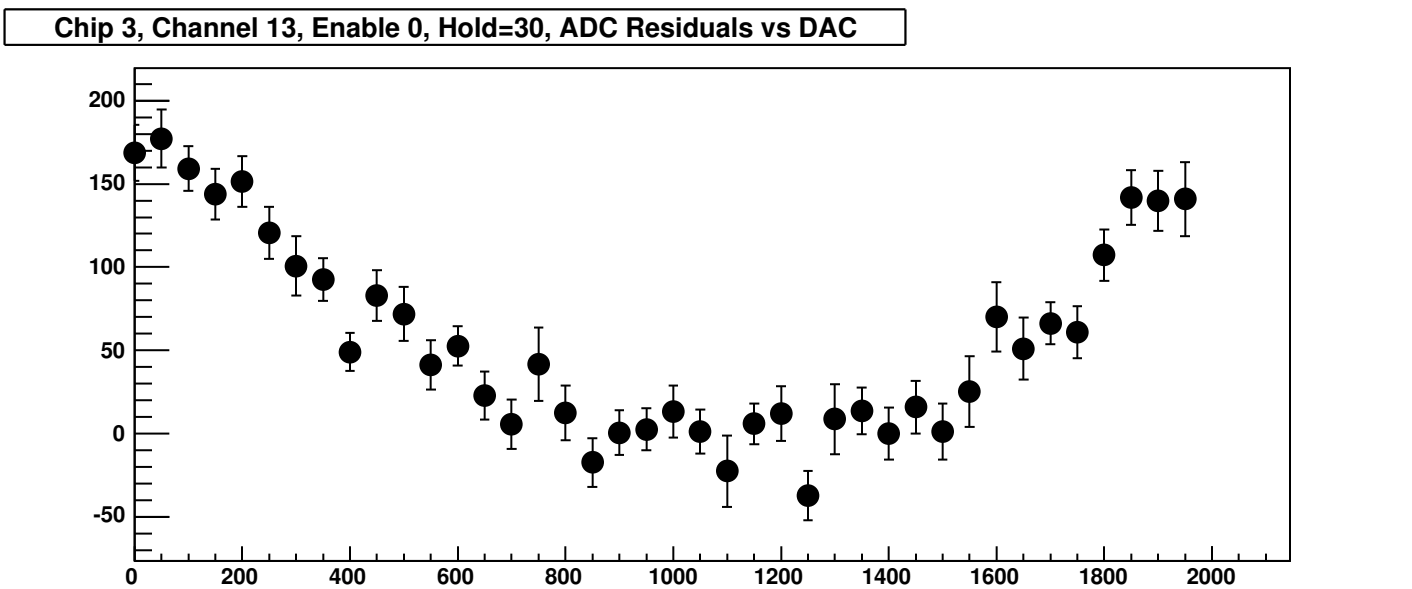

Chip 3, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 59.09 / 11
p0 -1834 ± 29.46
p1 0.6139 ± 0.02752
-1000
-1200
-1400
-1600
-1800
-2000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

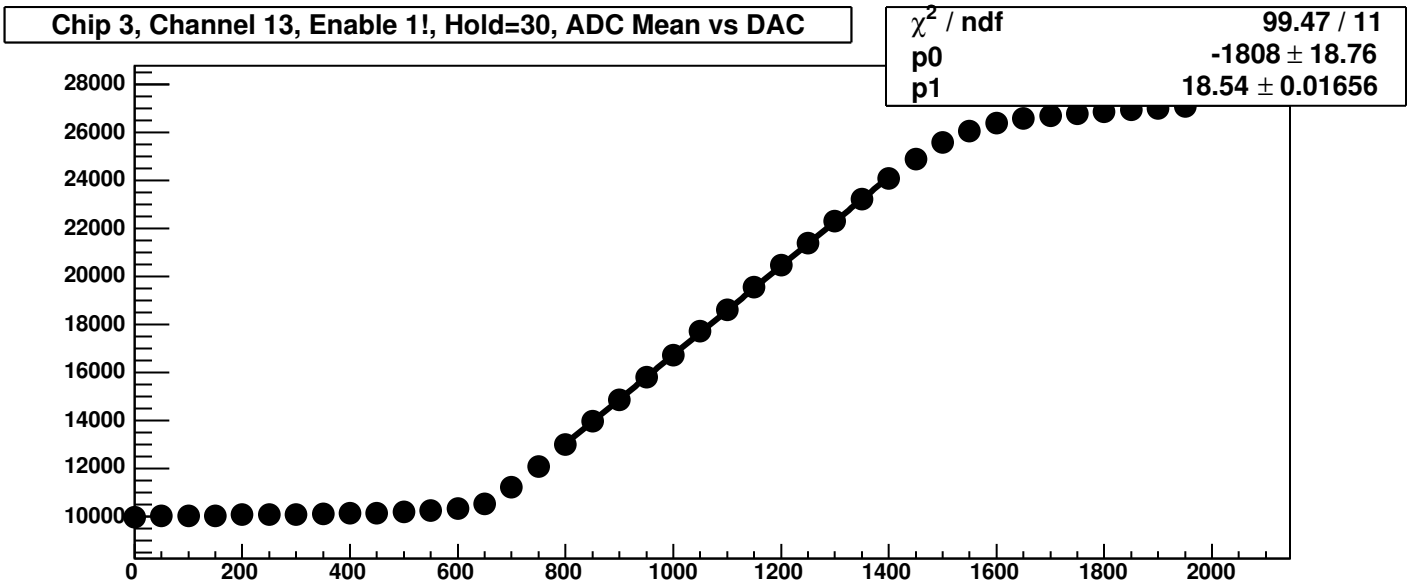

Chip 3, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

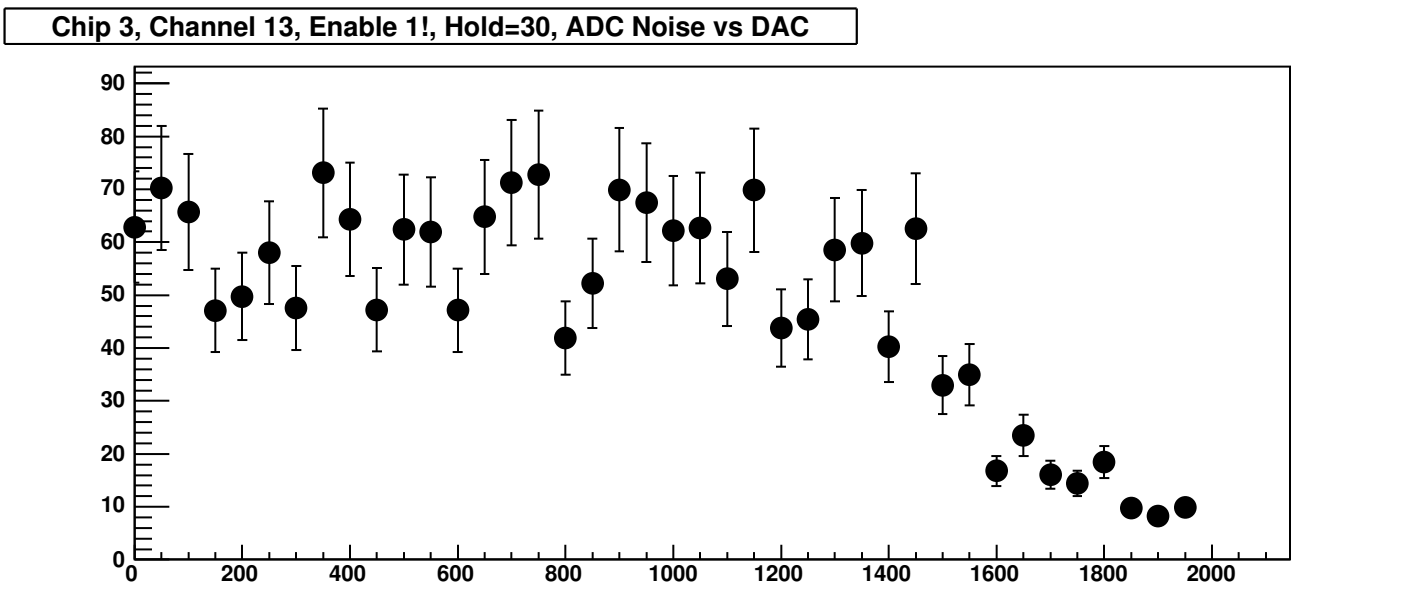

Chip 3, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

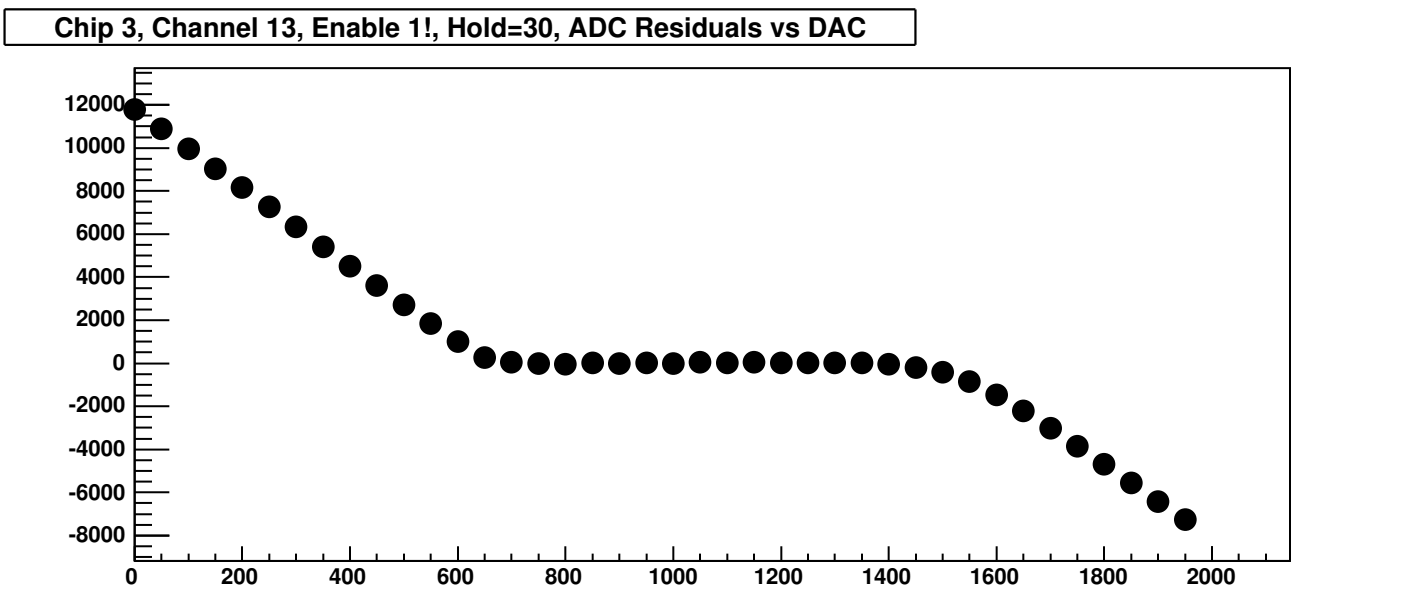

Chip 3, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 11.99 / 11
p0 -2084 ± 24.87
p1 0.2489 ± 0.02255
-1400
-1500
-1600
-1700
-1800
-1900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

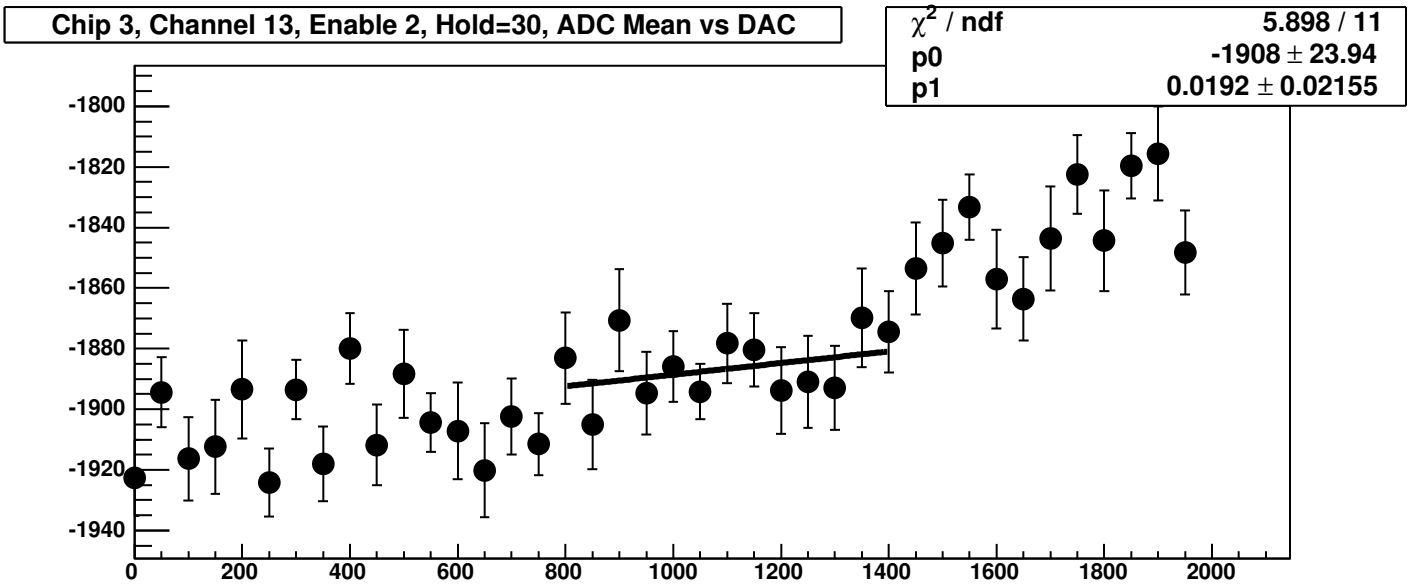

Chip 3, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
120
110
100
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

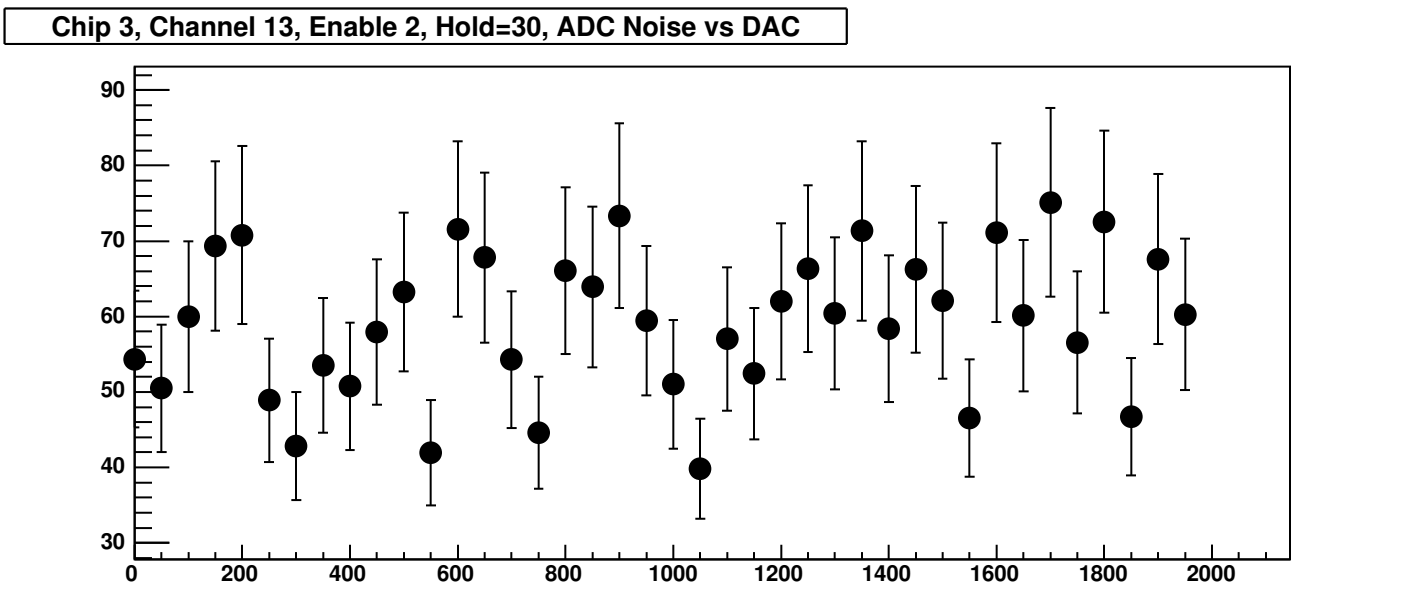

Chip 3, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

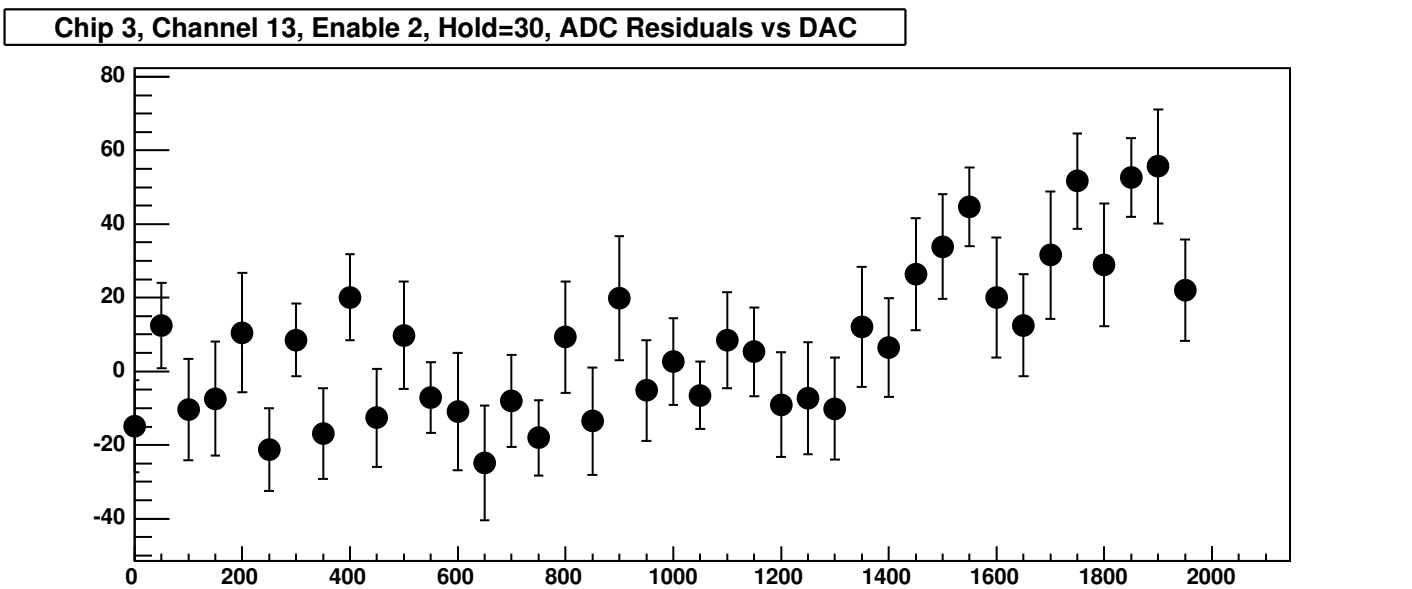

Chip 3, Channel 13, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 99.47 / 11
p0 -1808 ± 18.76
p1 18.54 ± 0.01656
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

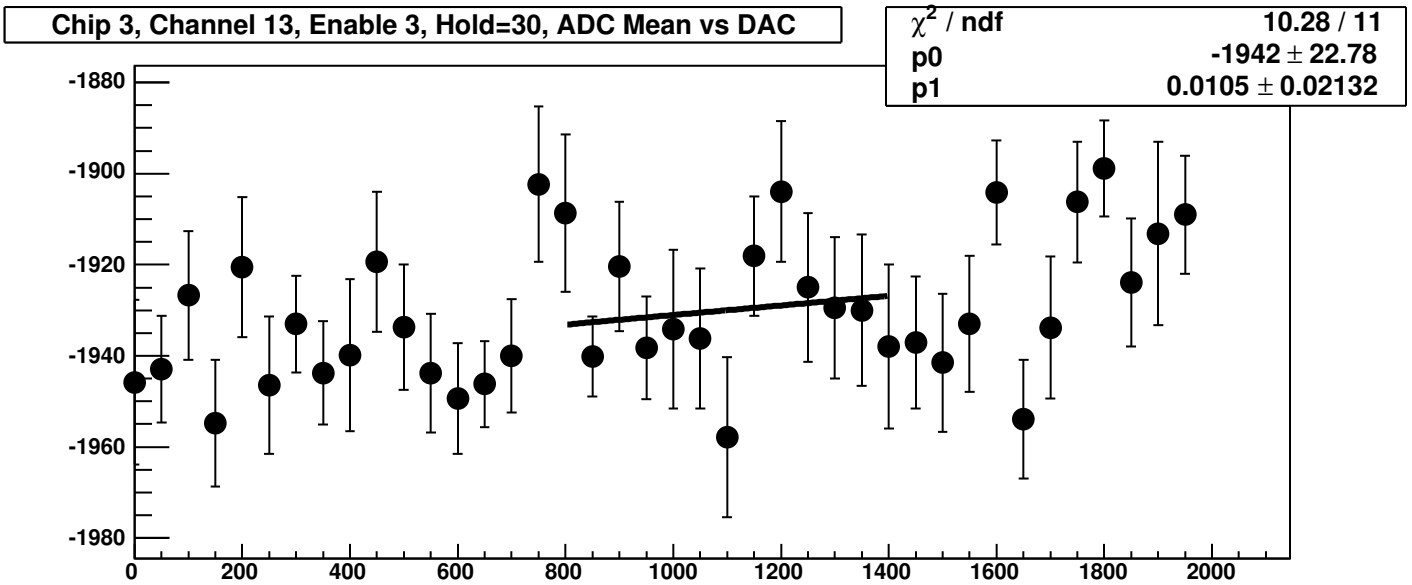

Chip 3, Channel 13, Enable 1!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

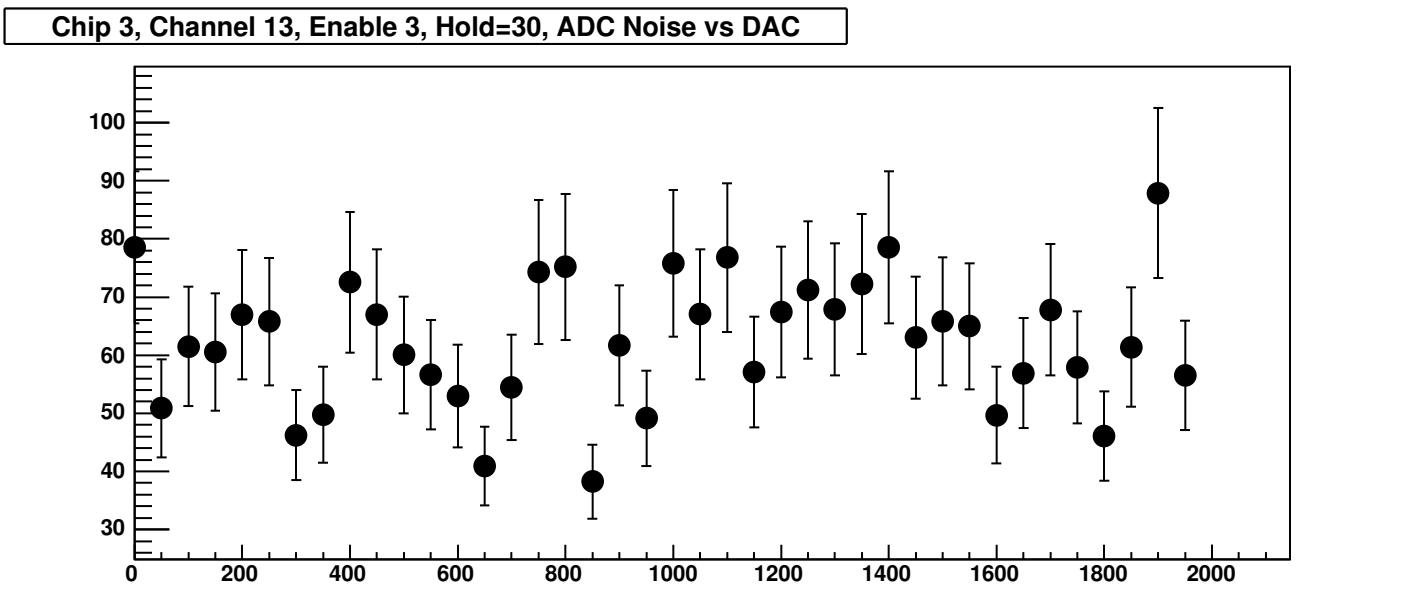

Chip 3, Channel 13, Enable 1!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

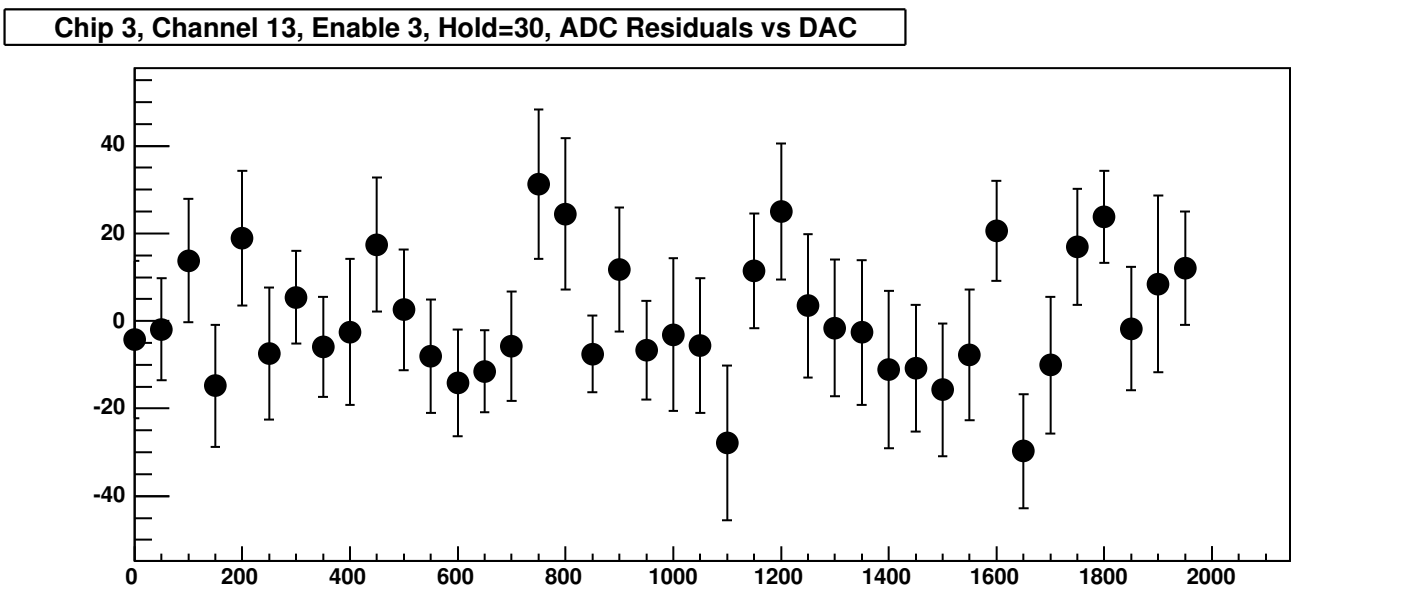

Chip 3, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 5.898 / 11
p0 -1908 ± 23.94
p1 0.0192 ± 0.02155
-1800
-1820
-1840
-1860
-1880
-1900
-1920
-1940
0
200
400
600
800
1000
1200
1400
1600
1800
2000

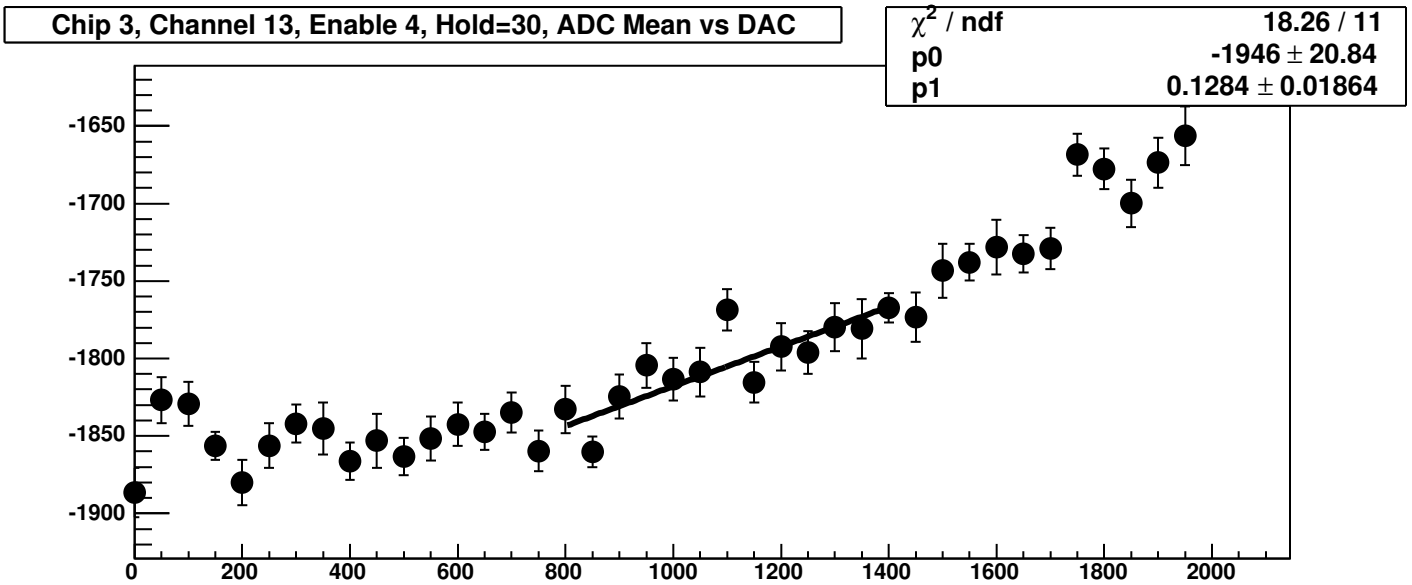

Chip 3, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

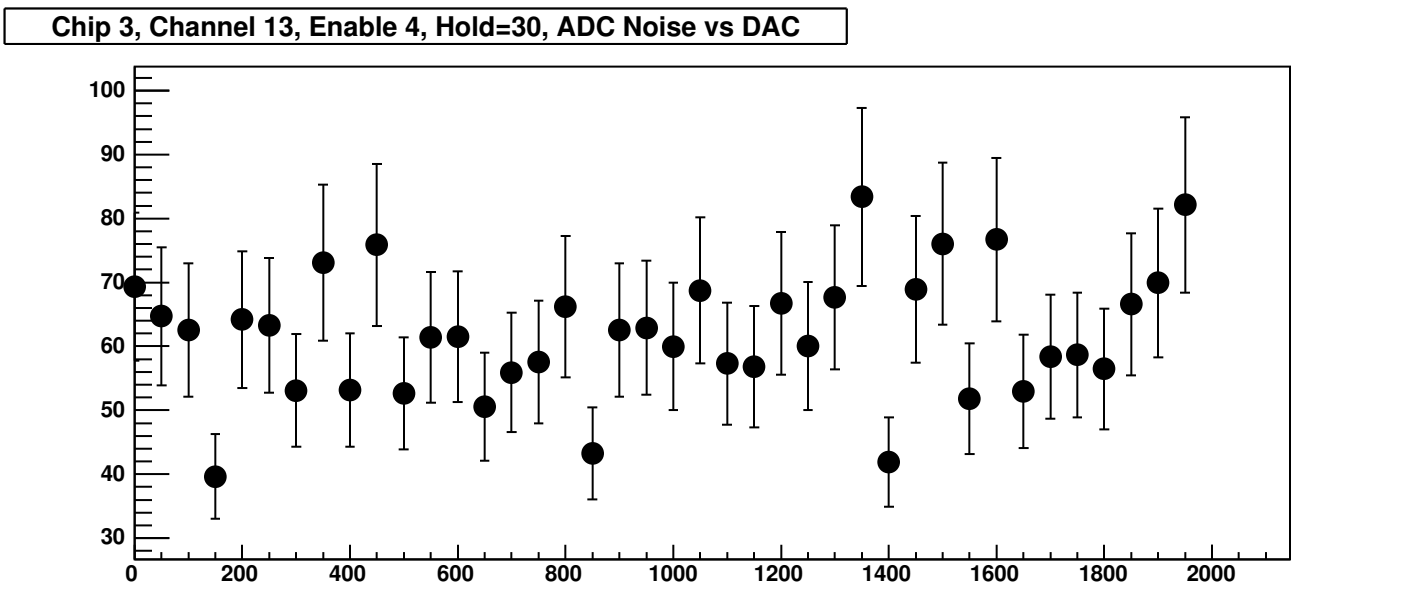

Chip 3, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

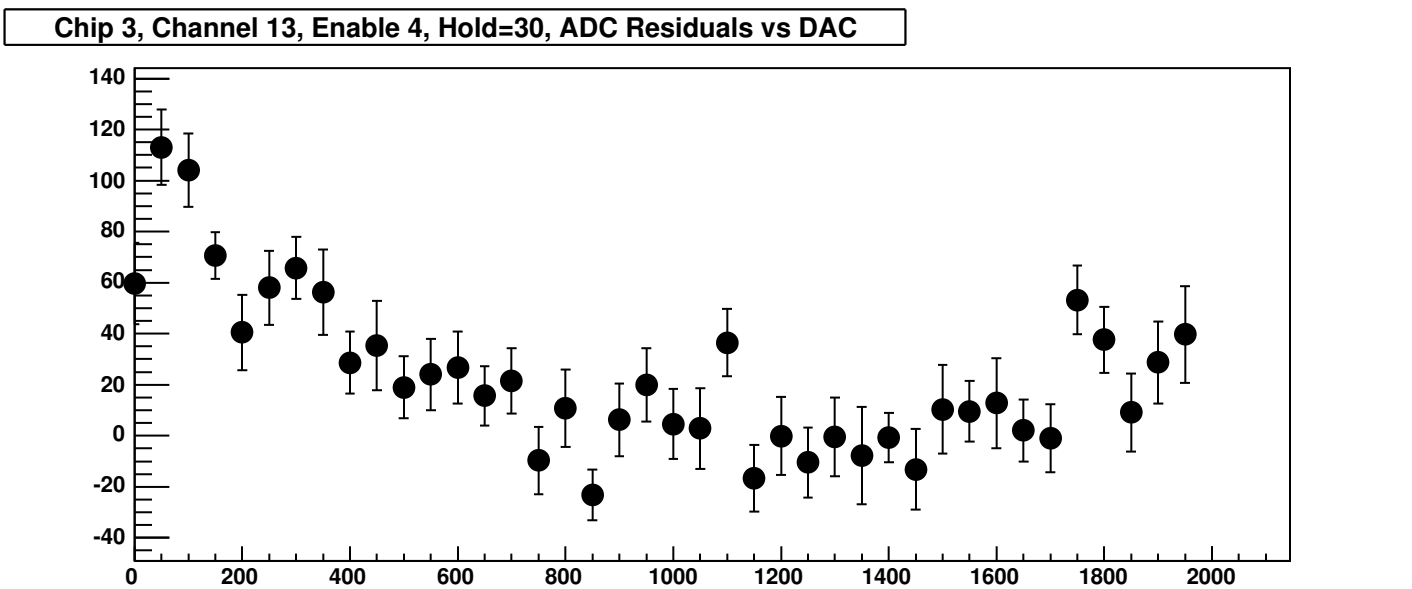

Chip 3, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 10.28 / 11
p0 -1942 ± 22.78
p1 0.0105 ± 0.02132
-1880
-1900
-1920
-1940
-1960
-1980
0
200
400
600
800
1000
1200
1400
1600
1800
2000

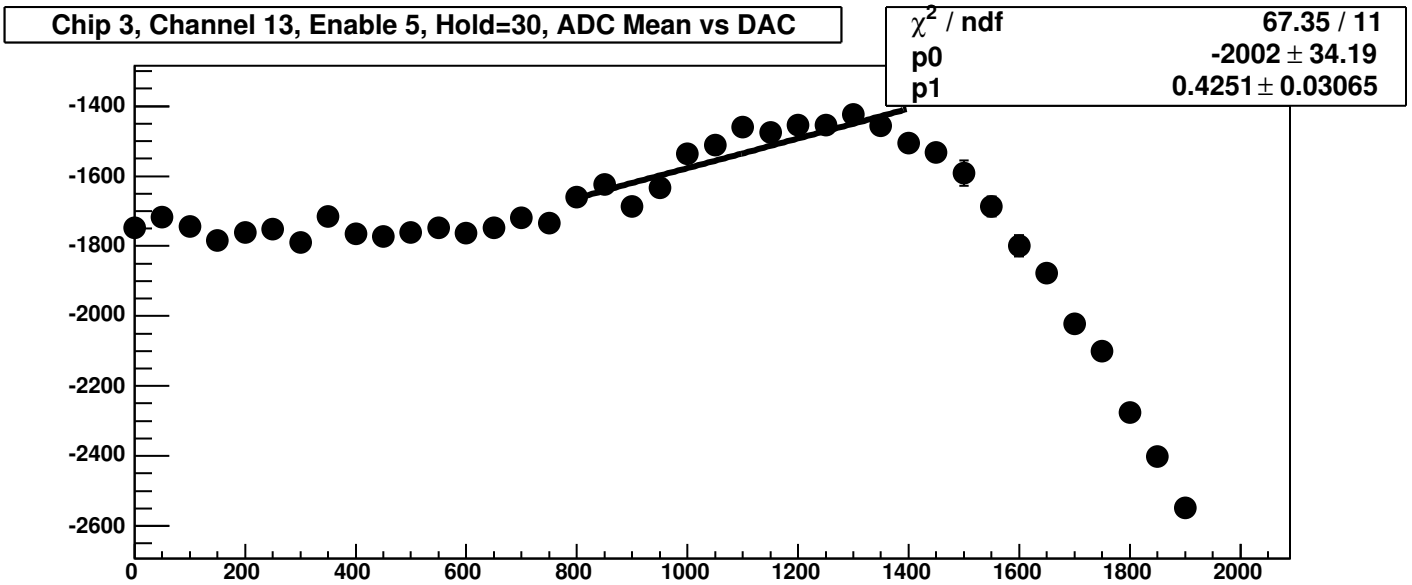

Chip 3, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

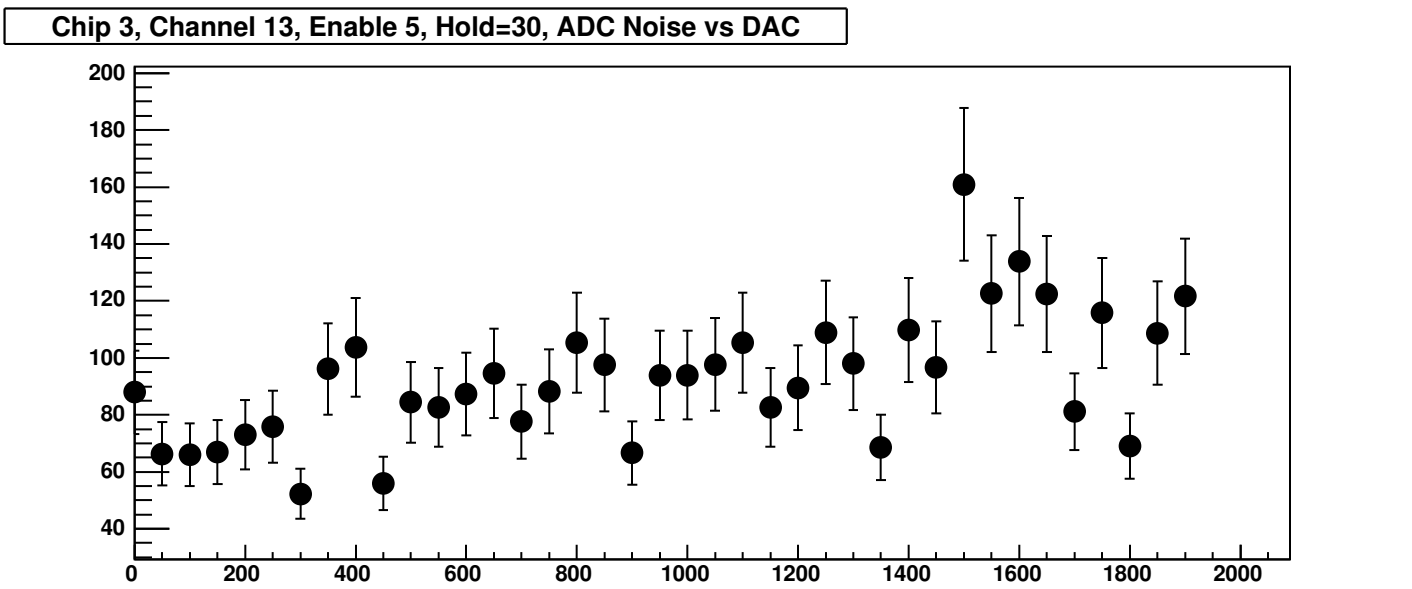

Chip 3, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

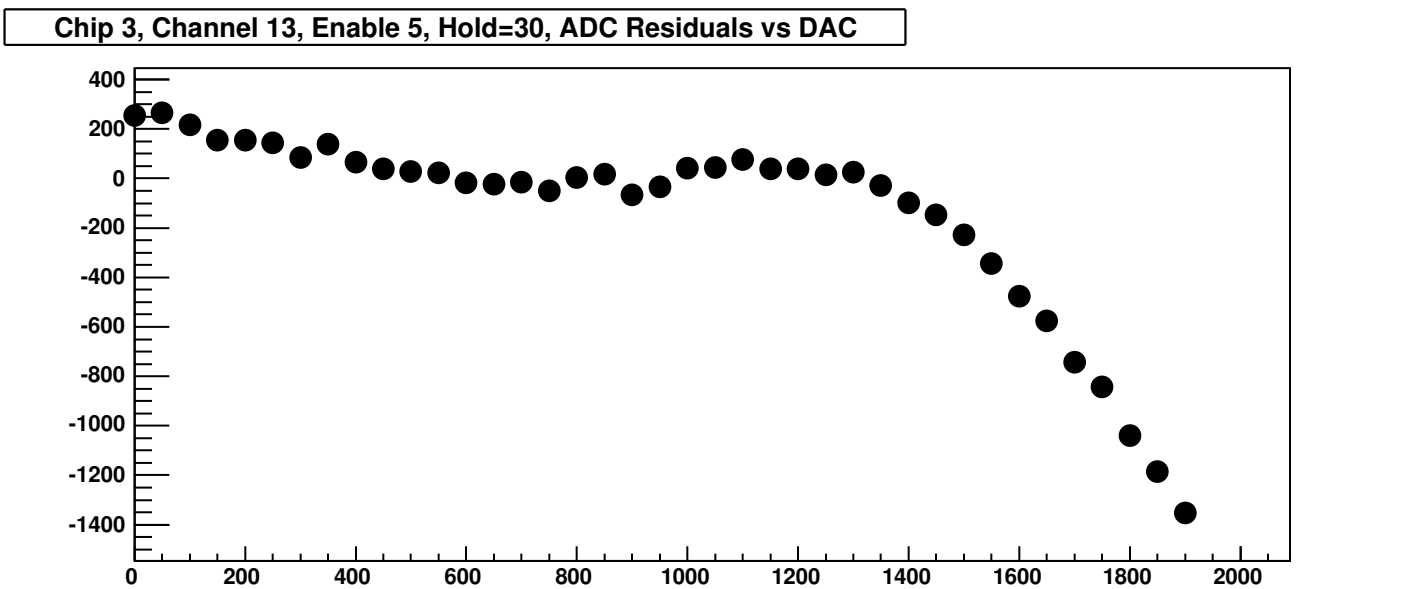

Chip 3, Channel 13, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 18.26 / 11
p0 -1946 ± 20.84
p1 0.1284 ± 0.01864
-1650
-1700
-1750
-1800
-1850
-1900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

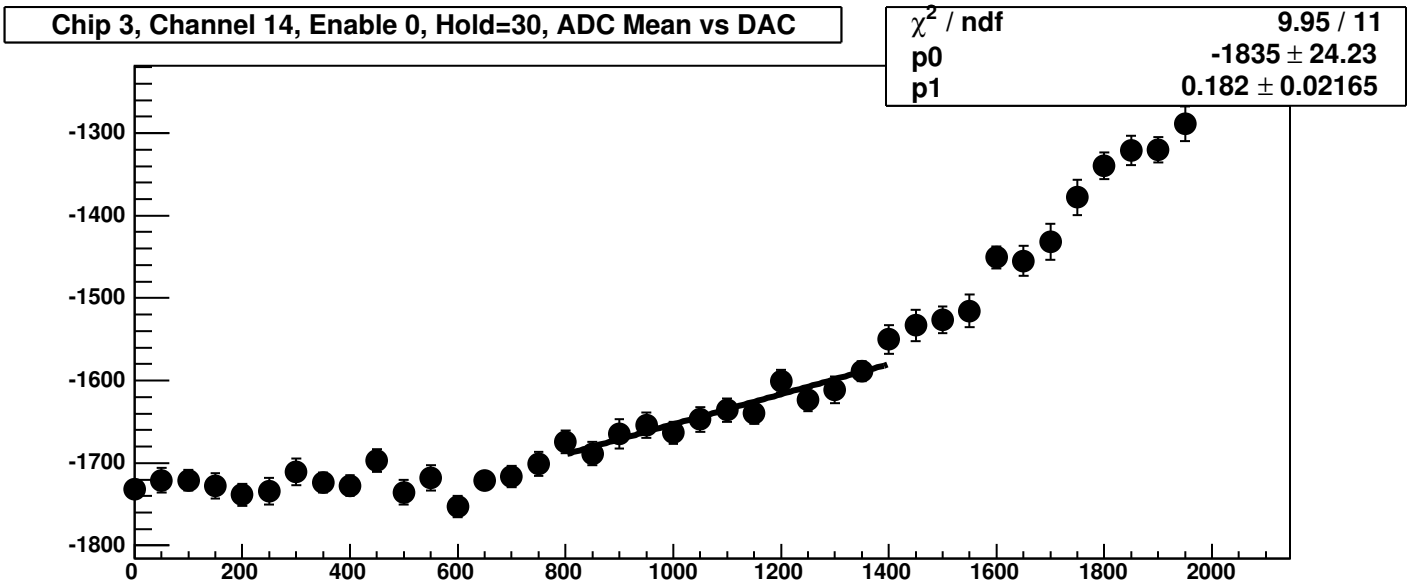

Chip 3, Channel 13, Enable 4, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

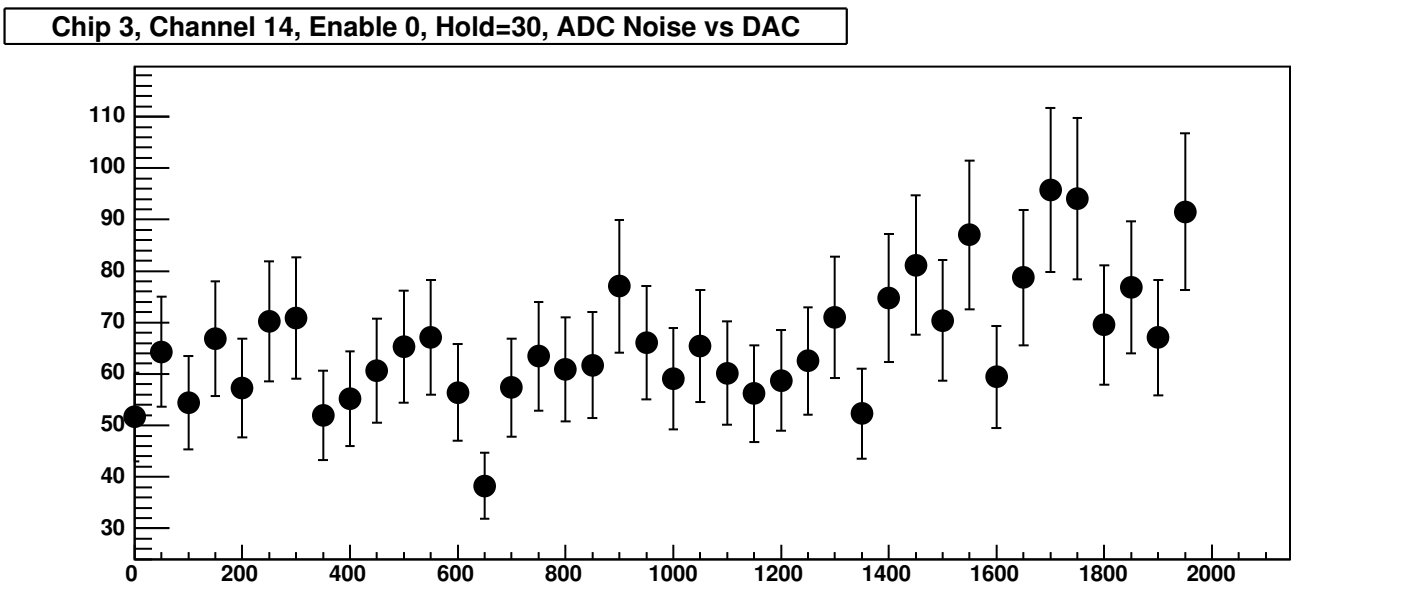

Chip 3, Channel 13, Enable 4, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

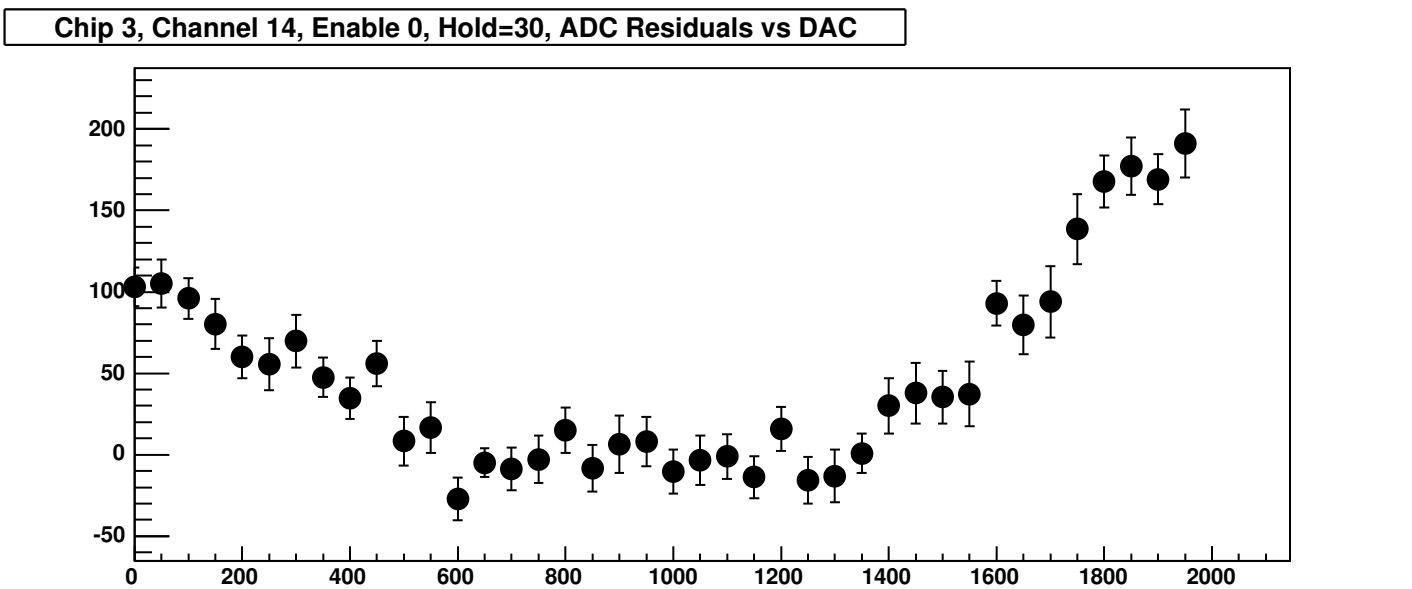

Chip 3, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 67.35 / 11
p0 -2002 ± 34.19
p1 0.4251 ± 0.03065
-1400
-1600
-1800
-2000
-2200
-2400
-2600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

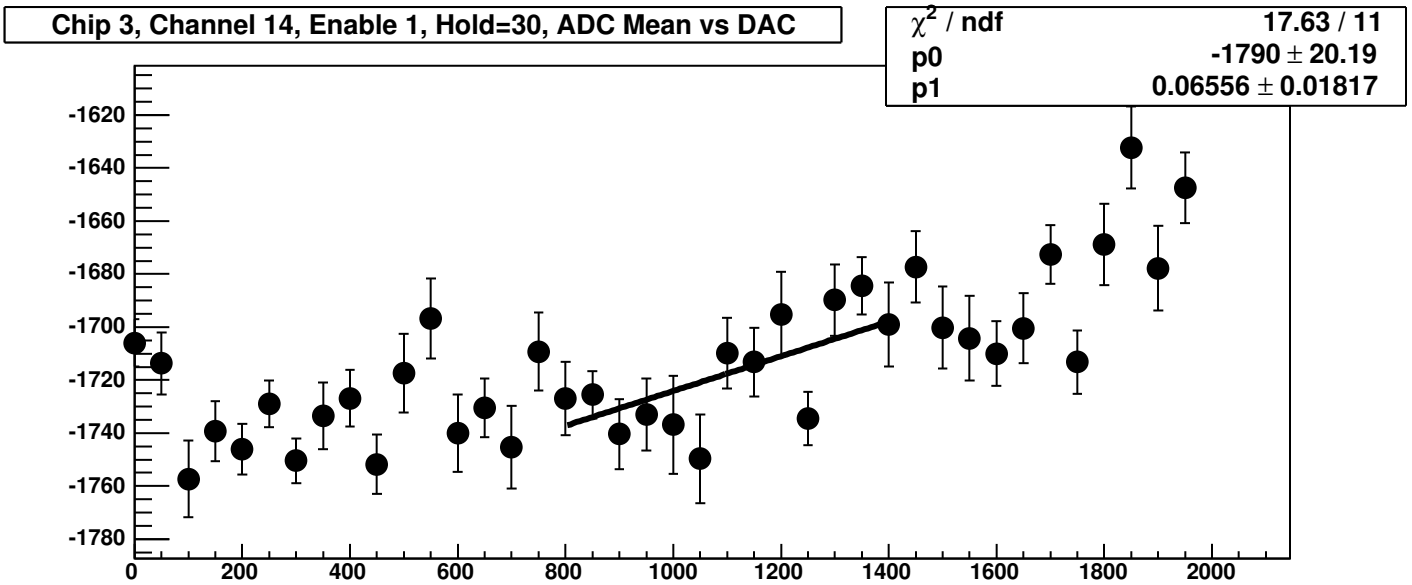

Chip 3, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

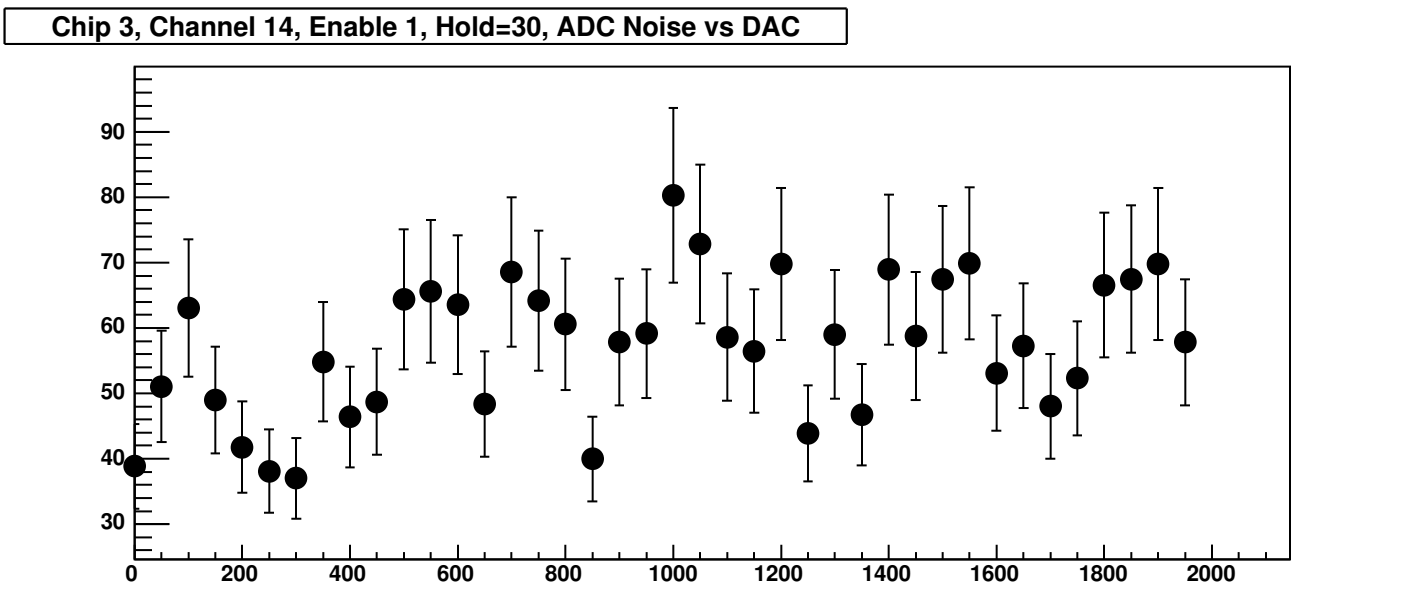

Chip 3, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

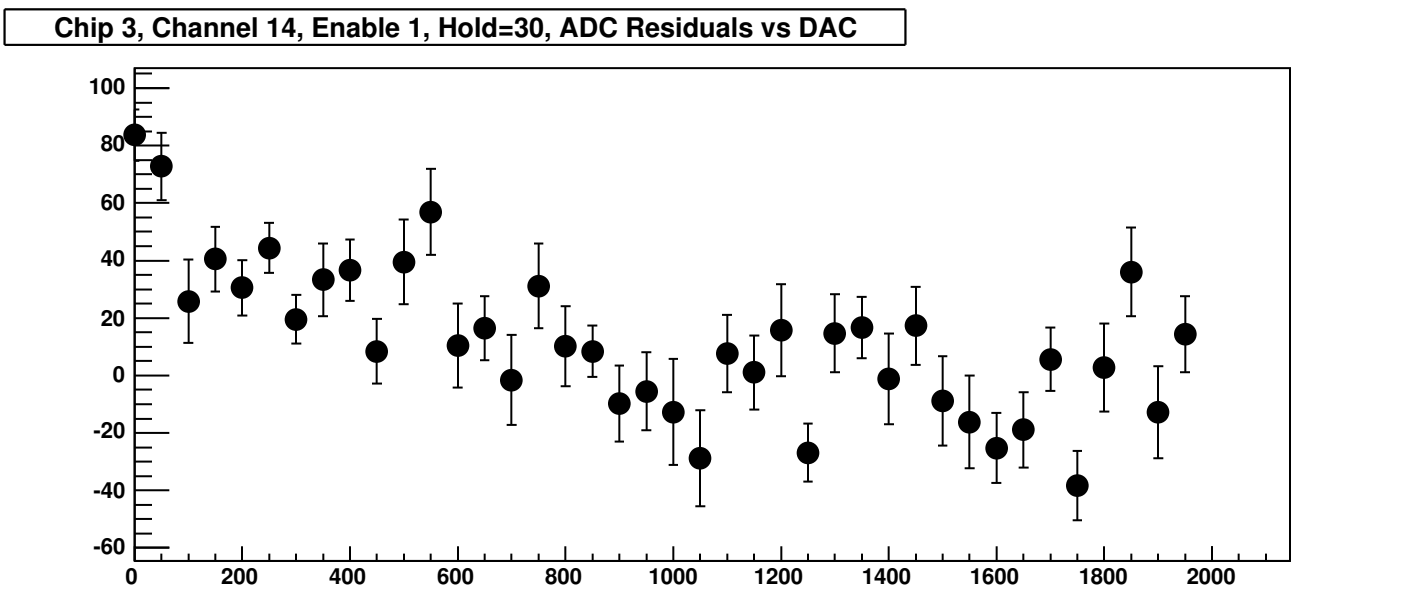

Chip 3, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 9.95 / 11
p0 -1835 ± 24.23
p1 0.182 ± 0.02165
-1300
-1400
-1500
-1600
-1700
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

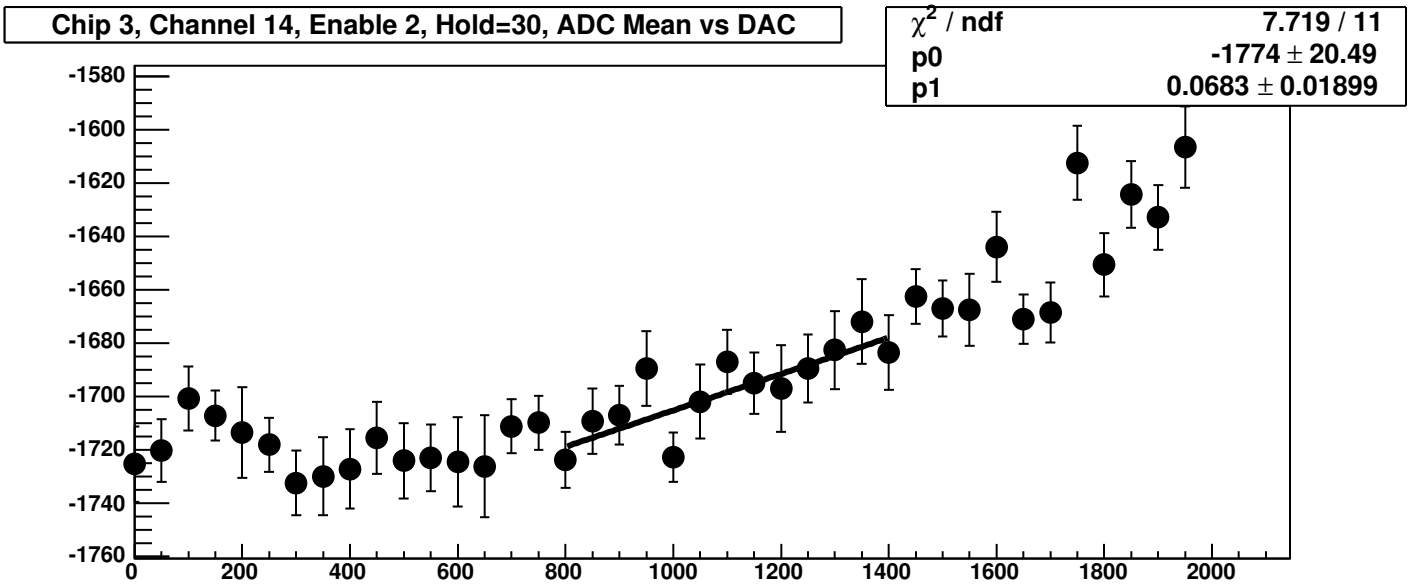

Chip 3, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

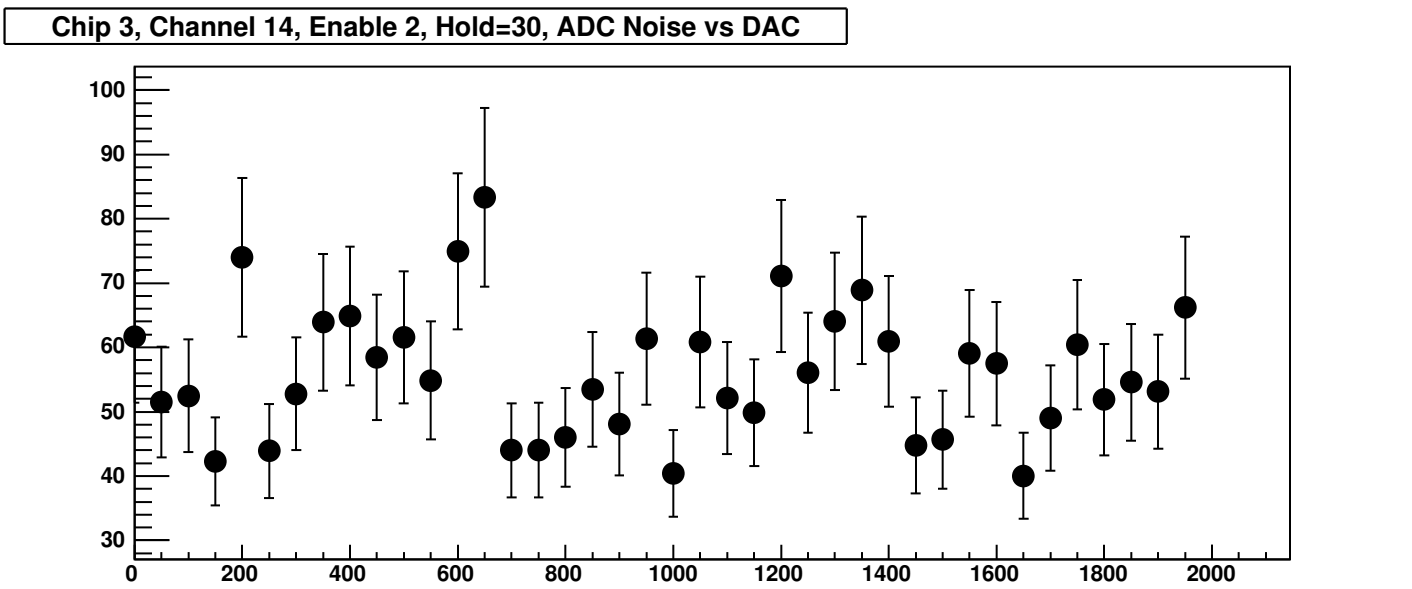

Chip 3, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

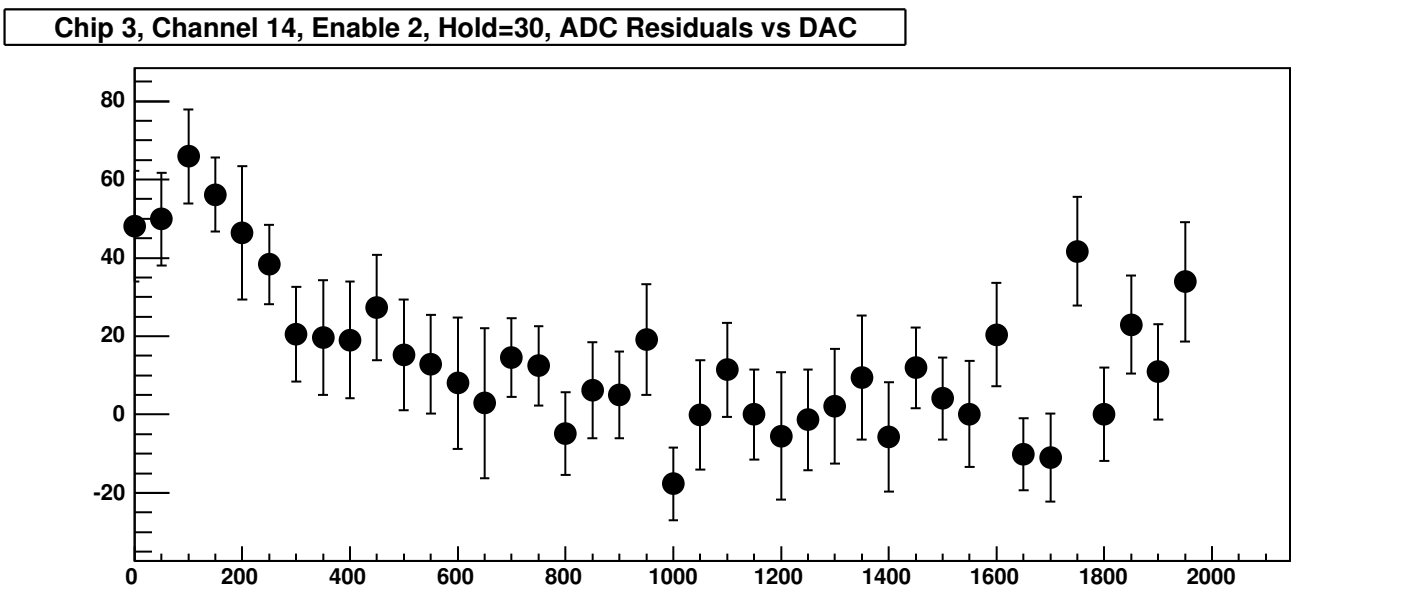

Chip 3, Channel 14, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 17.63 / 11
p0 -1790 ± 20.19
p1 0.06556 ± 0.01817
-1620
-1640
-1660
-1680
-1700
-1720
-1740
-1760
-1780
0
200
400
600
800
1000
1200
1400
1600
1800
2000

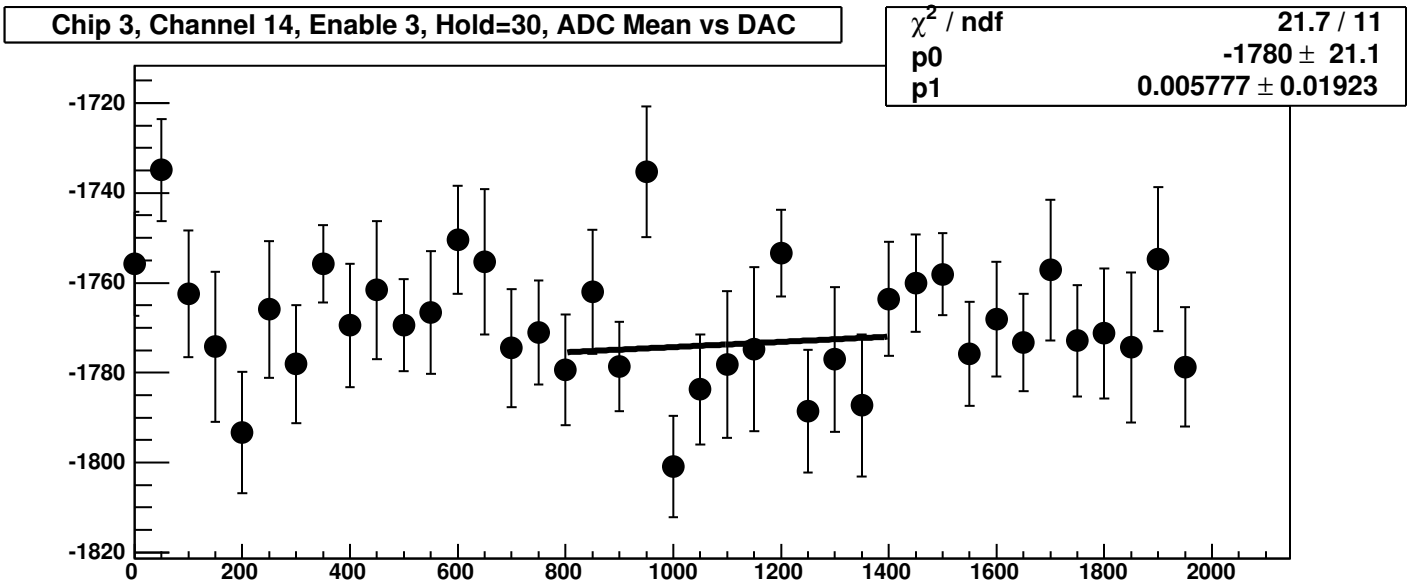

Chip 3, Channel 14, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

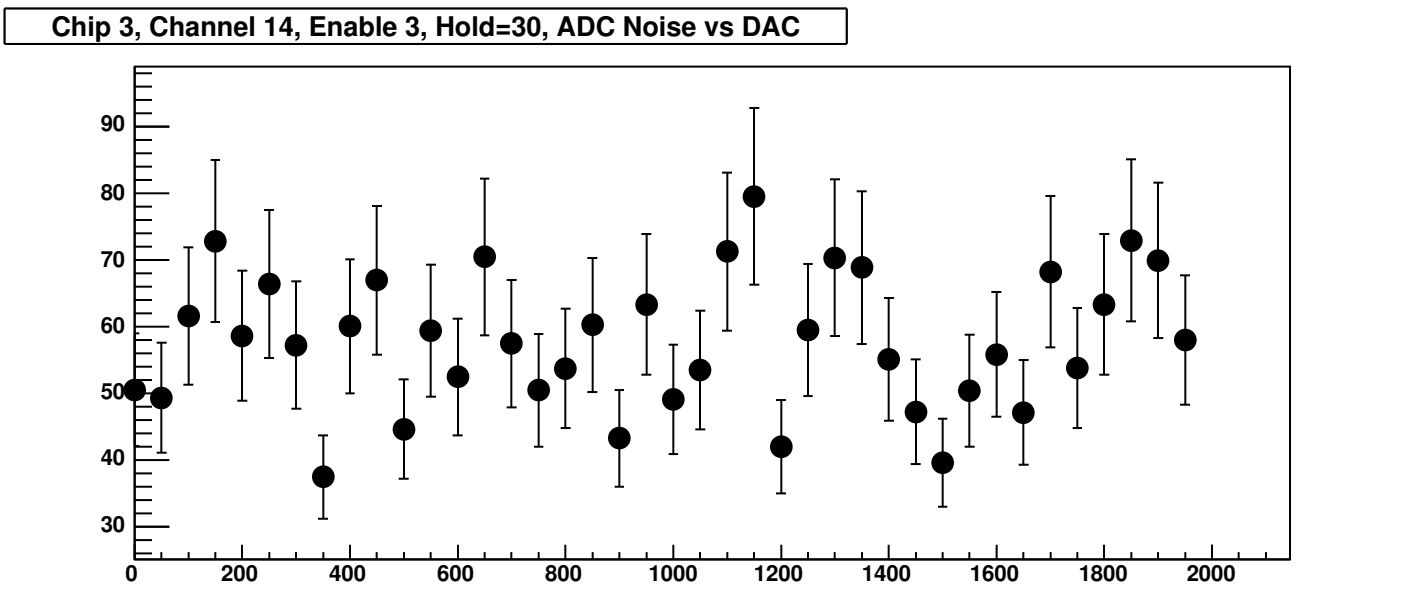

Chip 3, Channel 14, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

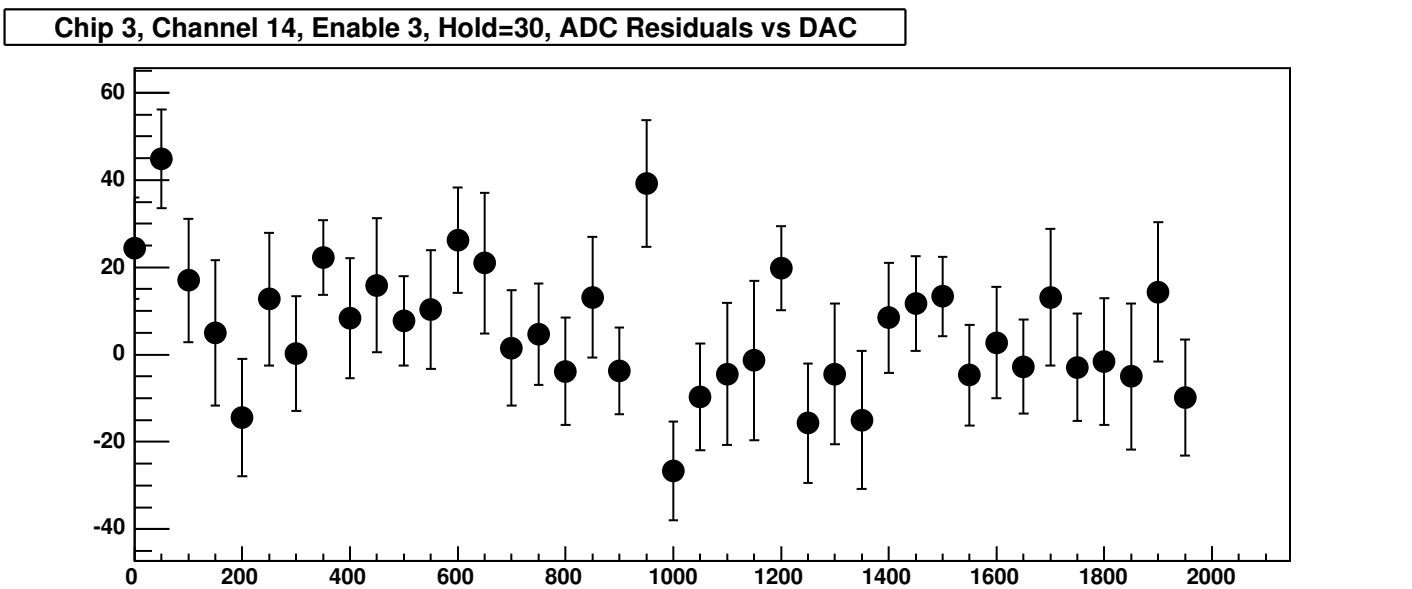

Chip 3, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 7.719 / 11
p0 -1774 ± 20.49
p1 0.0683 ± 0.01899
-1580
-1600
-1620
-1640
-1660
-1680
-1700
-1720
-1740
-1760
0
200
400
600
800
1000
1200
1400
1600
1800
2000

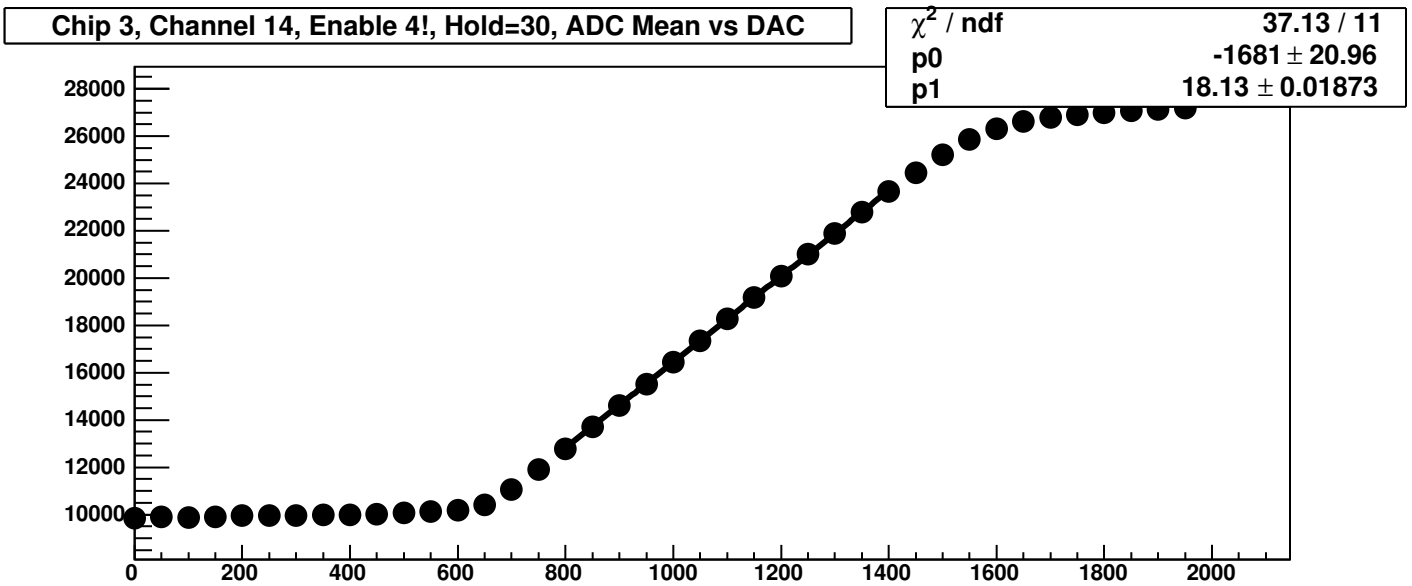

Chip 3, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

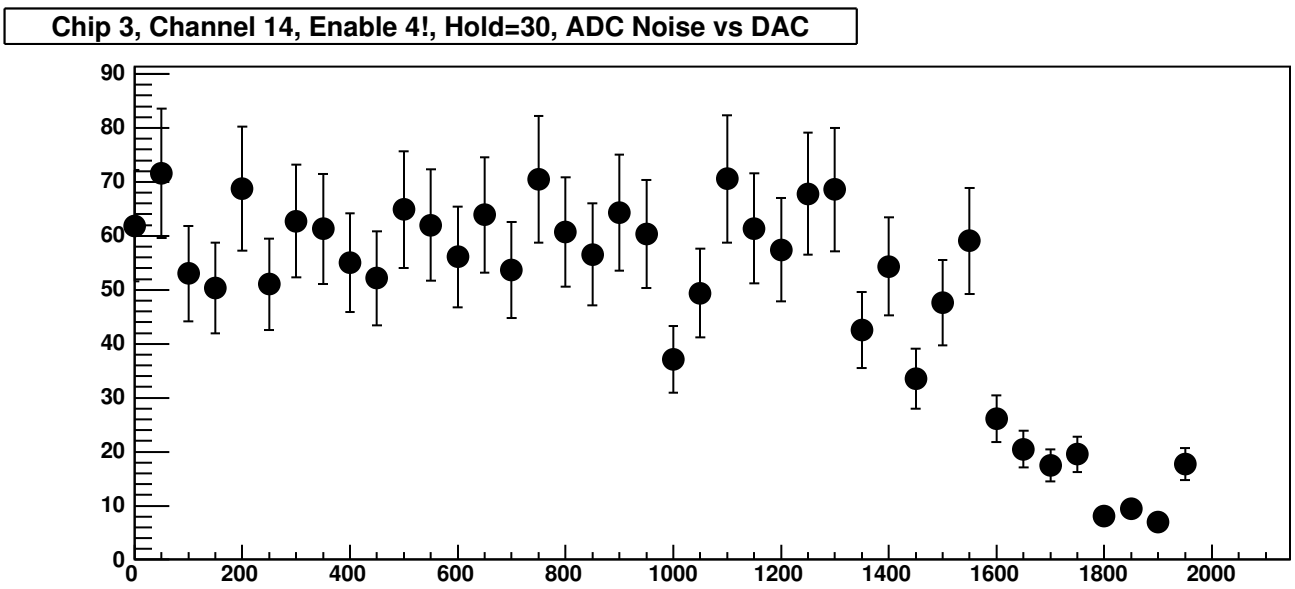

Chip 3, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

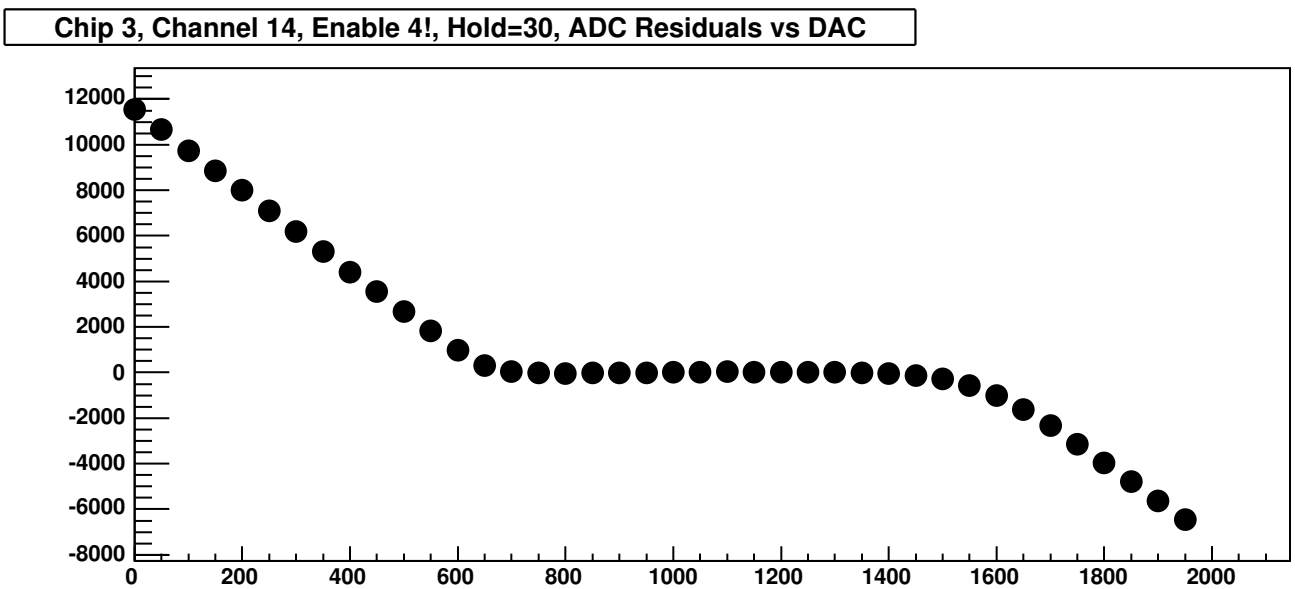

Chip 3, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 21.7 / 11
p0 -1780 ± 21.1
p1 0.005777 ± 0.01923
-1720
-1740
-1760
-1780
-1800
-1820
0
200
400
600
800
1000
1200
1400
1600
1800
2000

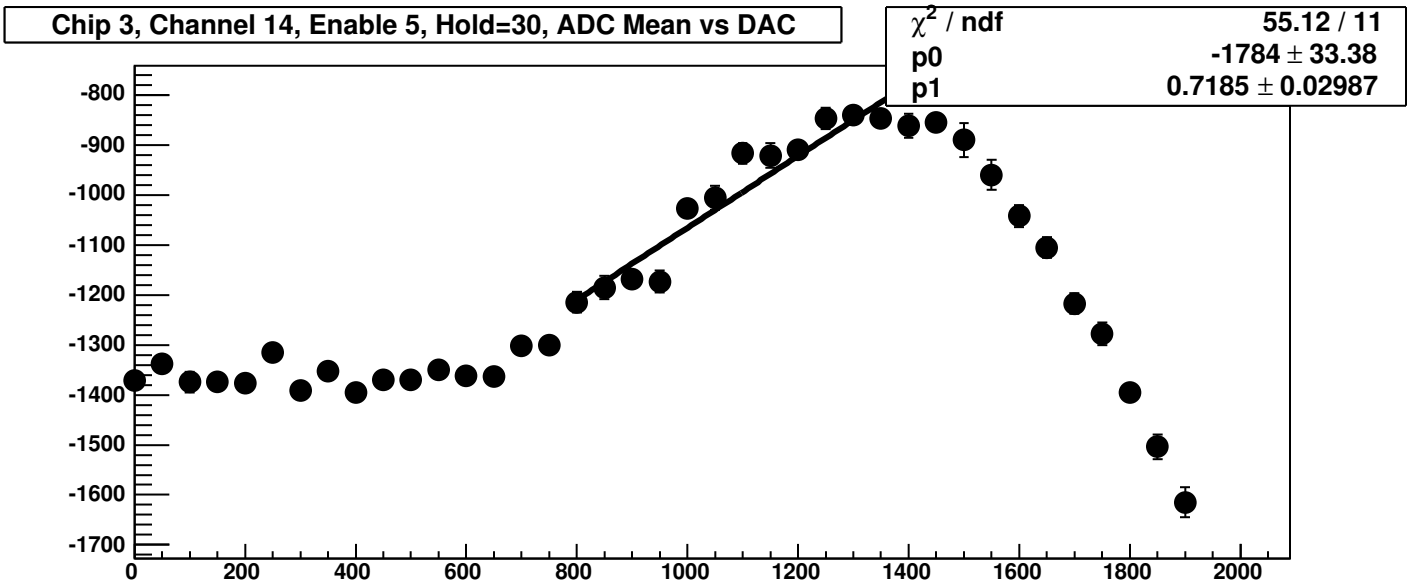

Chip 3, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

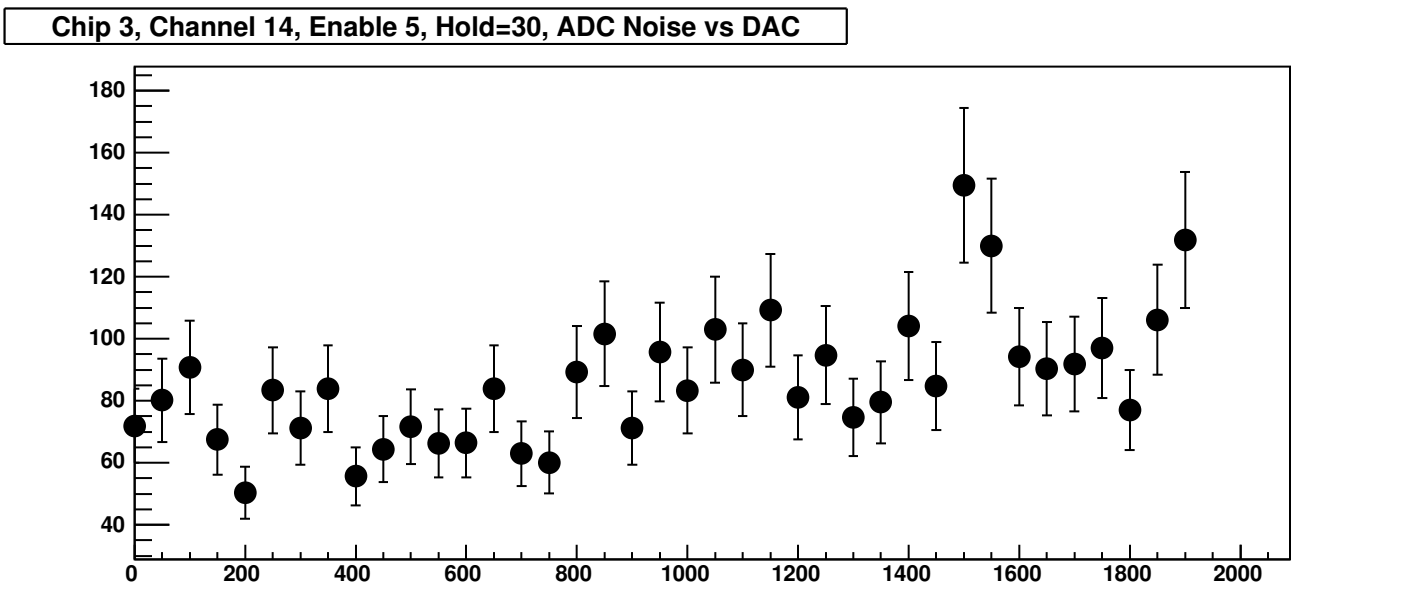

Chip 3, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

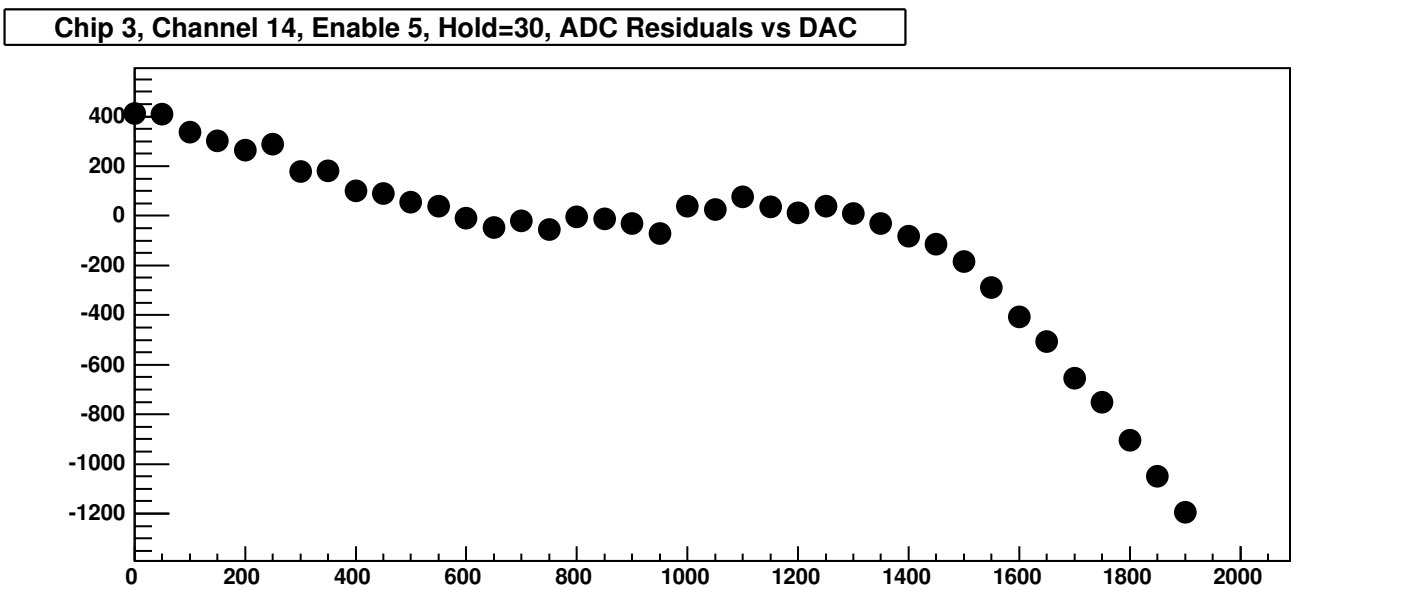

Chip 3, Channel 14, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 37.13 / 11
p0 -1681 ± 20.96
p1 18.13 ± 0.01873
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

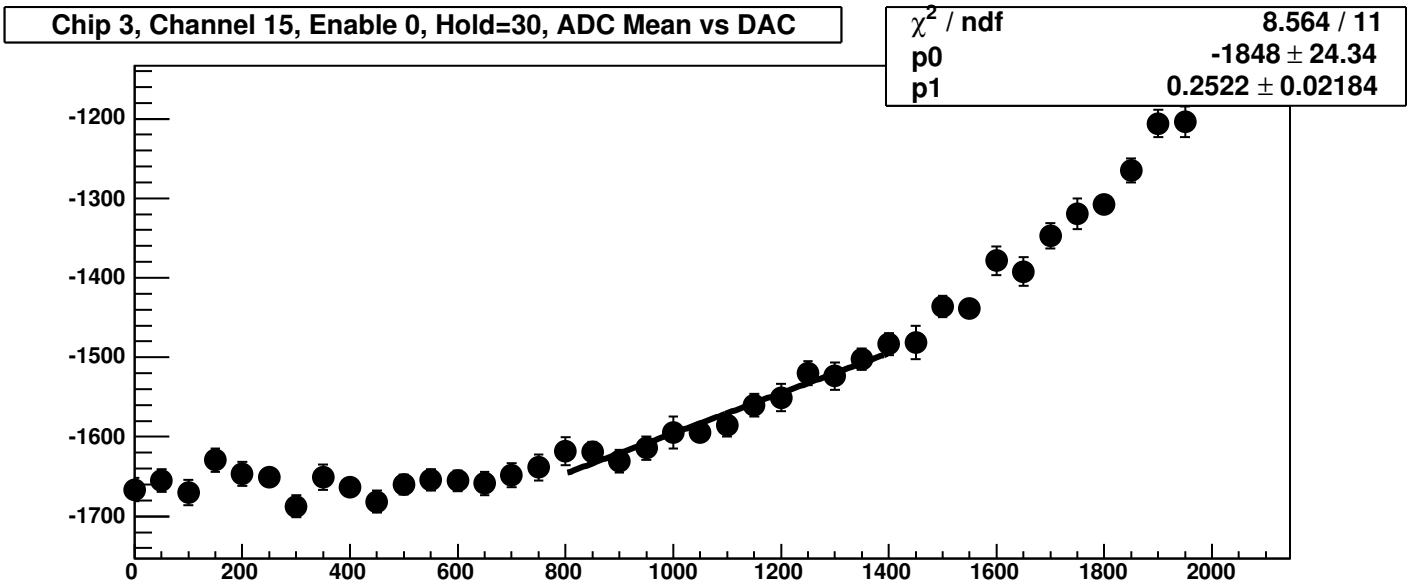

Chip 3, Channel 14, Enable 4!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

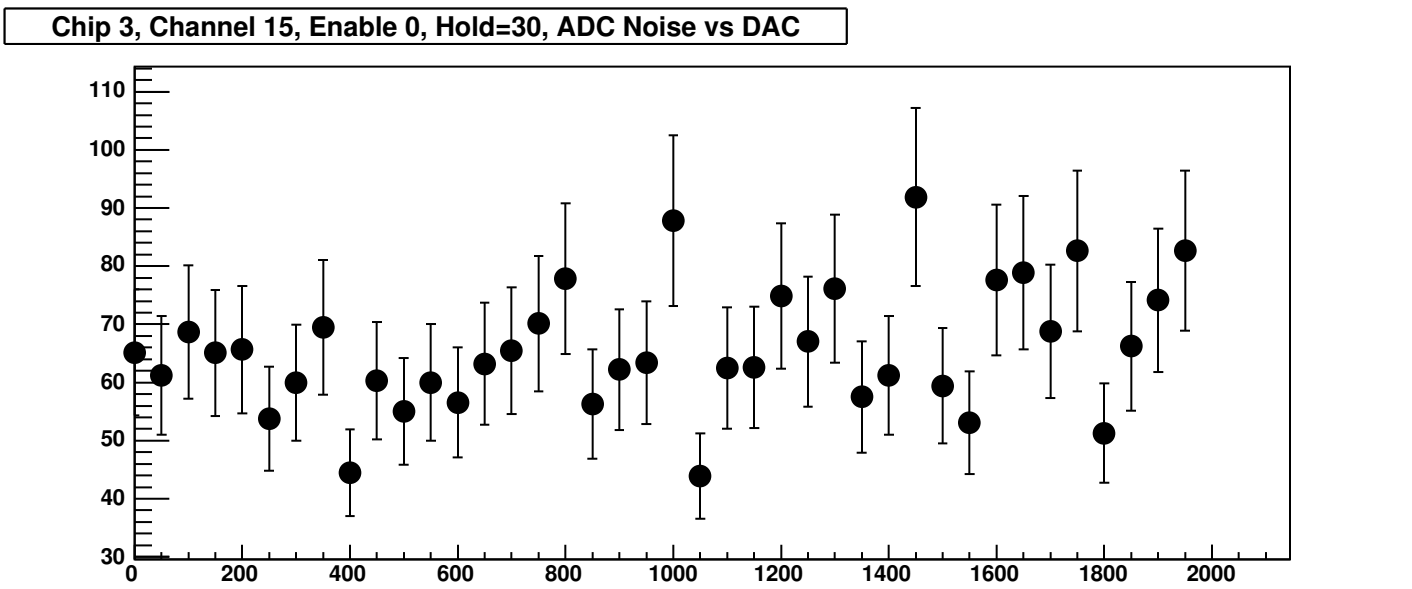

Chip 3, Channel 14, Enable 4!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

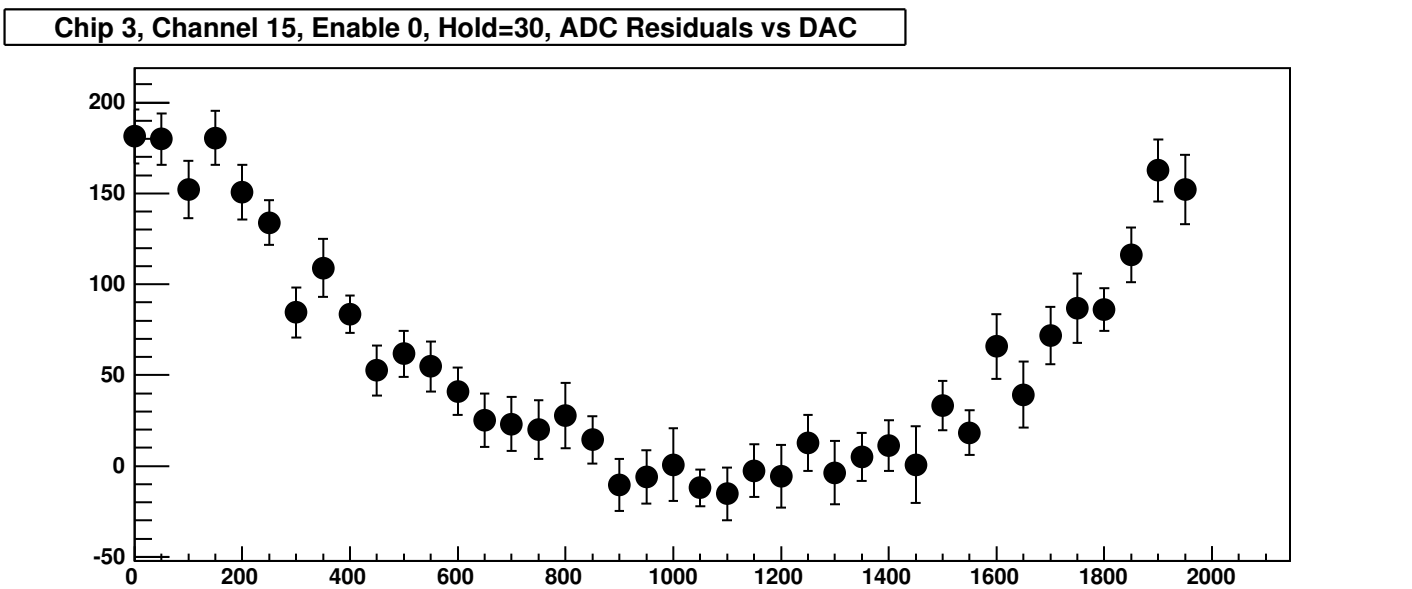

Chip 3, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 55.12 / 11
p0 -1784 ± 33.38
p1 0.7185 ± 0.02987
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

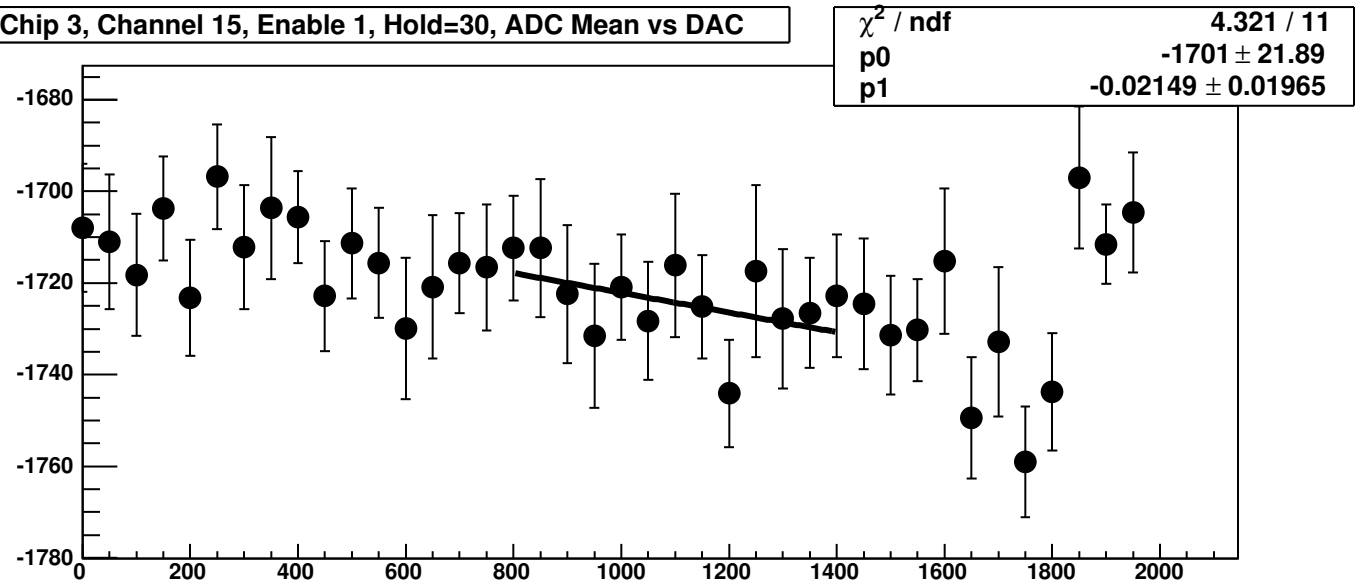

Chip 3, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 8.564 / 11
p0 -1848 ± 24.34
p1 0.2522 ± 0.02184
-1200
-1300
-1400
-1500
-1600
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

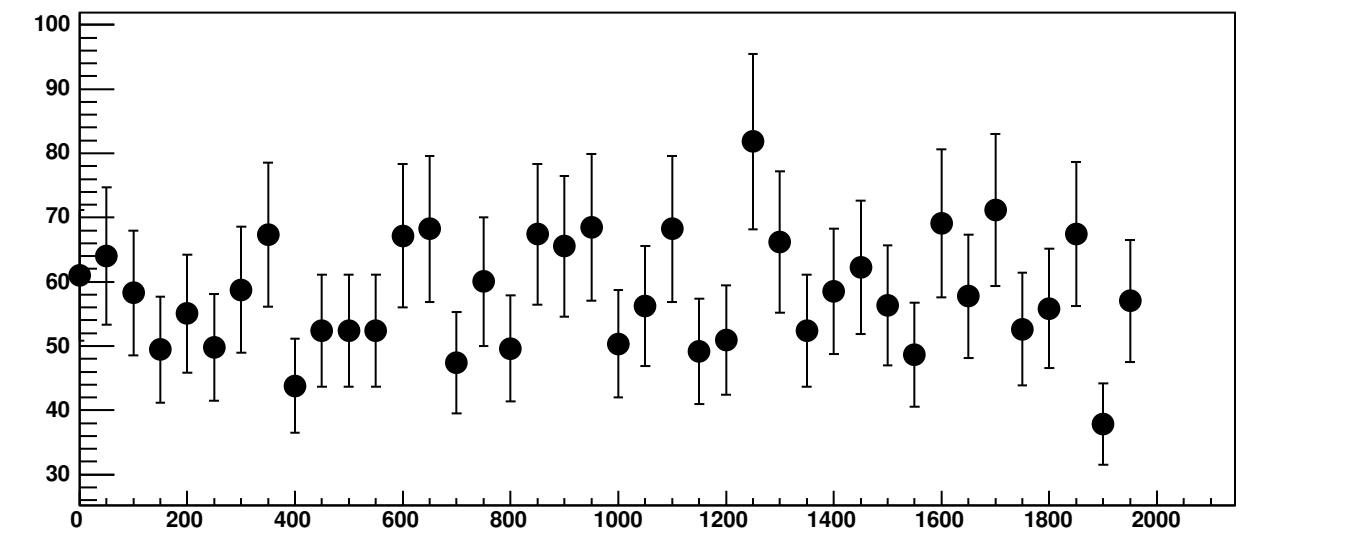

Chip 3, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

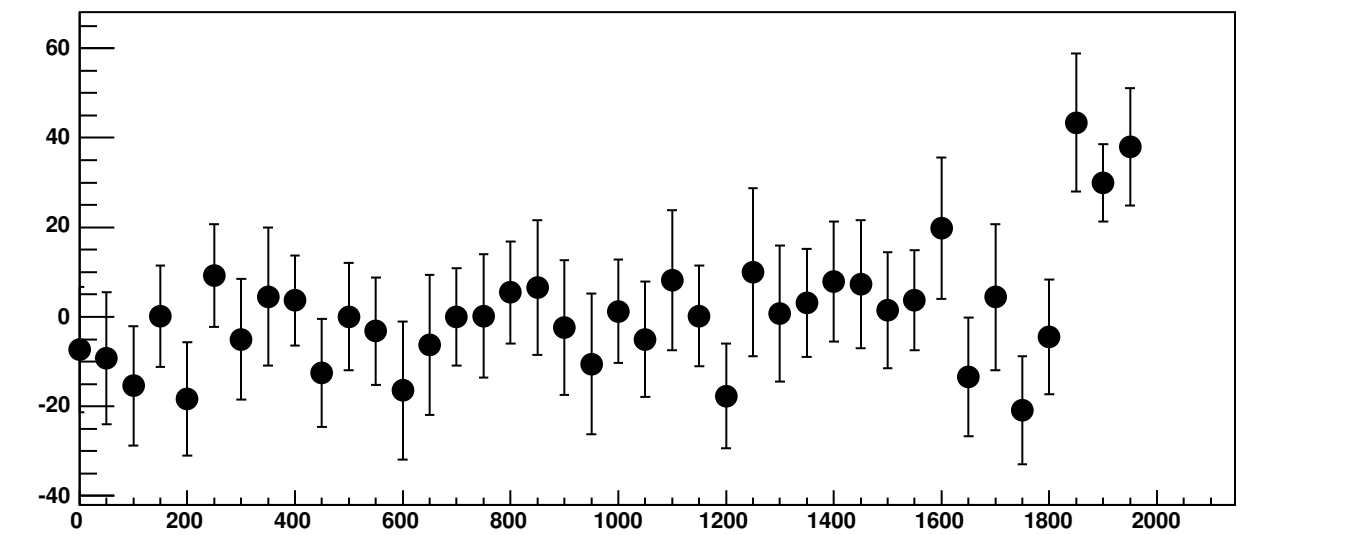
Chip 3, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC

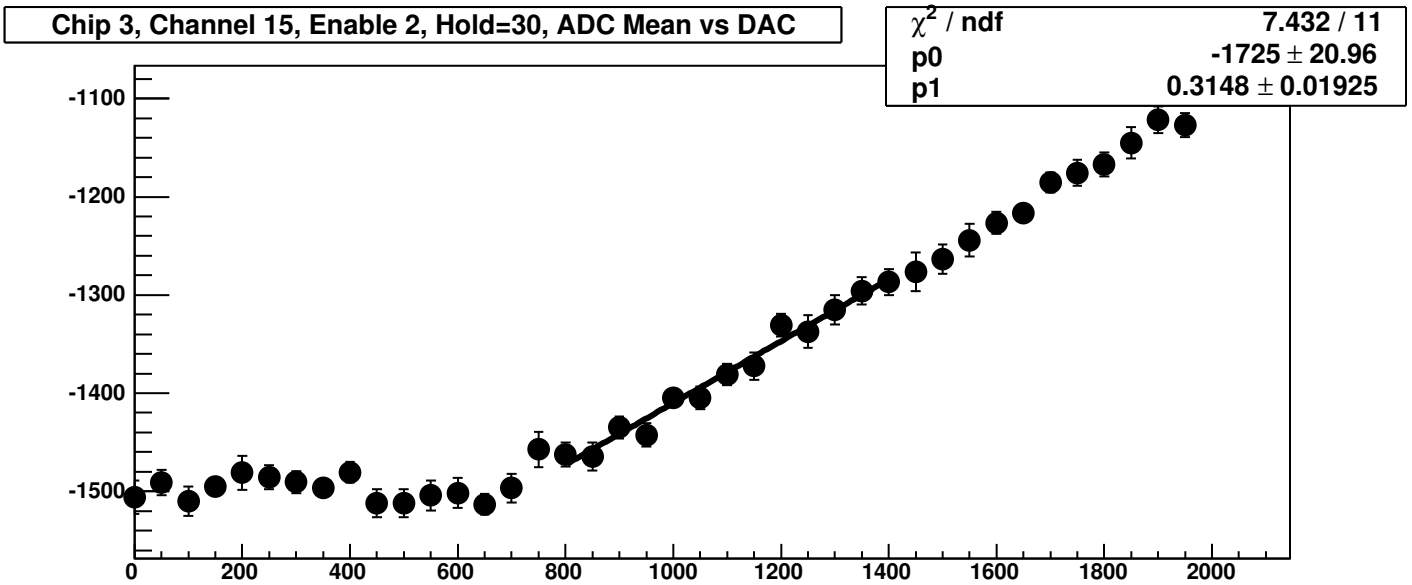


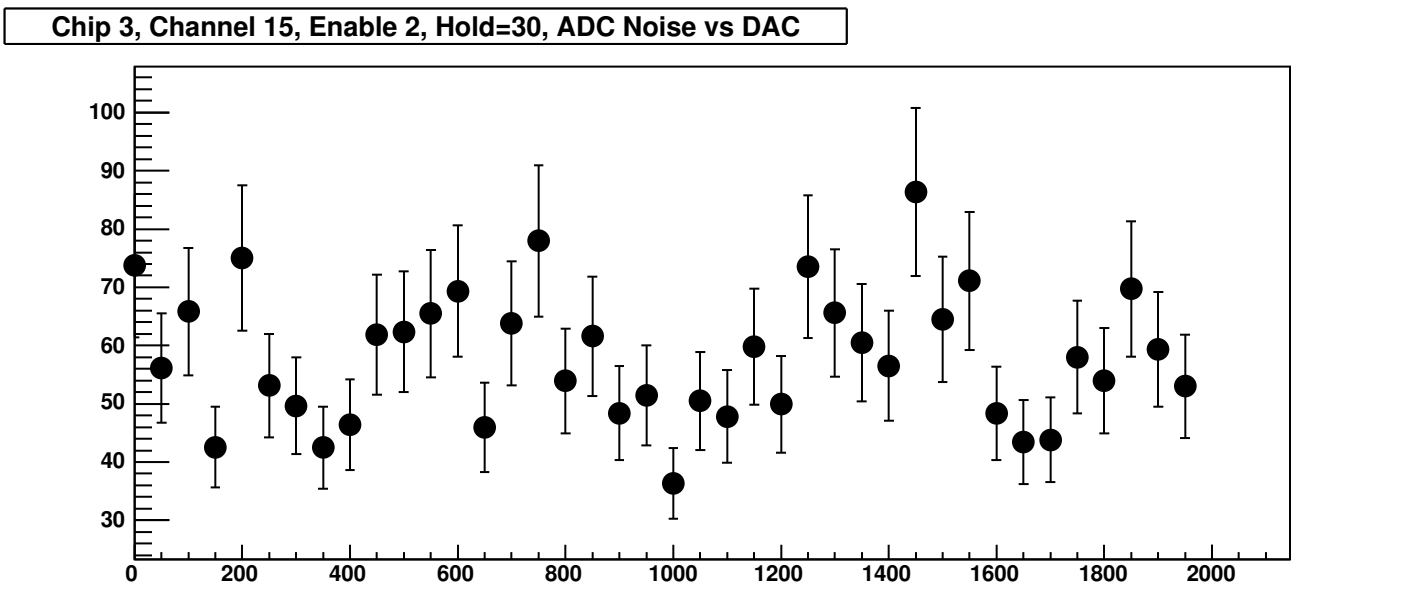
Chip 3, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC

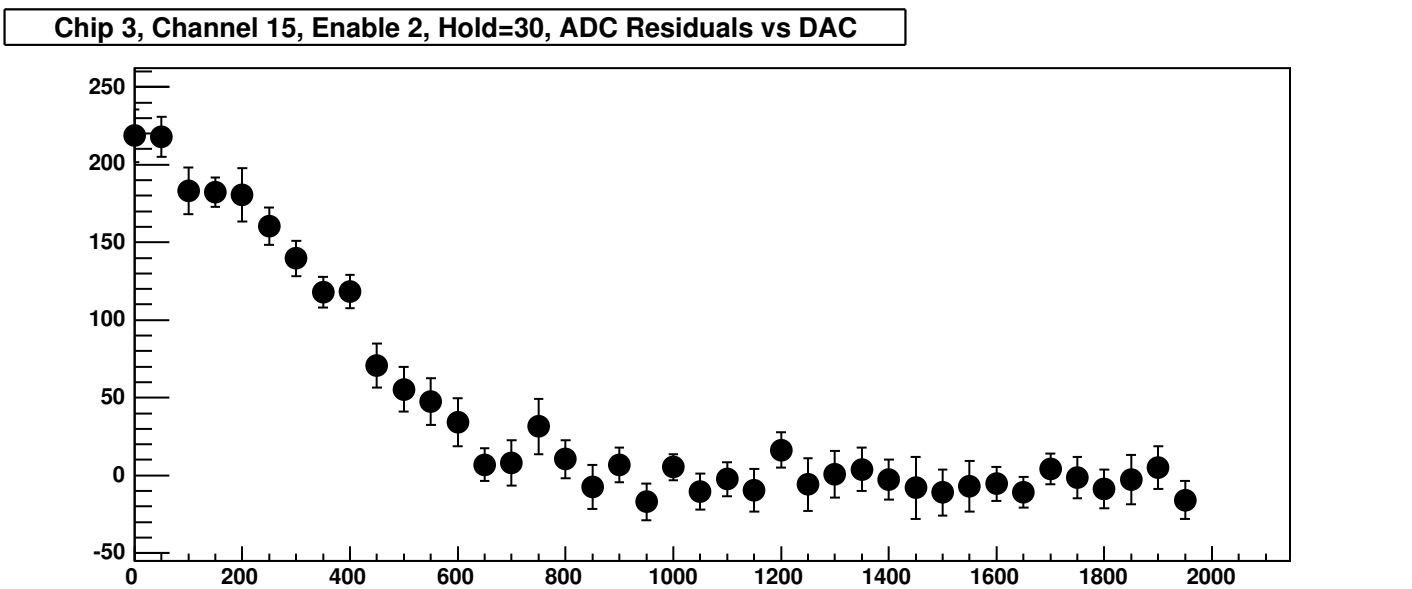


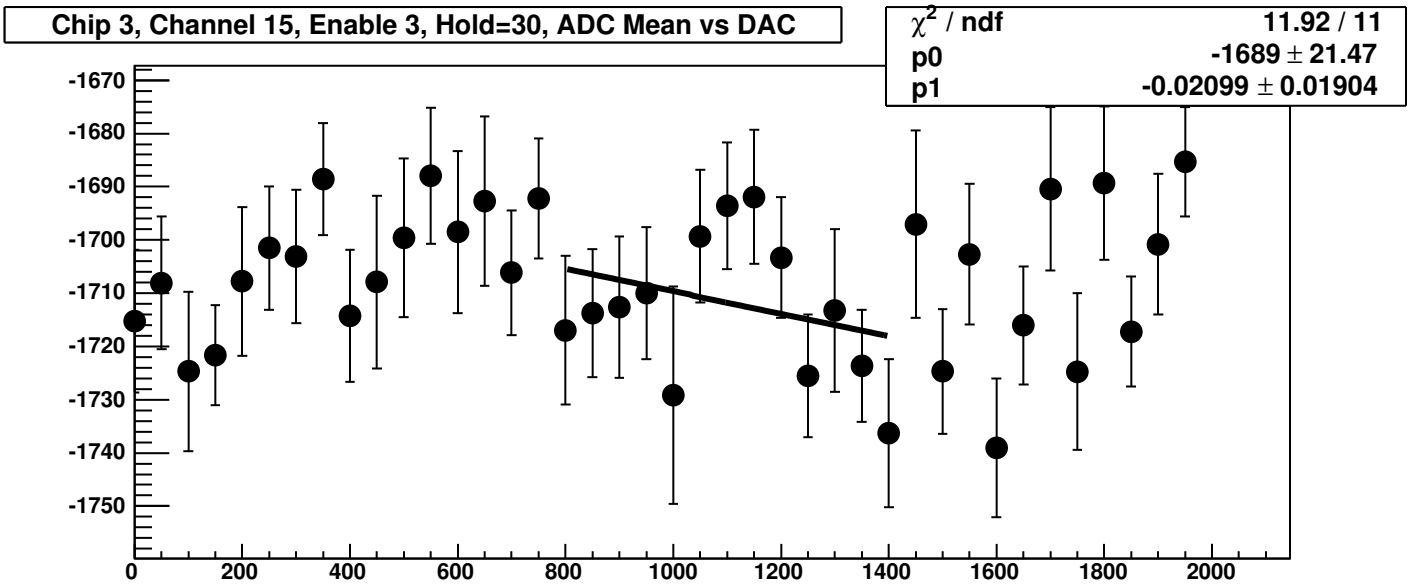
Chip 3, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC

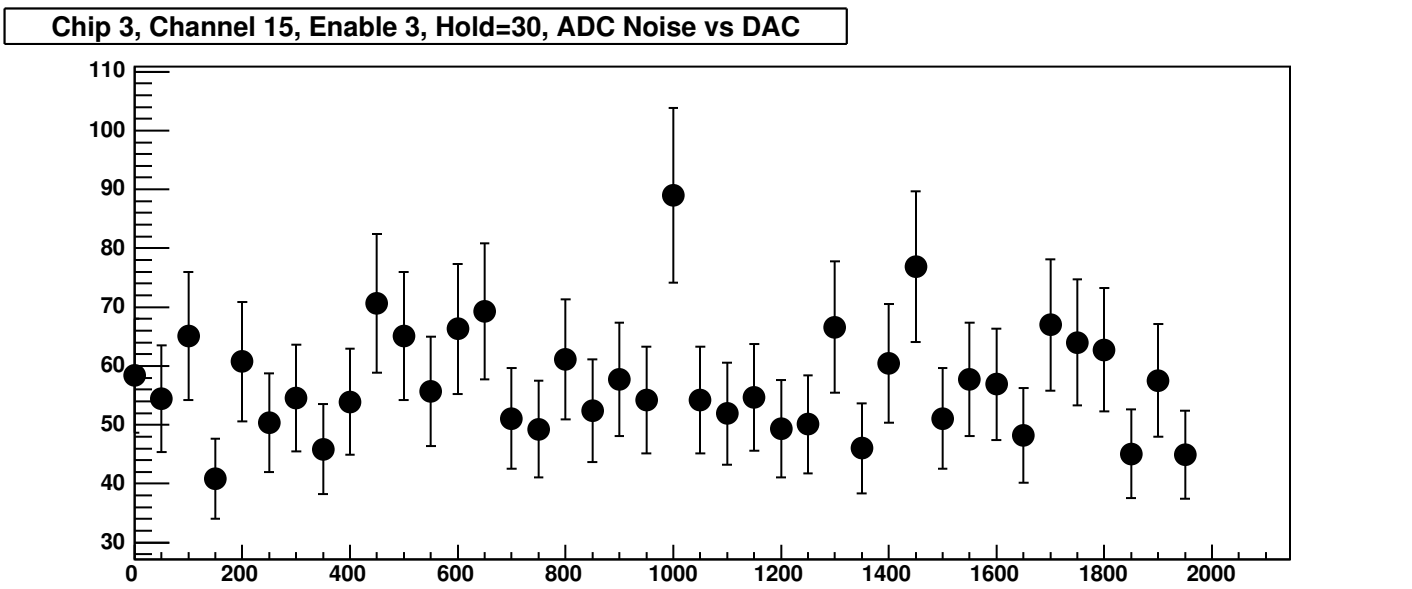


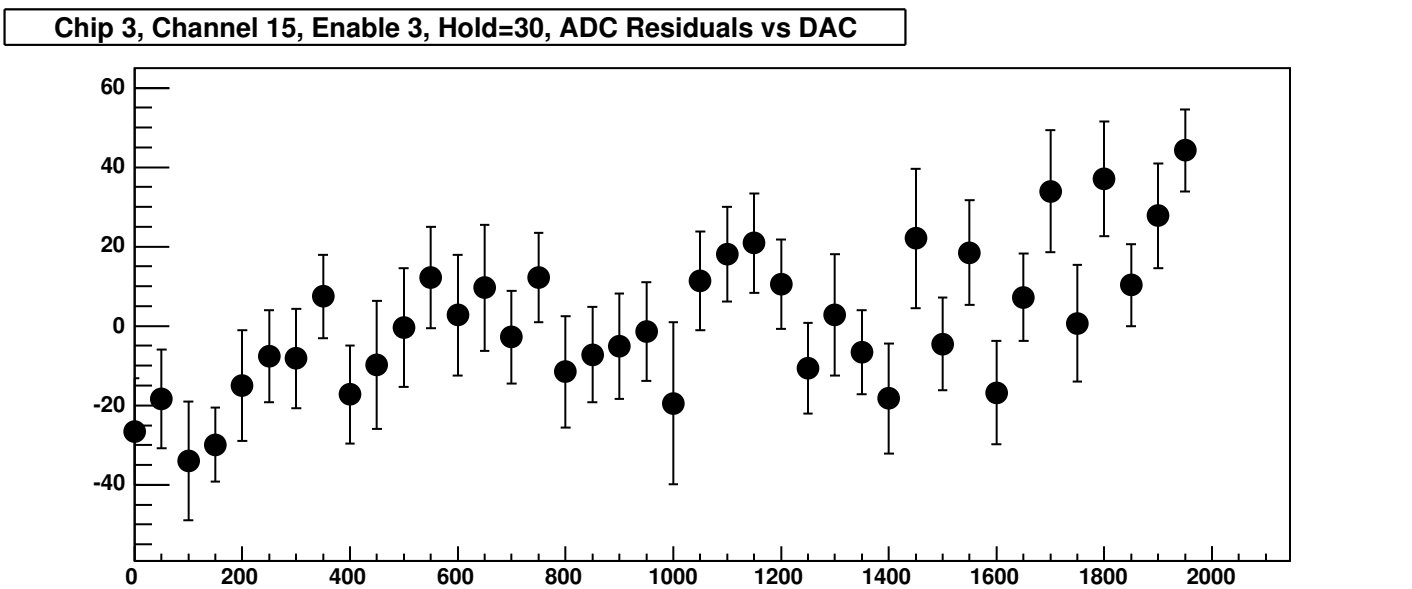

Chip 3, Channel 15, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 7.432 / 11
p0 -1725 ± 20.96
p1 0.3148 ± 0.01925
-1100
-1200
-1300
-1400
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

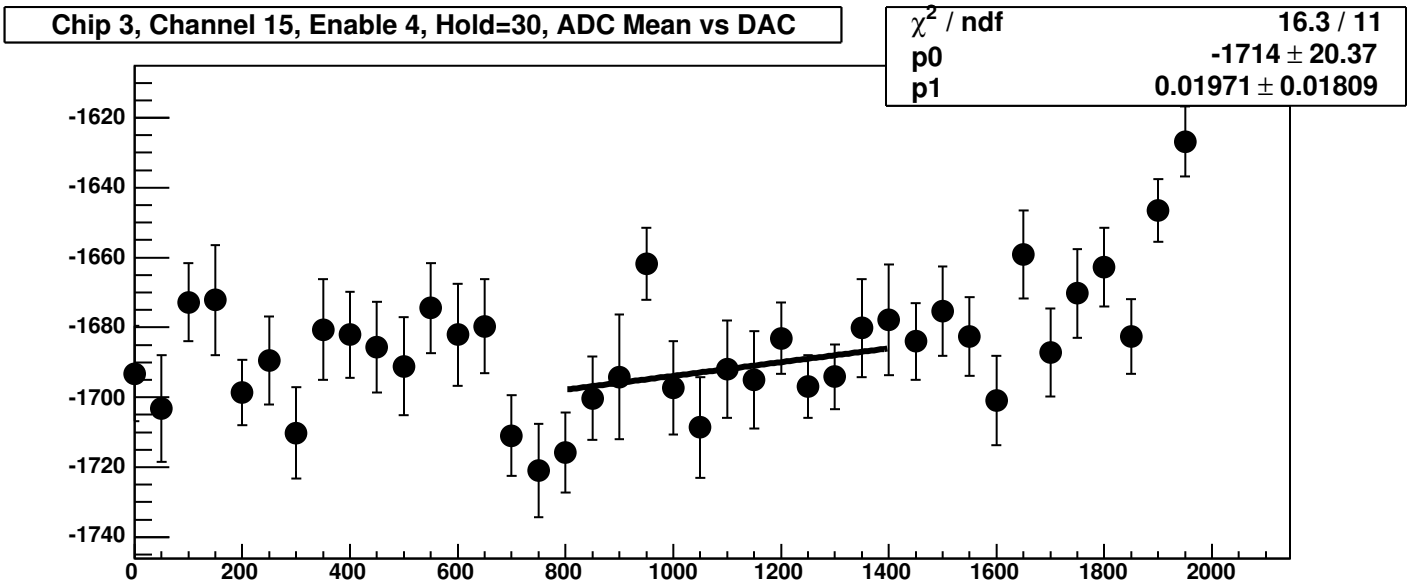

Chip 3, Channel 15, Enable 2, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

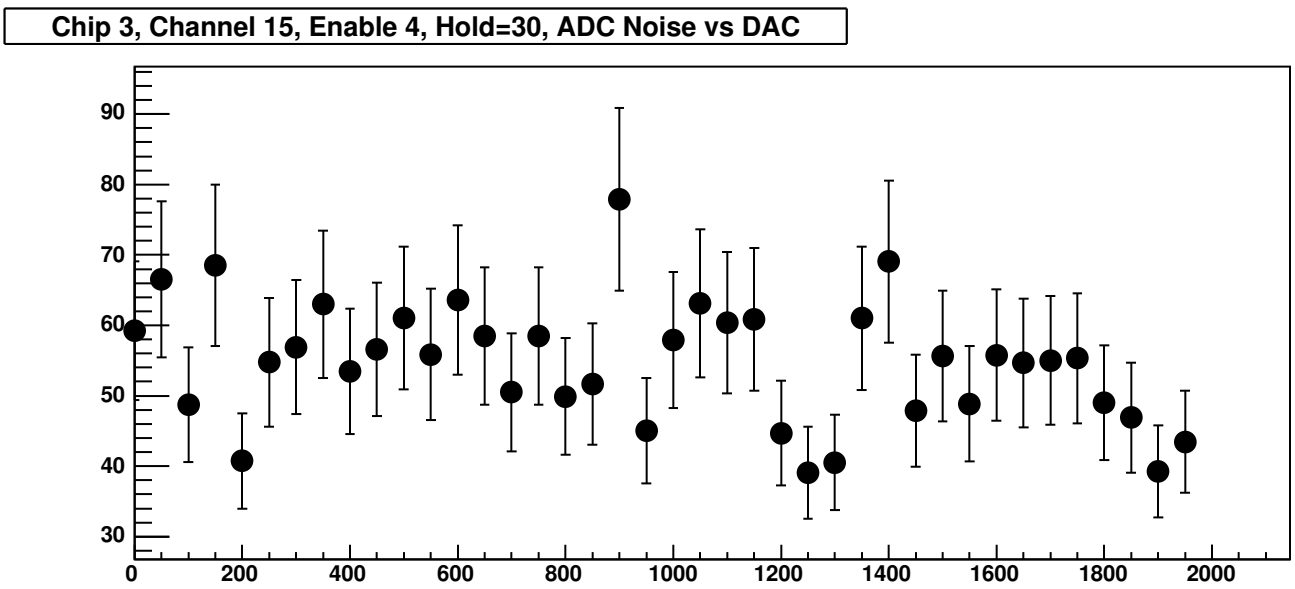

Chip 3, Channel 15, Enable 2, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

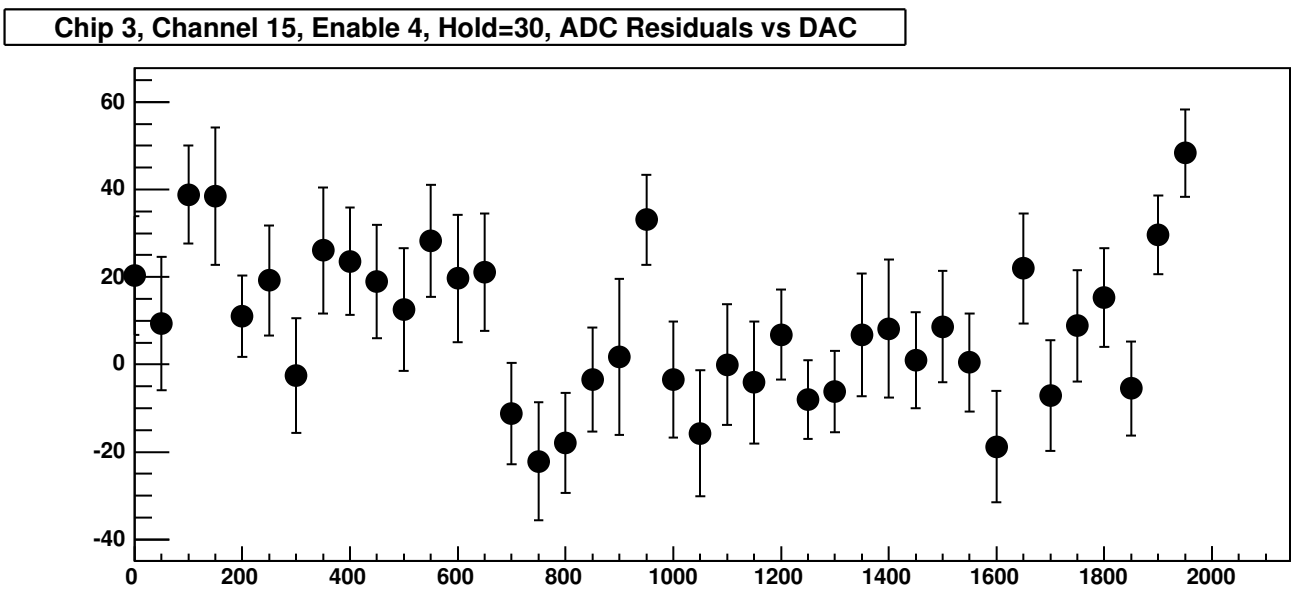

Chip 3, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 11.92 / 11
p0 -1689 ± 21.47
p1 -0.02099 ± 0.01904
-1670
-1680
-1690
-1700
-1710
-1720
-1730
-1740
-1750
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

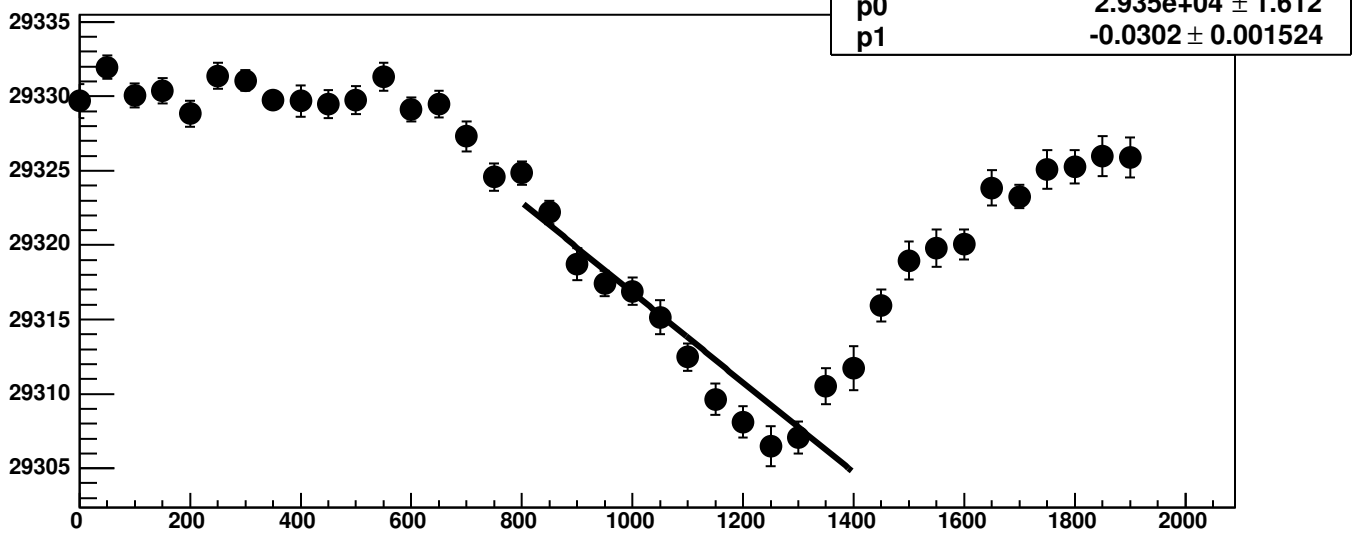

Chip 3, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 16.3 / 11
p0 -1714 ± 20.37
p1 0.01971 ± 0.01809
-1620
-1640
-1660
-1680
-1700
-1720
-1740
0
200
400
600
800
1000
1200
1400
1600
1800
2000

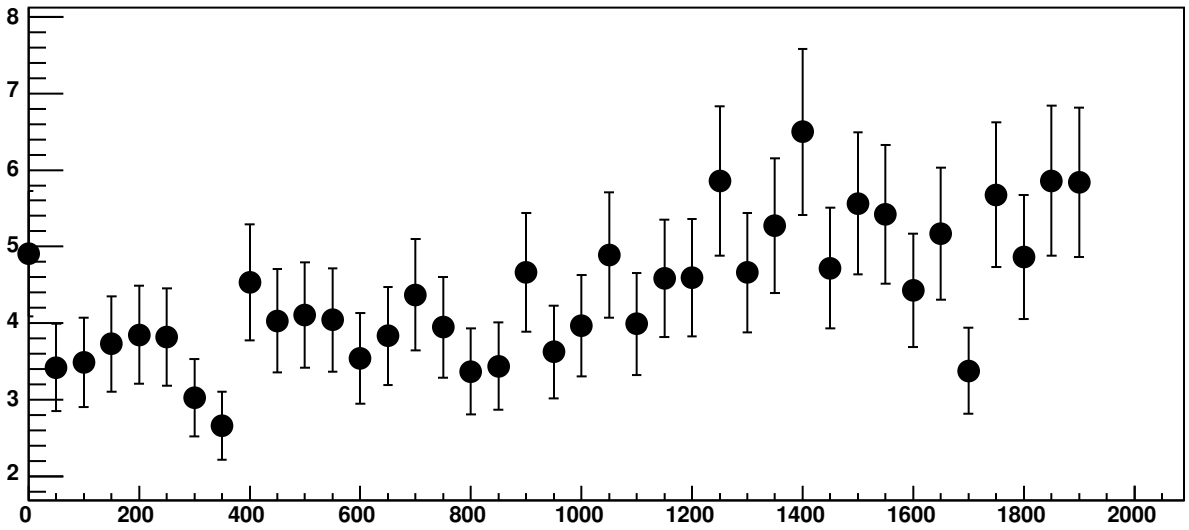

Chip 3, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

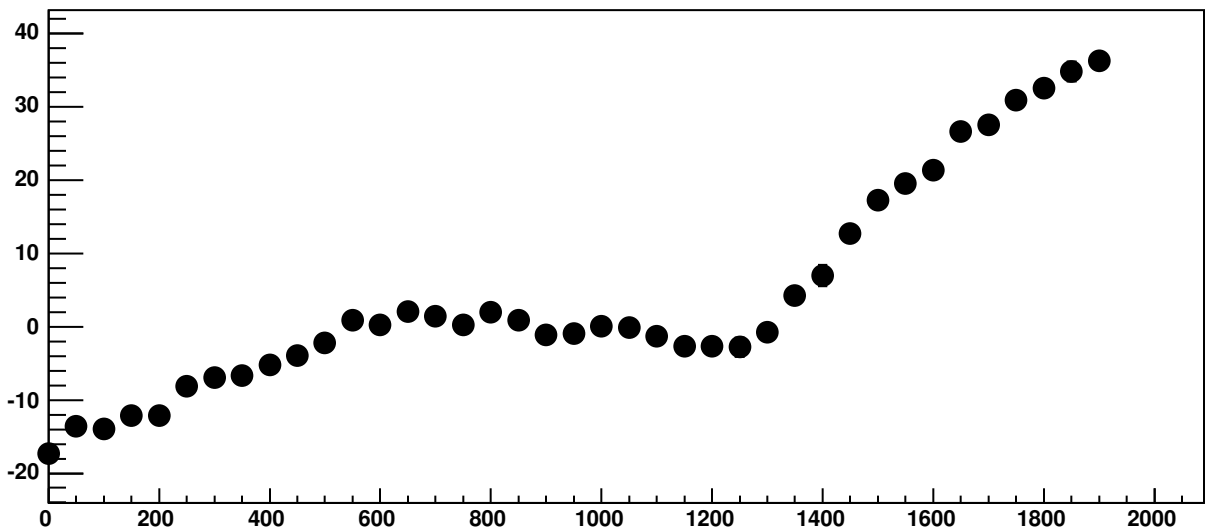
Chip 3, Channel 15, Enable 5!, Hold=30, ADC Mean vs DAC

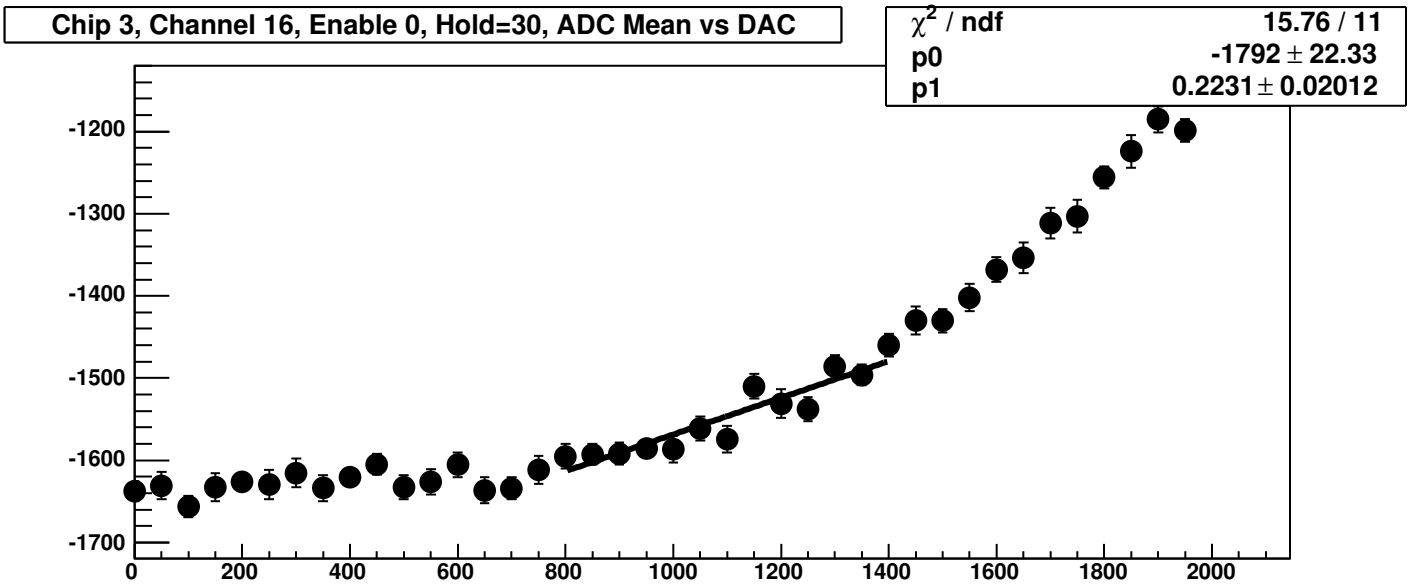


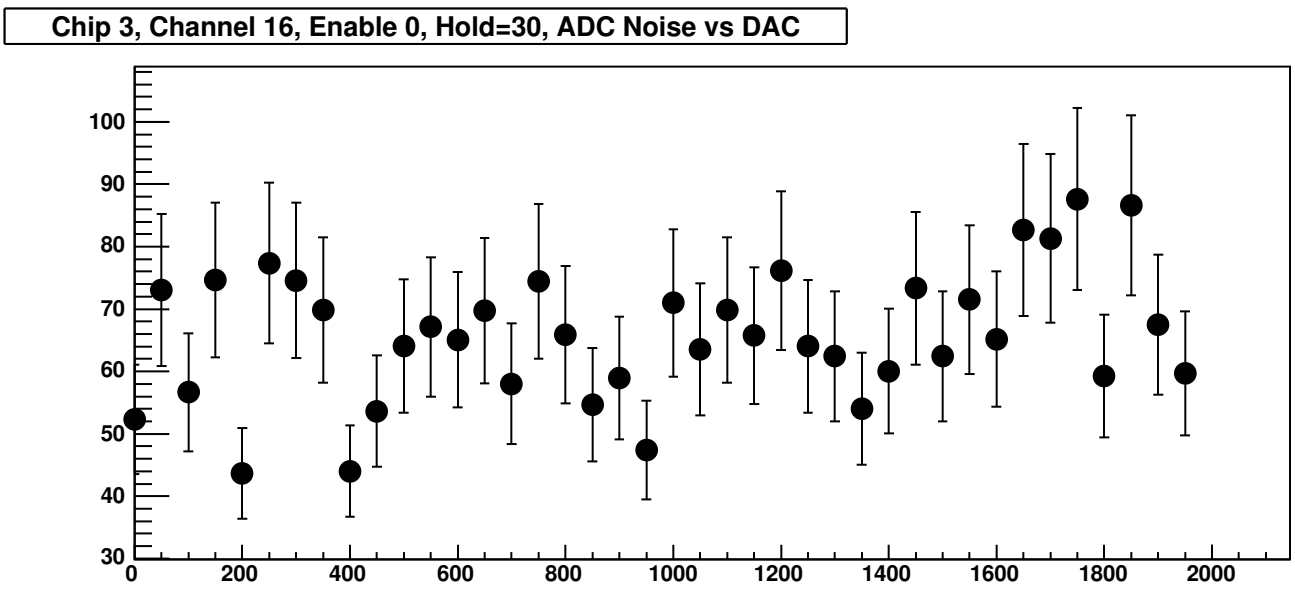
Chip 3, Channel 15, Enable 5!, Hold=30, ADC Noise vs DAC

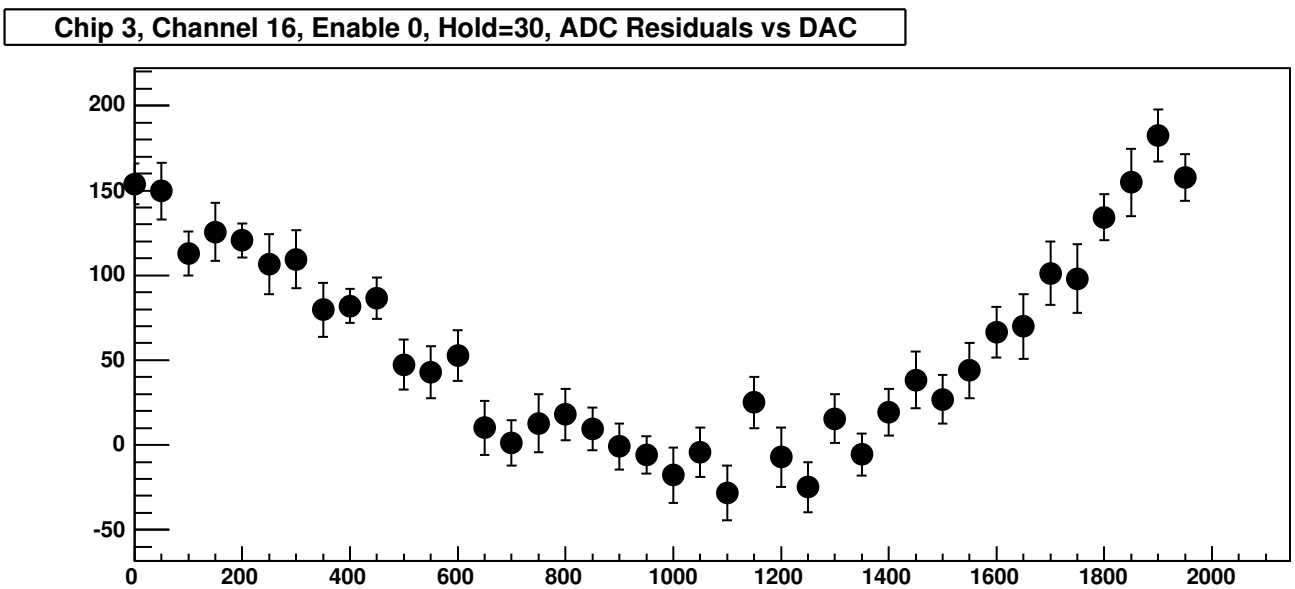


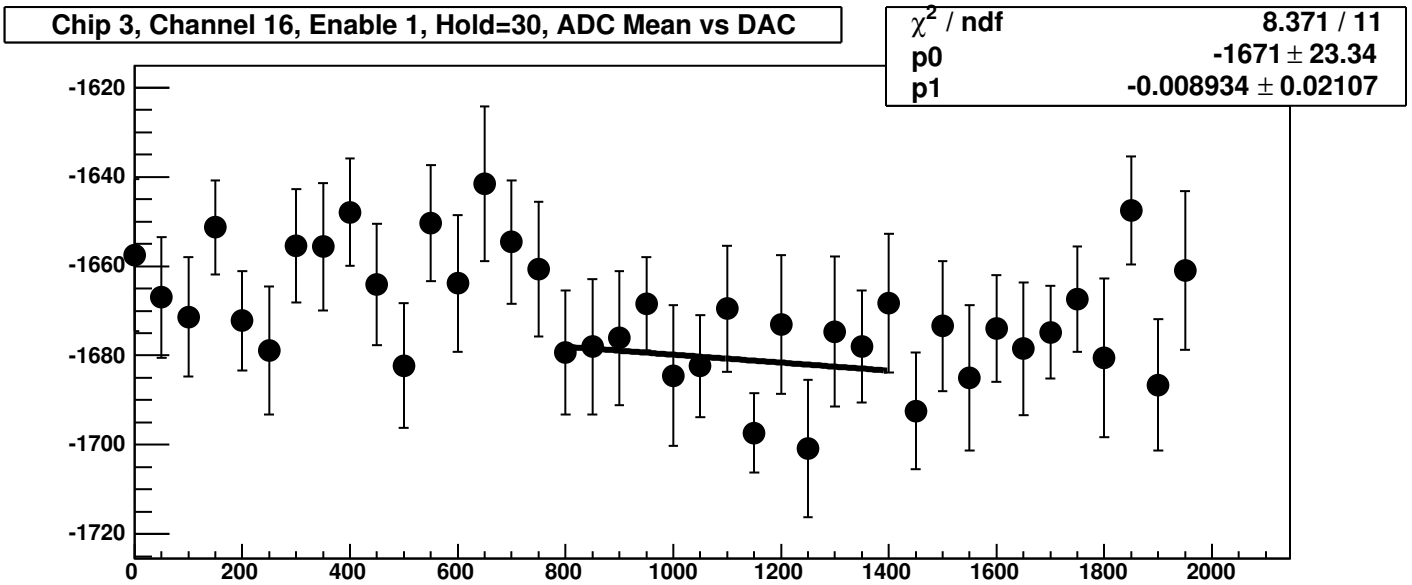
Chip 3, Channel 15, Enable 5!, Hold=30, ADC Residuals vs DAC

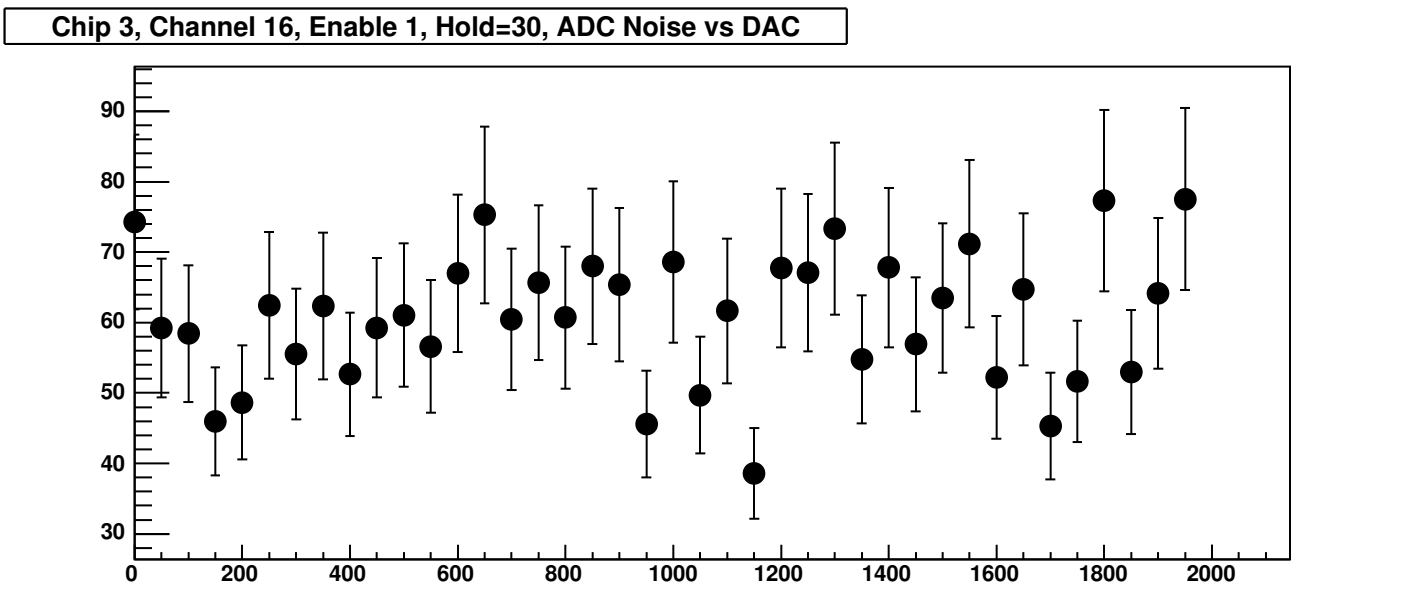


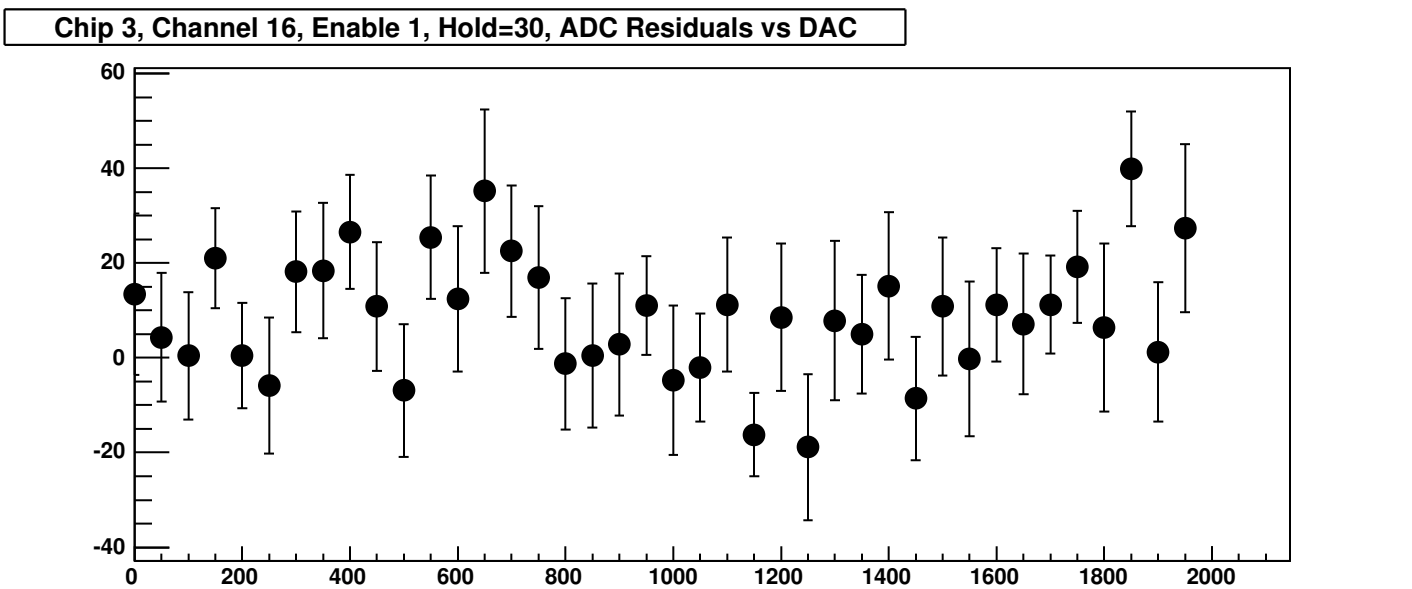

Chip 3, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 15.76 / 11
p0 -1792 ± 22.33
p1 0.2231 ± 0.02012
-1200
-1300
-1400
-1500
-1600
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

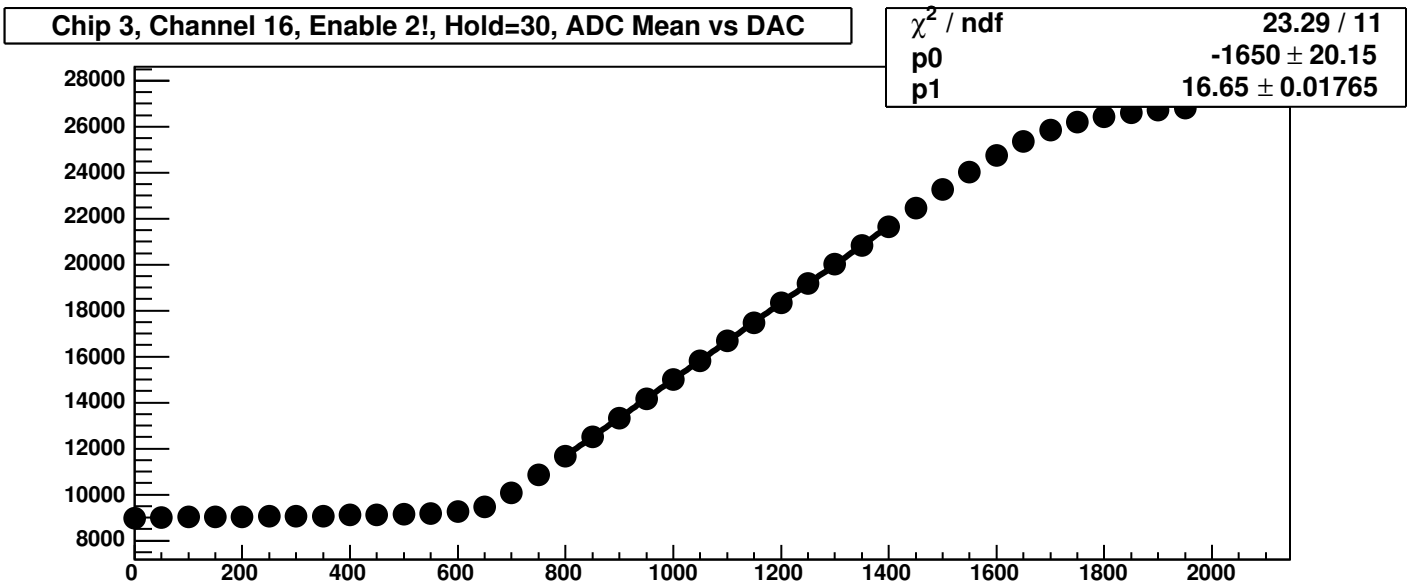

Chip 3, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

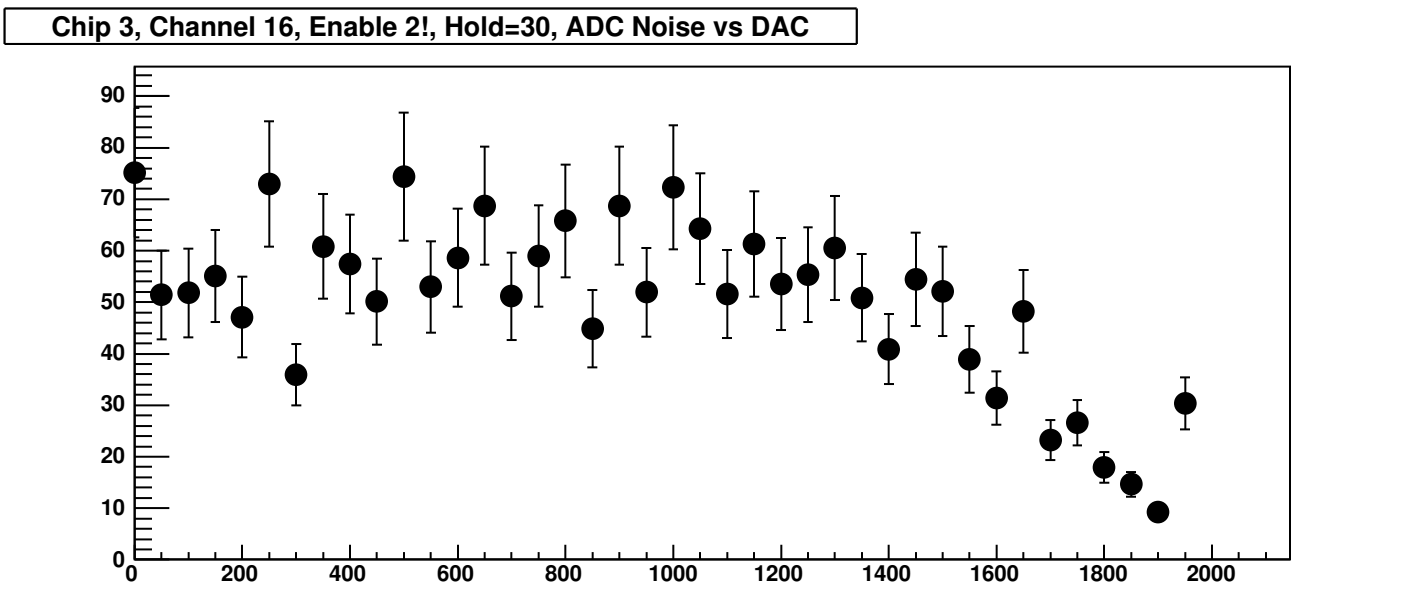

Chip 3, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

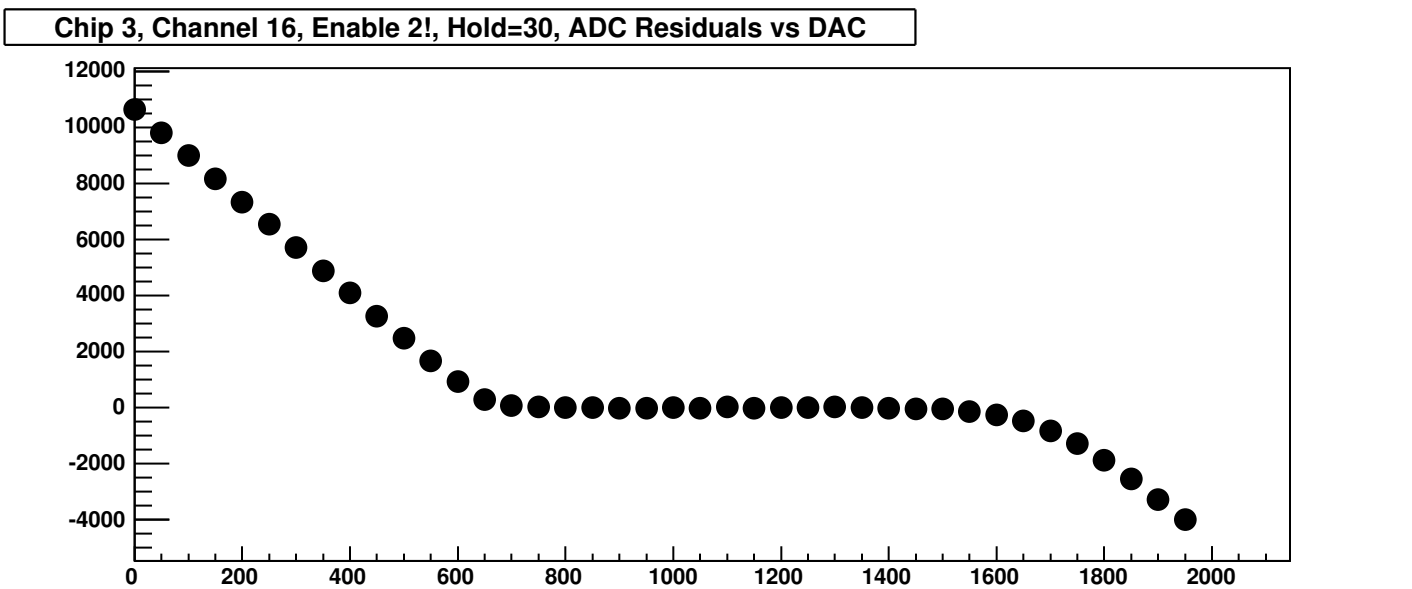

Chip 3, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 8.371 / 11
p0 -1671 ± 23.34
p1 -0.008934 ± 0.02107
-1620
-1640
-1660
-1680
-1700
-1720
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

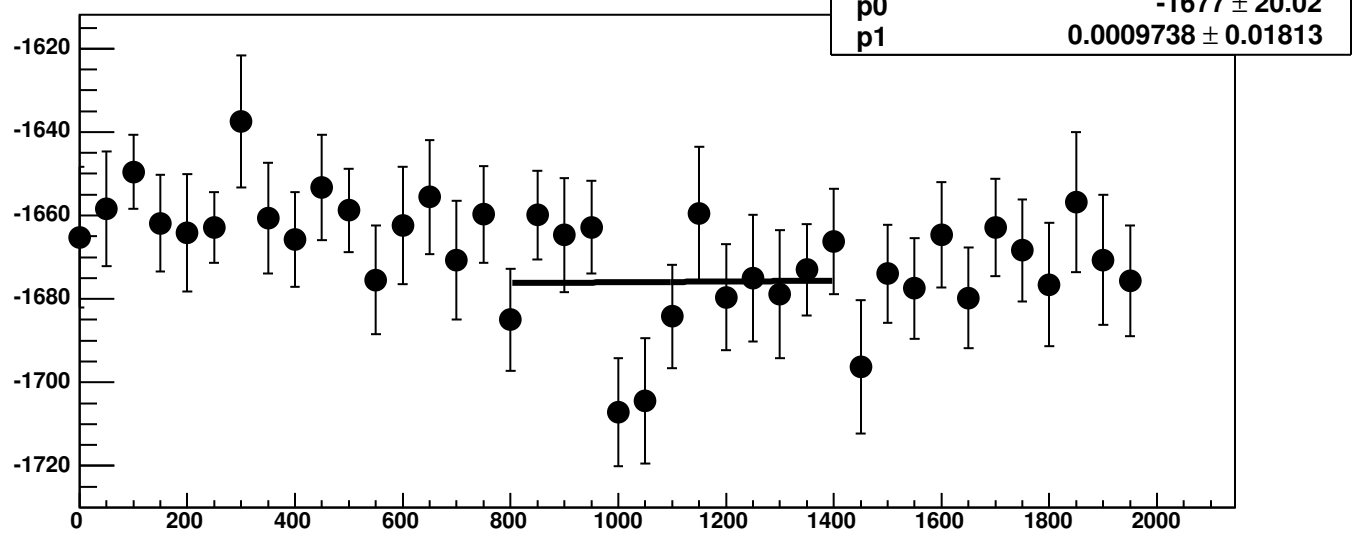

Chip 3, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 16, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 23.29 / 11
p0 -1650 ± 20.15
p1 16.65 ± 0.01765
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

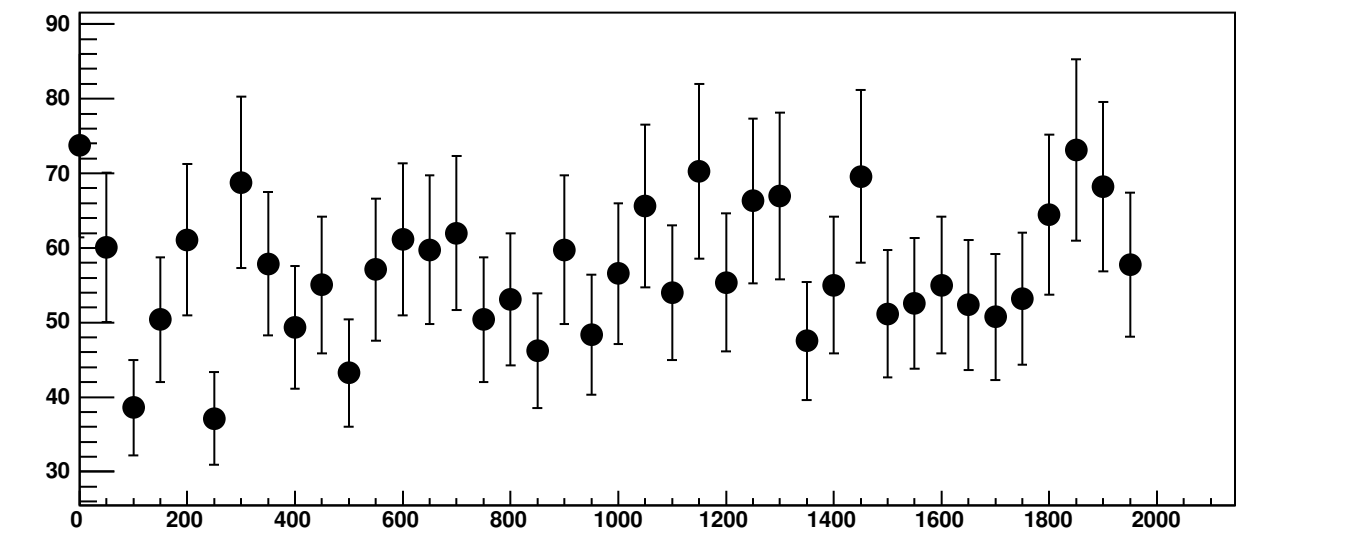

Chip 3, Channel 16, Enable 2!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 16, Enable 2!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

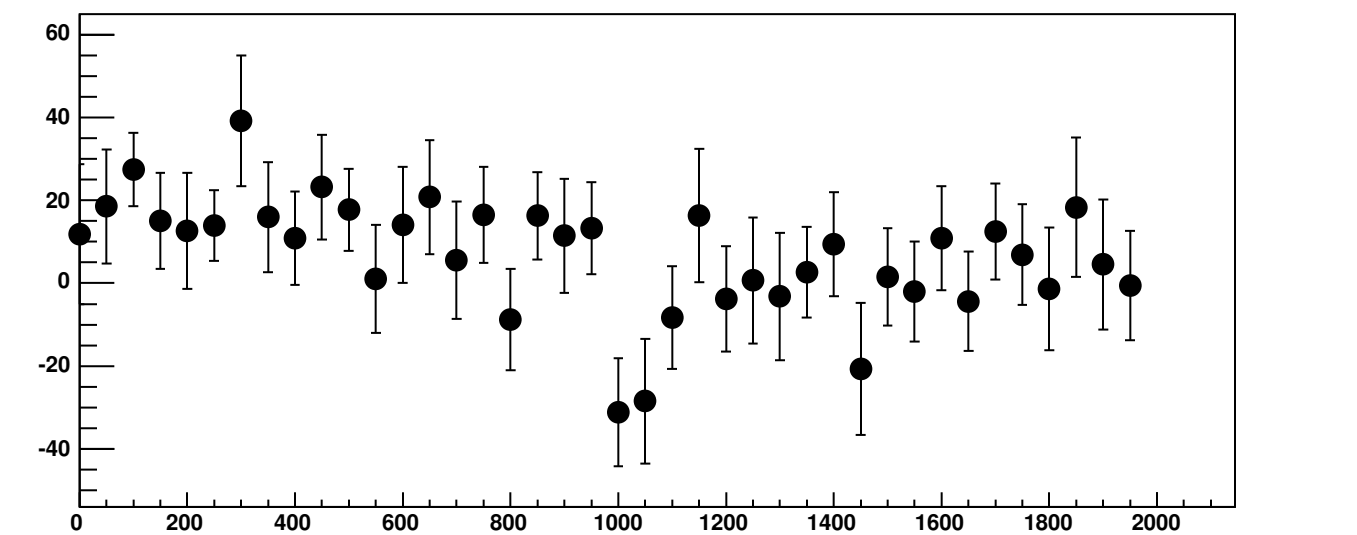
Chip 3, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC

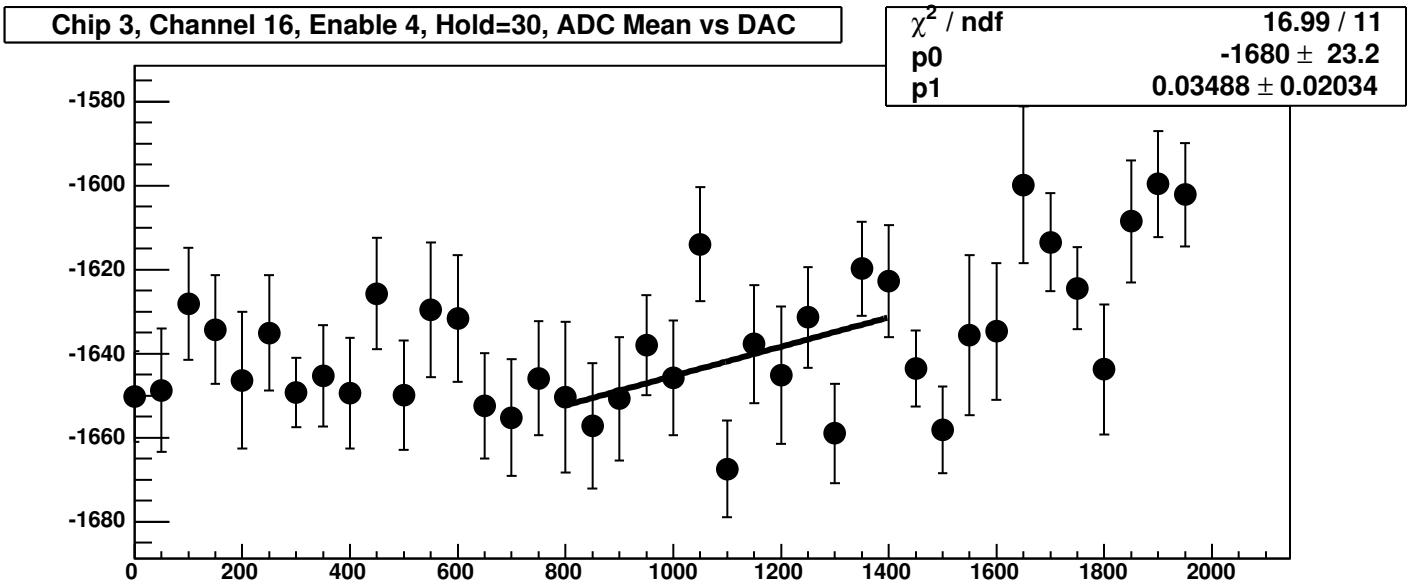


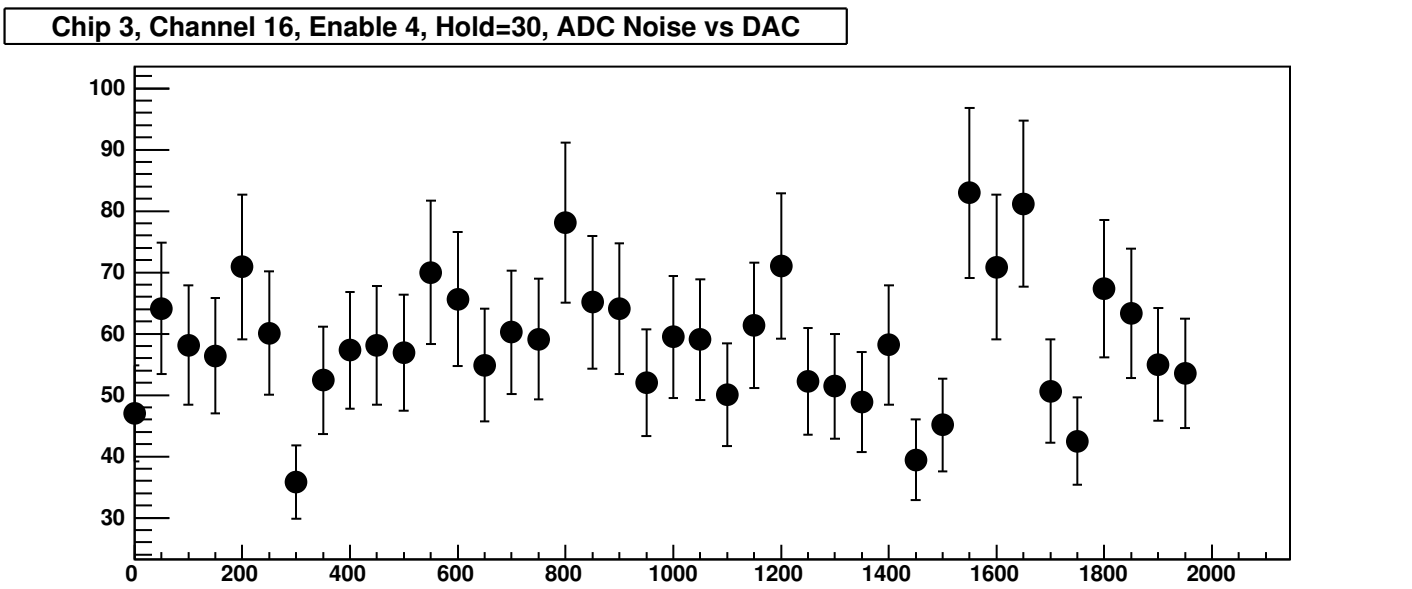
Chip 3, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC

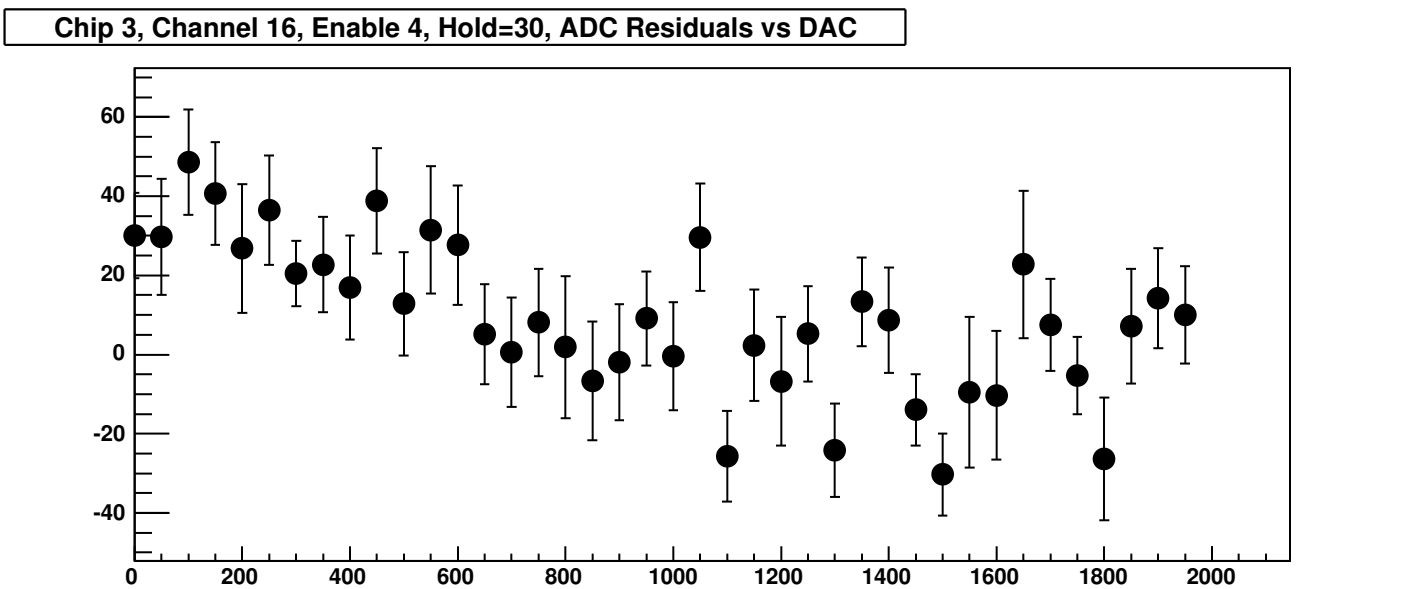


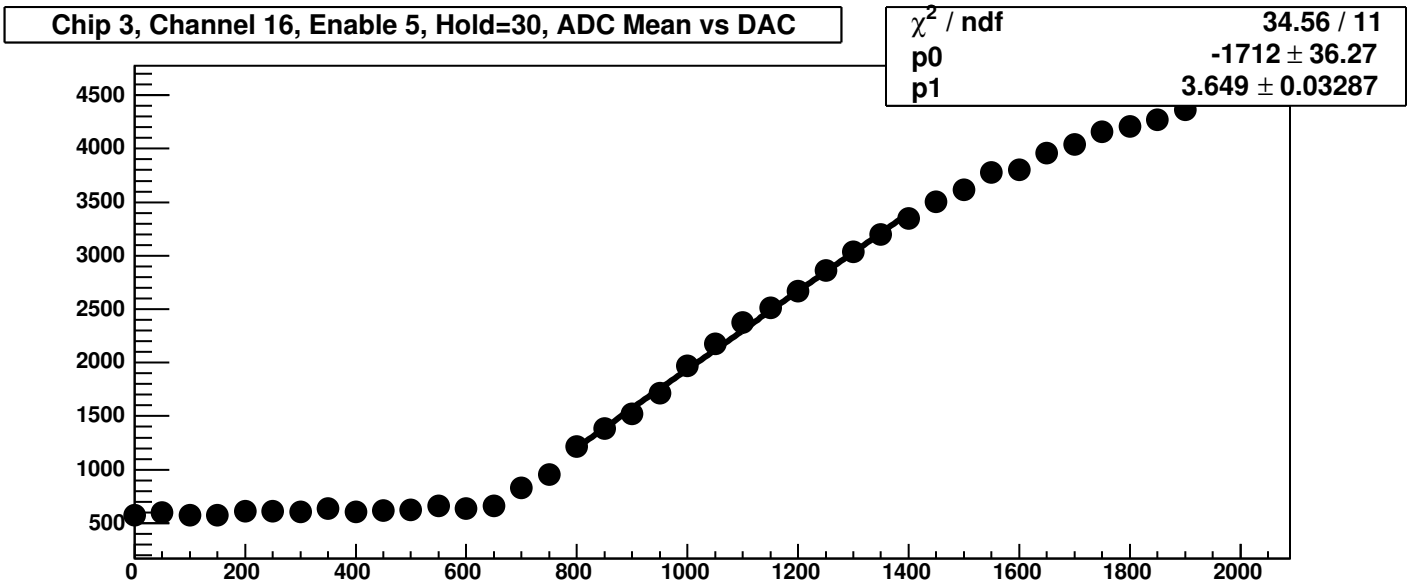
Chip 3, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC

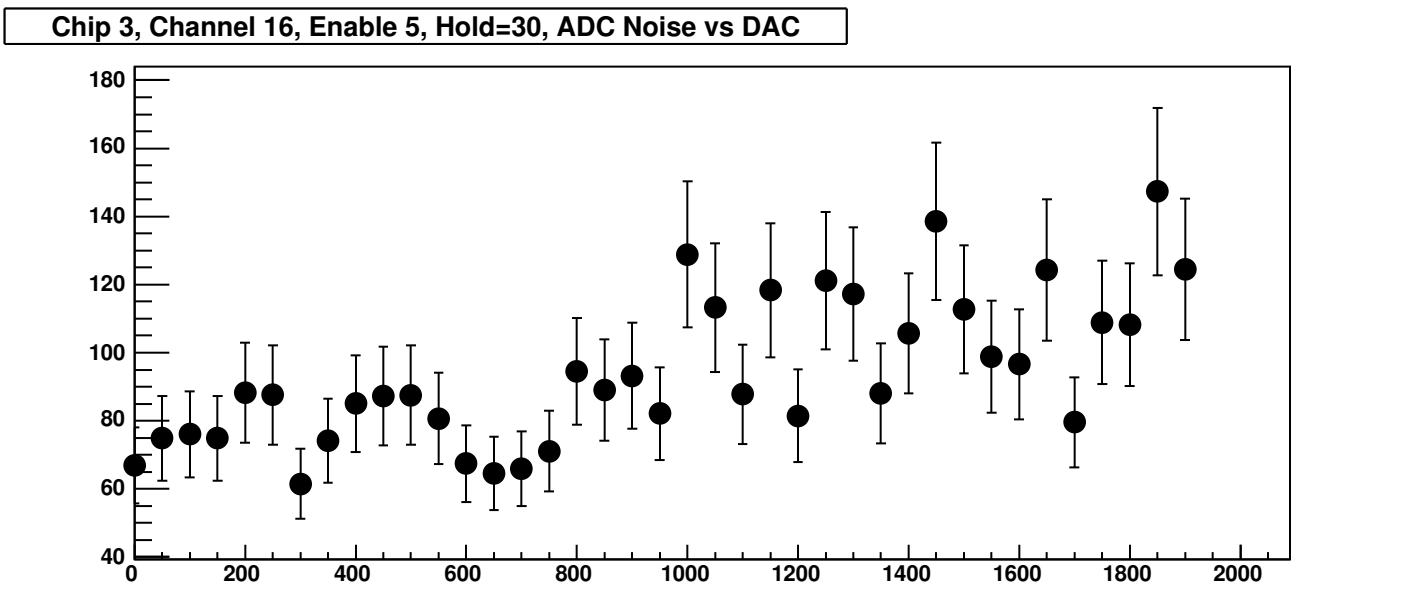


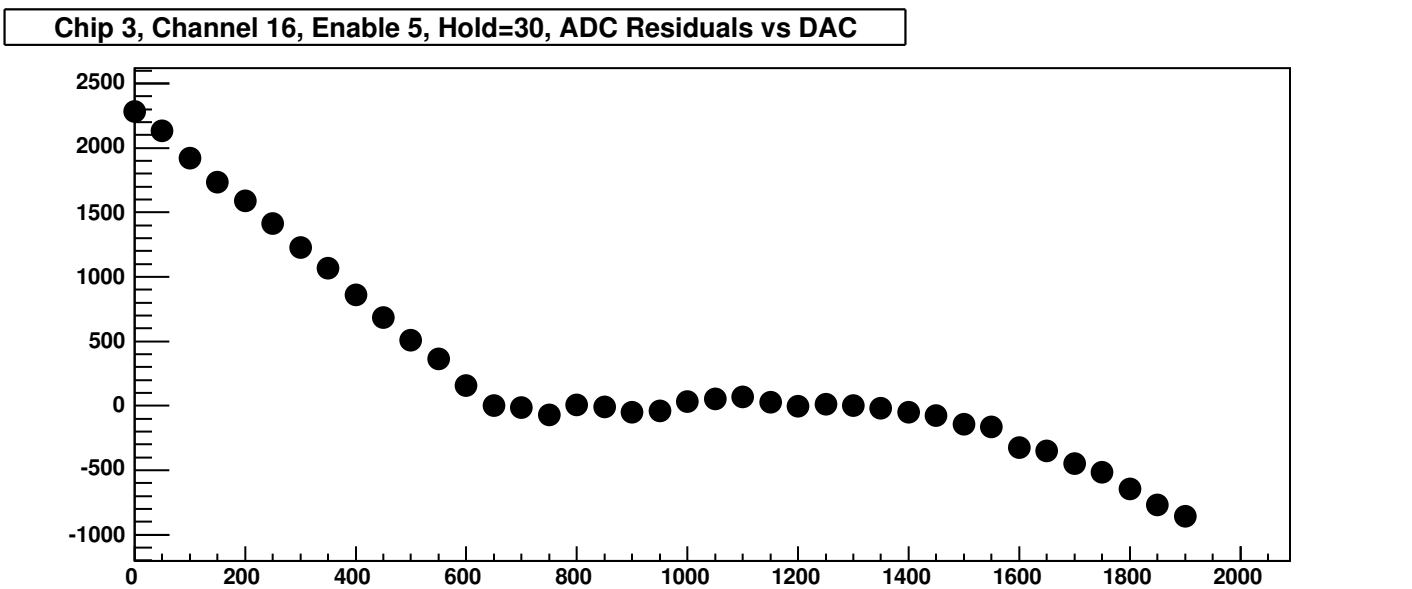

Chip 3, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 16.99 / 11
p0 -1680 ± 23.2
p1 0.03488 ± 0.02034
-1580
-1600
-1620
-1640
-1660
-1680
0
200
400
600
800
1000
1200
1400
1600
1800
2000

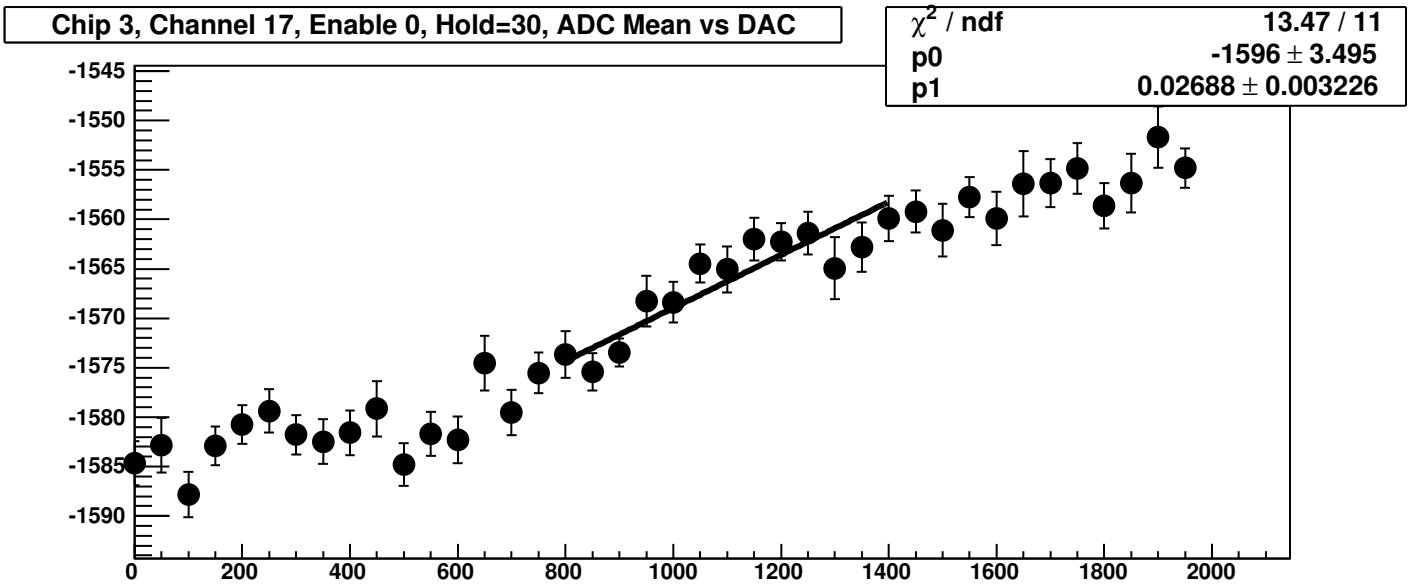

Chip 3, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

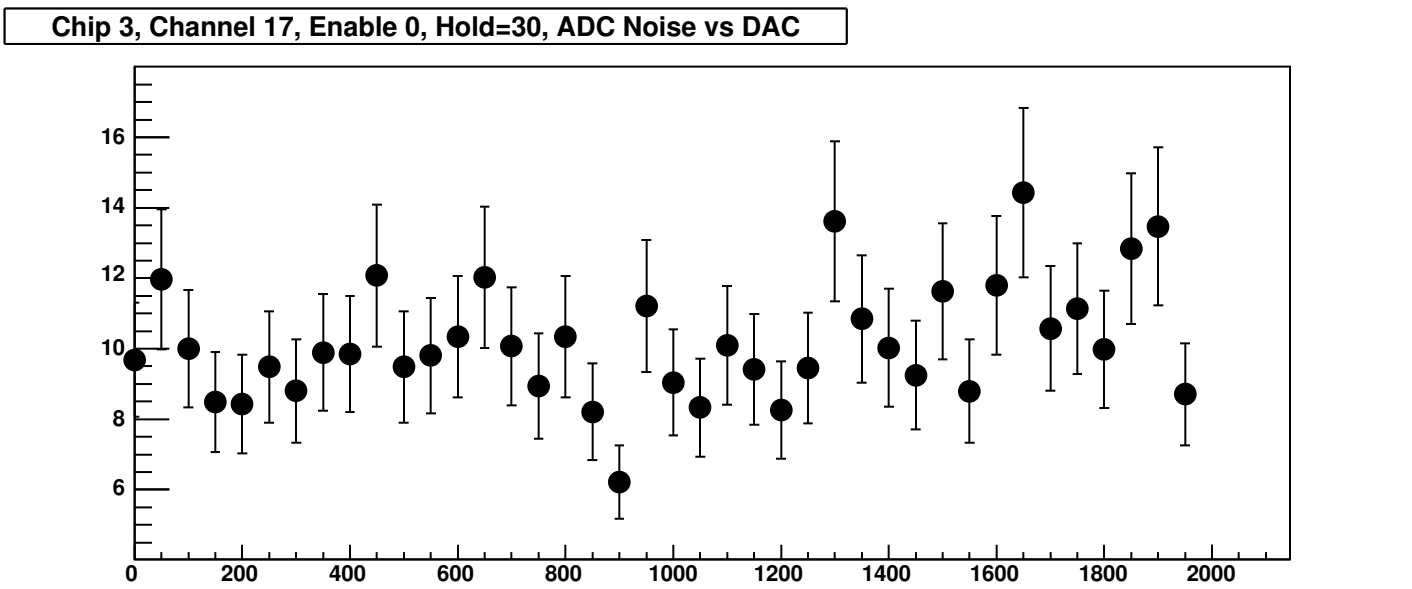

Chip 3, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

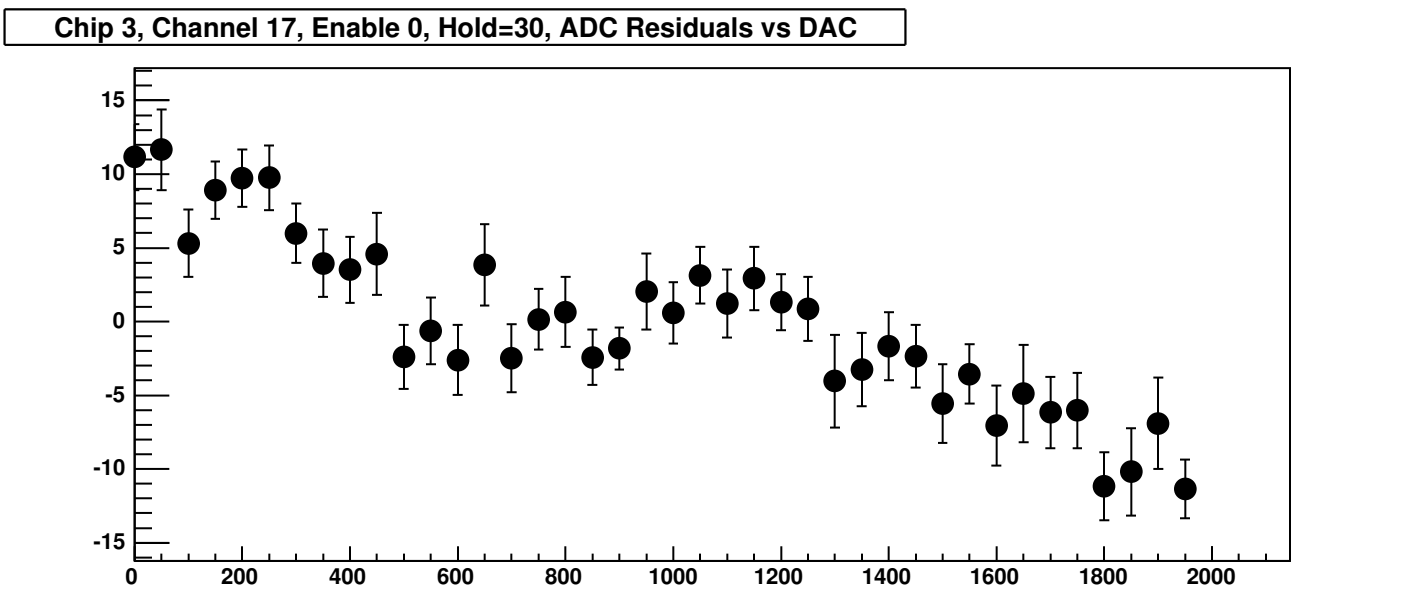

Chip 3, Channel 16, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 34.56 / 11
p0 -1712 ± 36.27
p1 3.649 ± 0.03287
4500
4000
3500
3000
2500
2000
1500
1000
500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

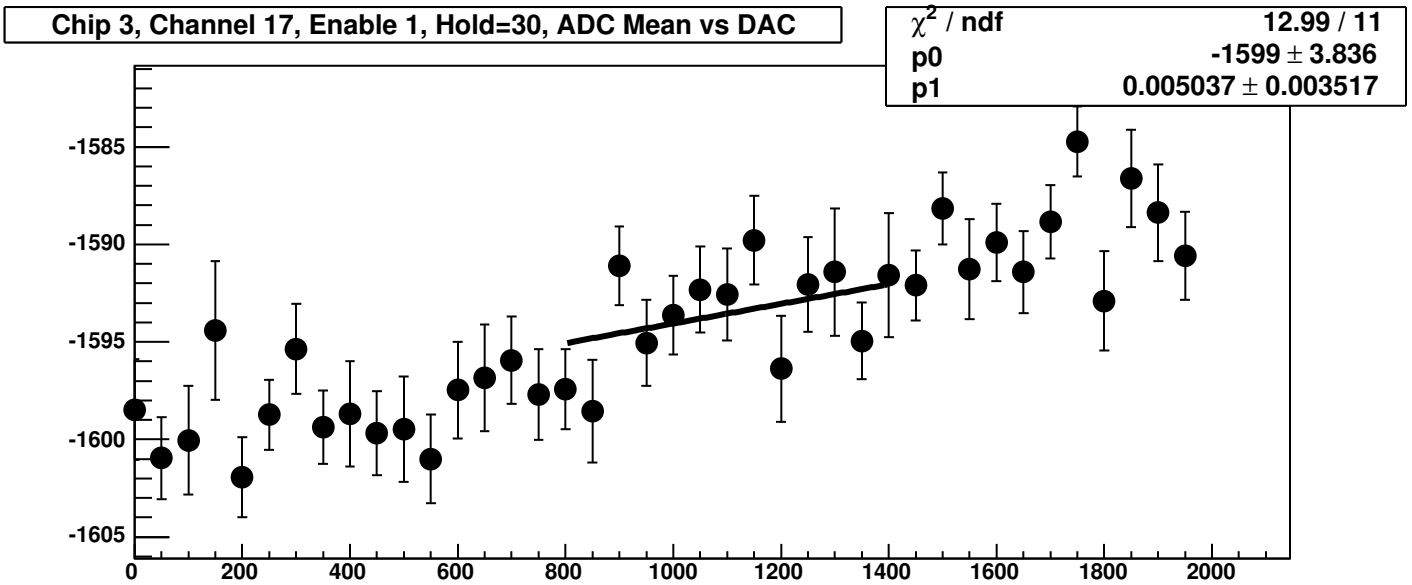

Chip 3, Channel 16, Enable 5, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

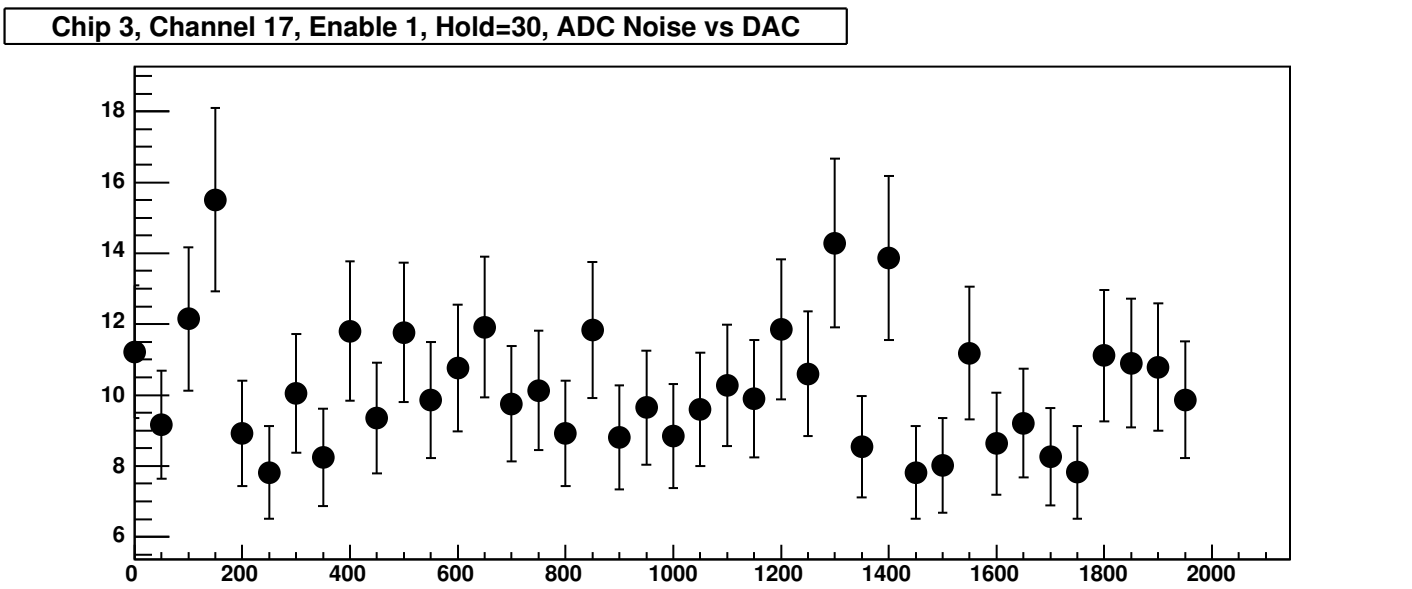

Chip 3, Channel 16, Enable 5, Hold=30, ADC Residuals vs DAC
2500
2000
1500
1000
500
0
-500
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

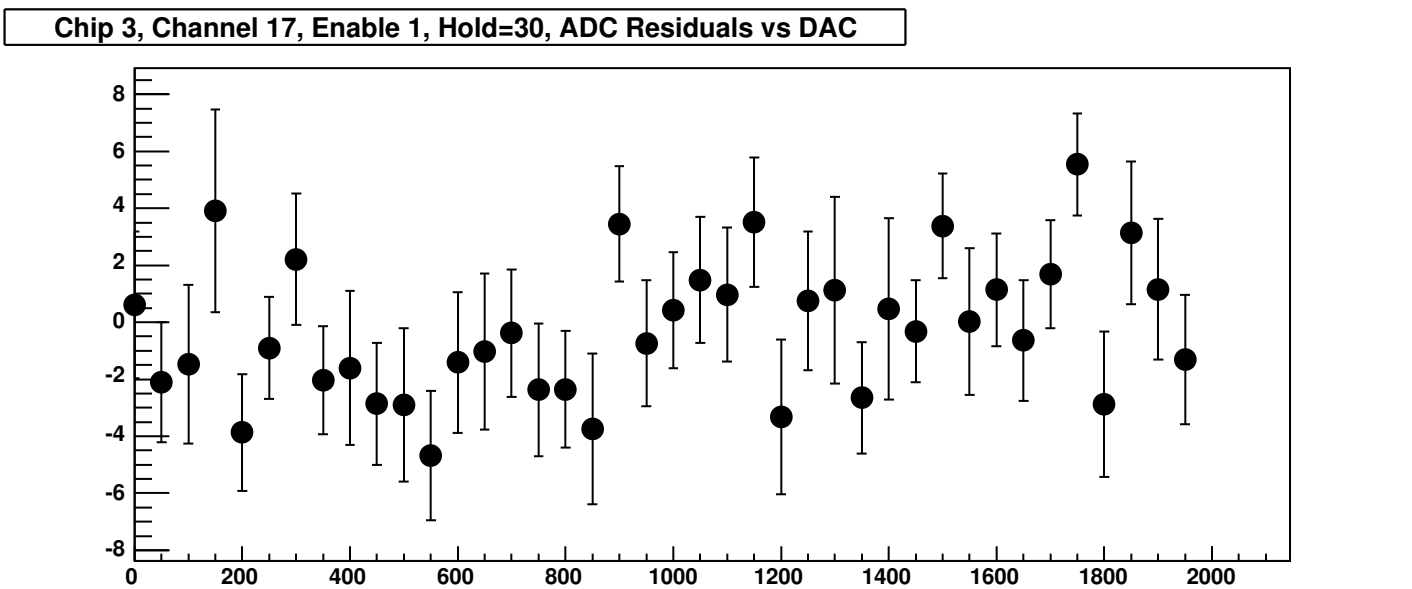

Chip 3, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 13.47 / 11
p0 -1596 ± 3.495
p1 0.02688 ± 0.003226
-1545
-1550
-1555
-1560
-1565
-1570
-1575
-1580
-1585
-1590
0
200
400
600
800
1000
1200
1400
1600
1800
2000

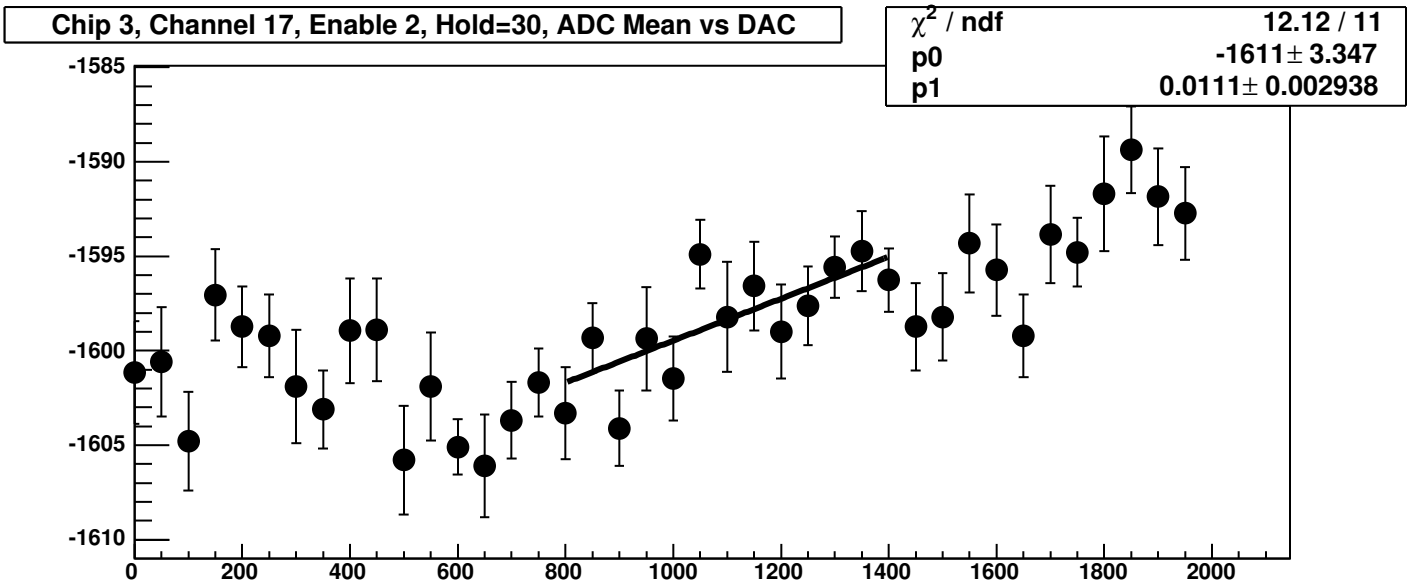

Chip 3, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

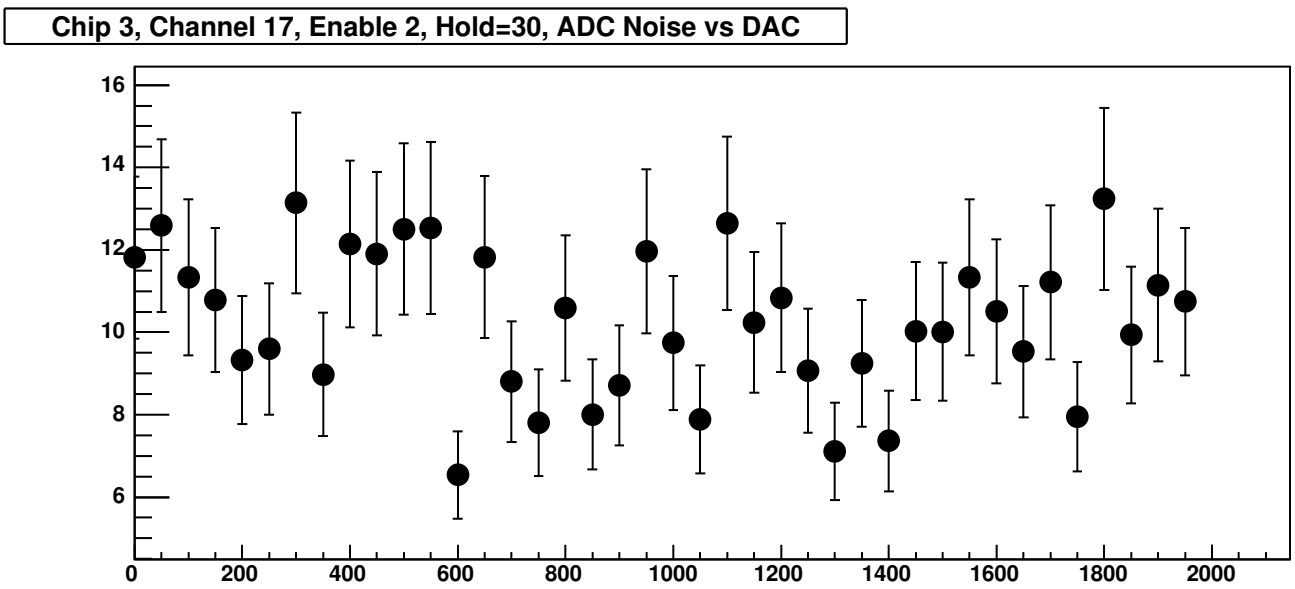

Chip 3, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
200
400
600
800
1000
1200
1400
1600
1800
2000

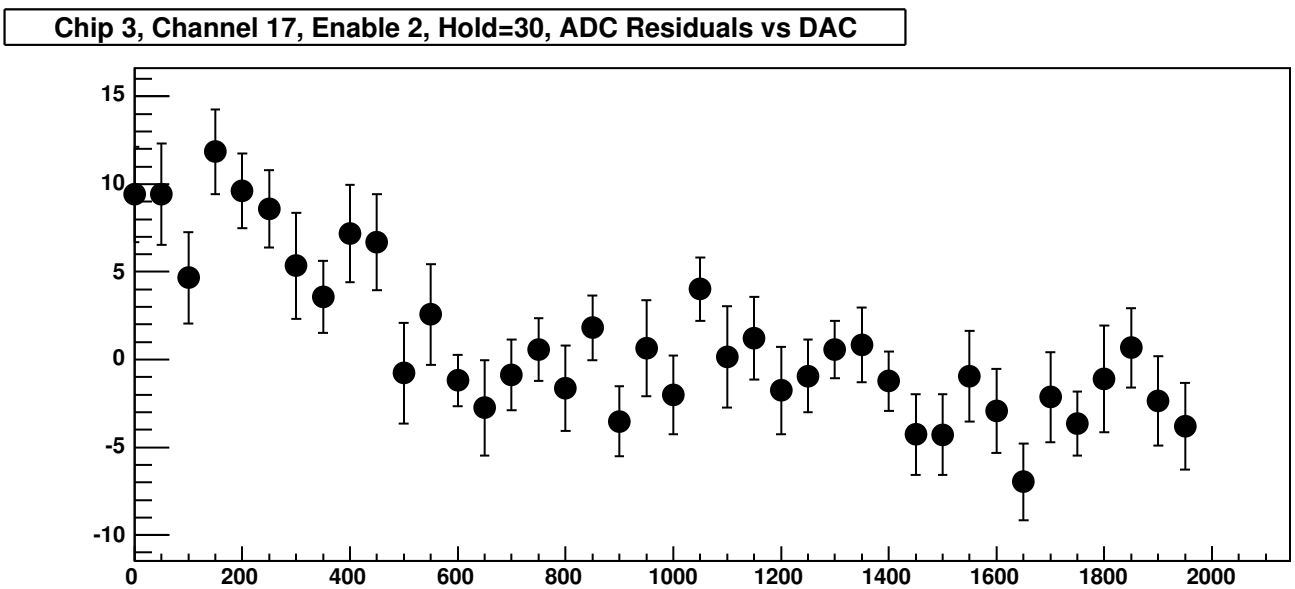

Chip 3, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 12.99 / 11
p0 -1599 ± 3.836
p1 0.005037 ± 0.003517
-1585
-1590
-1595
-1600
-1605
0
200
400
600
800
1000
1200
1400
1600
1800
2000

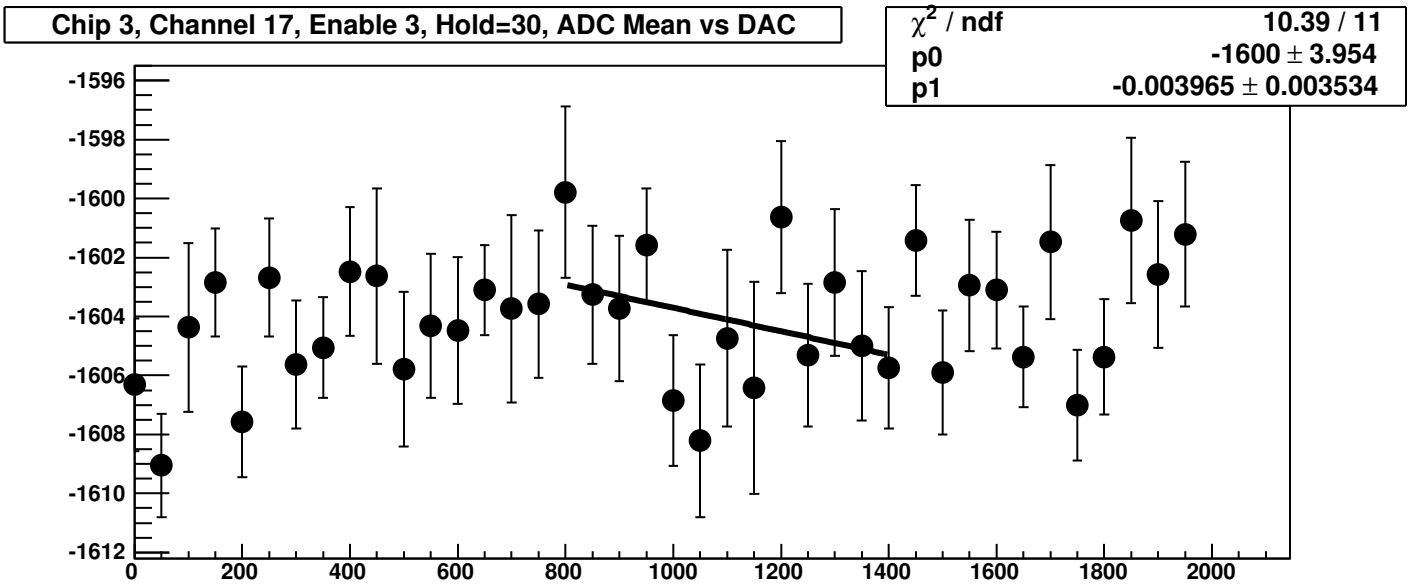

Chip 3, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
18
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

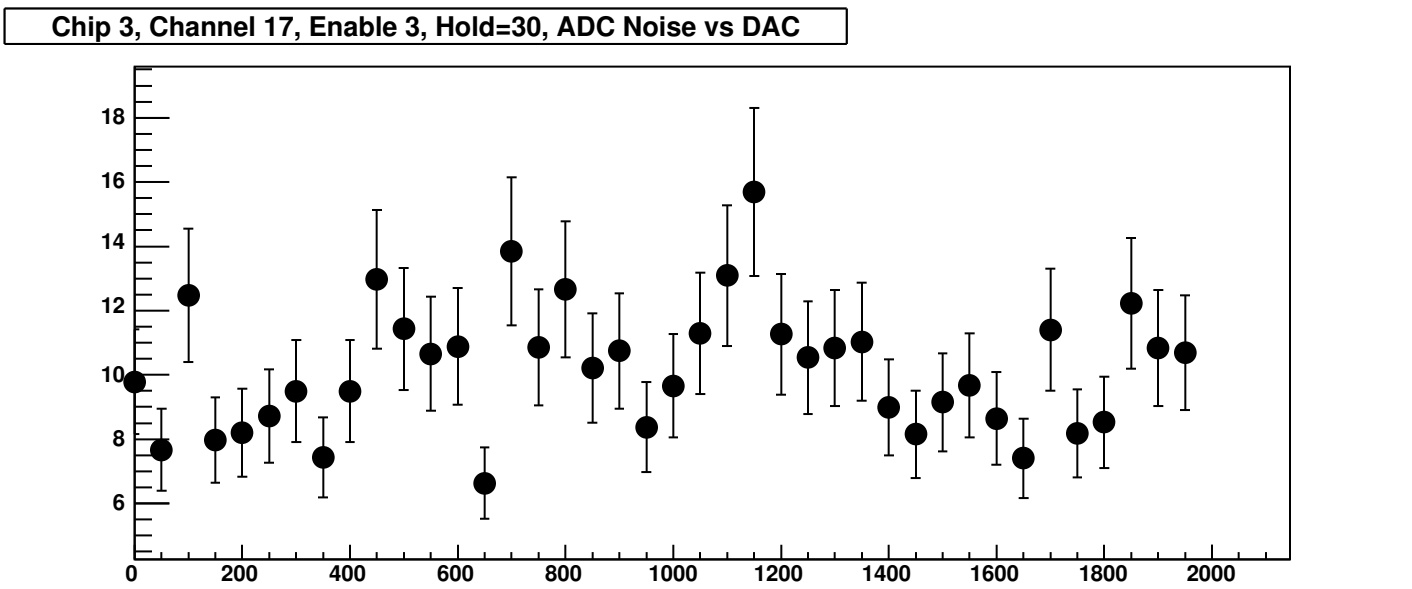

Chip 3, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
0
200
400
600
800
1000
1200
1400
1600
1800
2000

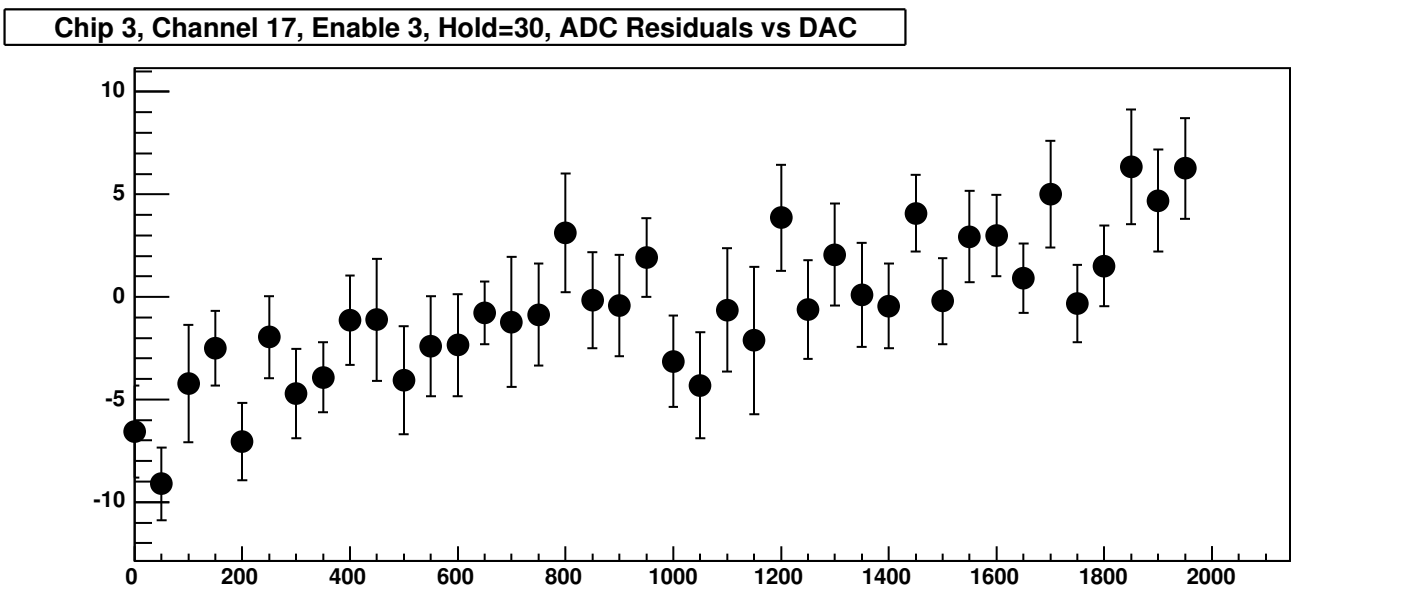

Chip 3, Channel 17, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 12.12 / 11
p0 -1611 ± 3.347
p1 0.0111 ± 0.002938
-1585
-1590
-1595
-1600
-1605
-1610
0
200
400
600
800
1000
1200
1400
1600
1800
2000

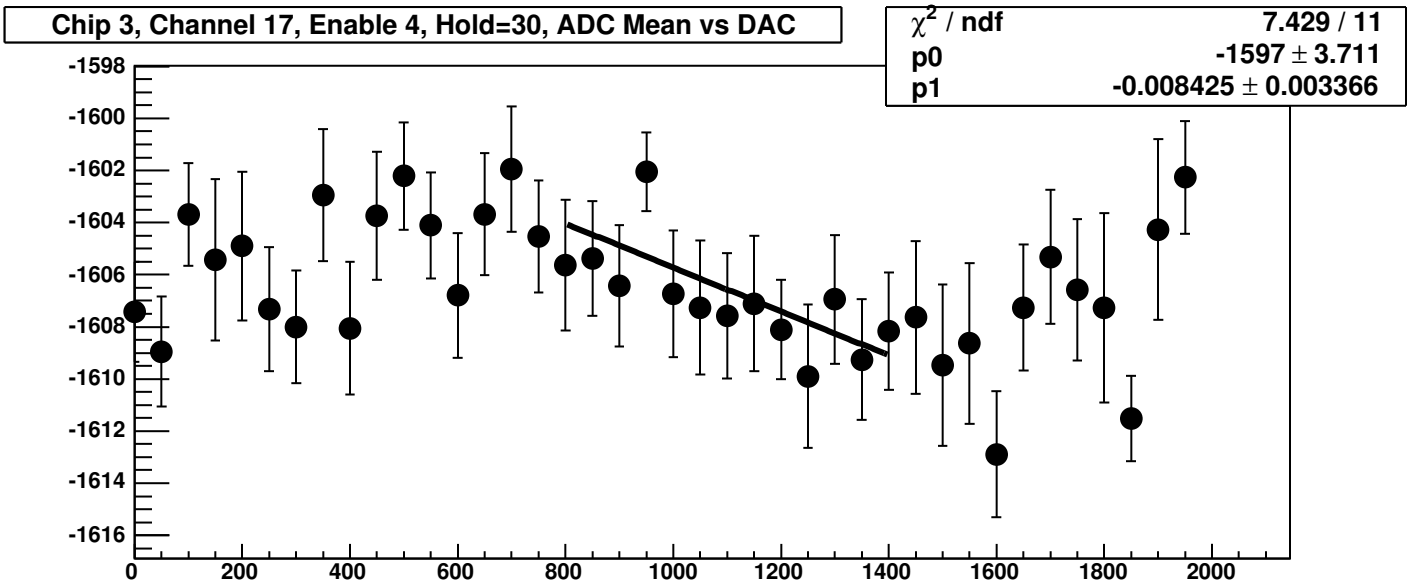

Chip 3, Channel 17, Enable 2, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

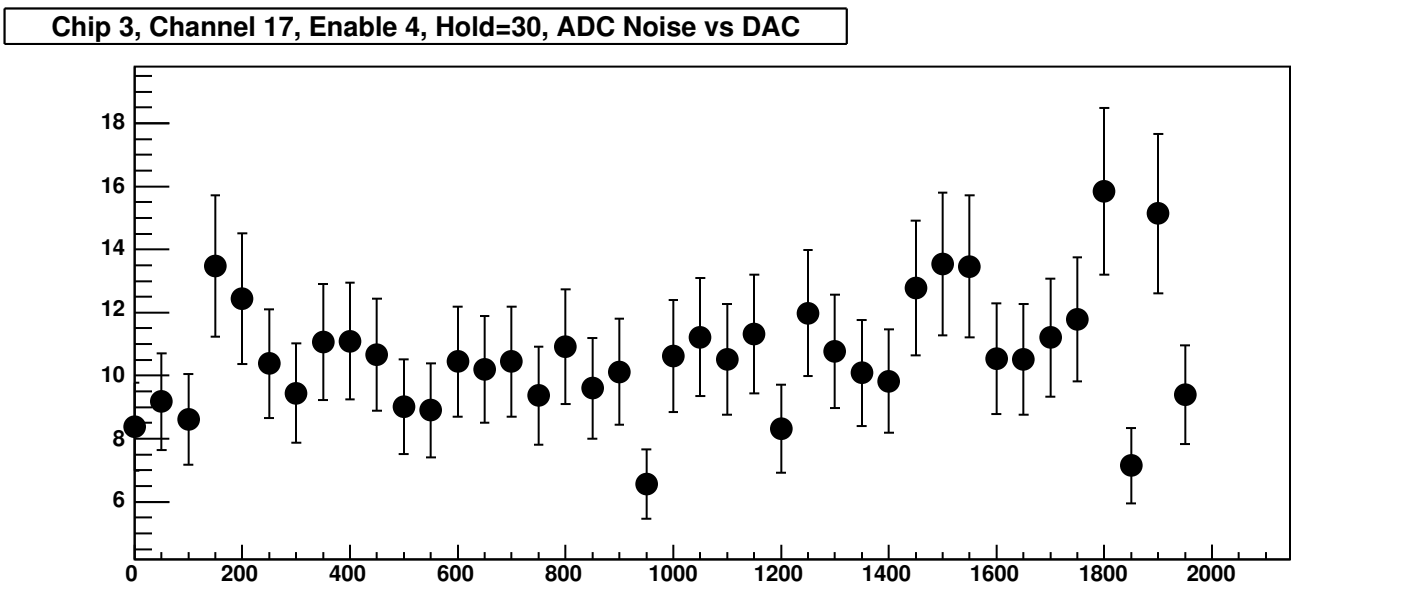

Chip 3, Channel 17, Enable 2, Hold=30, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

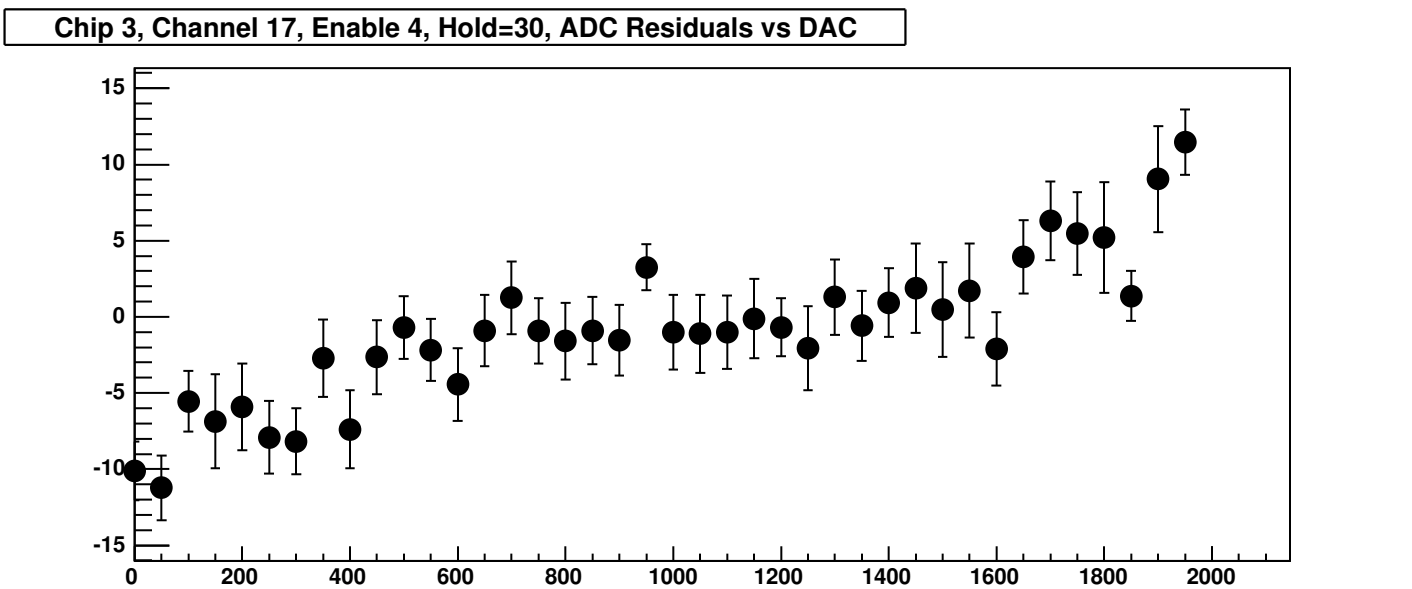

Chip 3, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 10.39 / 11
p0 -1600 ± 3.954
p1 -0.003965 ± 0.003534
-1596
-1598
-1600
-1602
-1604
-1606
-1608
-1610
-1612
0
200
400
600
800
1000
1200
1400
1600
1800
2000

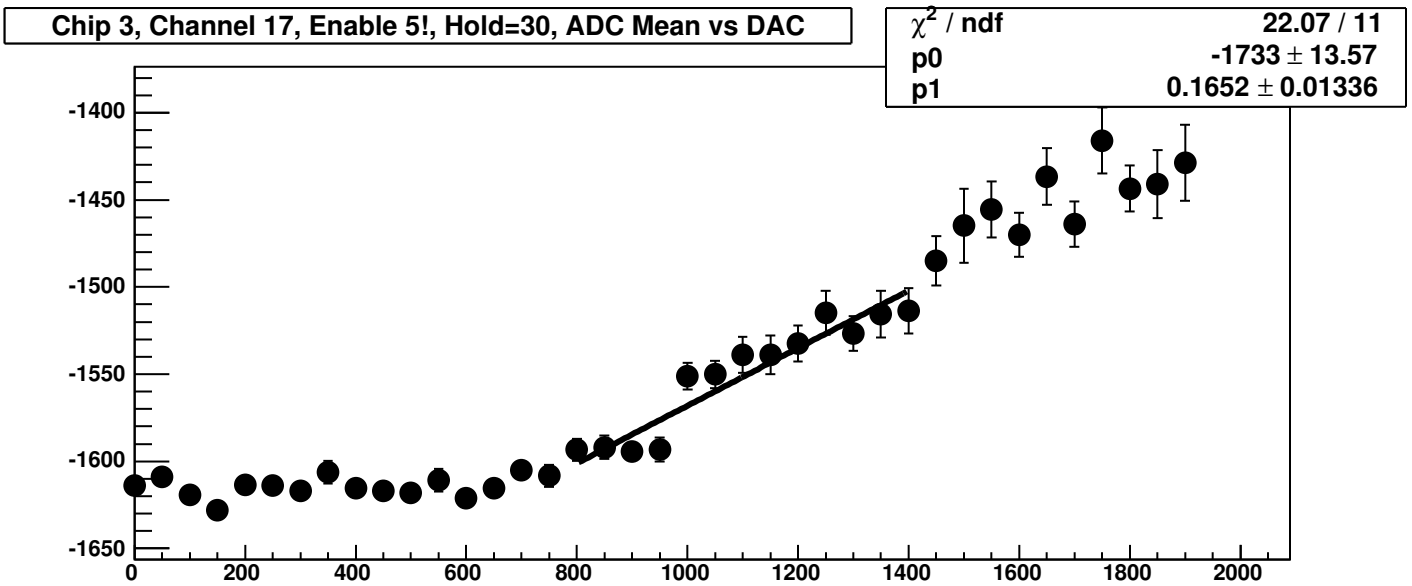

Chip 3, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
18
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

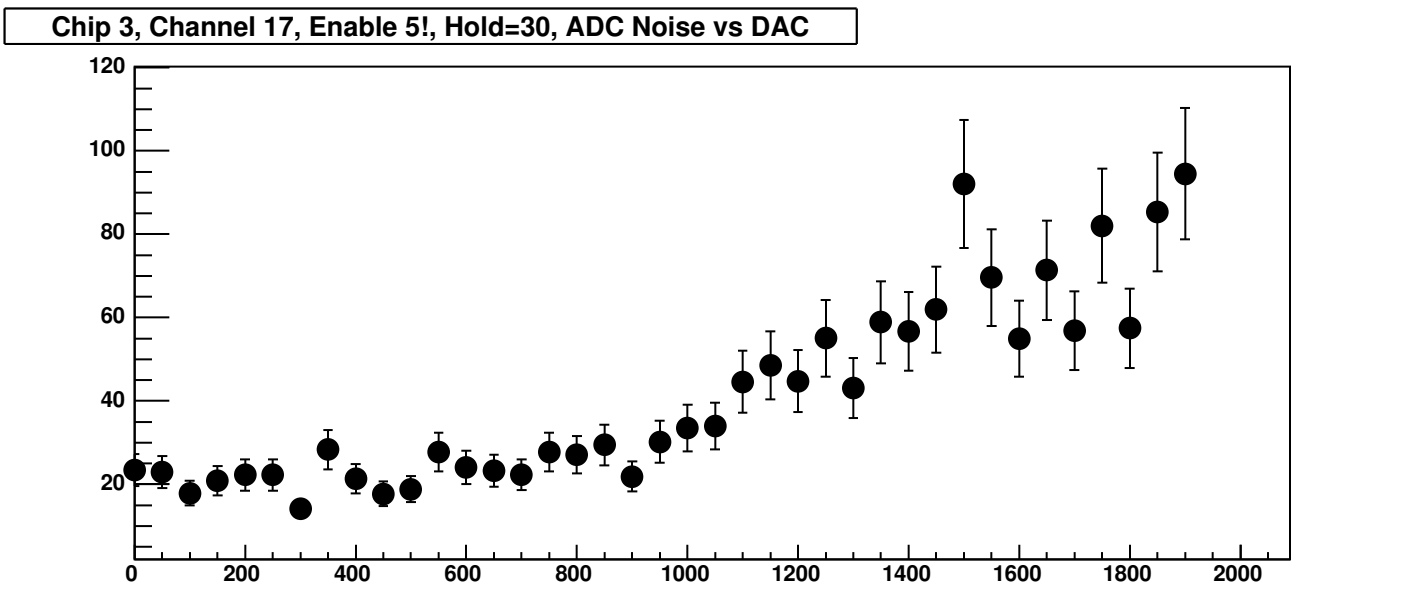

Chip 3, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

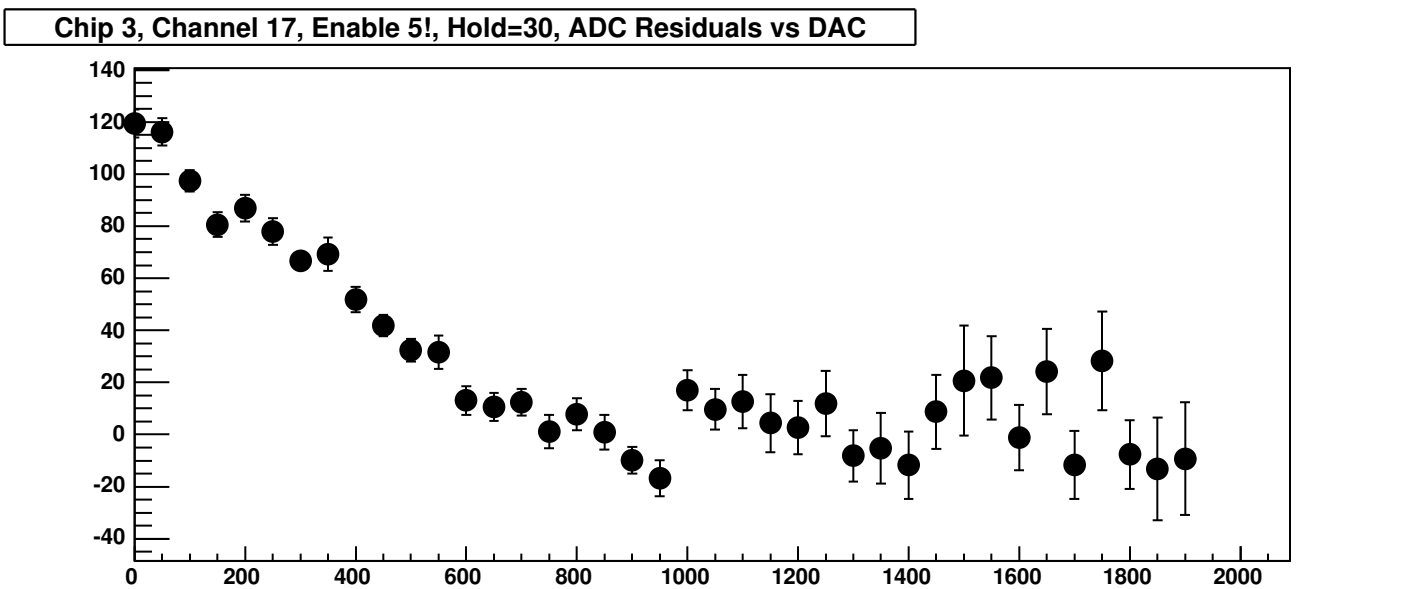

Chip 3, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7.429 / 11
p0 -1597 ± 3.711
p1 -0.008425 ± 0.003366
-1598
-1600
-1602
-1604
-1606
-1608
-1610
-1612
-1614
-1616
0
200
400
600
800
1000
1200
1400
1600
1800
2000

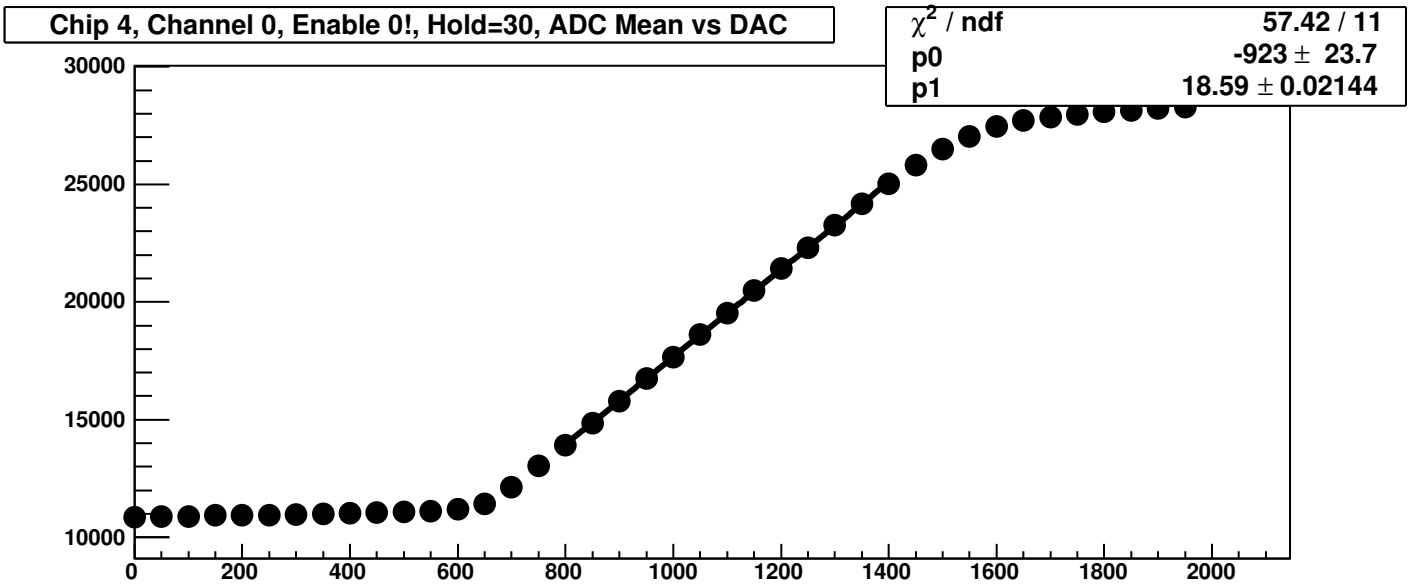

Chip 3, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC
18
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

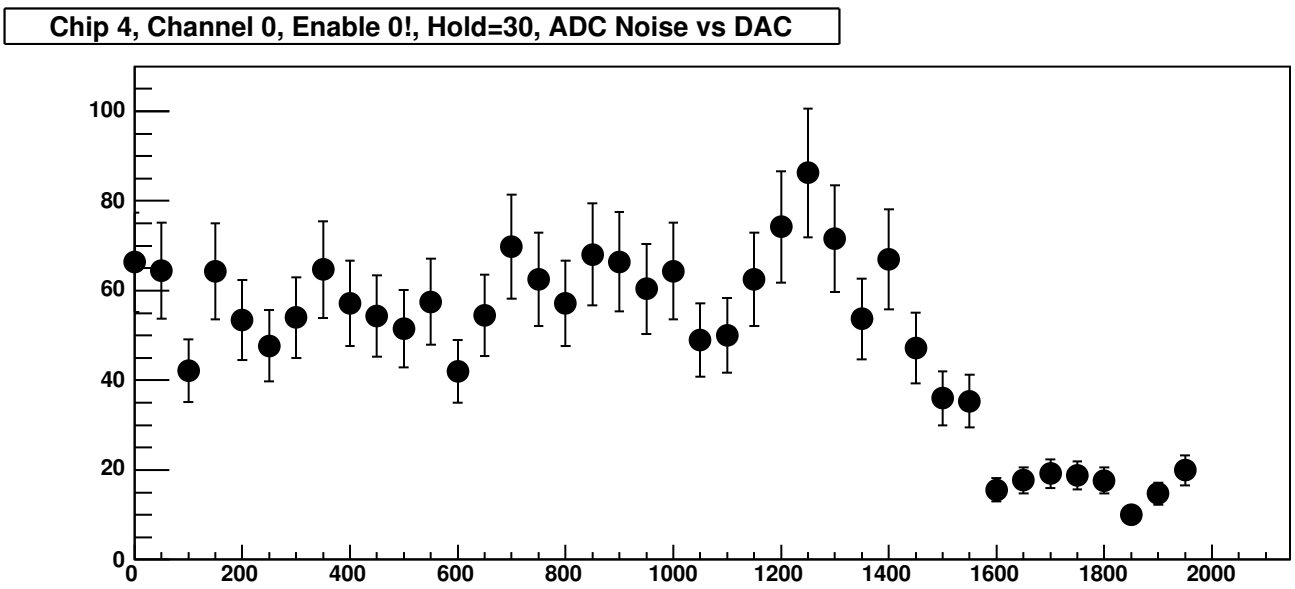

Chip 3, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
200
400
600
800
1000
1200
1400
1600
1800
2000

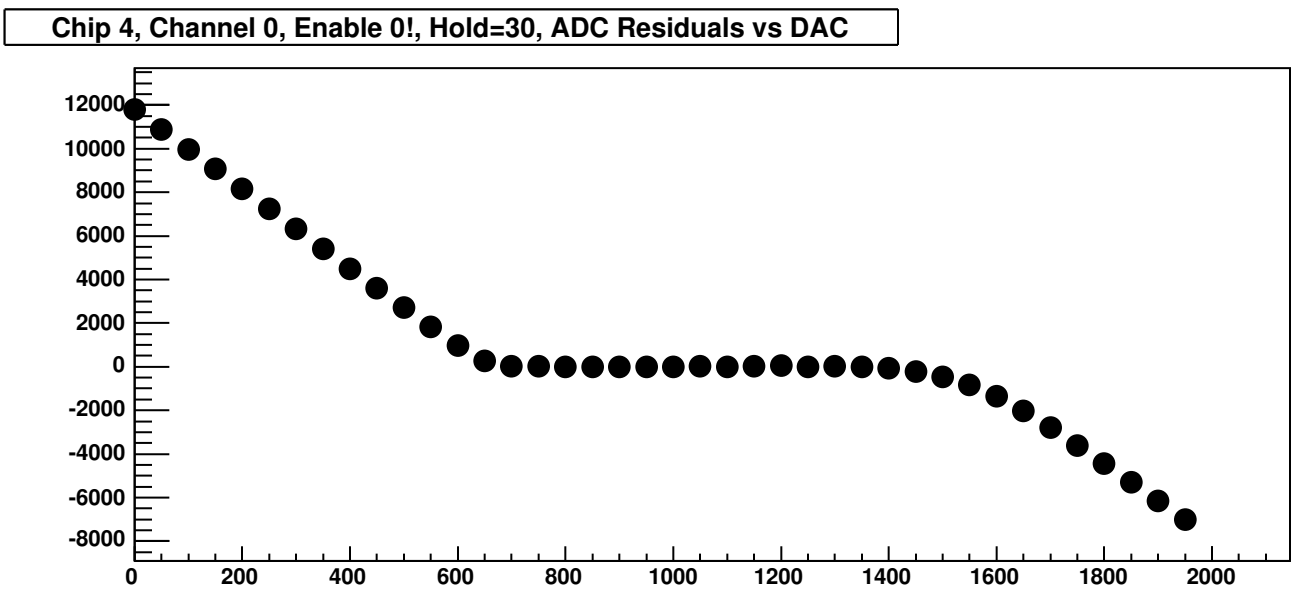

Chip 3, Channel 17, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 22.07 / 11
p0 -1733 ± 13.57
p1 0.1652 ± 0.01336
-1400
-1450
-1500
-1550
-1600
-1650
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 3, Channel 17, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

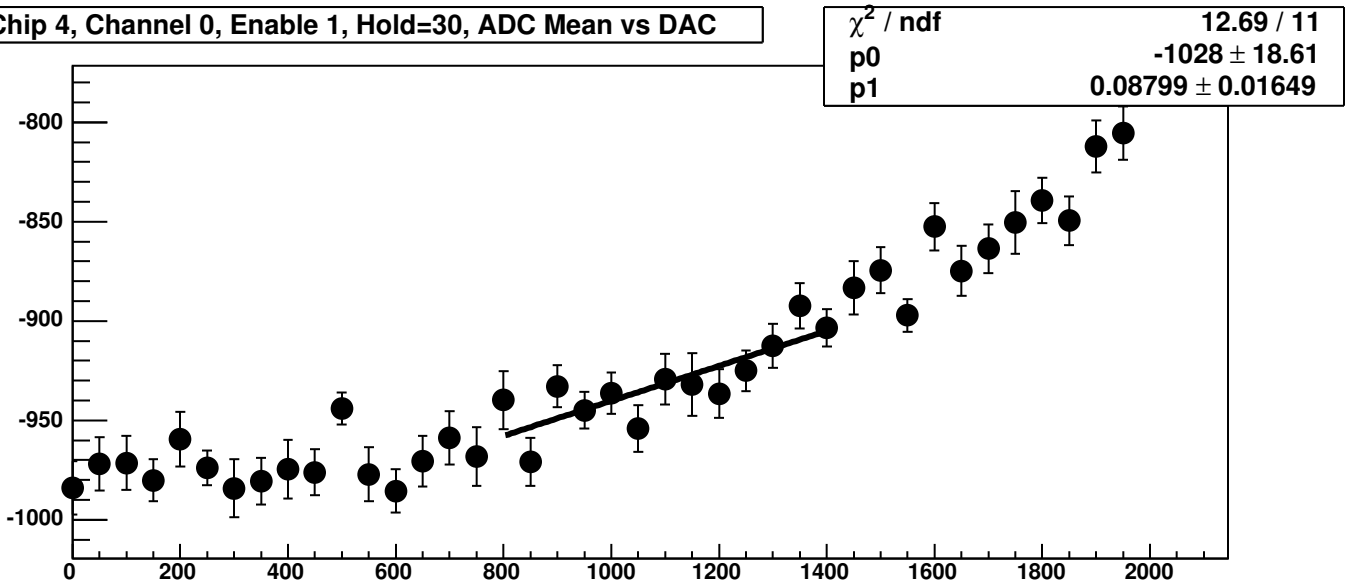

Chip 3, Channel 17, Enable 5!, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 0, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 57.42 / 11
p0 -923 ± 23.7
p1 18.59 ± 0.02144
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

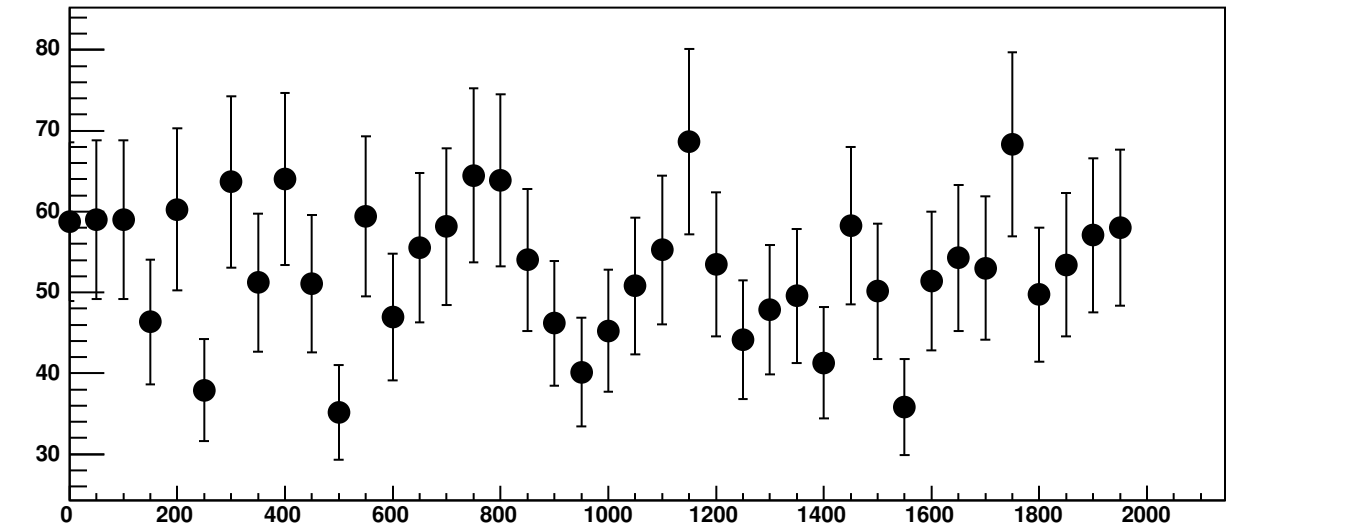

Chip 4, Channel 0, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 0, Enable 0!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

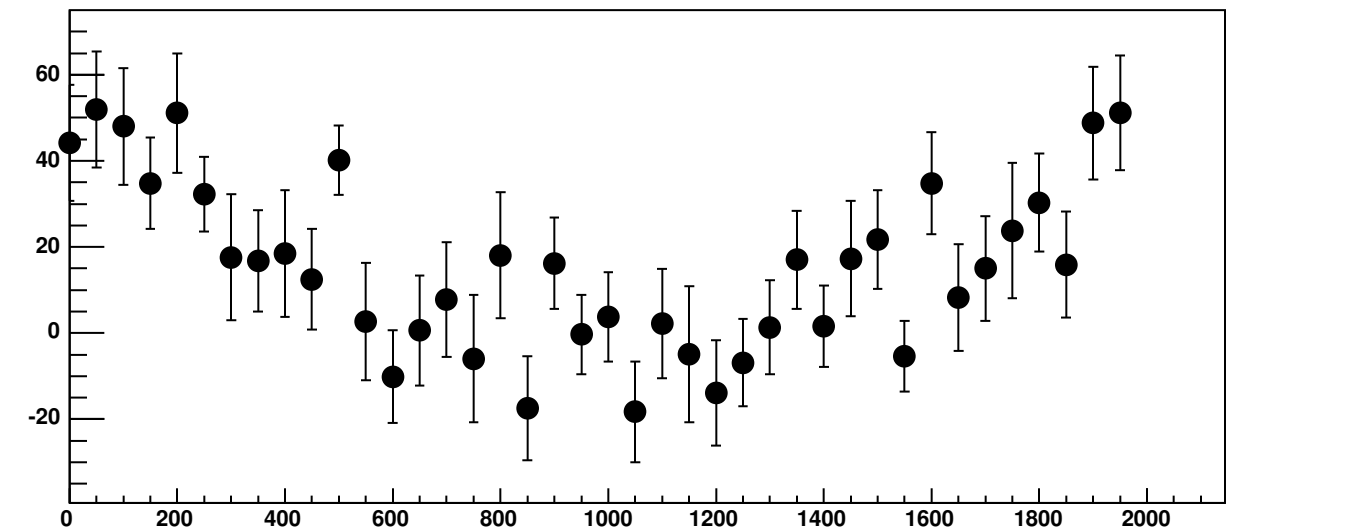
Chip 4, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC



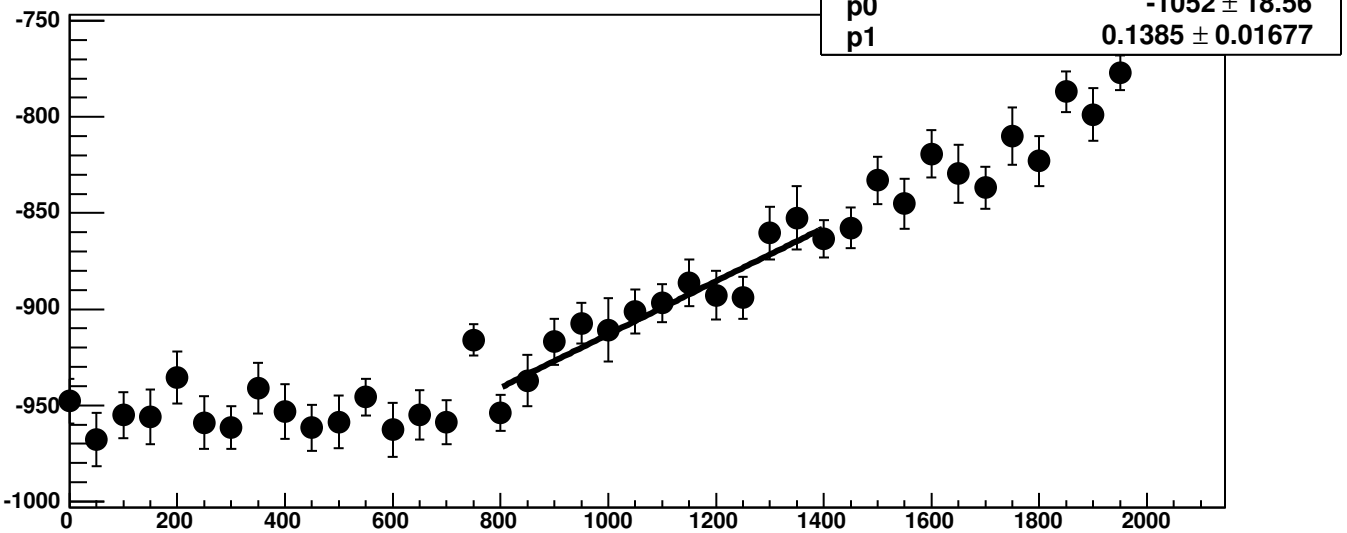
Chip 4, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC



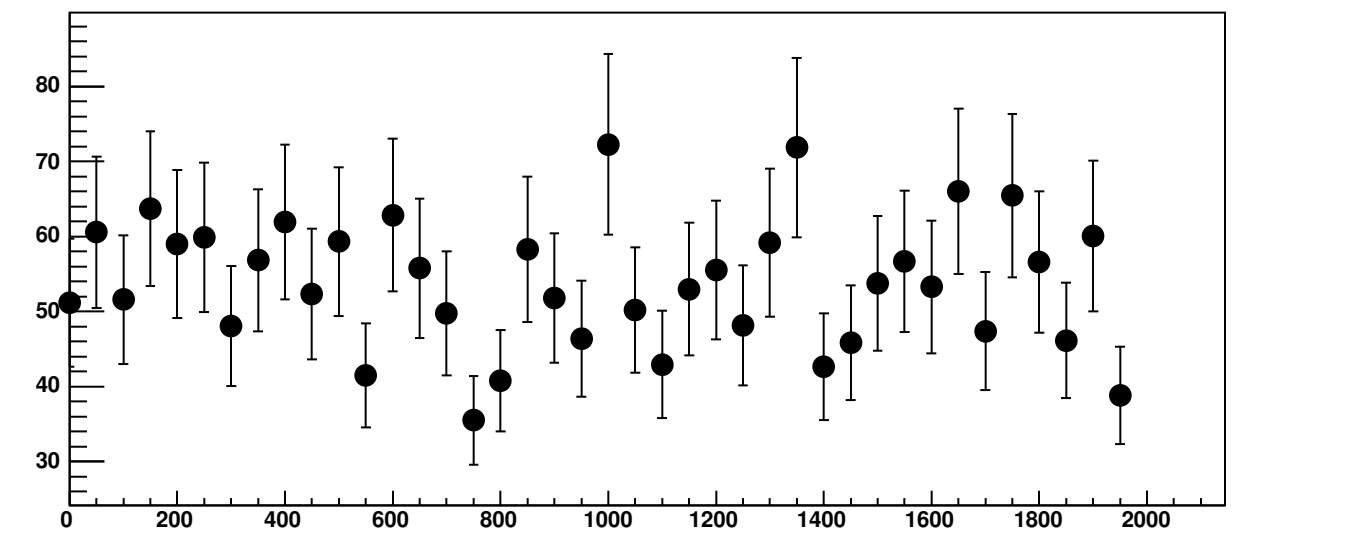
Chip 4, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC



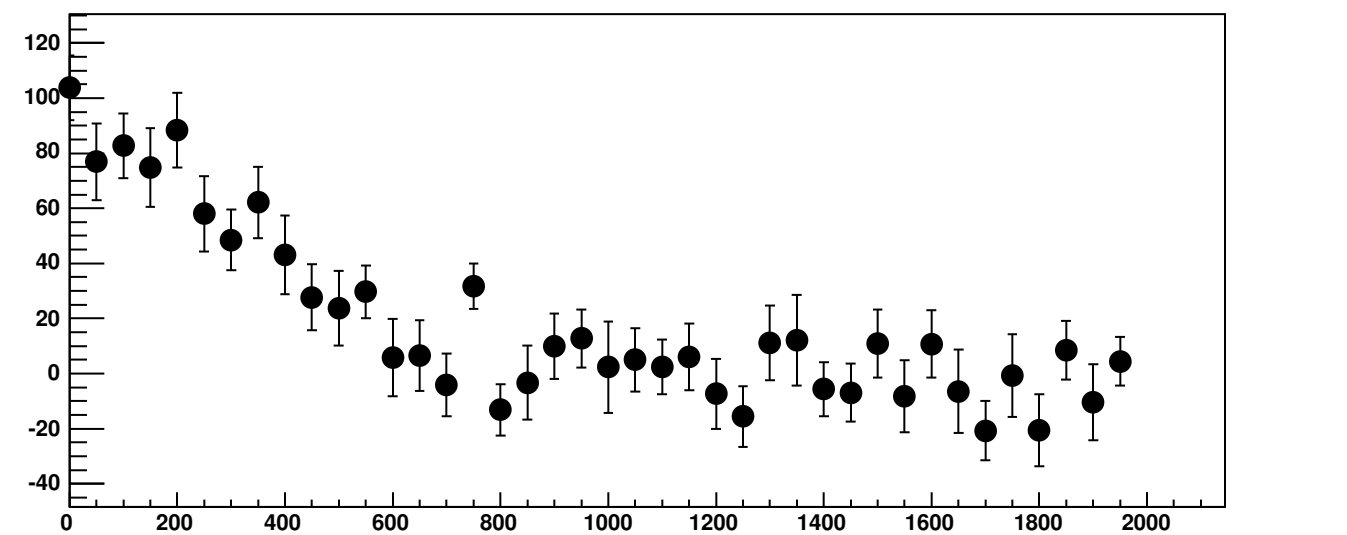
Chip 4, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC

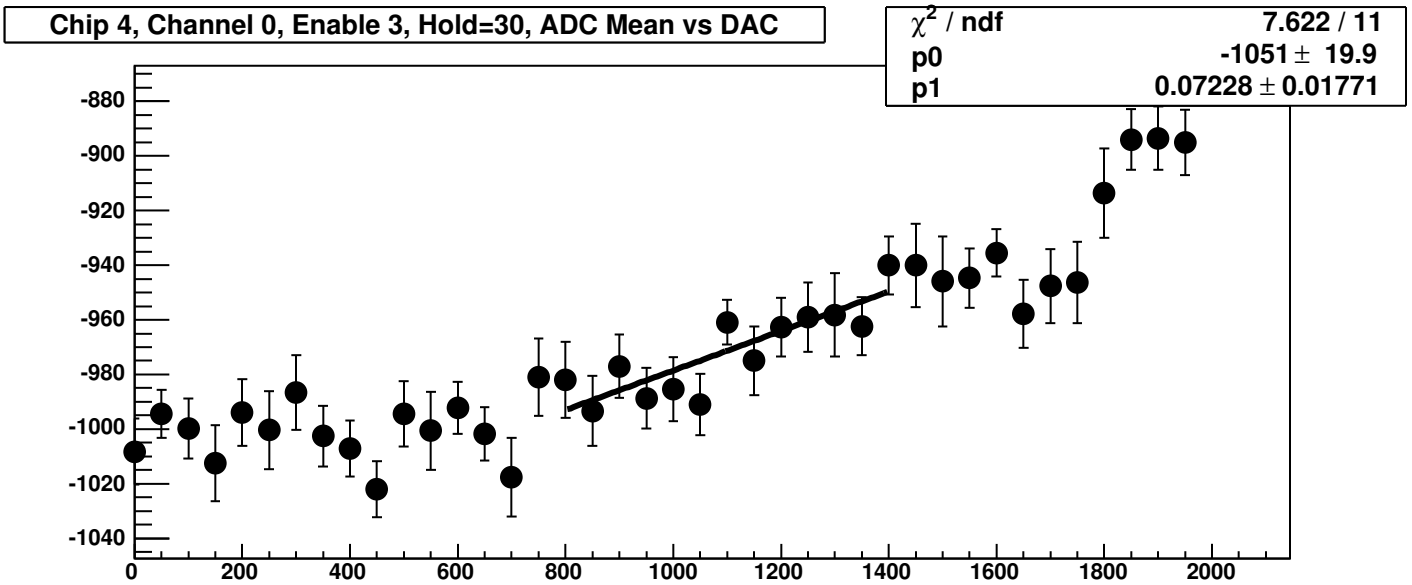


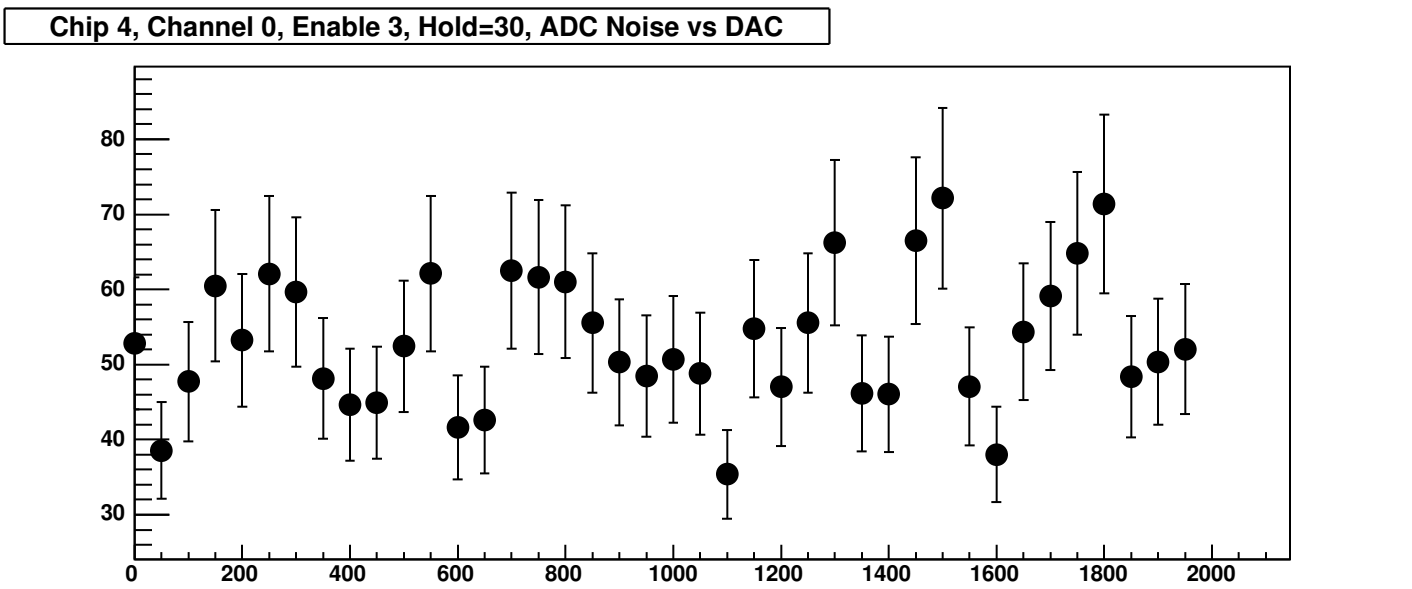
Chip 4, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC

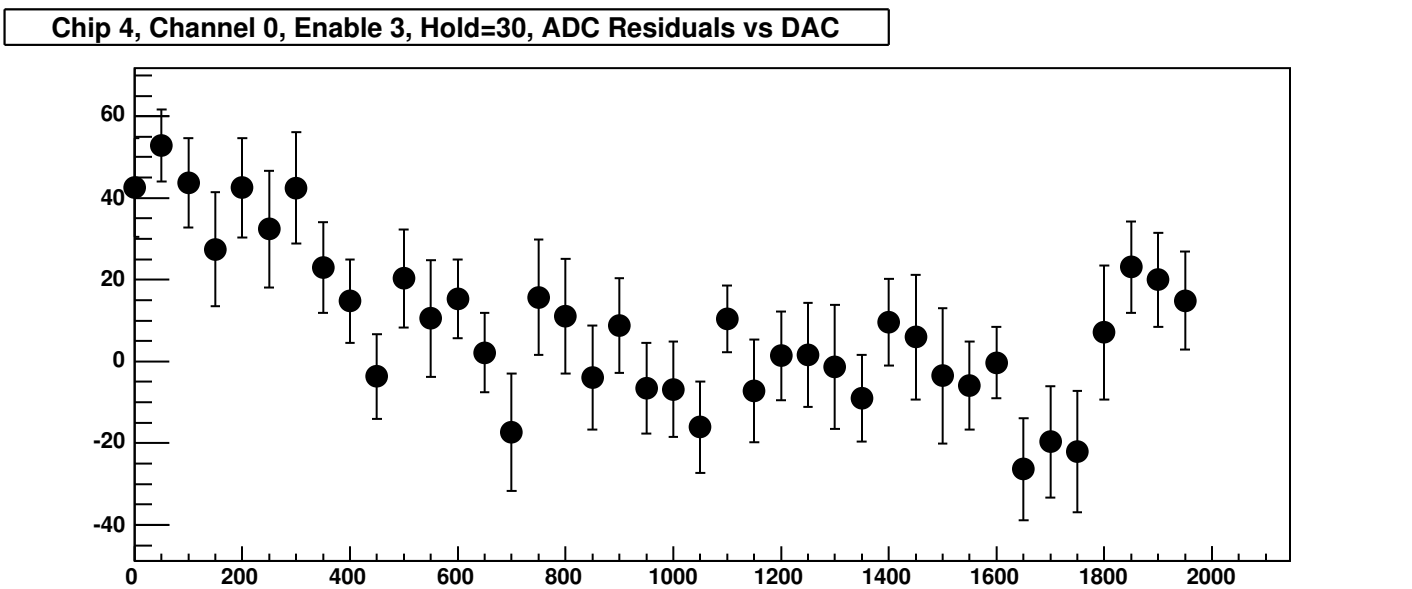


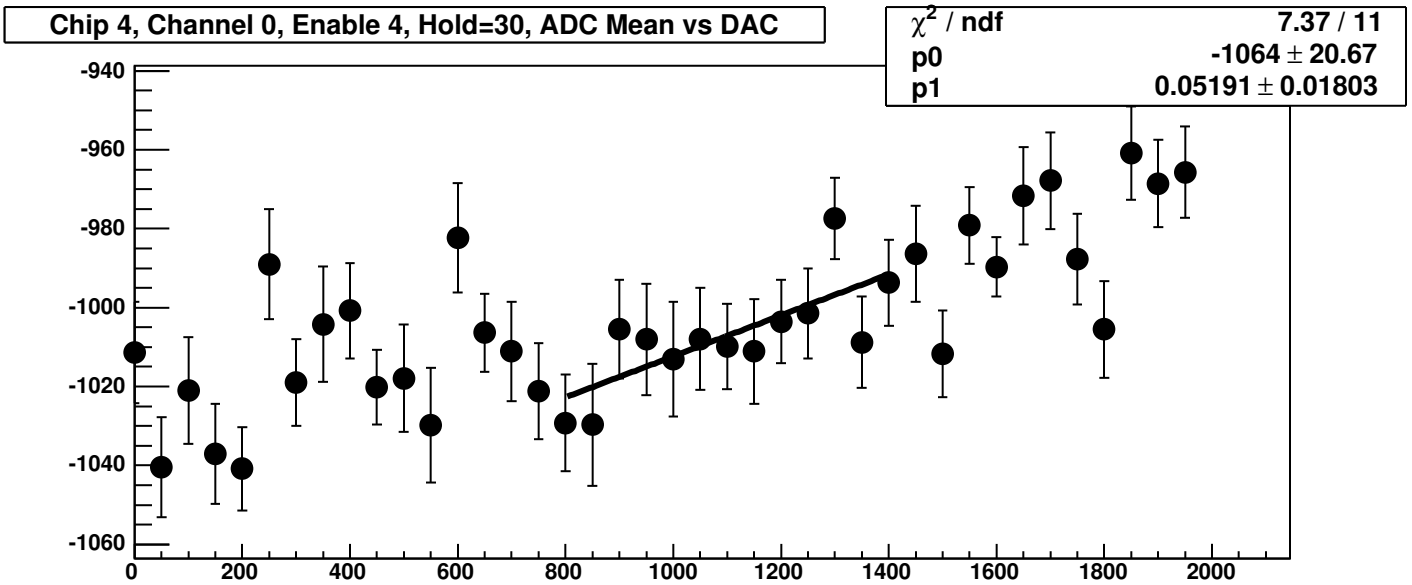
Chip 4, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC

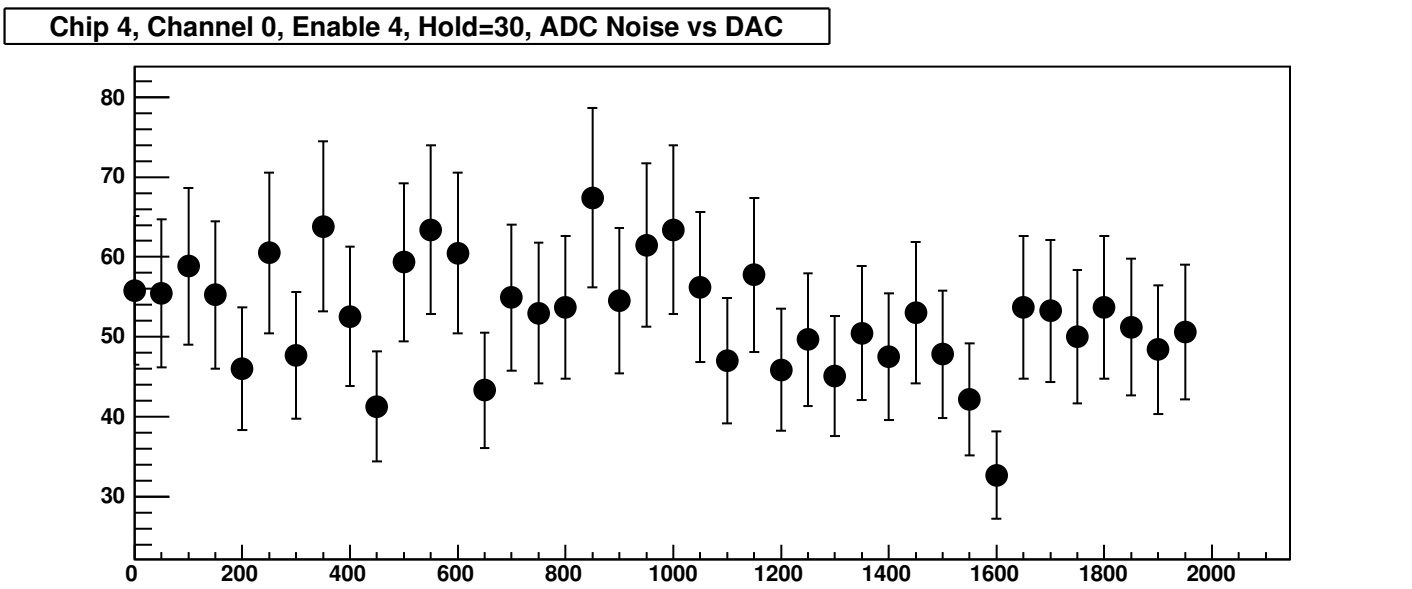


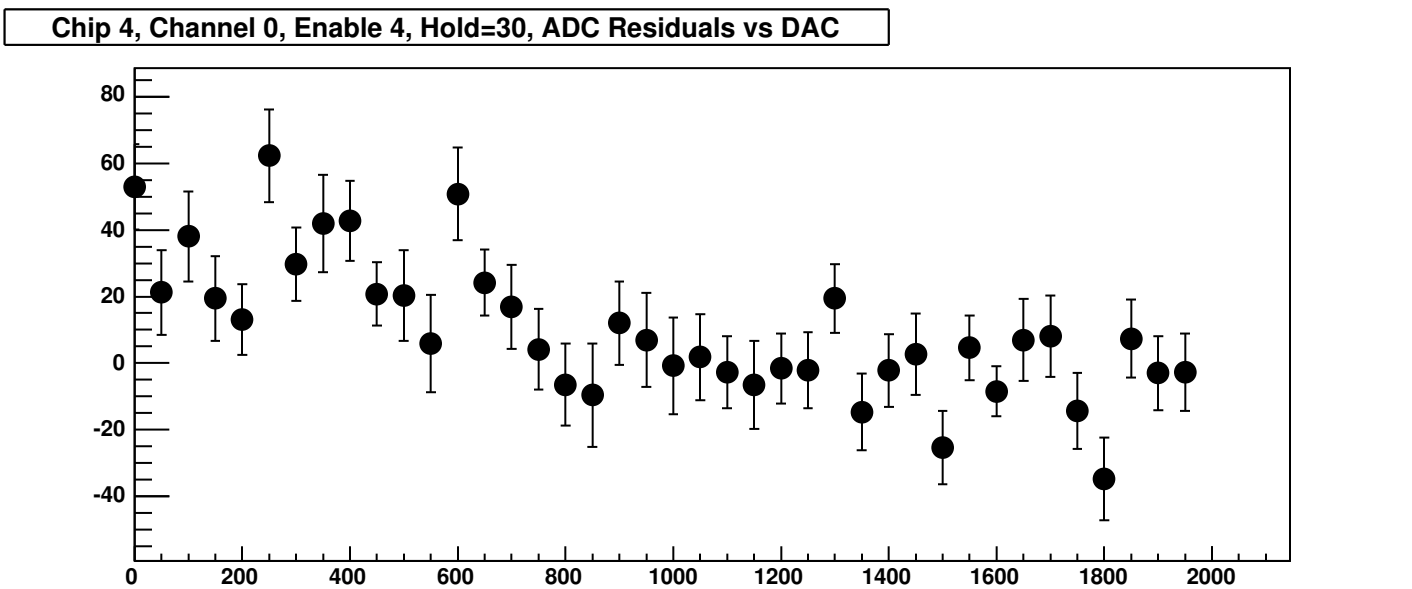

Chip 4, Channel 0, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7.622 / 11
p0 -1051 ± 19.9
p1 0.07228 ± 0.01771
-880
-900
-920
-940
-960
-980
-1000
-1020
-1040
0
200
400
600
800
1000
1200
1400
1600
1800
2000

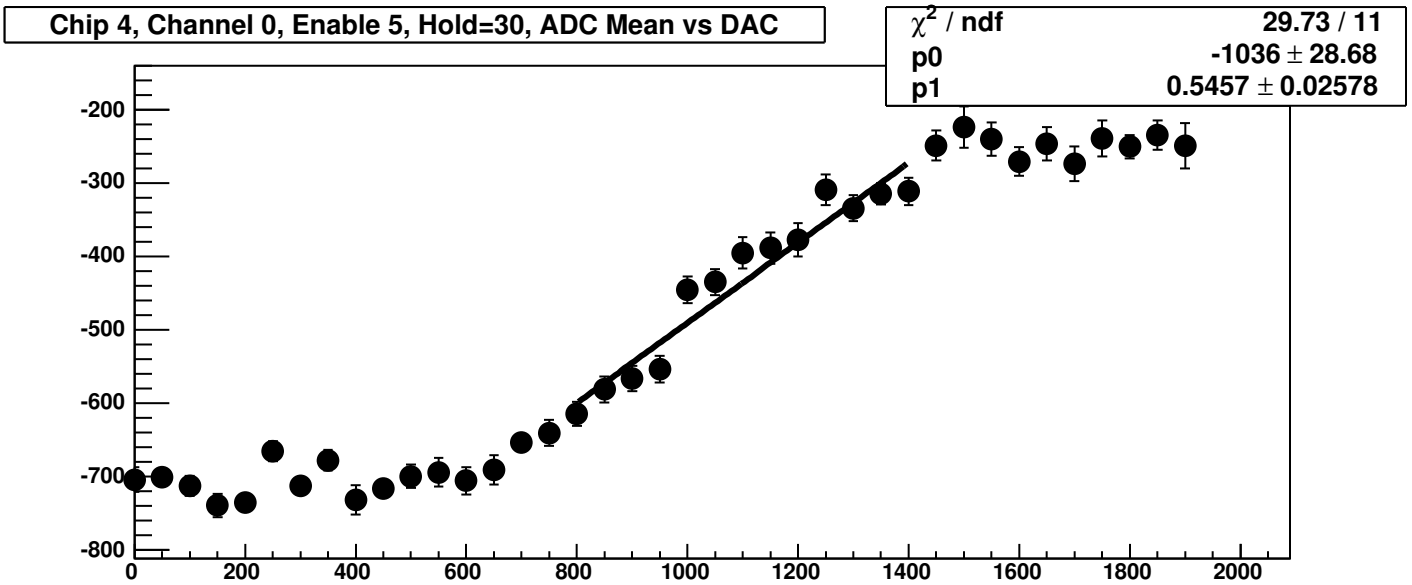

Chip 4, Channel 0, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

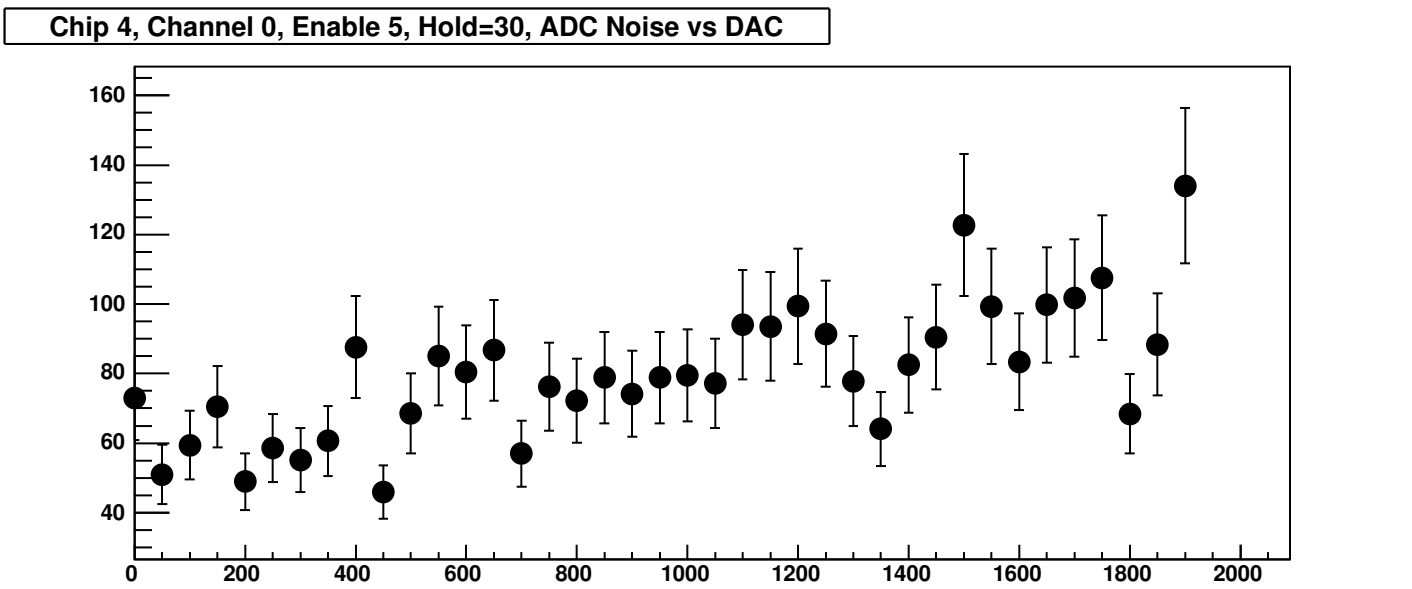

Chip 4, Channel 0, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

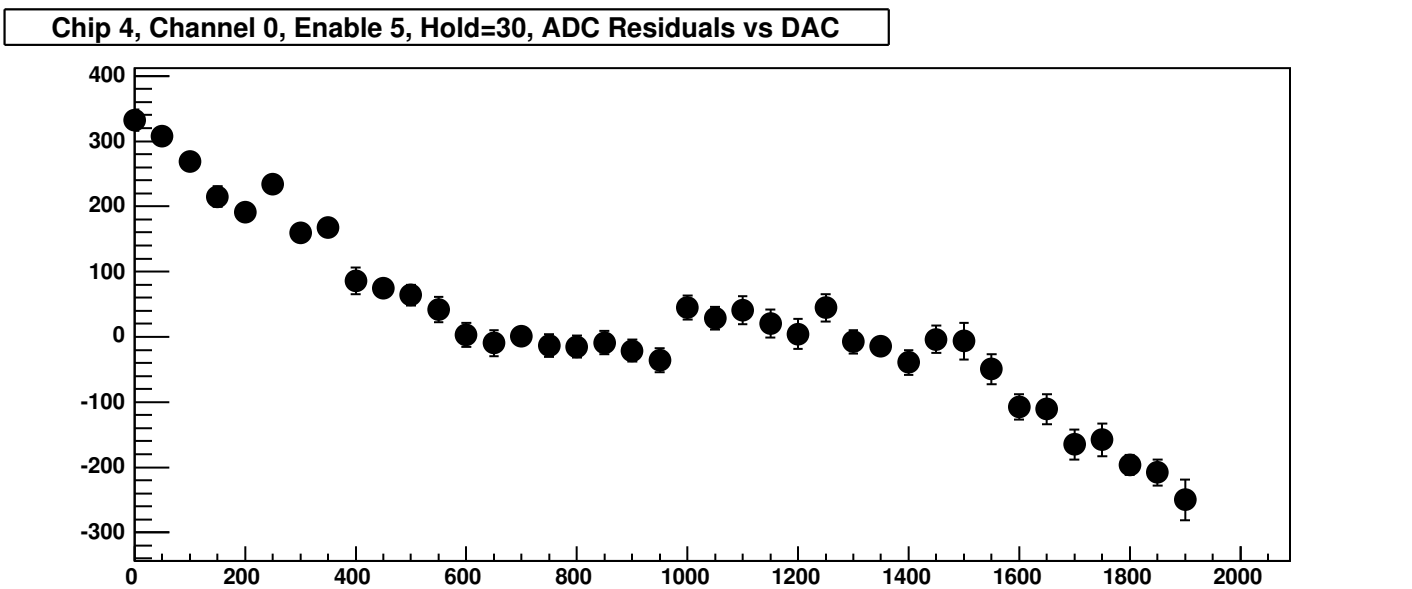

Chip 4, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7.37 / 11
p0 -1064 ± 20.67
p1 0.05191 ± 0.01803
-940
-960
-980
-1000
-1020
-1040
-1060
0
200
400
600
800
1000
1200
1400
1600
1800
2000

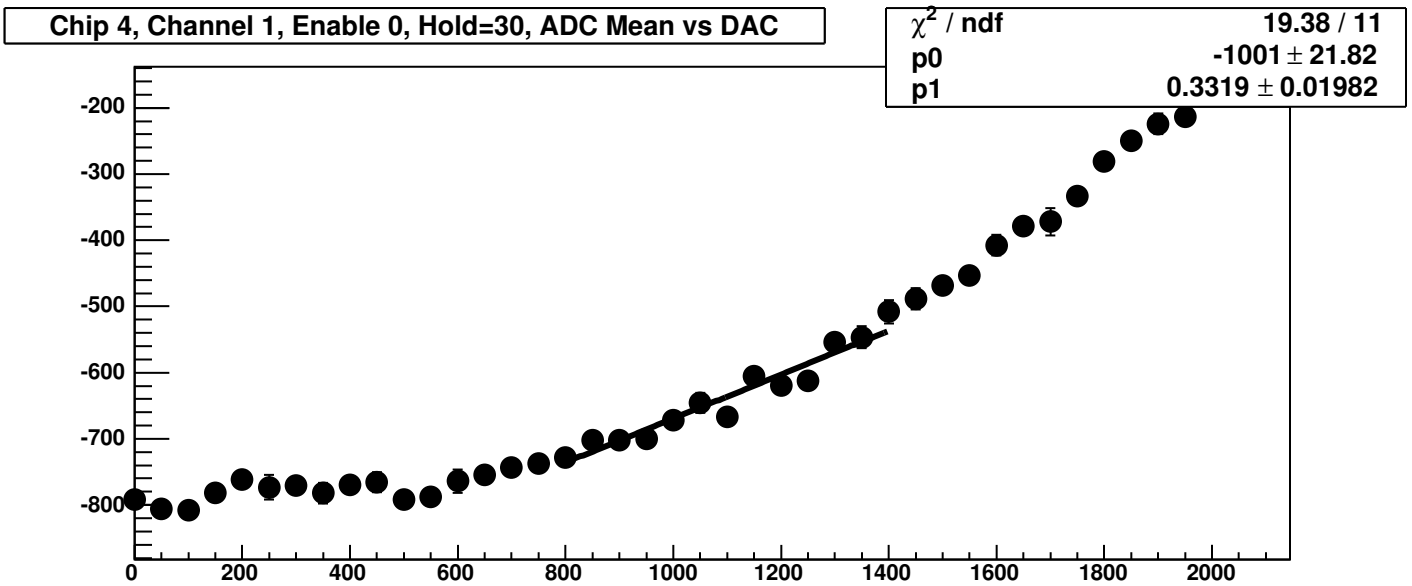

Chip 4, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

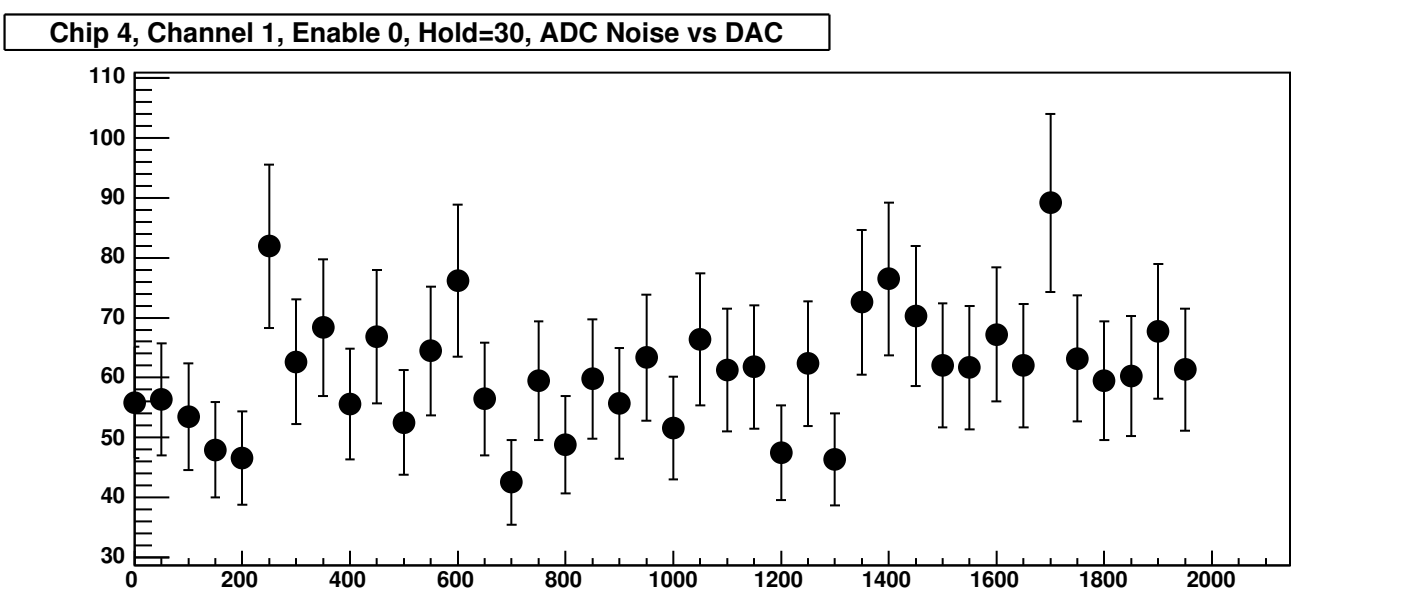

Chip 4, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

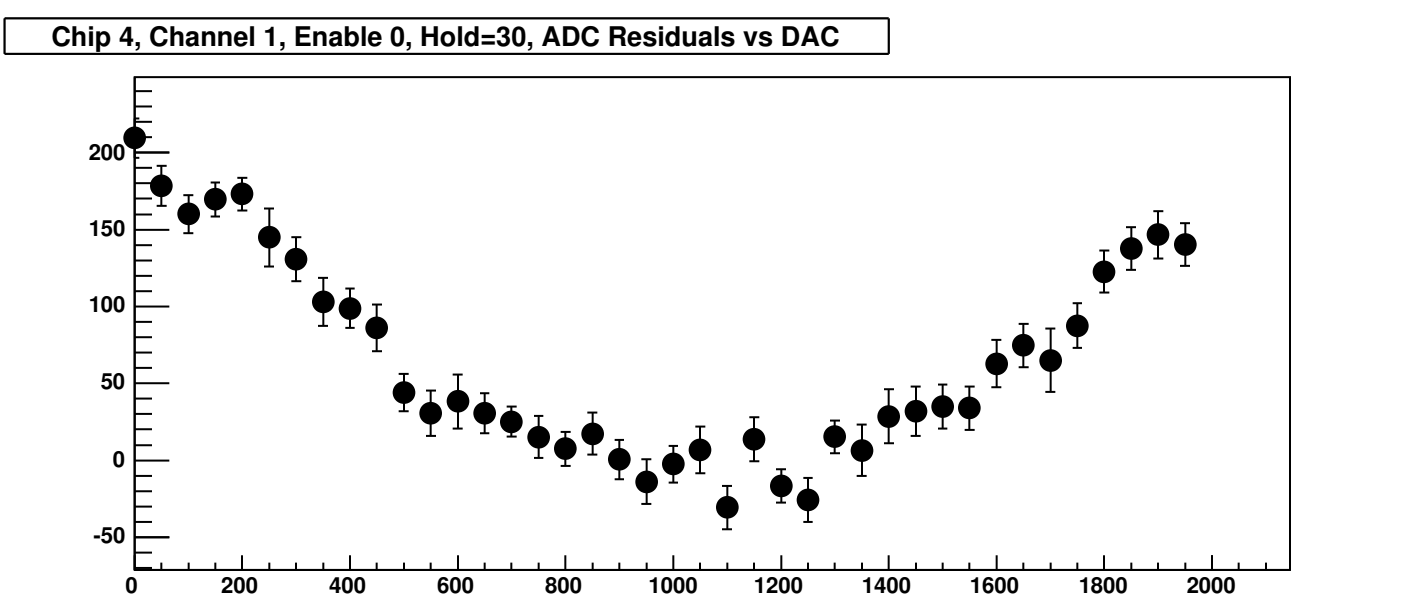

Chip 4, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 29.73 / 11
p0 -1036 ± 28.68
p1 0.5457 ± 0.02578
-200
-300
-400
-500
-600
-700
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

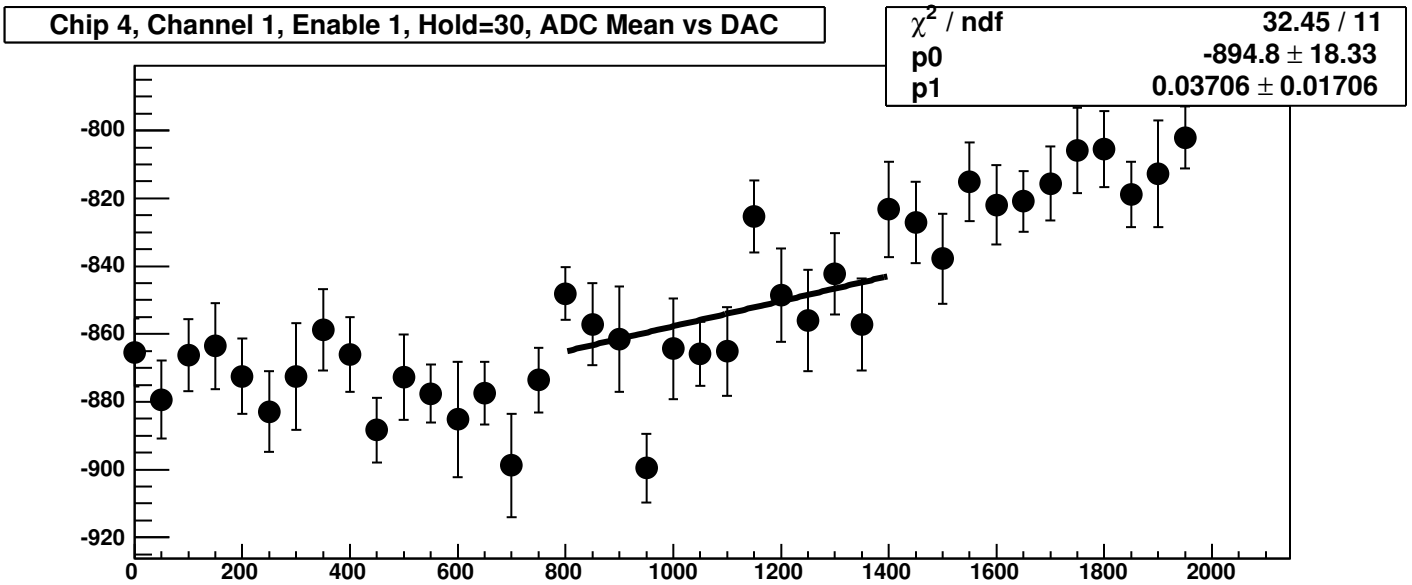

Chip 4, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

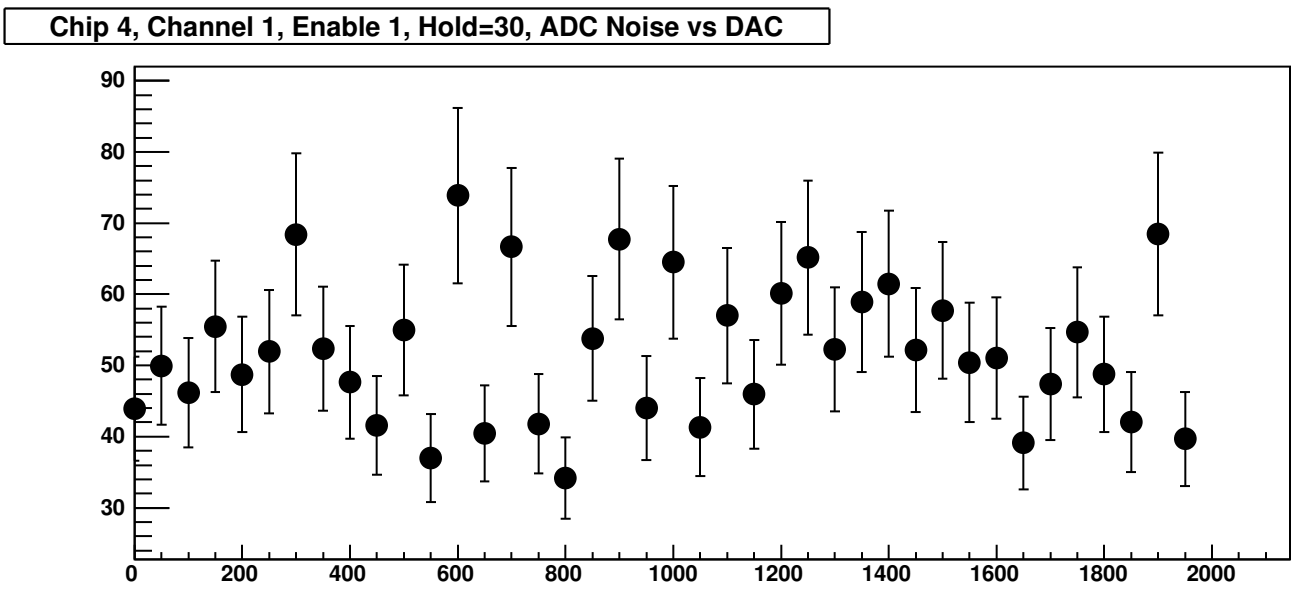

Chip 4, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
-300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

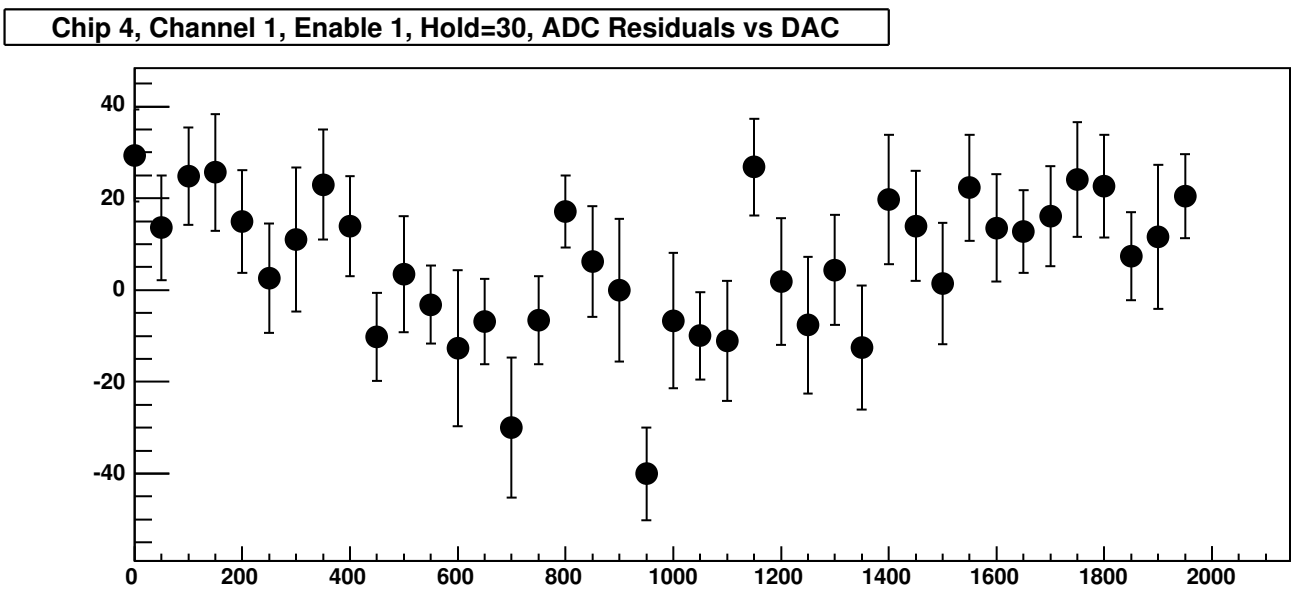

Chip 4, Channel 1, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 19.38 / 11
p0 -1001 ± 21.82
p1 0.3319 ± 0.01982
-200
-300
-400
-500
-600
-700
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

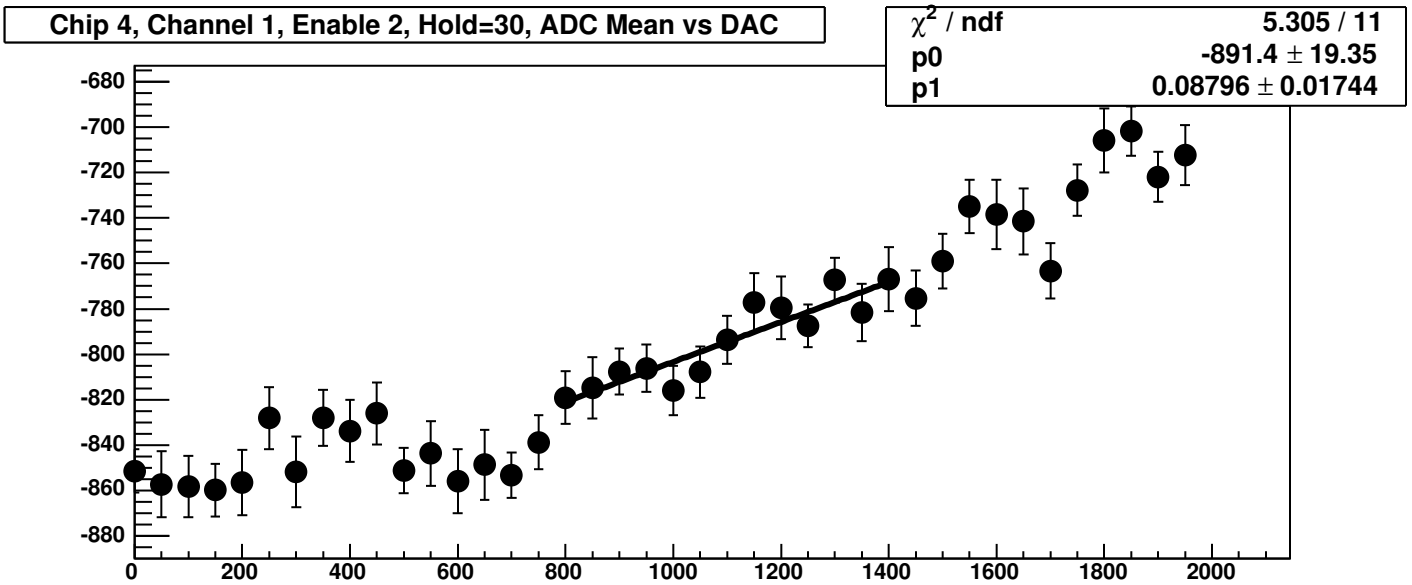

Chip 4, Channel 1, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

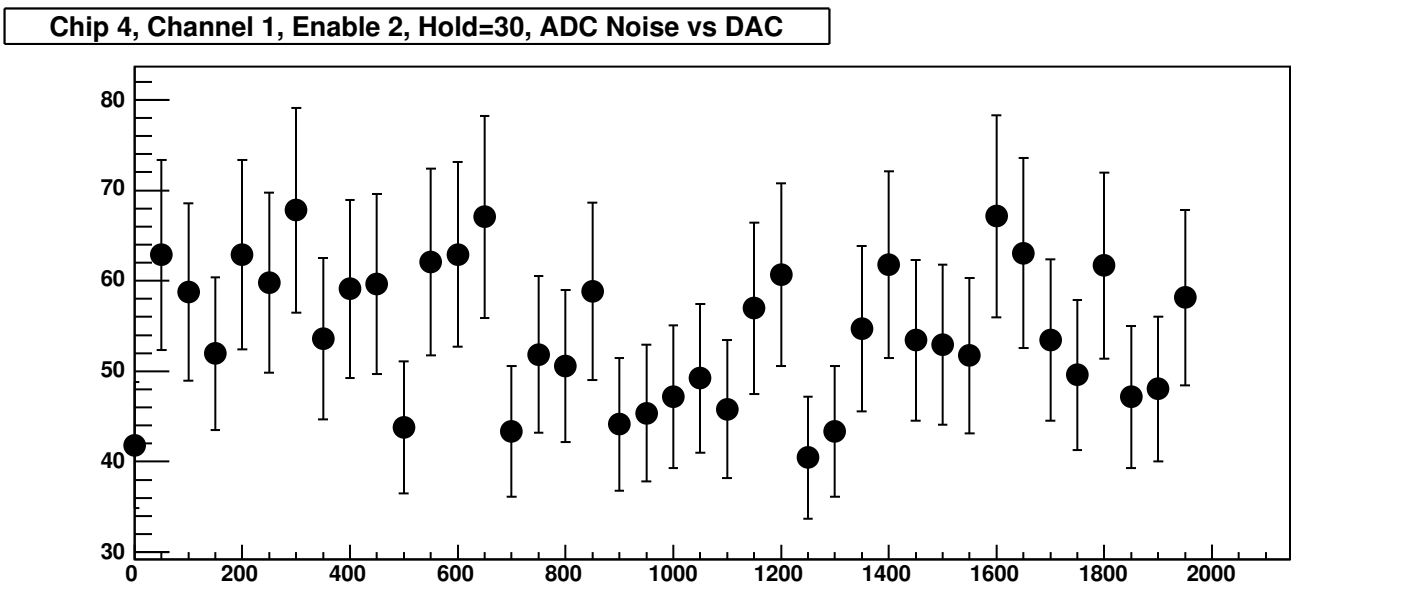

Chip 4, Channel 1, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

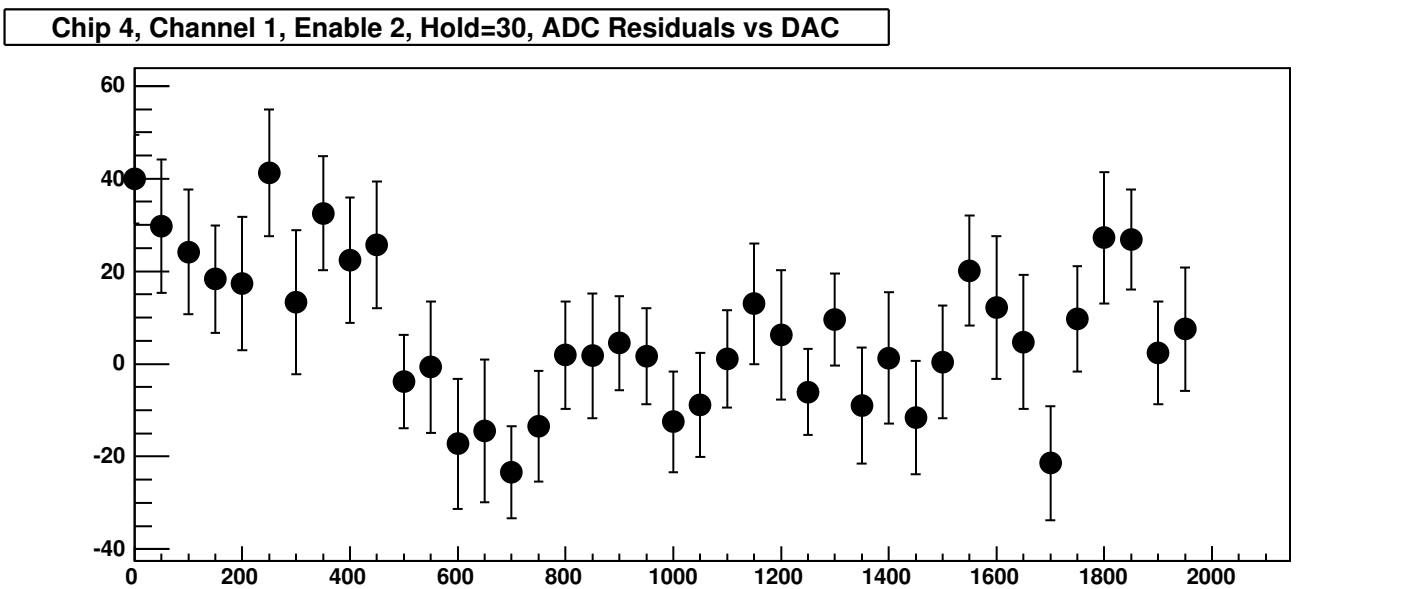

Chip 4, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 32.45 / 11
p0 -894.8 ± 18.33
p1 0.03706 ± 0.01706
-800
-820
-840
-860
-880
-900
-920
0
200
400
600
800
1000
1200
1400
1600
1800
2000

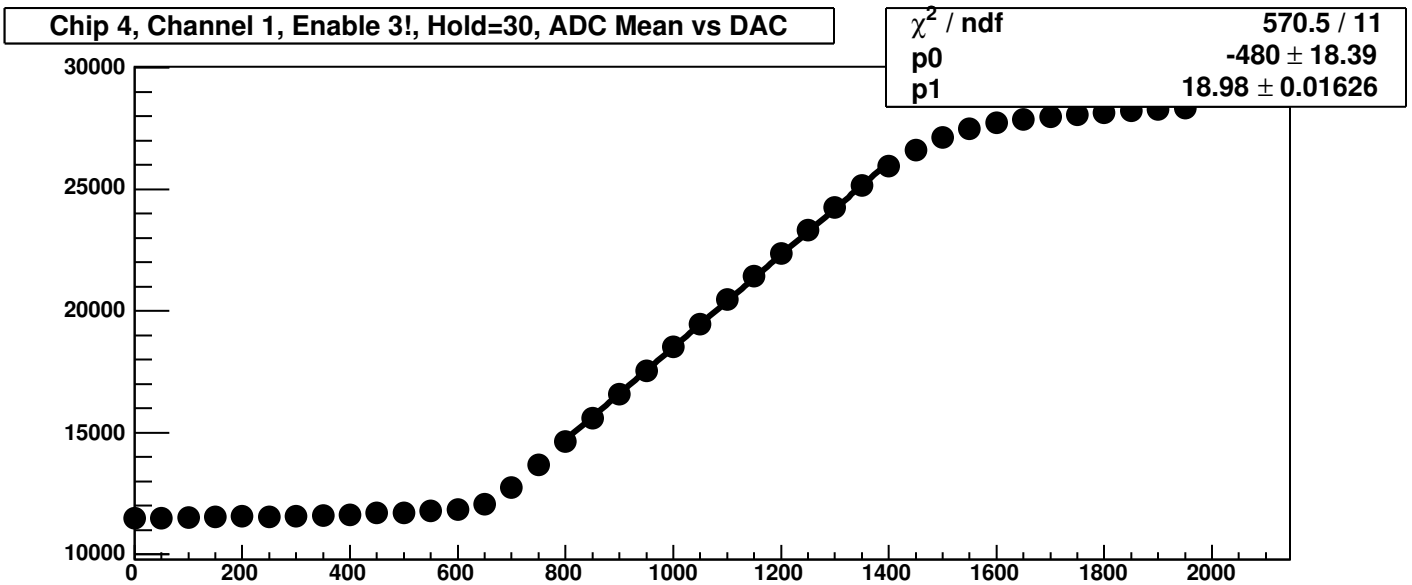

Chip 4, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

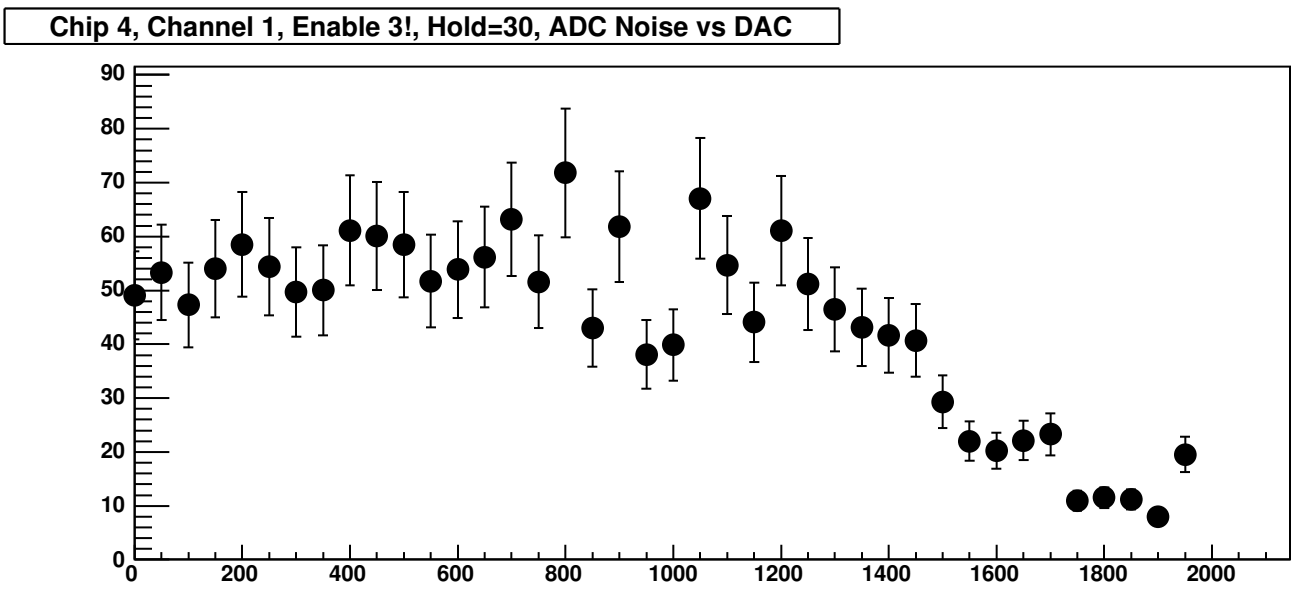

Chip 4, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

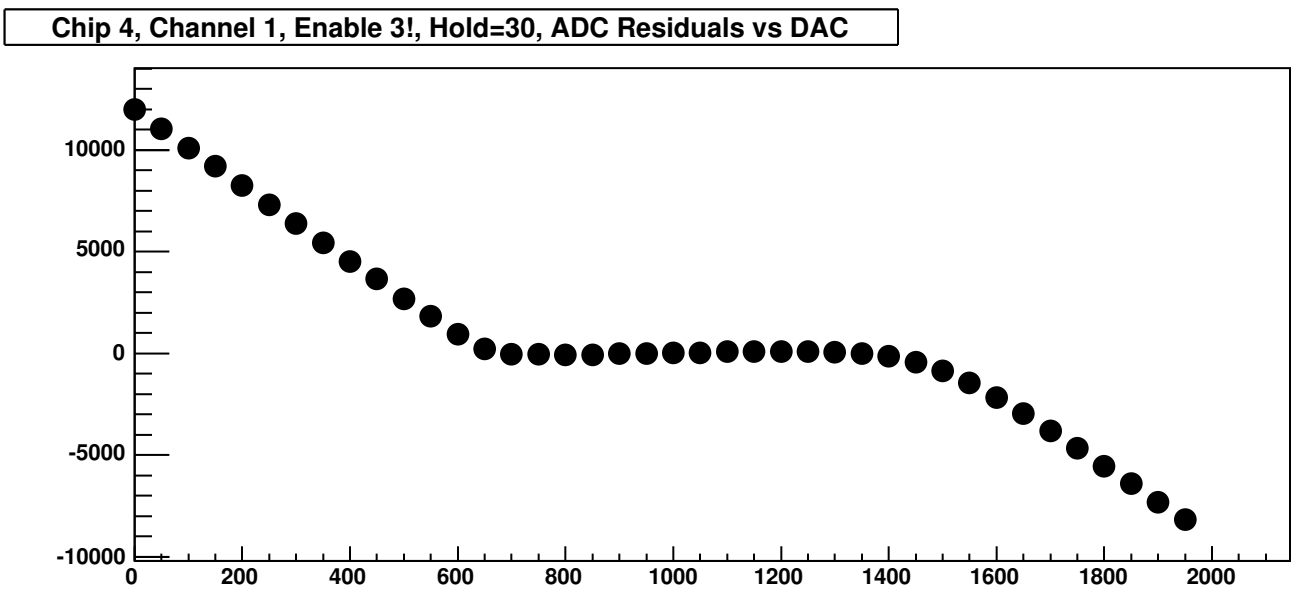

Chip 4, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 5.305 / 11
p0 -891.4 ± 19.35
p1 0.08796 ± 0.01744
-680
-700
-720
-740
-760
-780
-800
-820
-840
-860
-880
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

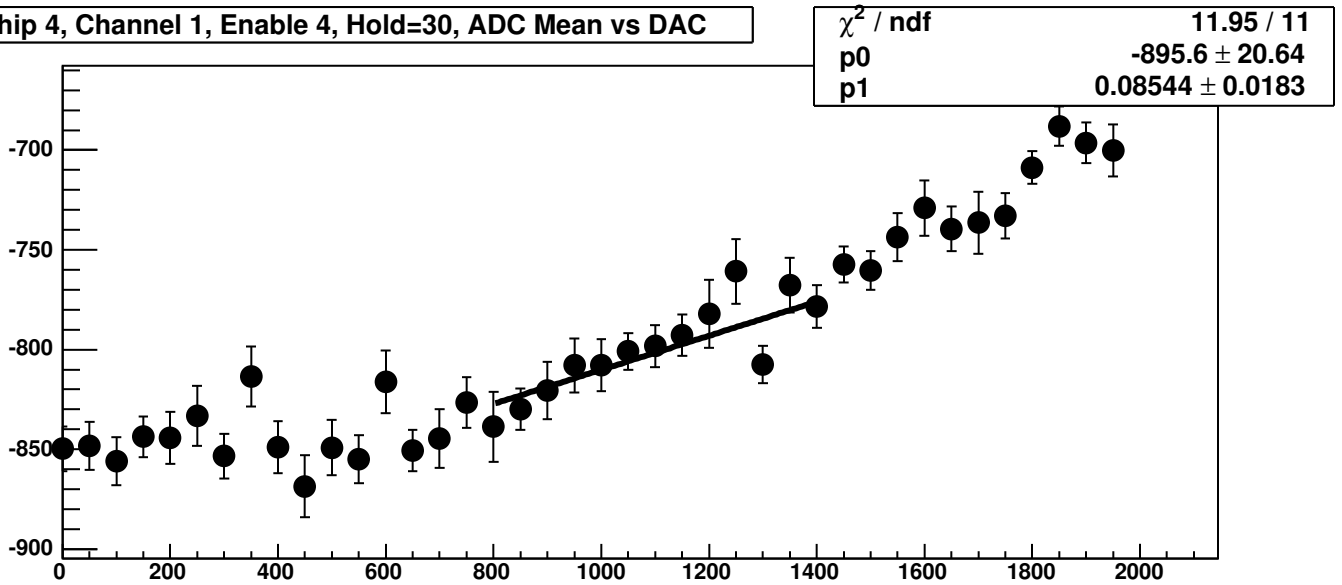

Chip 4, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 1, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 570.5 / 11
p0 -480 ± 18.39
p1 18.98 ± 0.01626
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

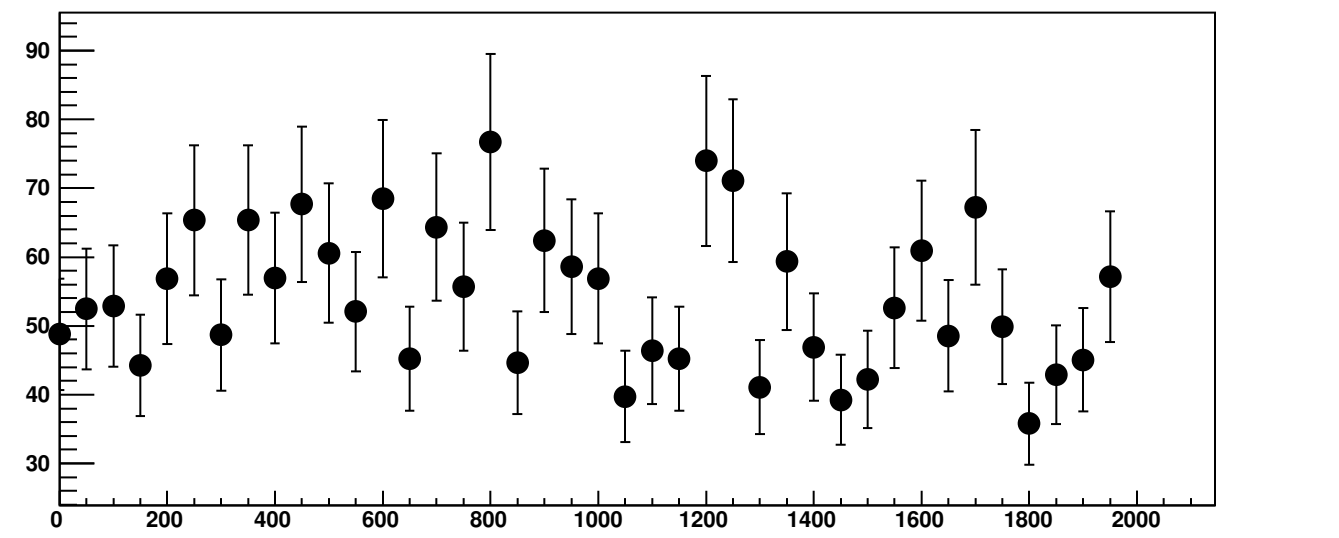

Chip 4, Channel 1, Enable 3!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 1, Enable 3!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

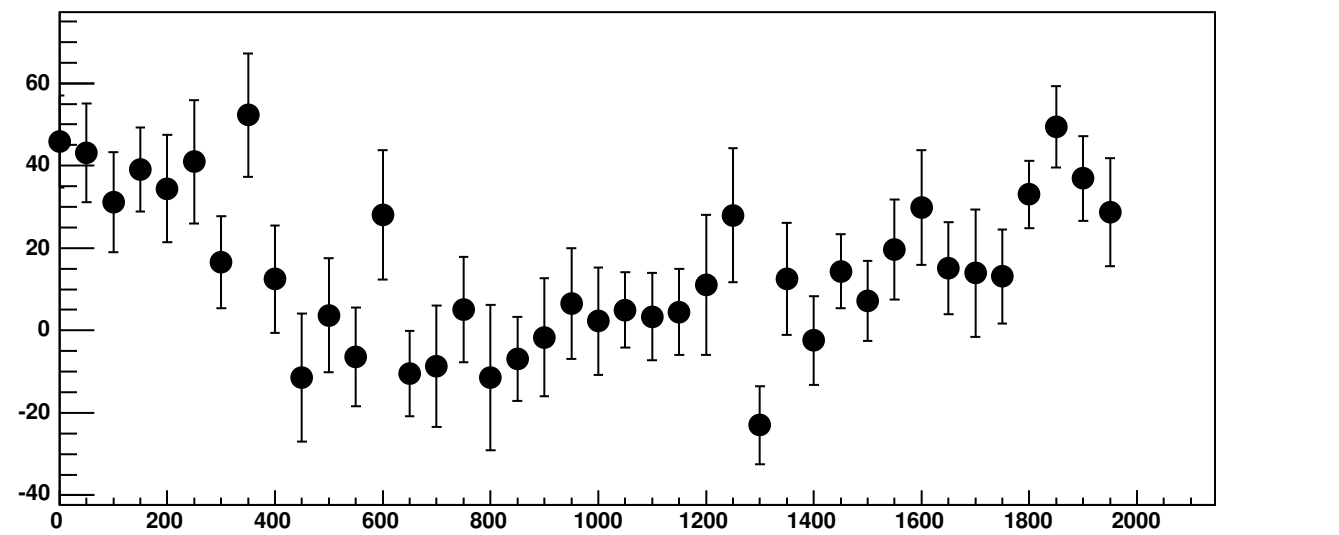
Chip 4, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC

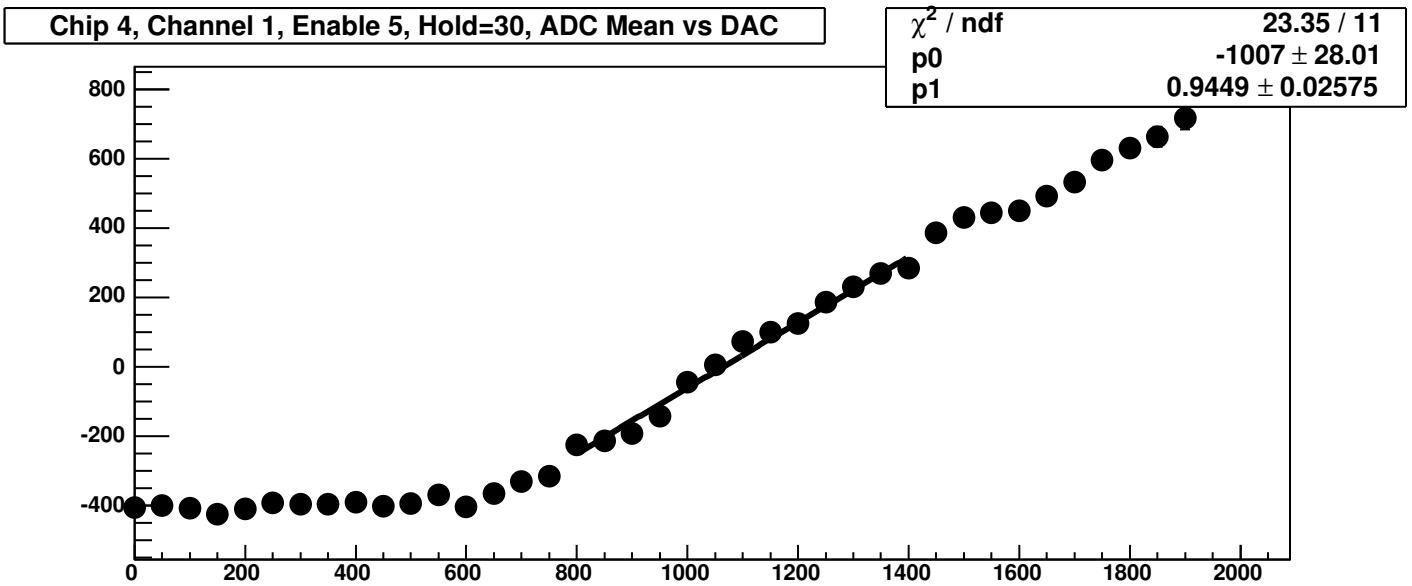


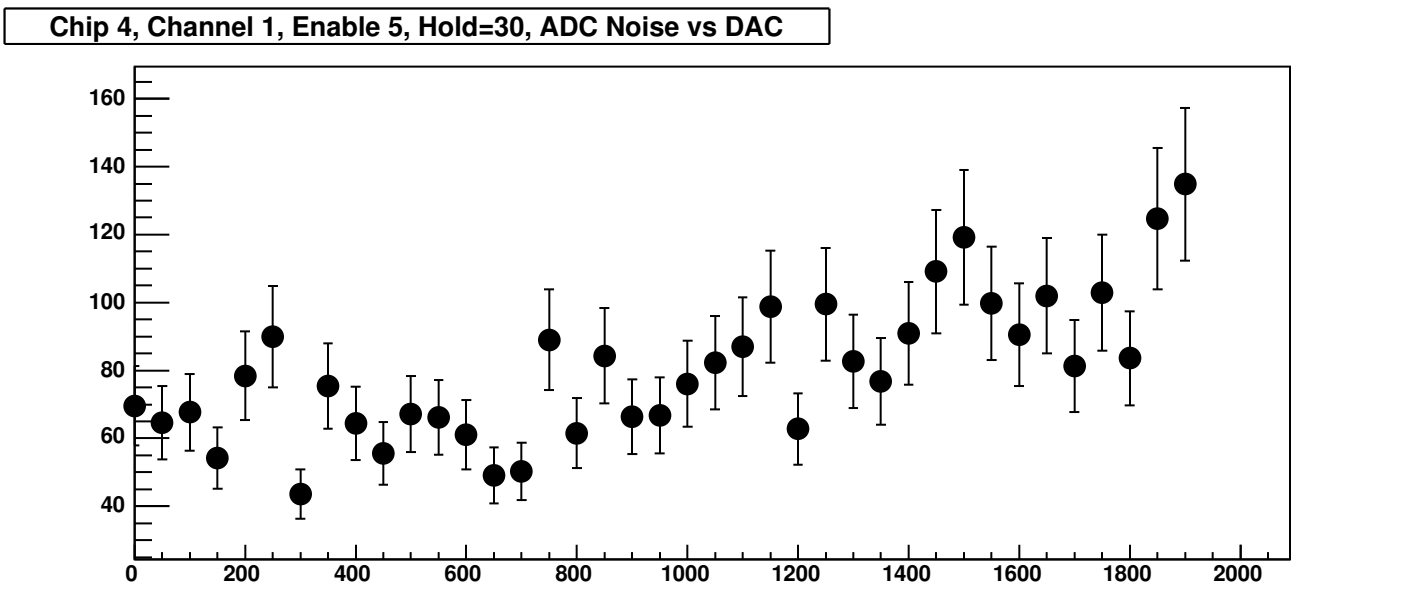
Chip 4, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC

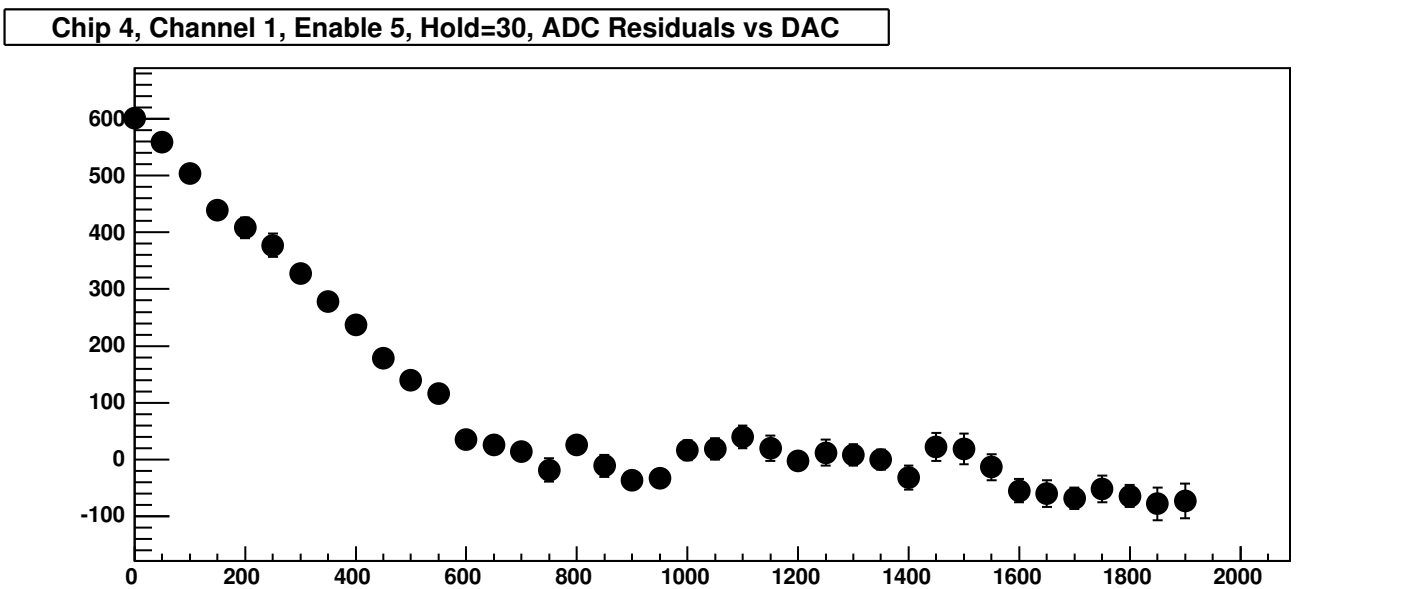


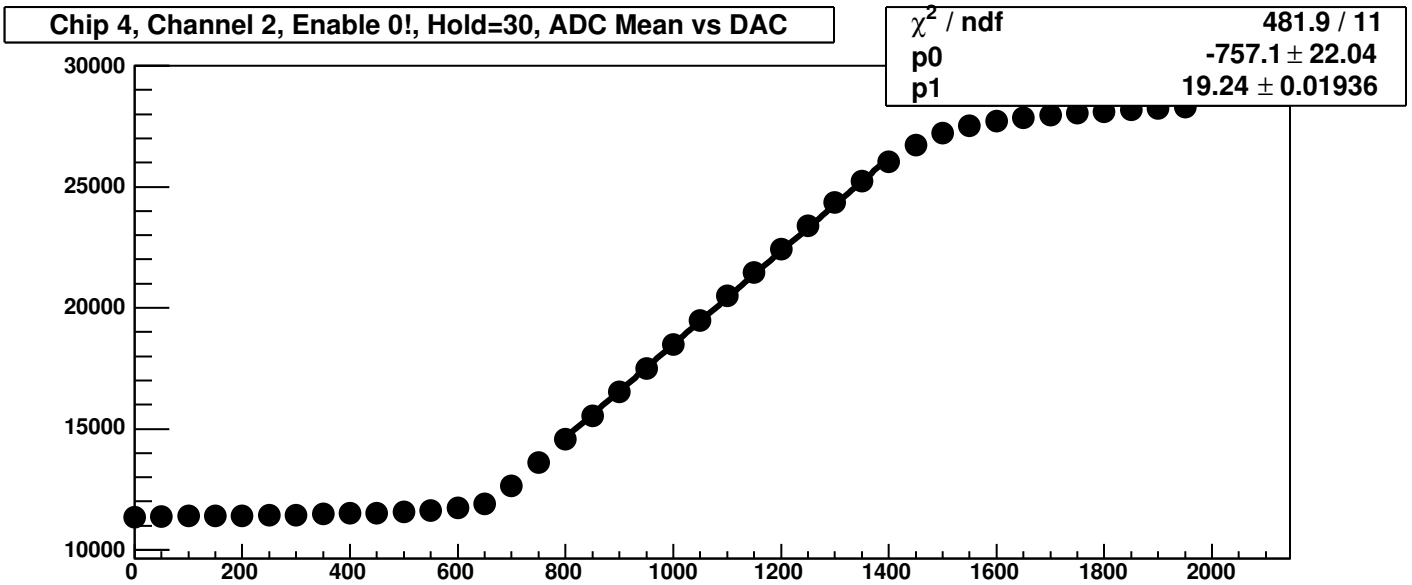
Chip 4, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC

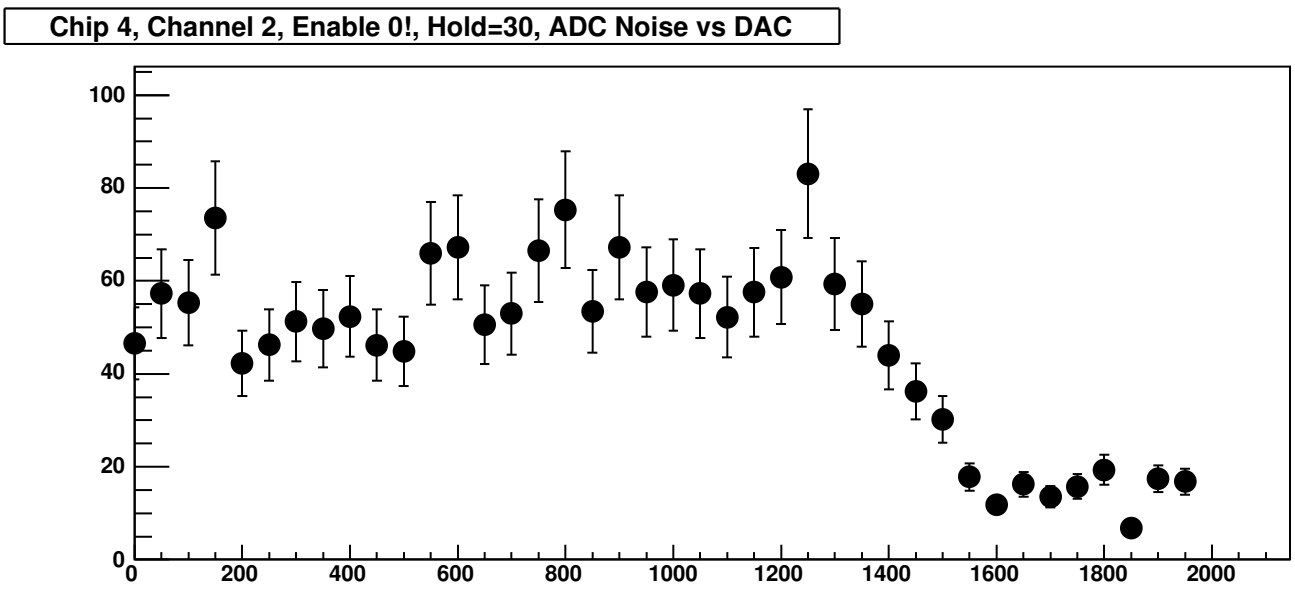


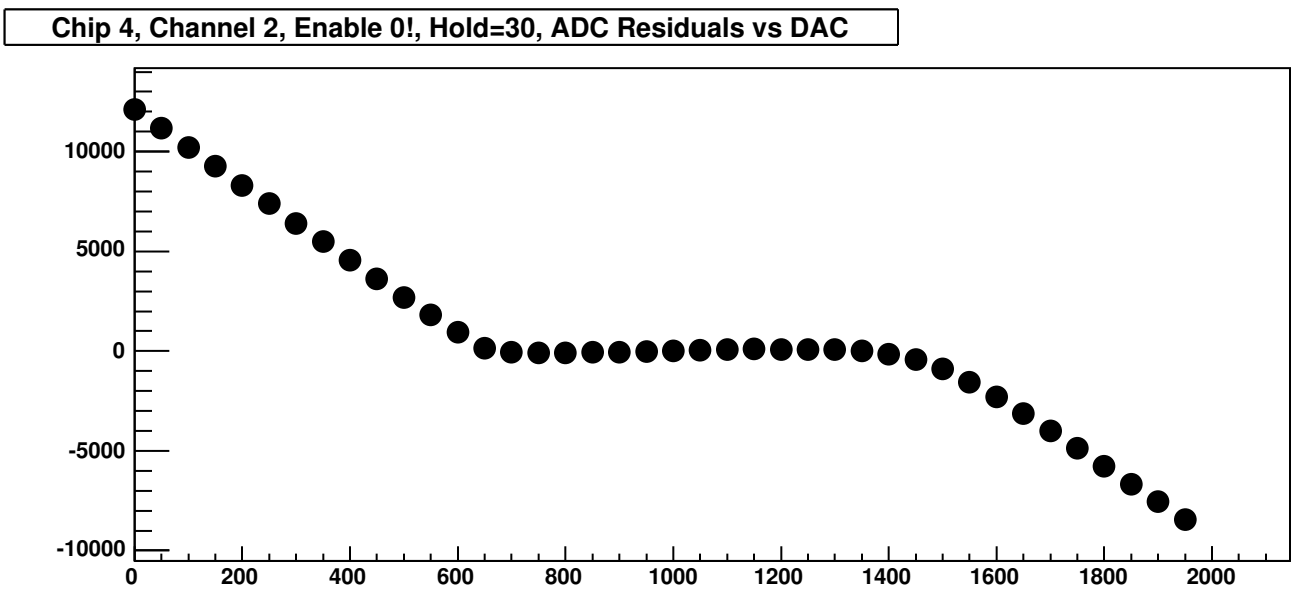

Chip 4, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 23.35 / 11
p0 -1007 ± 28.01
p1 0.9449 ± 0.02575
800
600
400
200
0
-200
-400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

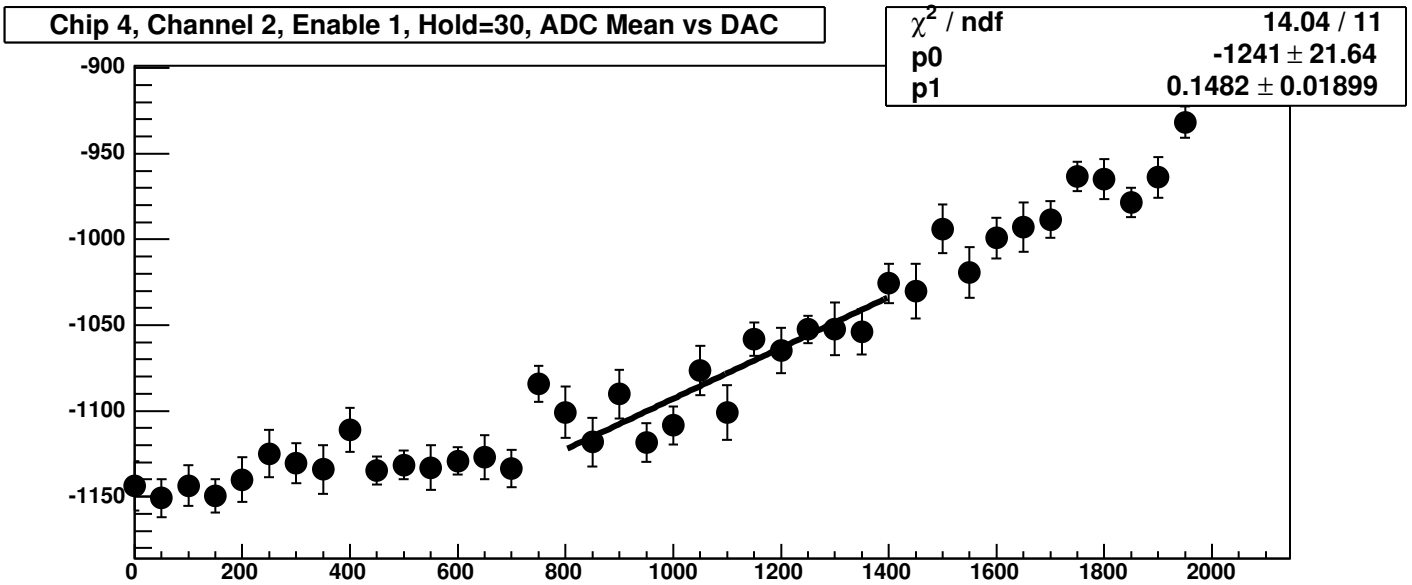

Chip 4, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

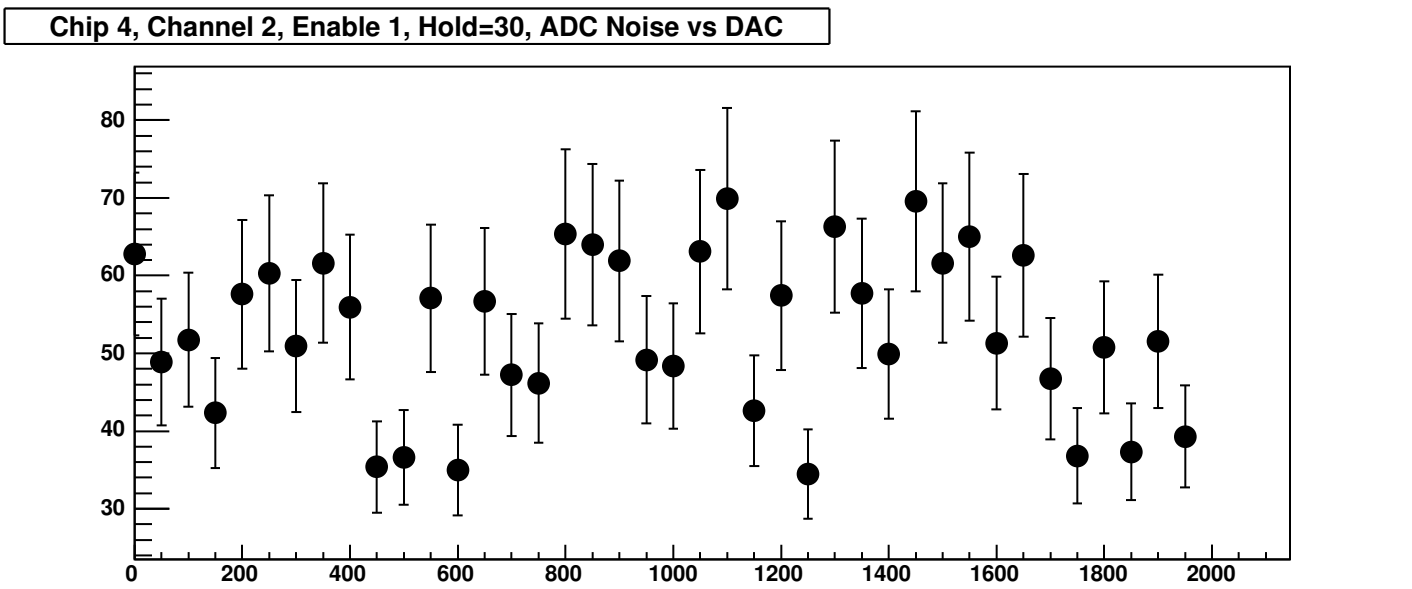

Chip 4, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

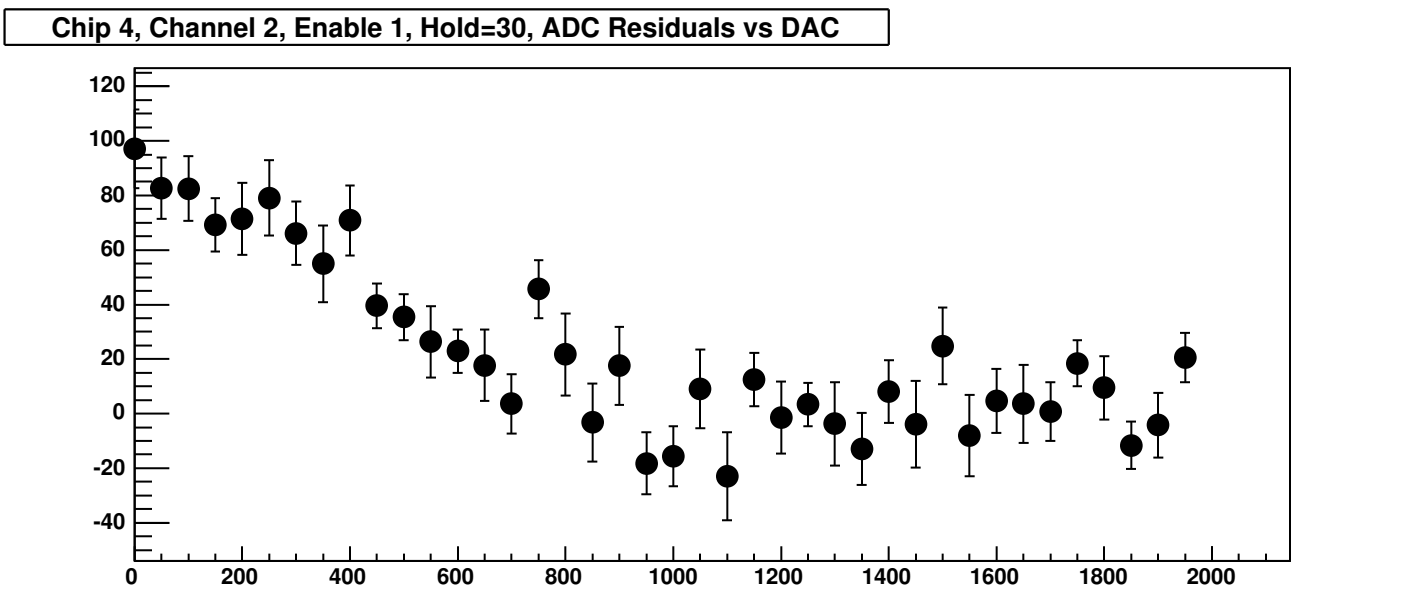

Chip 4, Channel 2, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 481.9 / 11
p0 -757.1 ± 22.04
p1 19.24 ± 0.01936
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 2, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

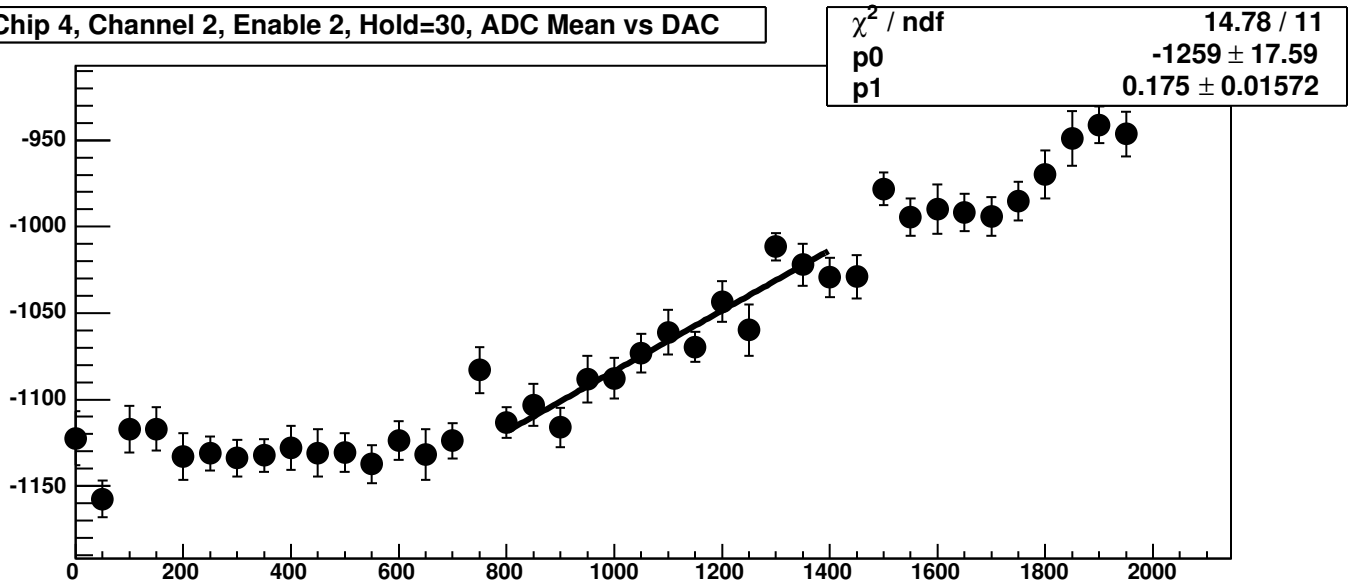

Chip 4, Channel 2, Enable 0!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 14.04 / 11
p0 -1241 ± 21.64
p1 0.1482 ± 0.01899
-900
-950
-1000
-1050
-1100
-1150
0
200
400
600
800
1000
1200
1400
1600
1800
2000

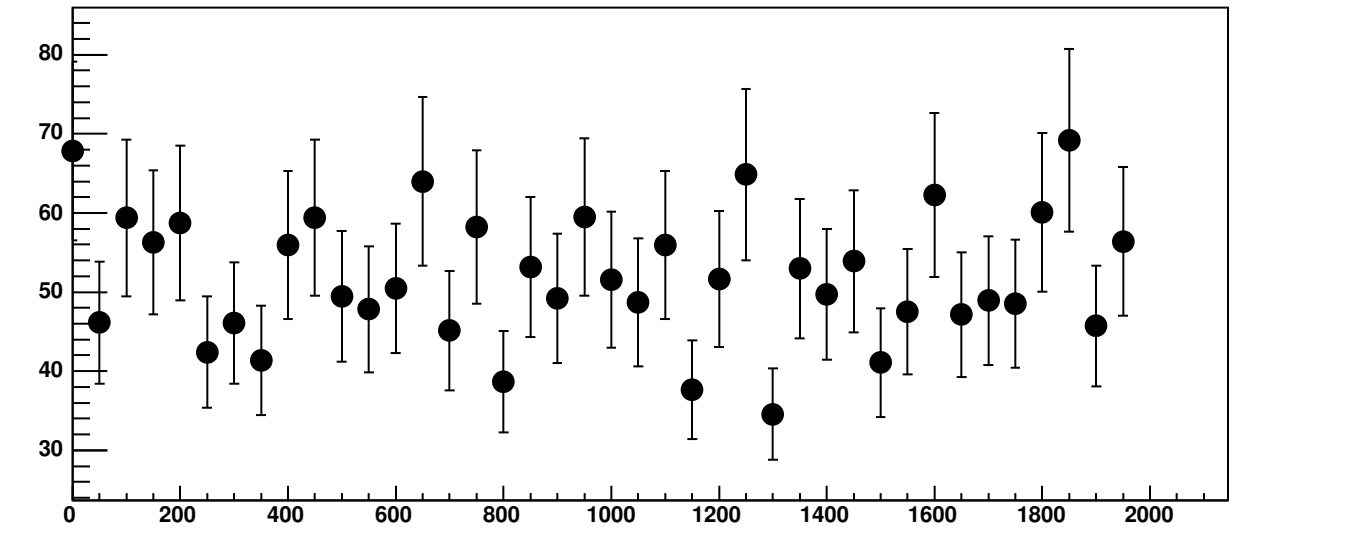

Chip 4, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

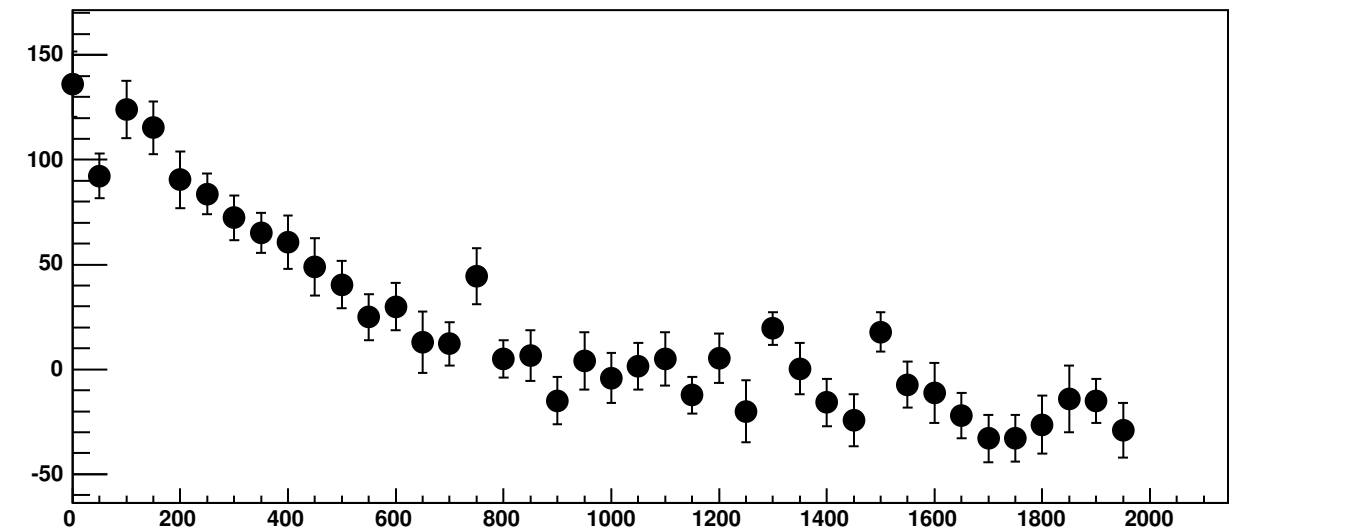
Chip 4, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC

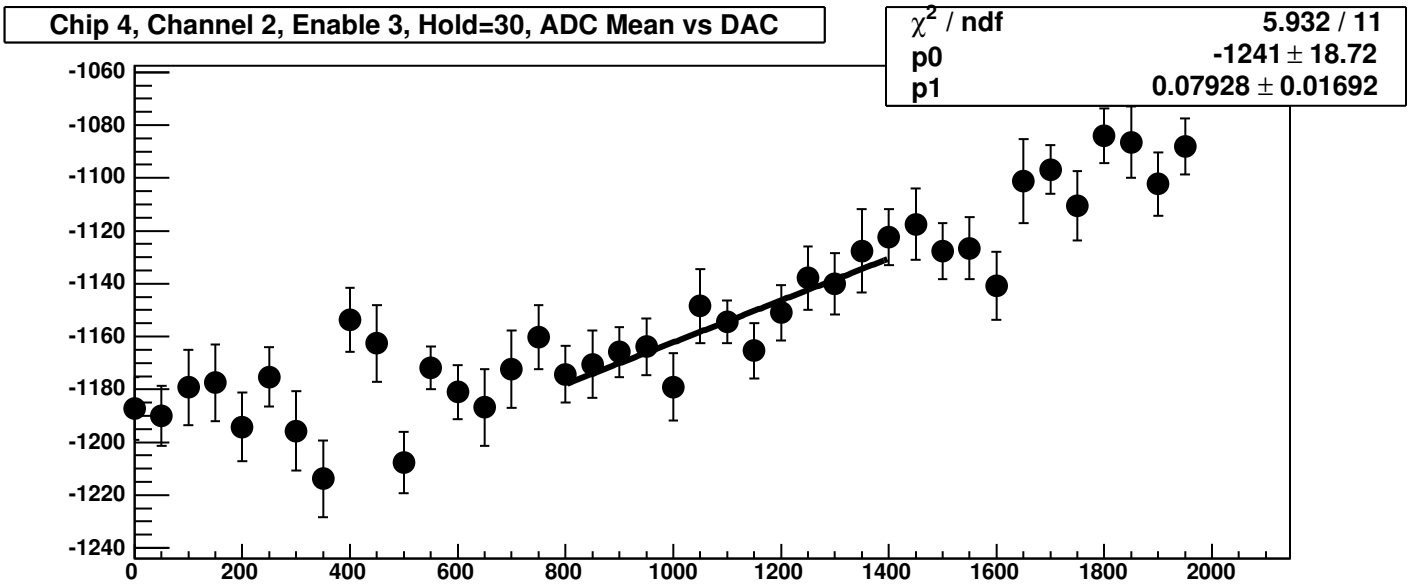


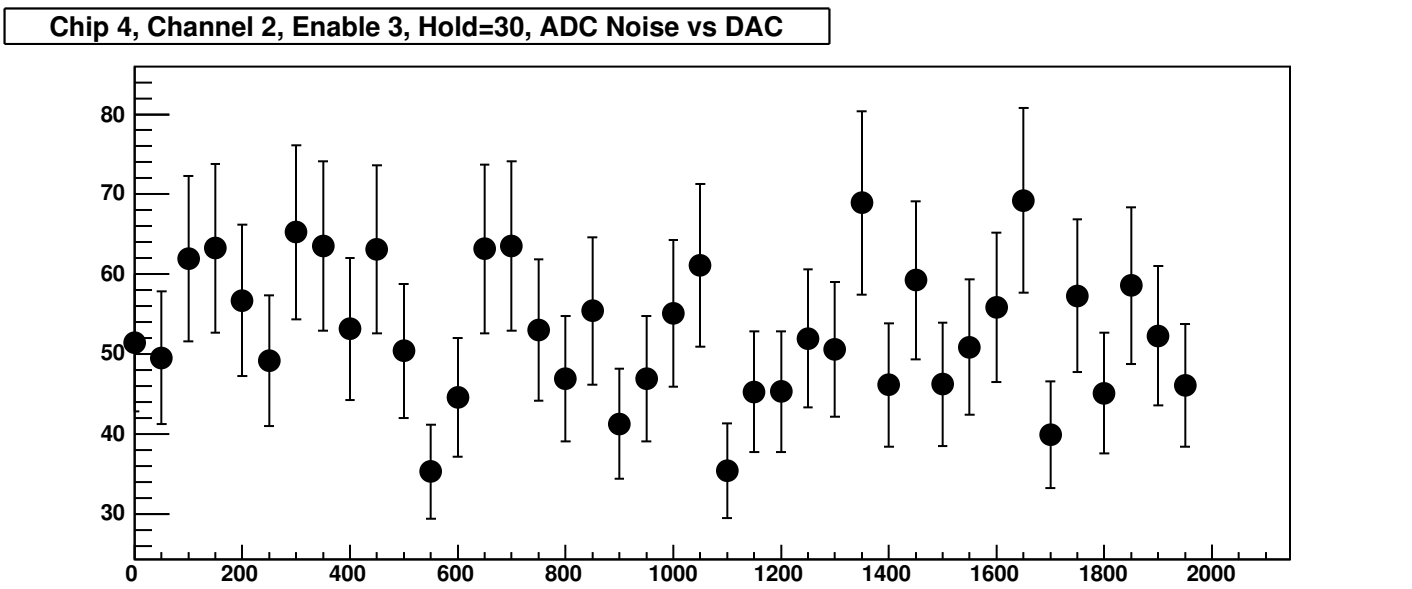
Chip 4, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC

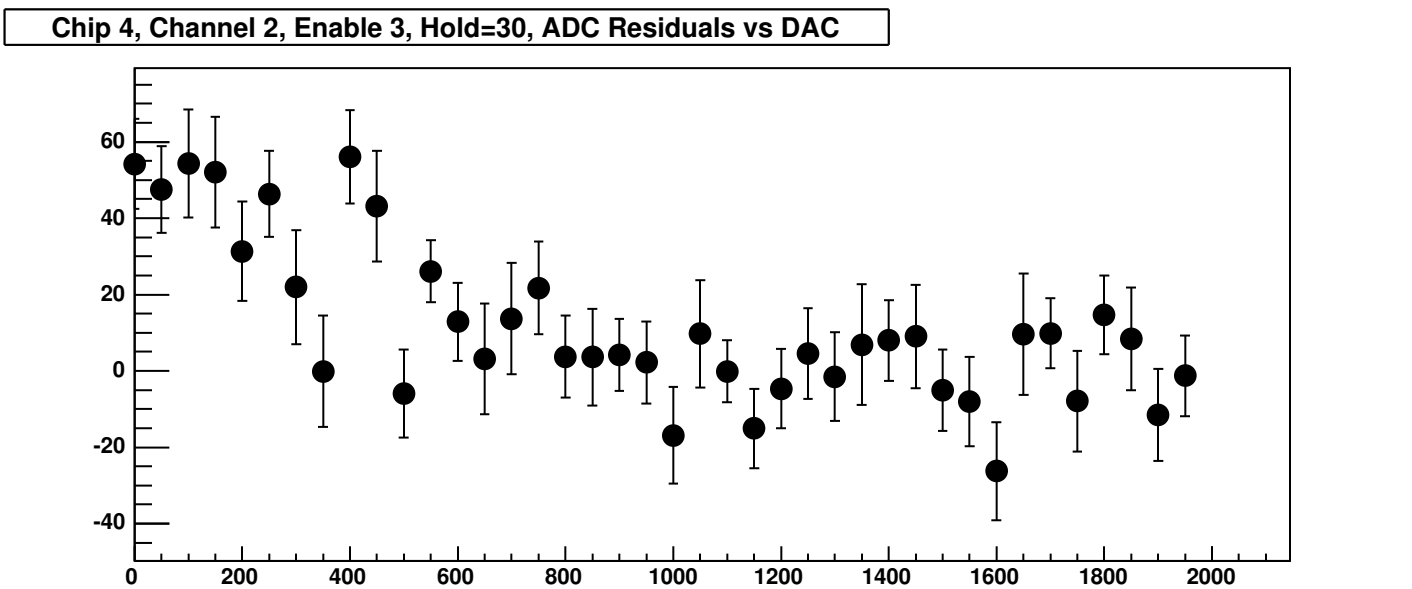


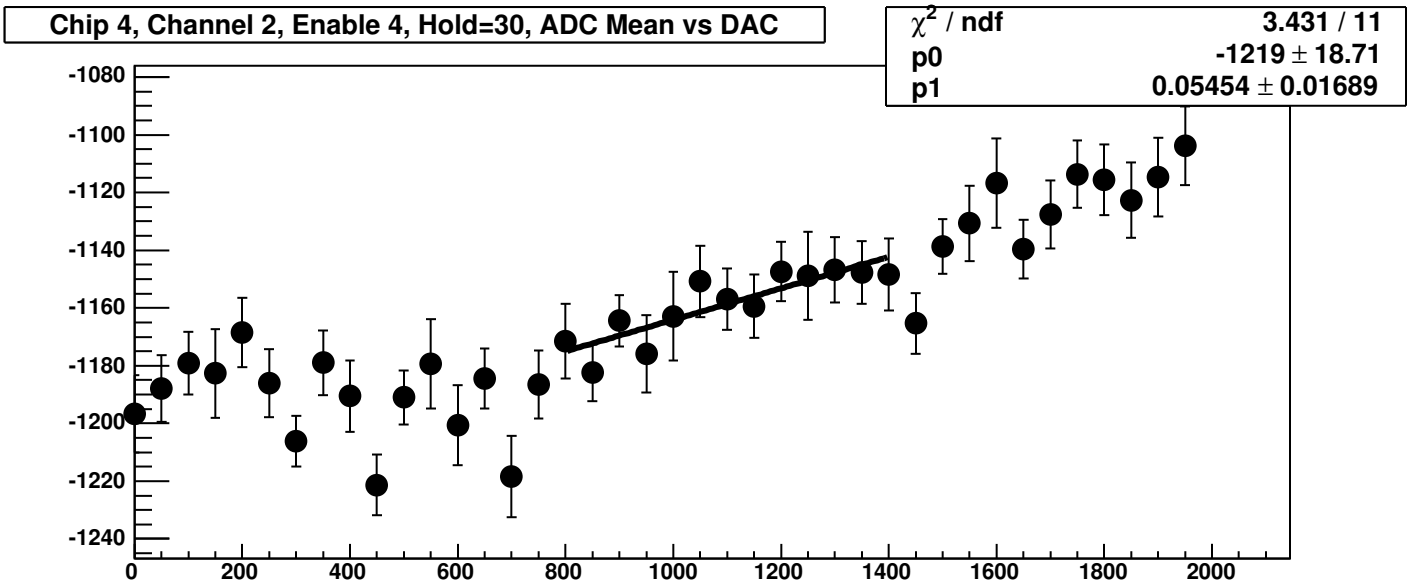
Chip 4, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC

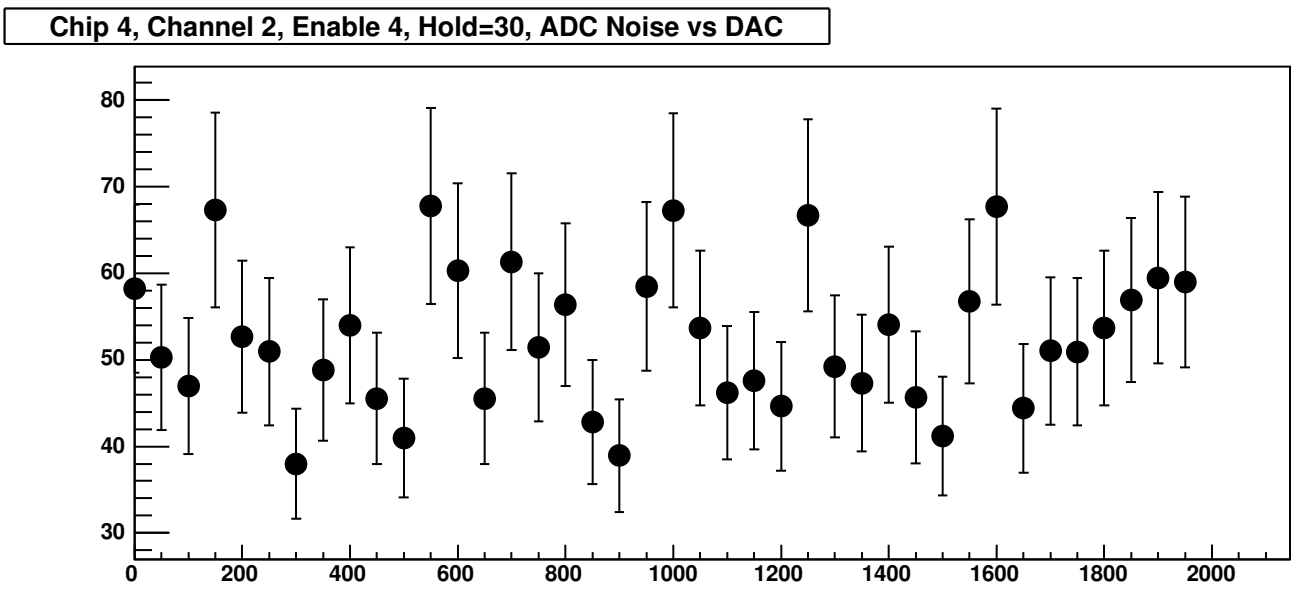


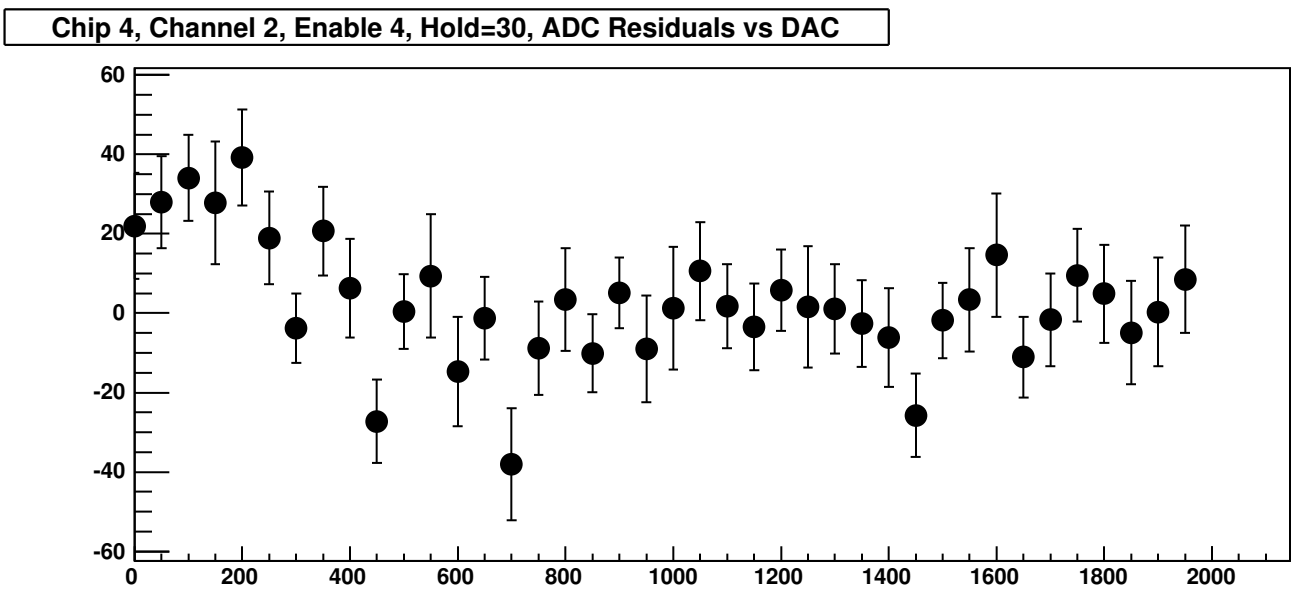

Chip 4, Channel 2, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5.932 / 11
p0 -1241 ± 18.72
p1 0.07928 ± 0.01692
-1060
-1080
-1100
-1120
-1140
-1160
-1180
-1200
-1220
-1240
0
200
400
600
800
1000
1200
1400
1600
1800
2000

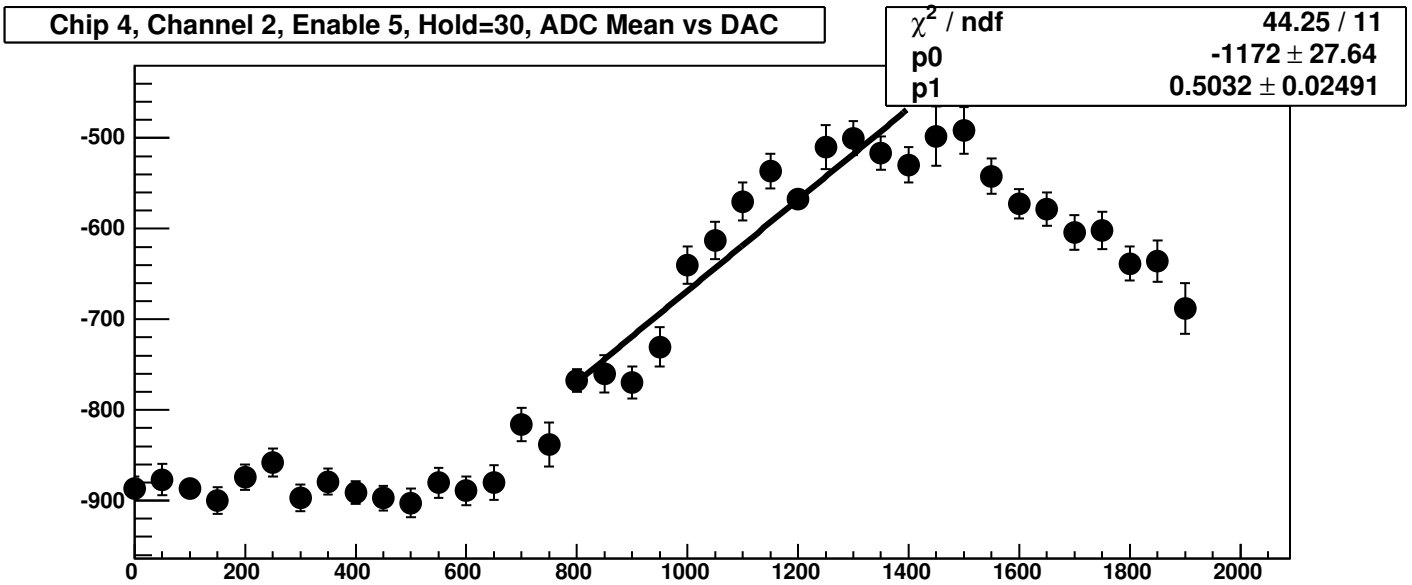

Chip 4, Channel 2, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

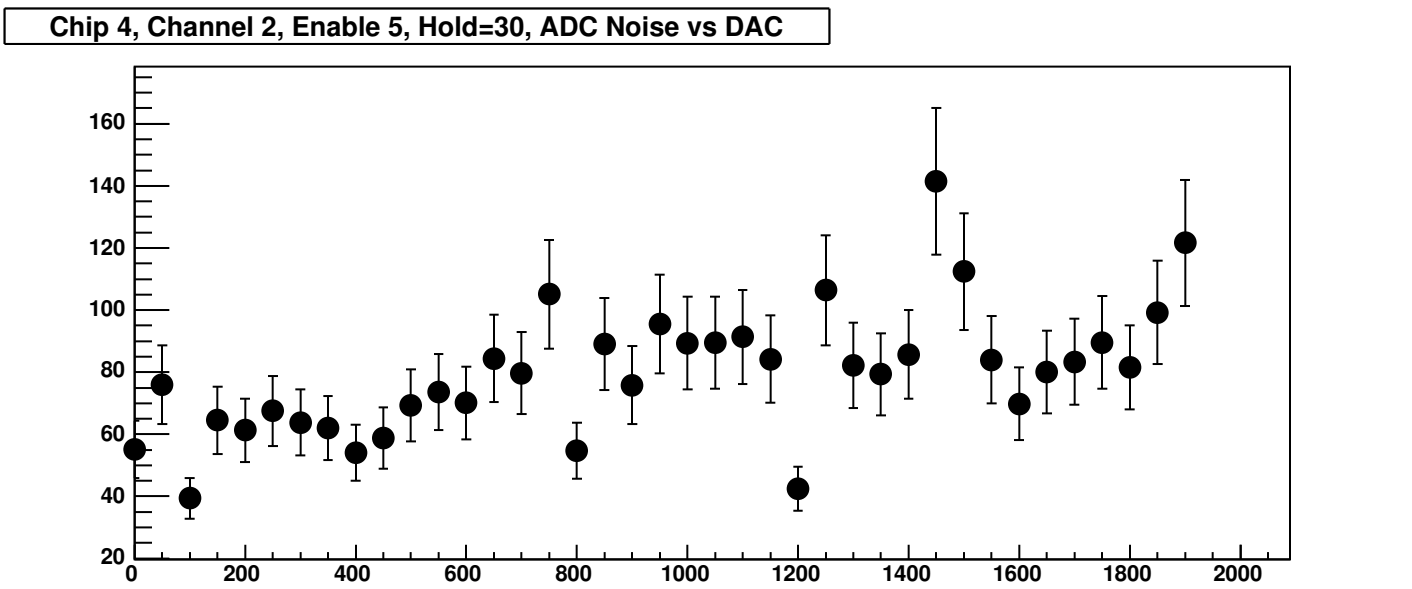

Chip 4, Channel 2, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

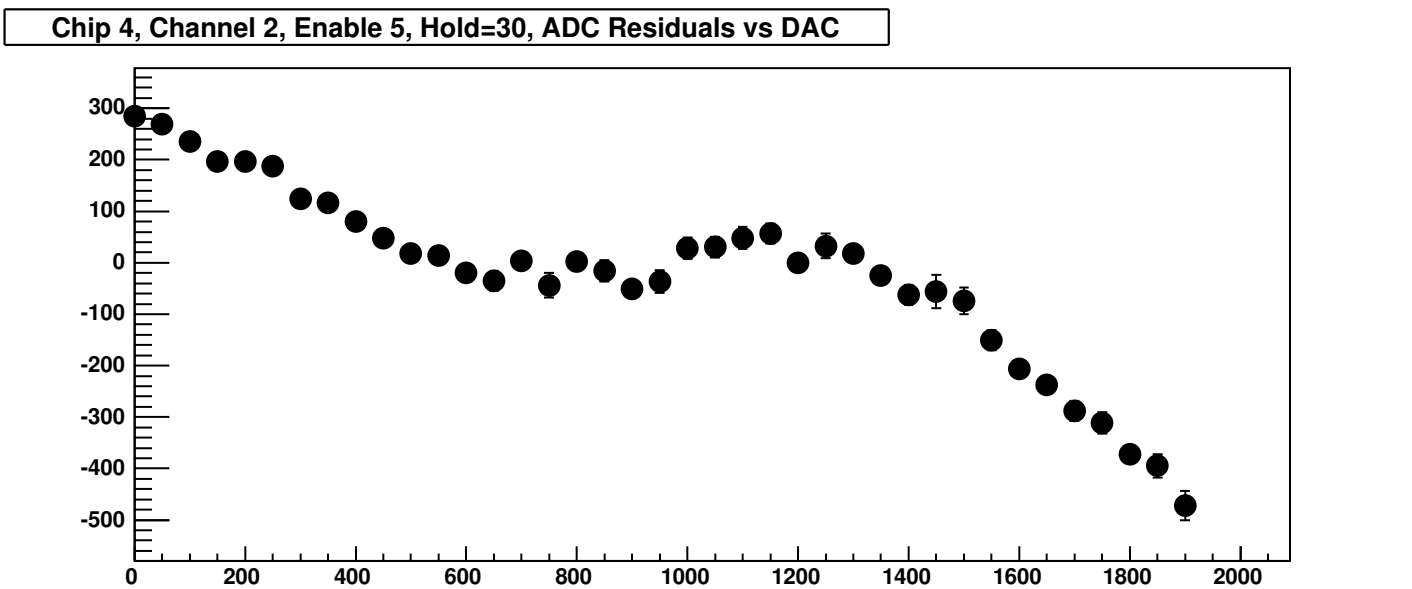

Chip 4, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 3.431 / 11
p0 -1219 ± 18.71
p1 0.05454 ± 0.01689
-1080
-1100
-1120
-1140
-1160
-1180
-1200
-1220
-1240
0
200
400
600
800
1000
1200
1400
1600
1800
2000

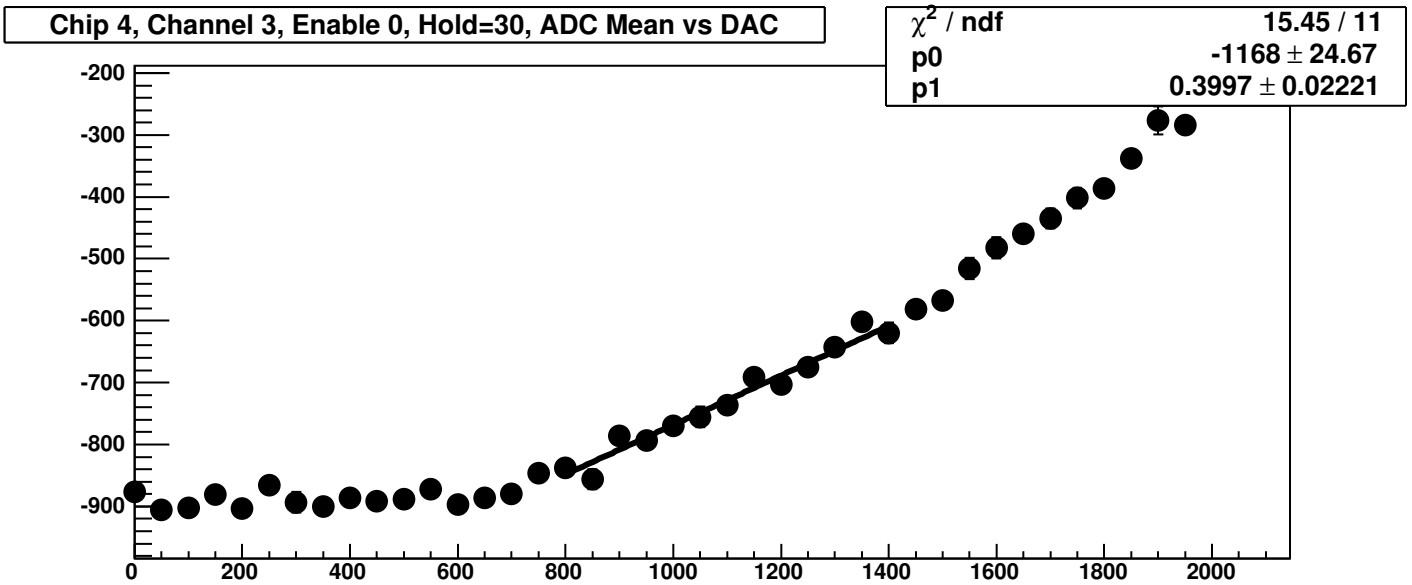

Chip 4, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

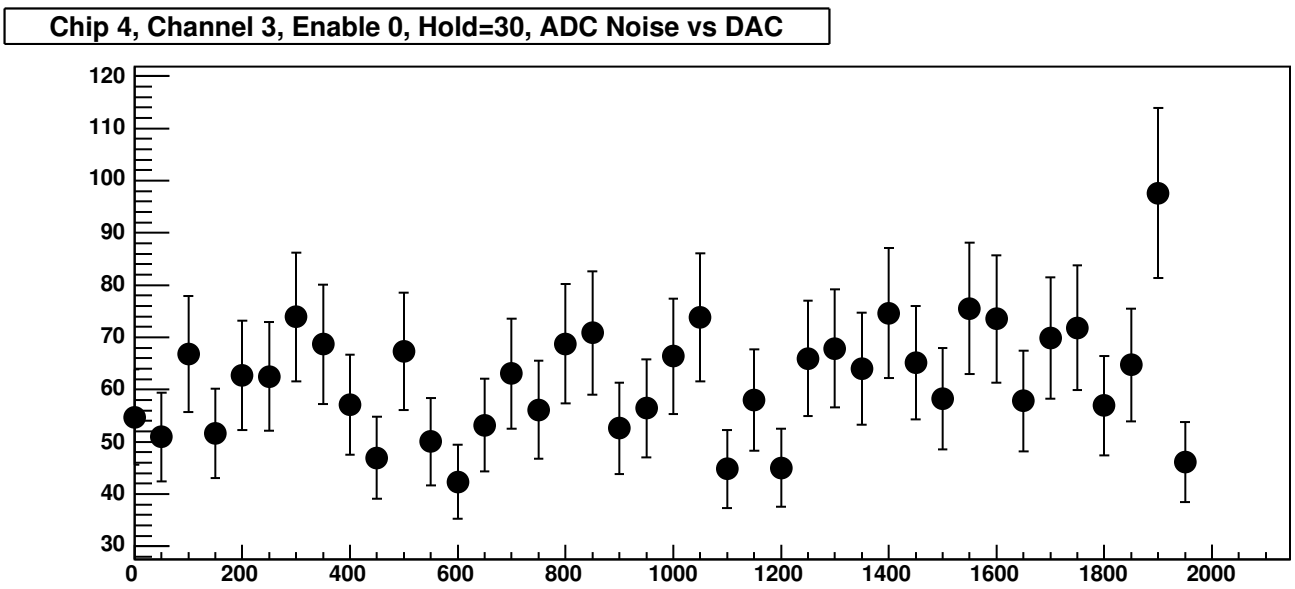

Chip 4, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

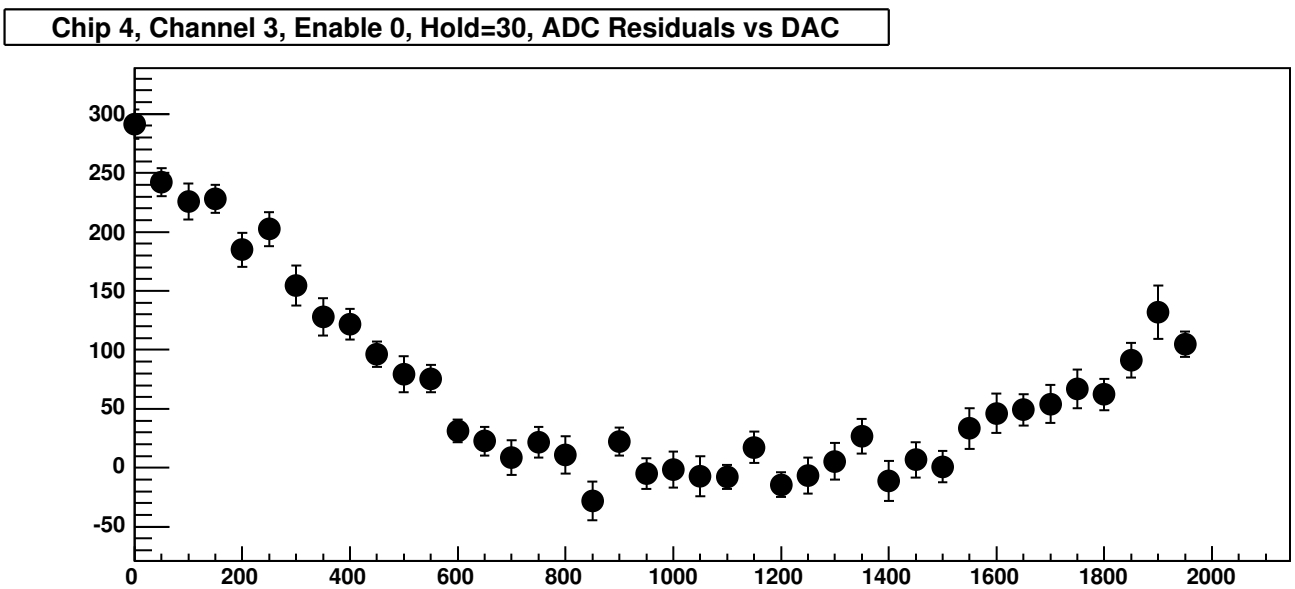

Chip 4, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 44.25 / 11
p0 -1172 ± 27.64
p1 0.5032 ± 0.02491
-500
-600
-700
-800
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

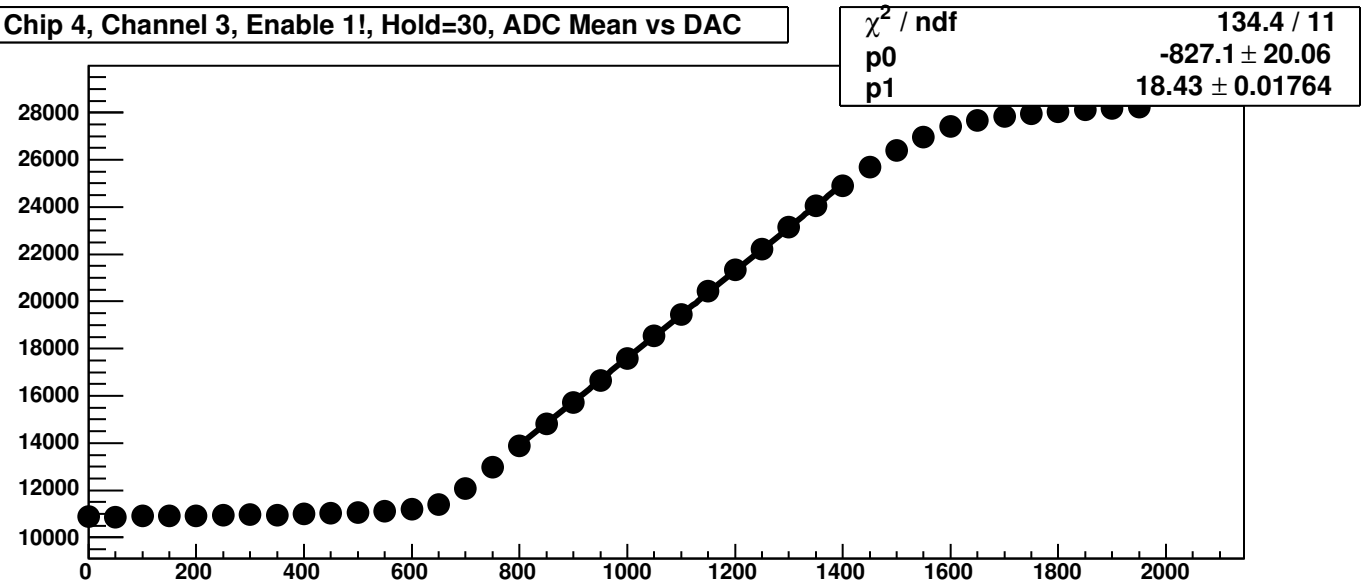

Chip 4, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC
300
200
100
0
-100
-200
-300
-400
-500
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 15.45 / 11
p0 -1168 ± 24.67
p1 0.3997 ± 0.02221
-200
-300
-400
-500
-600
-700
-800
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

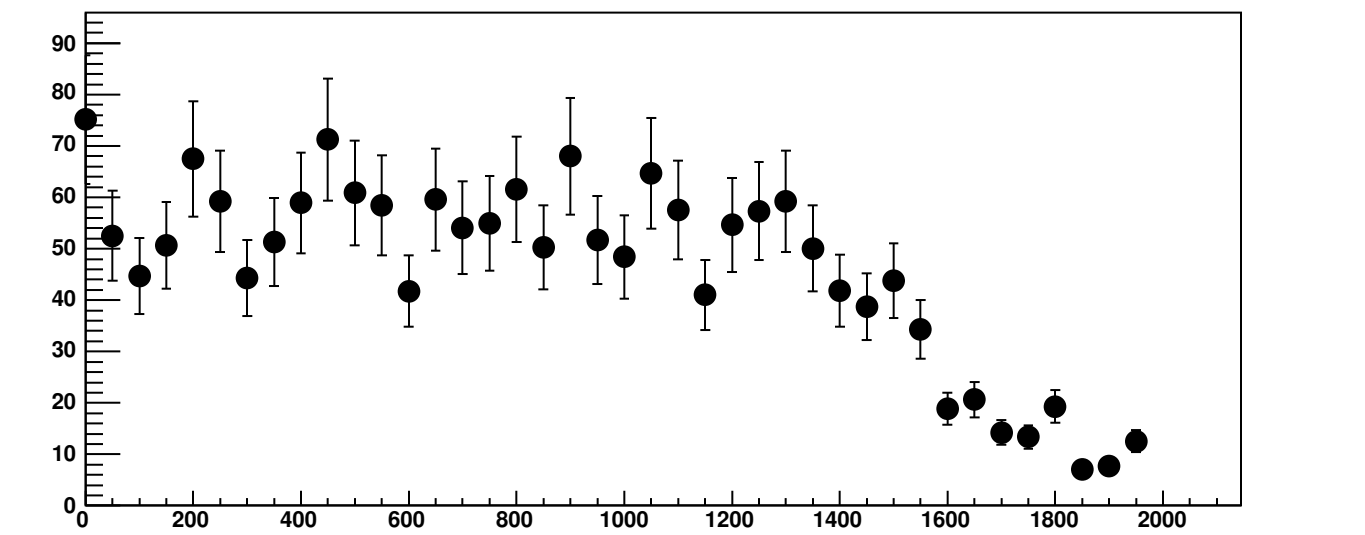

Chip 4, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC
120
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

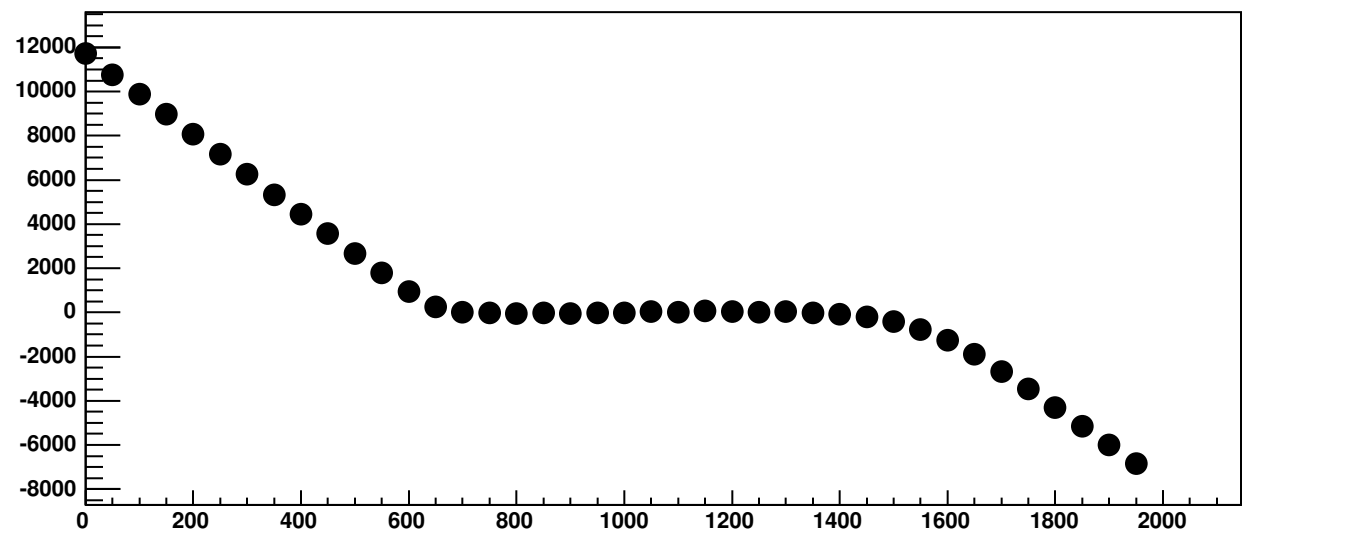
Chip 4, Channel 3, Enable 1!, Hold=30, ADC Mean vs DAC



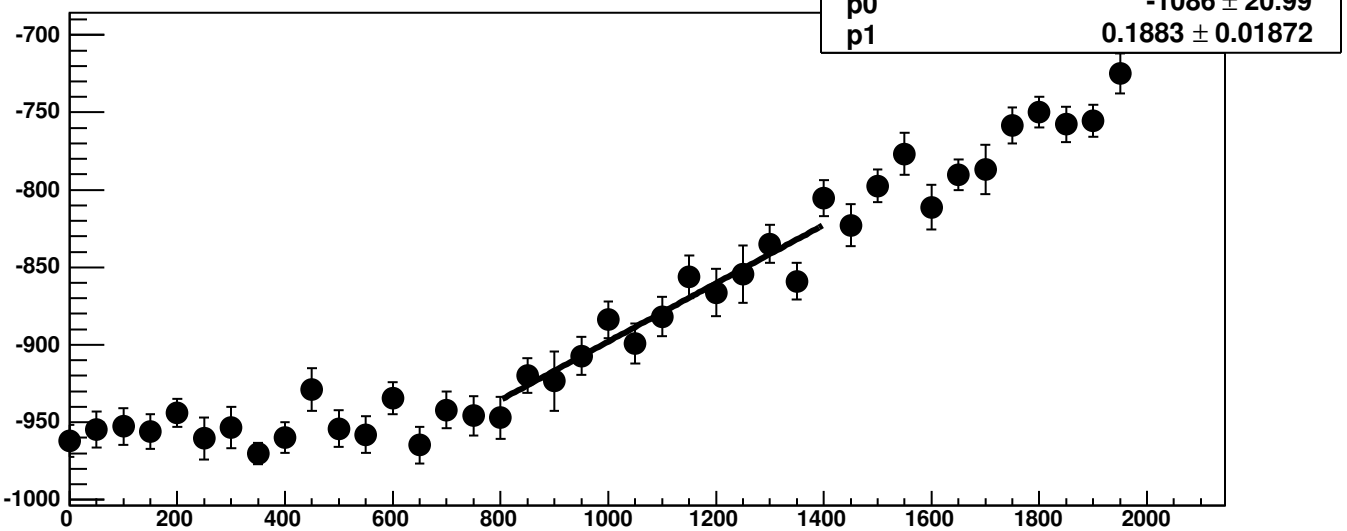
Chip 4, Channel 3, Enable 1!, Hold=30, ADC Noise vs DAC



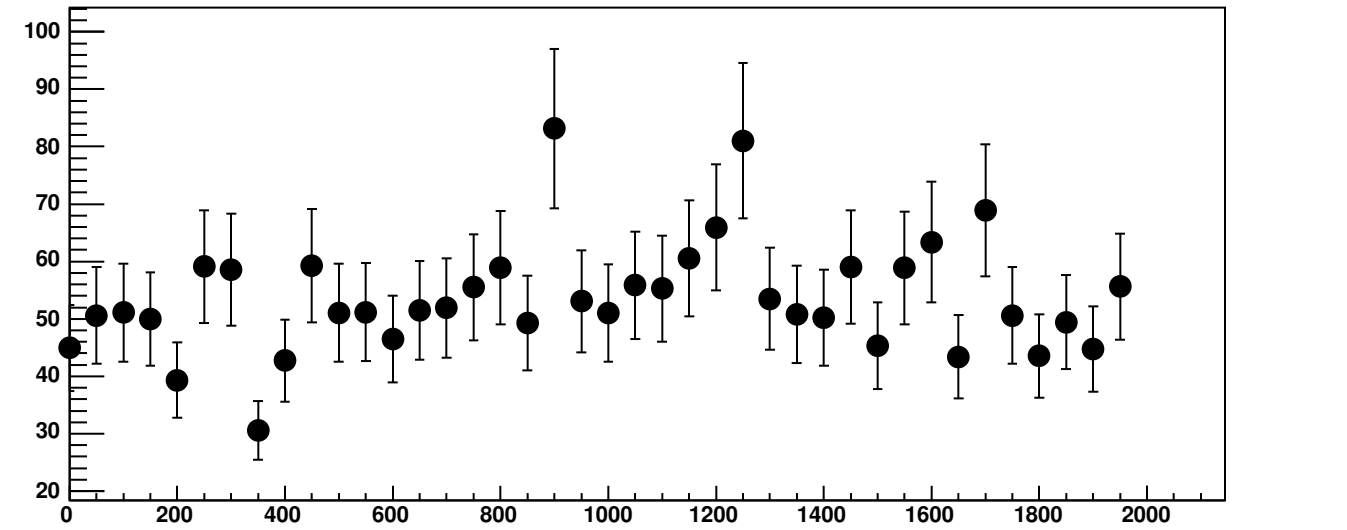
Chip 4, Channel 3, Enable 1!, Hold=30, ADC Residuals vs DAC



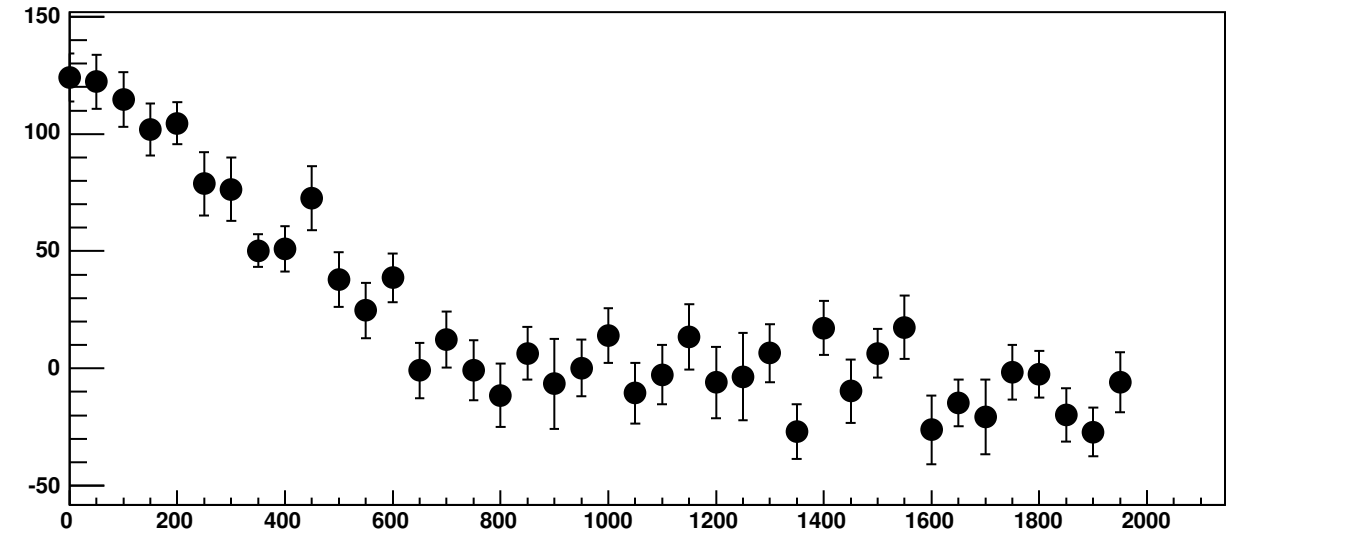
Chip 4, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC

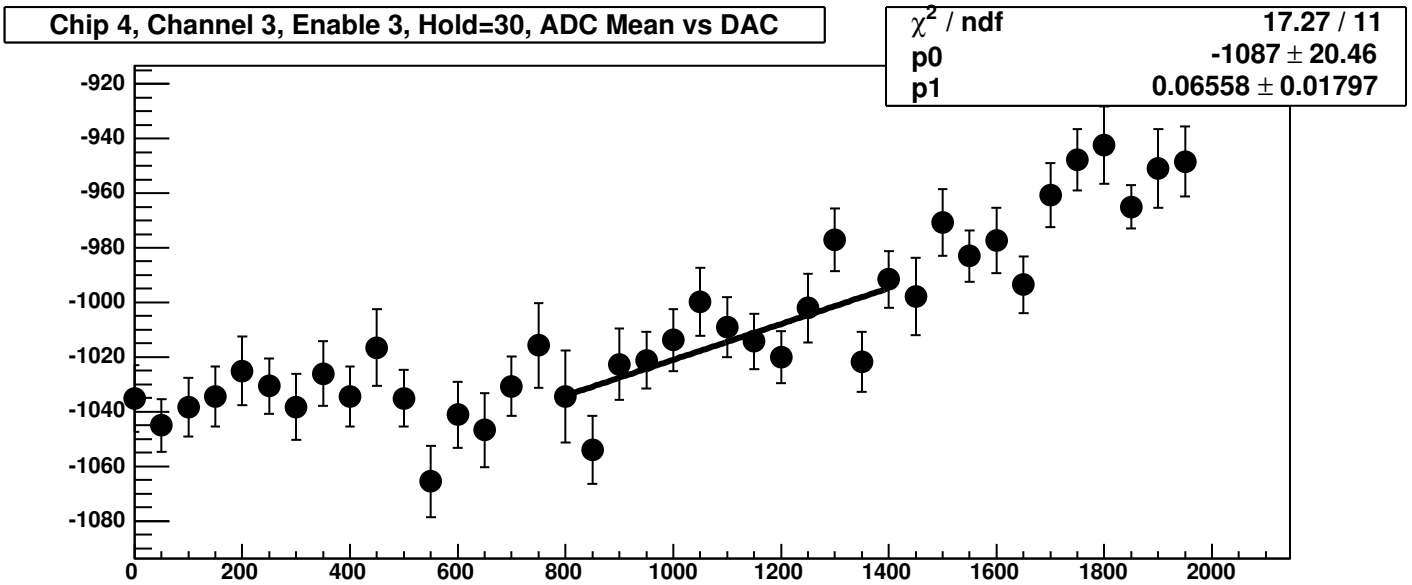


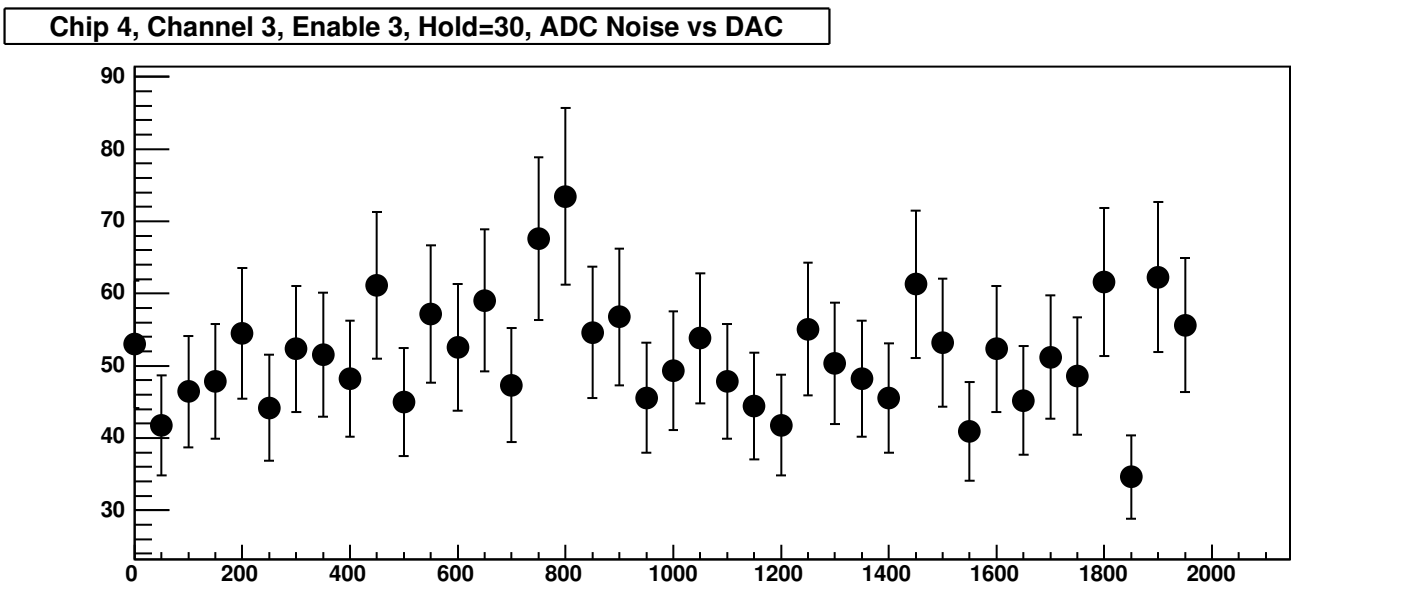
Chip 4, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC

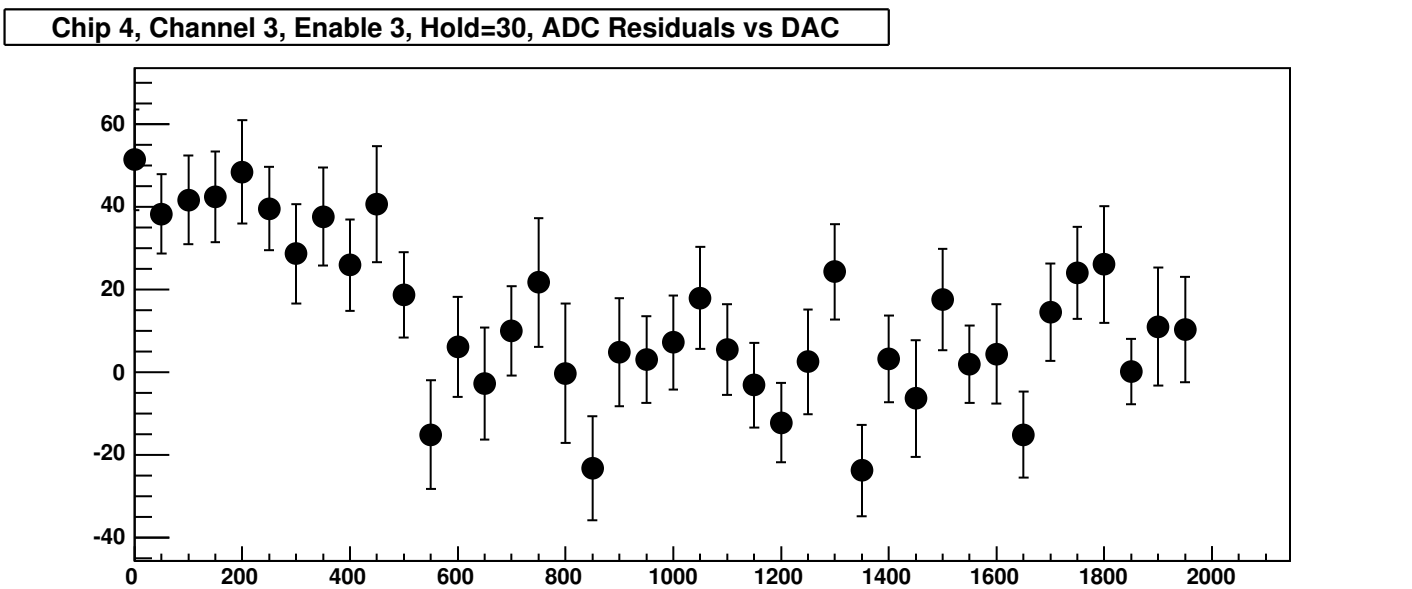


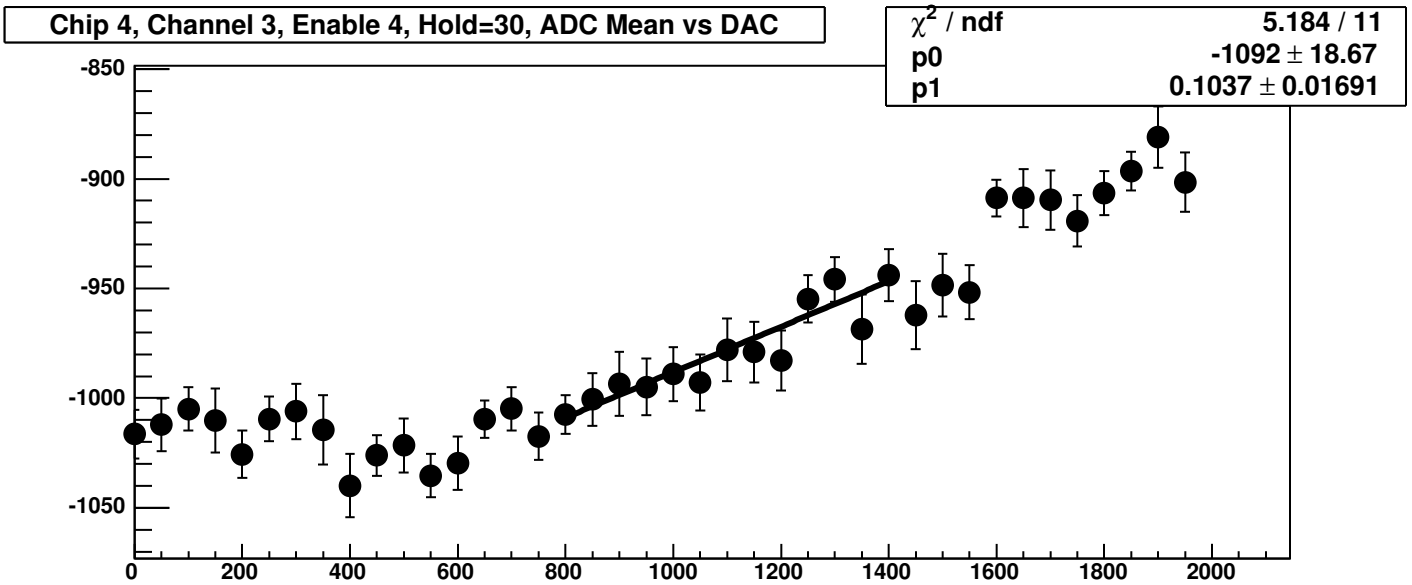
Chip 4, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC

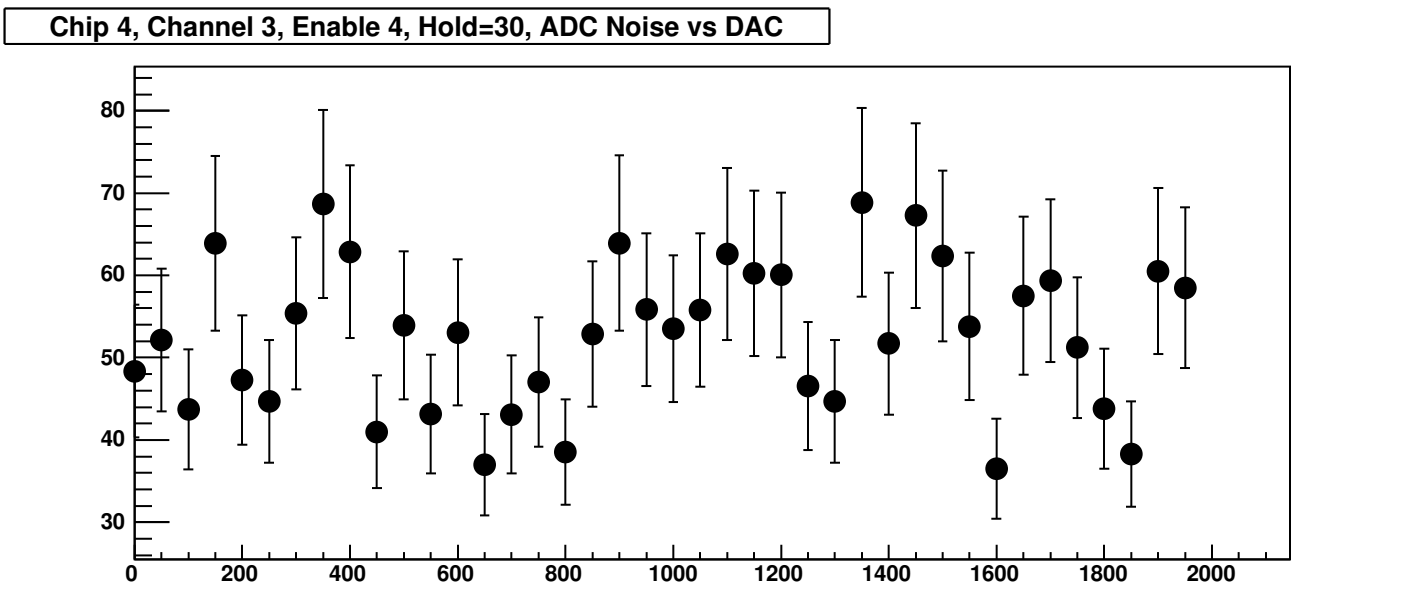


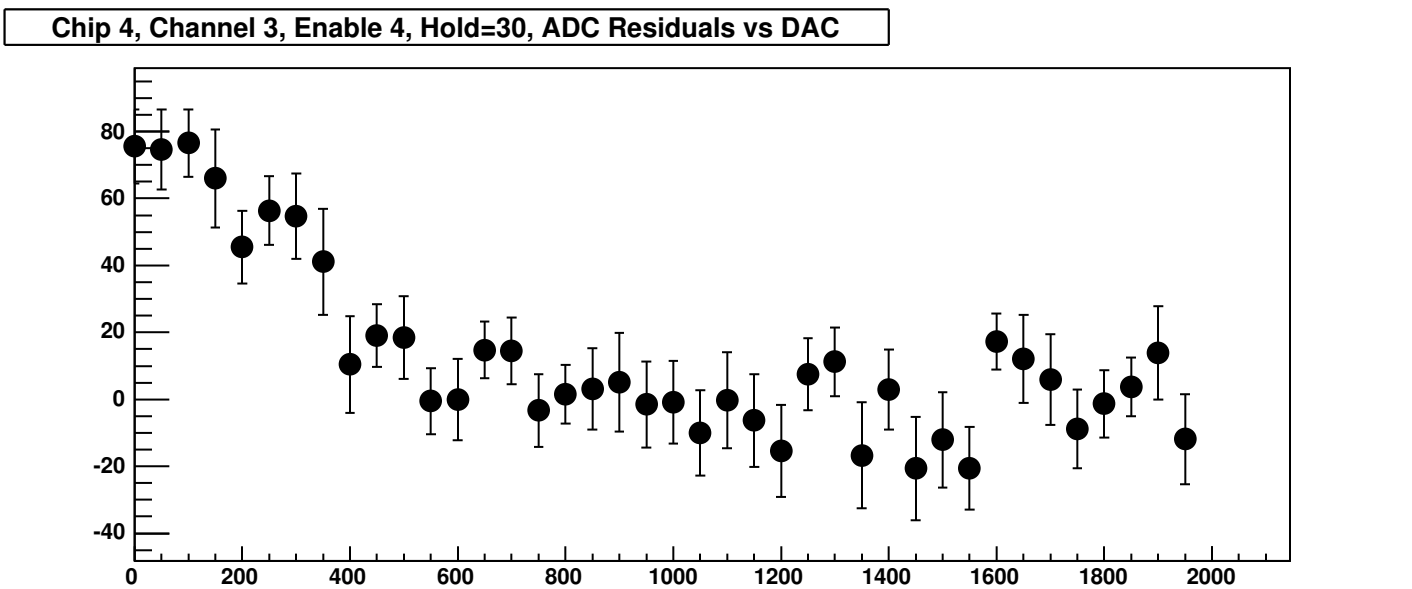

Chip 4, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 17.27 / 11
p0 -1087 ± 20.46
p1 0.06558 ± 0.01797
-920
-940
-960
-980
-1000
-1020
-1040
-1060
-1080
0
200
400
600
800
1000
1200
1400
1600
1800
2000

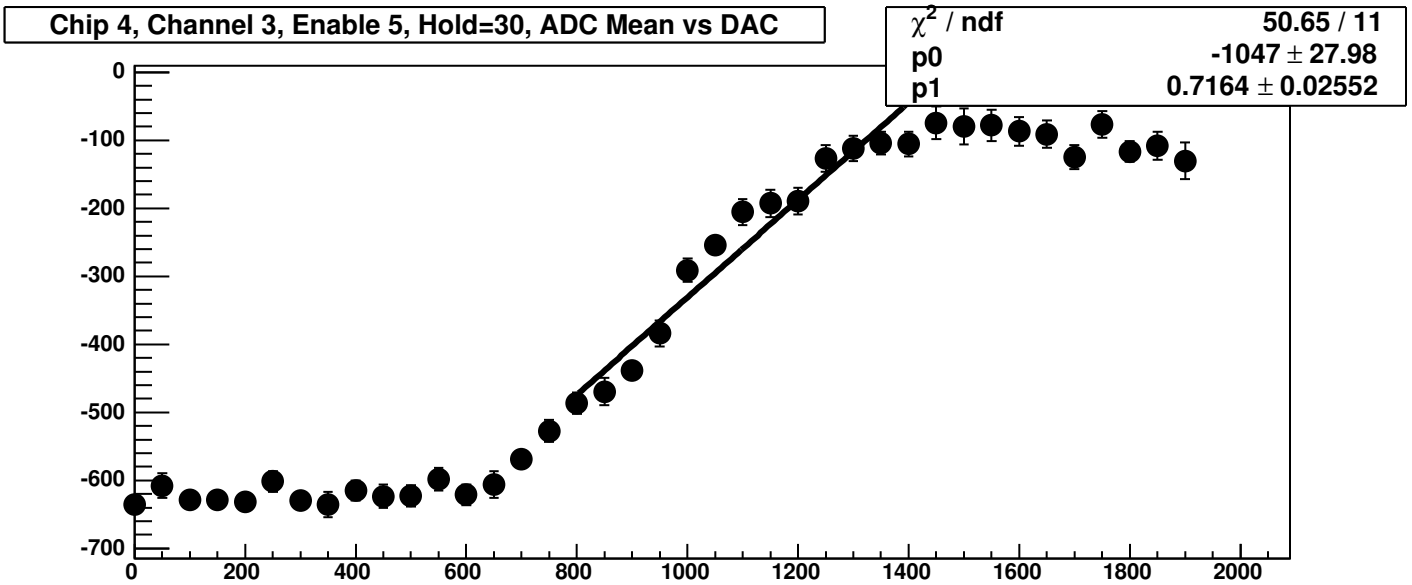

Chip 4, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

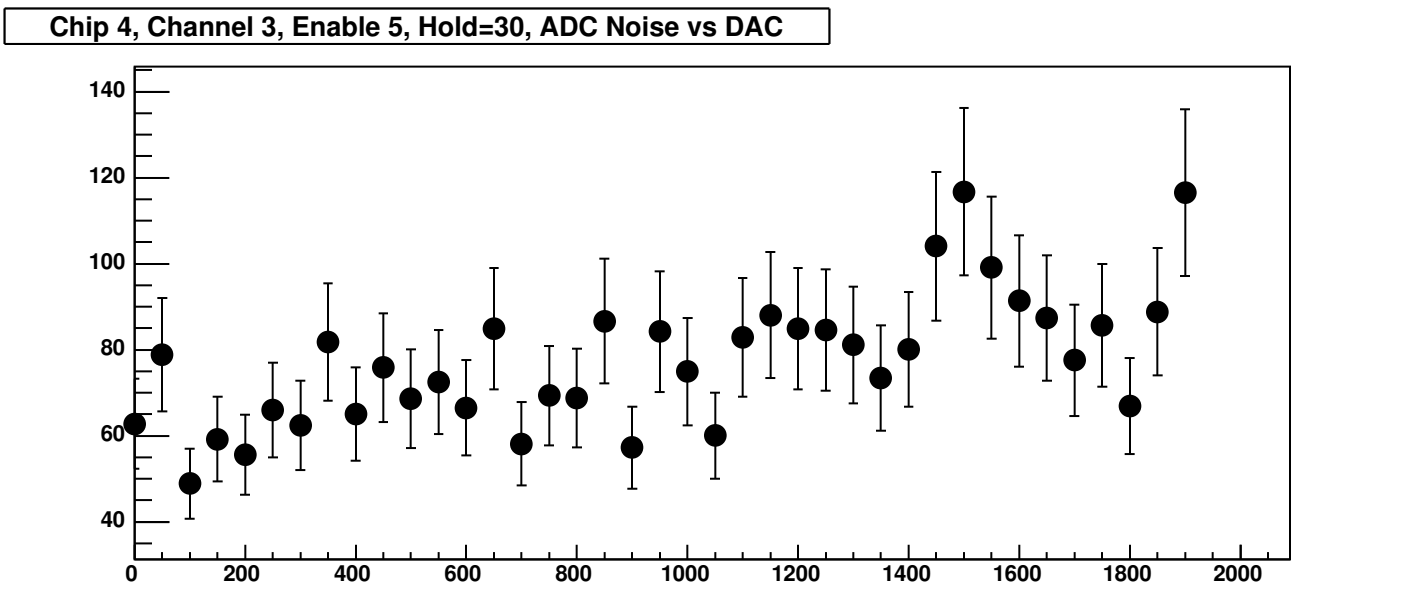

Chip 4, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

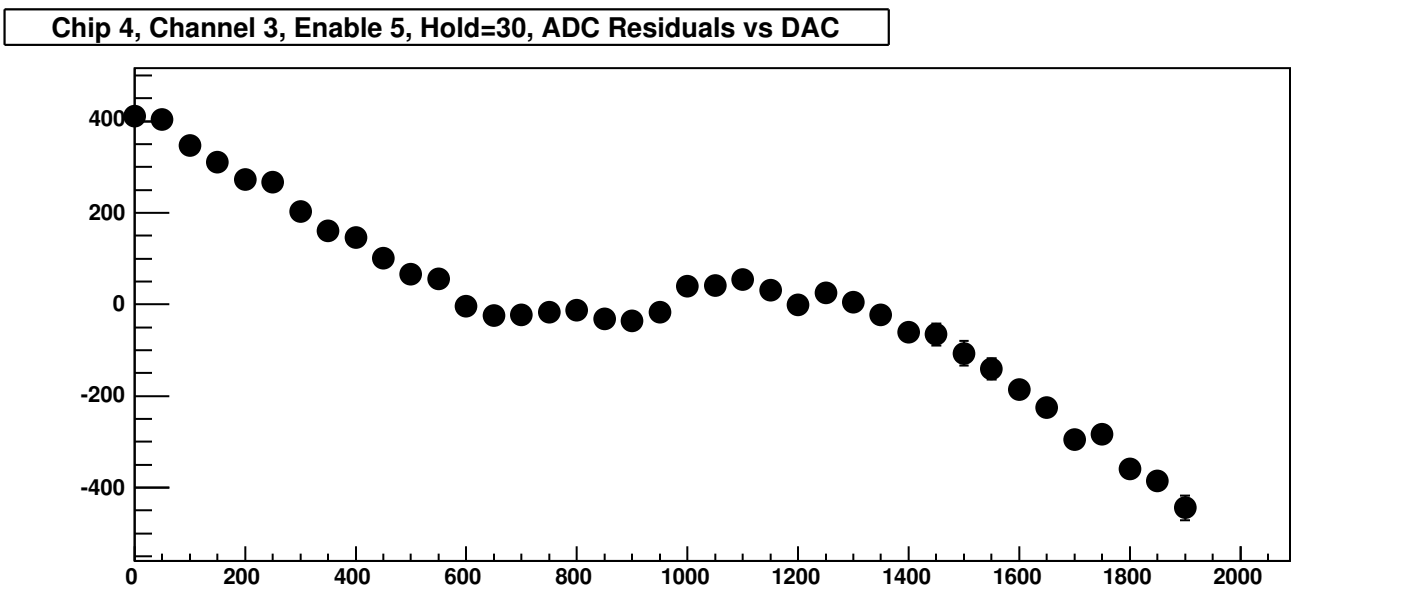

Chip 4, Channel 3, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5.184 / 11
p0 -1092 ± 18.67
p1 0.1037 ± 0.01691
-850
-900
-950
-1000
-1050
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 3, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

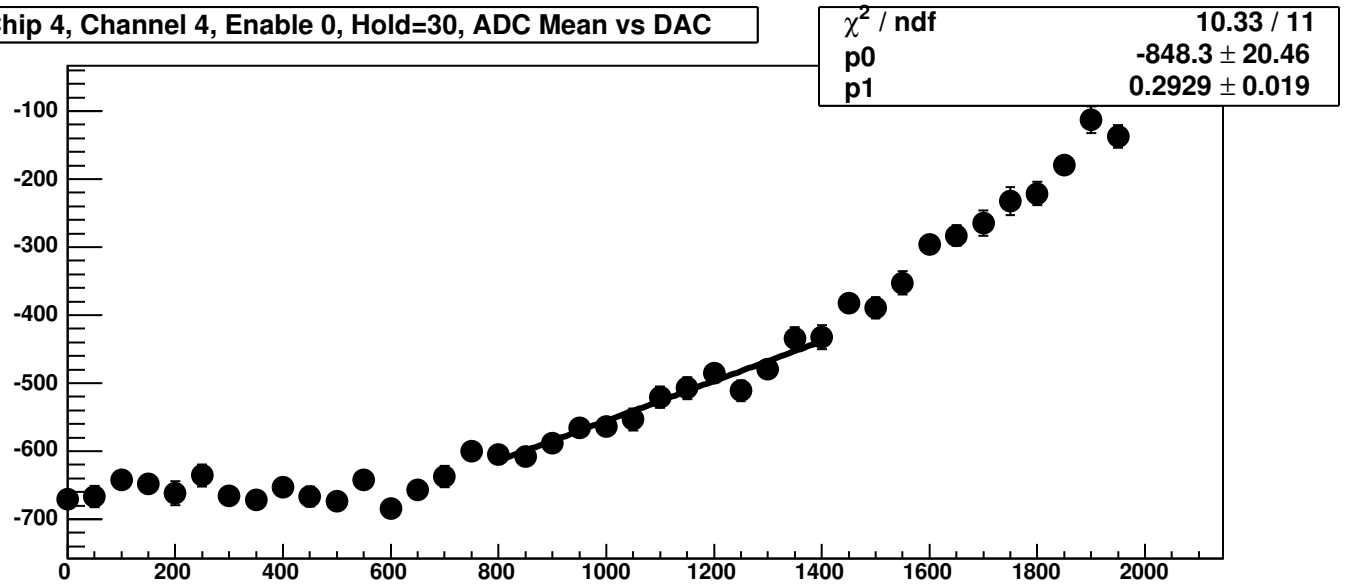

Chip 4, Channel 3, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 50.65 / 11
p0 -1047 ± 27.98
p1 0.7164 ± 0.02552
0
-100
-200
-300
-400
-500
-600
-700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

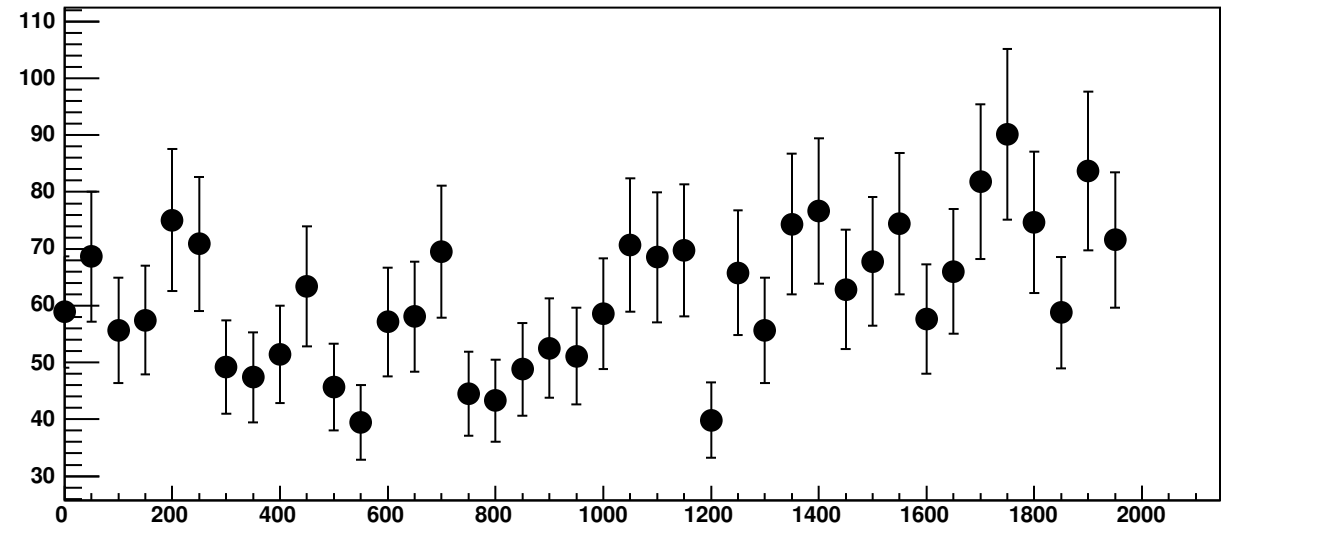

Chip 4, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

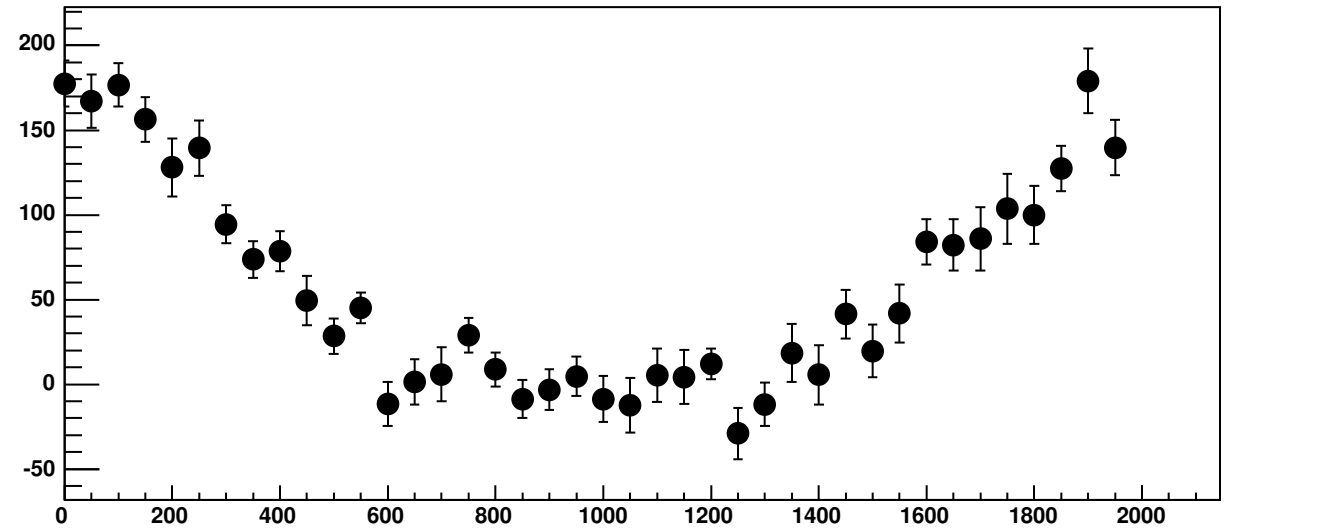
Chip 4, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC

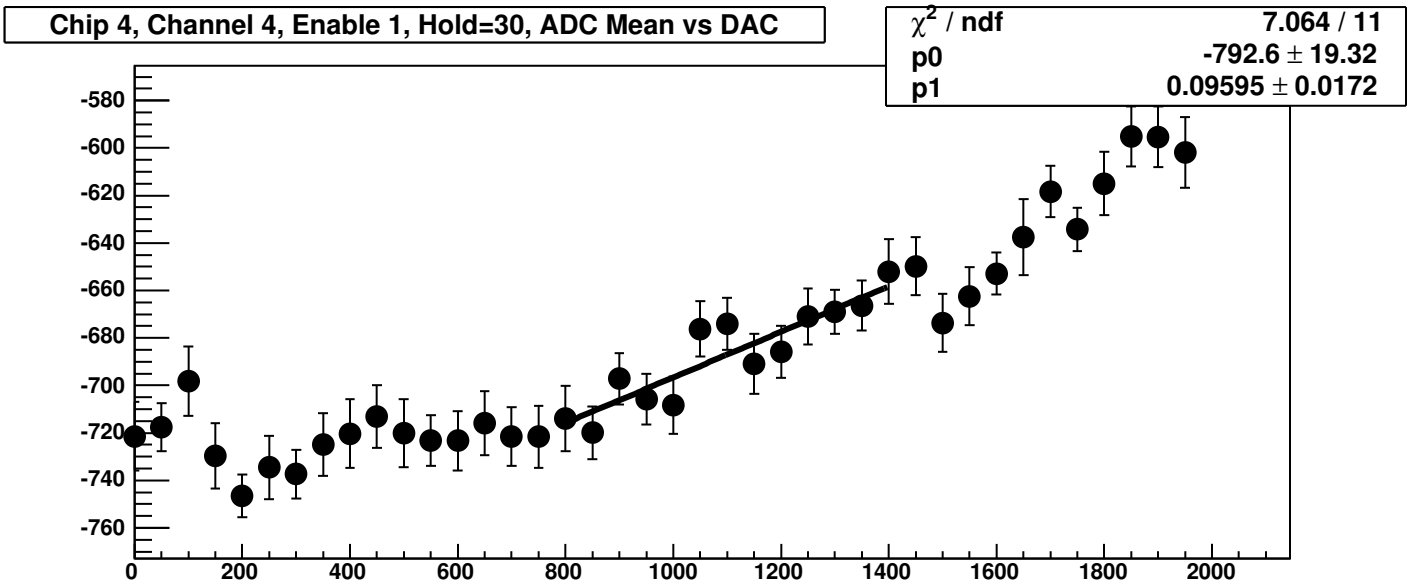


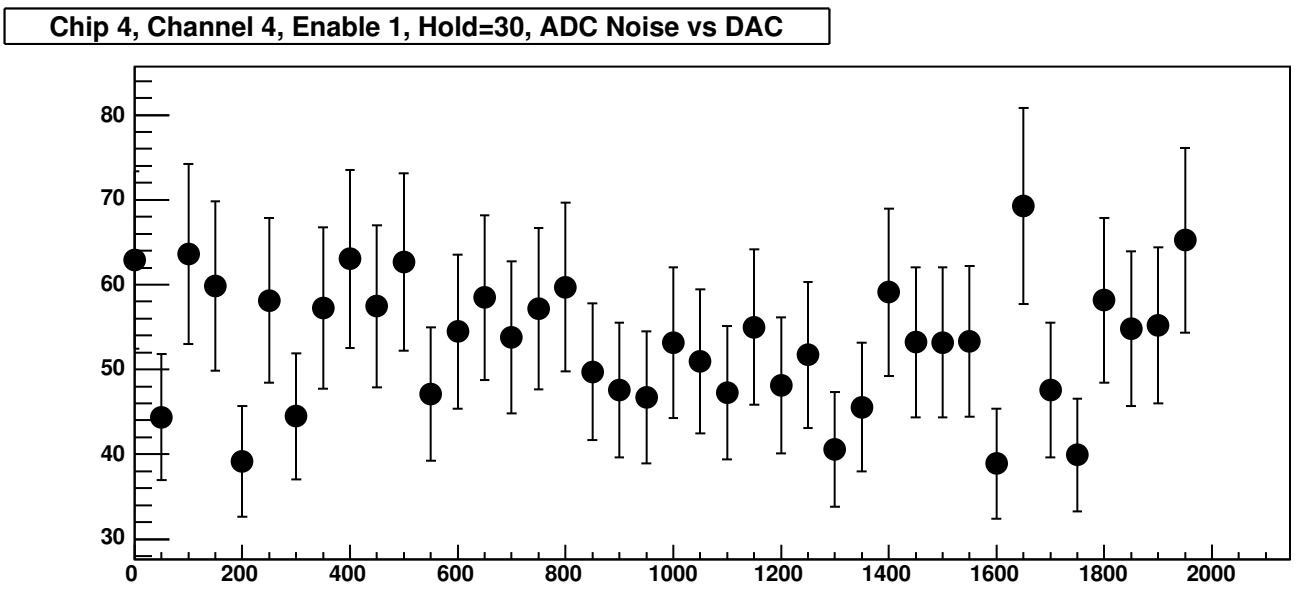
Chip 4, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC

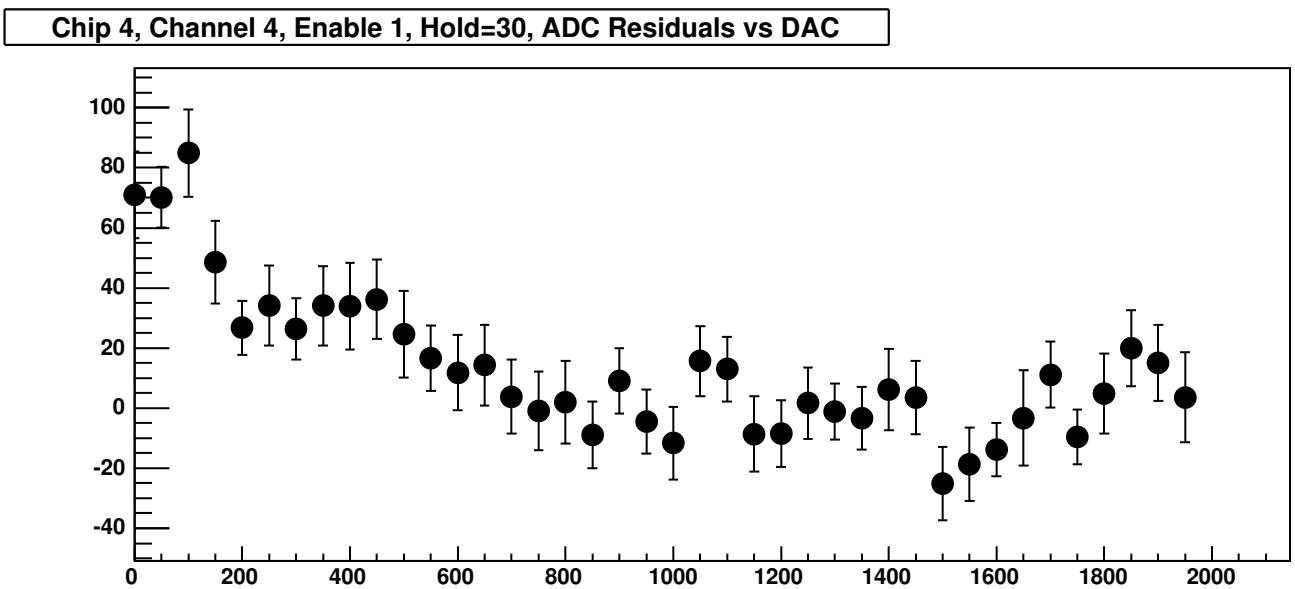


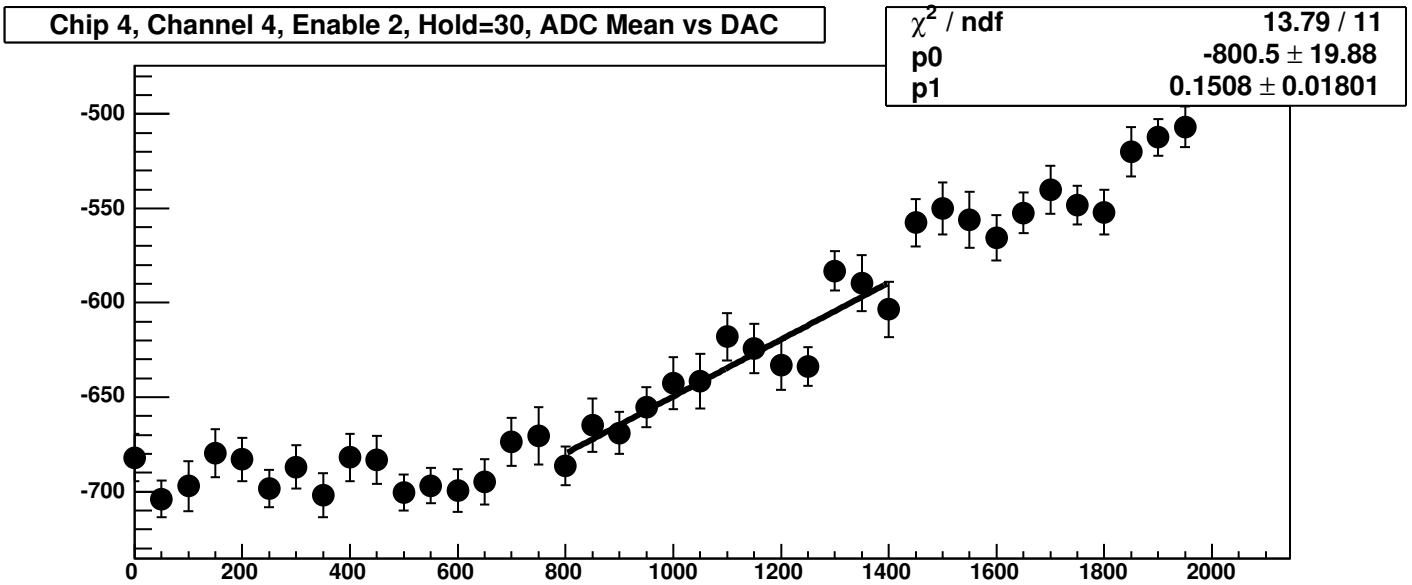
Chip 4, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC

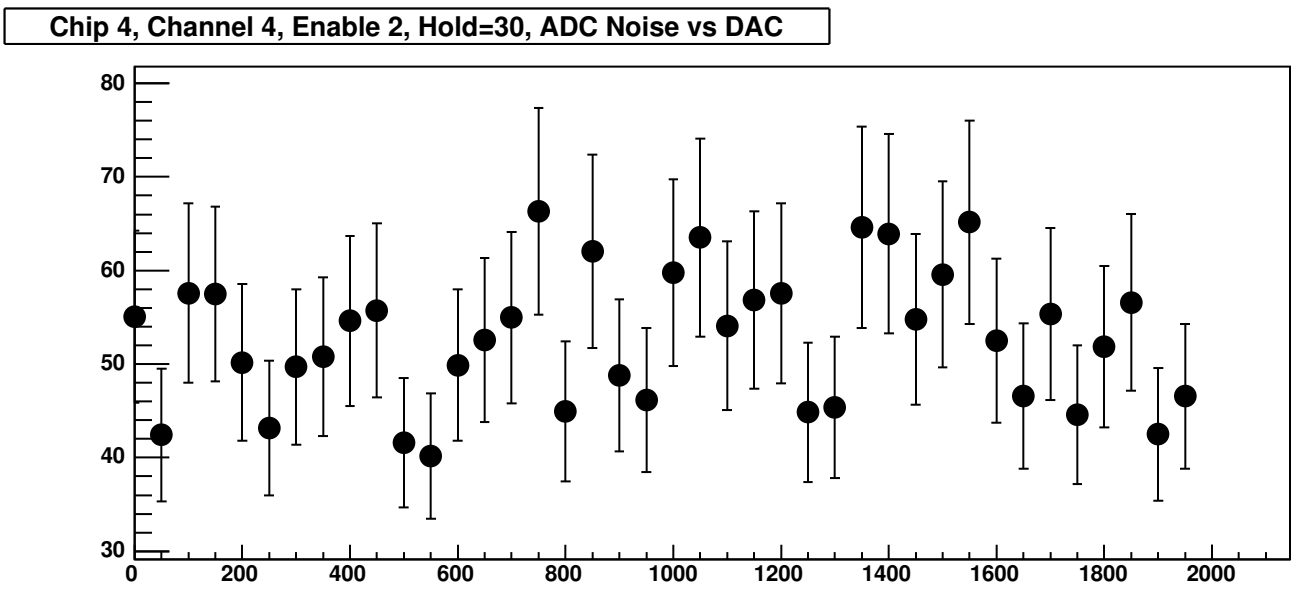


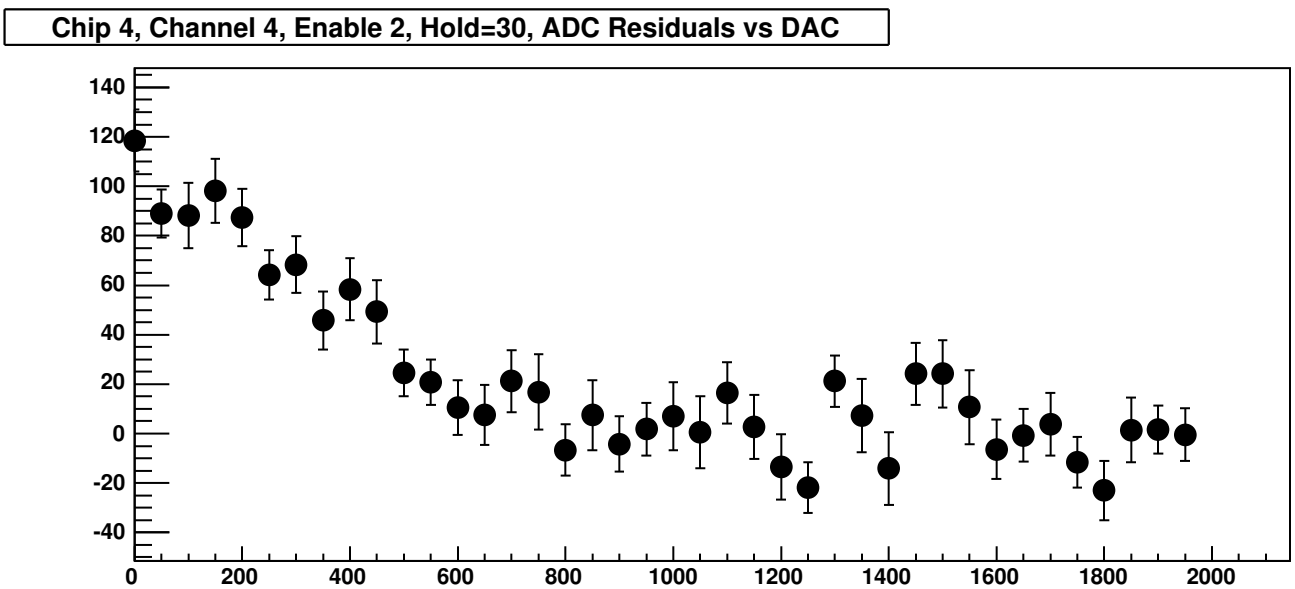

Chip 4, Channel 4, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7.064 / 11
p0 -792.6 ± 19.32
p1 0.09595 ± 0.0172
-580
-600
-620
-640
-660
-680
-700
-720
-740
-760
0
200
400
600
800
1000
1200
1400
1600
1800
2000

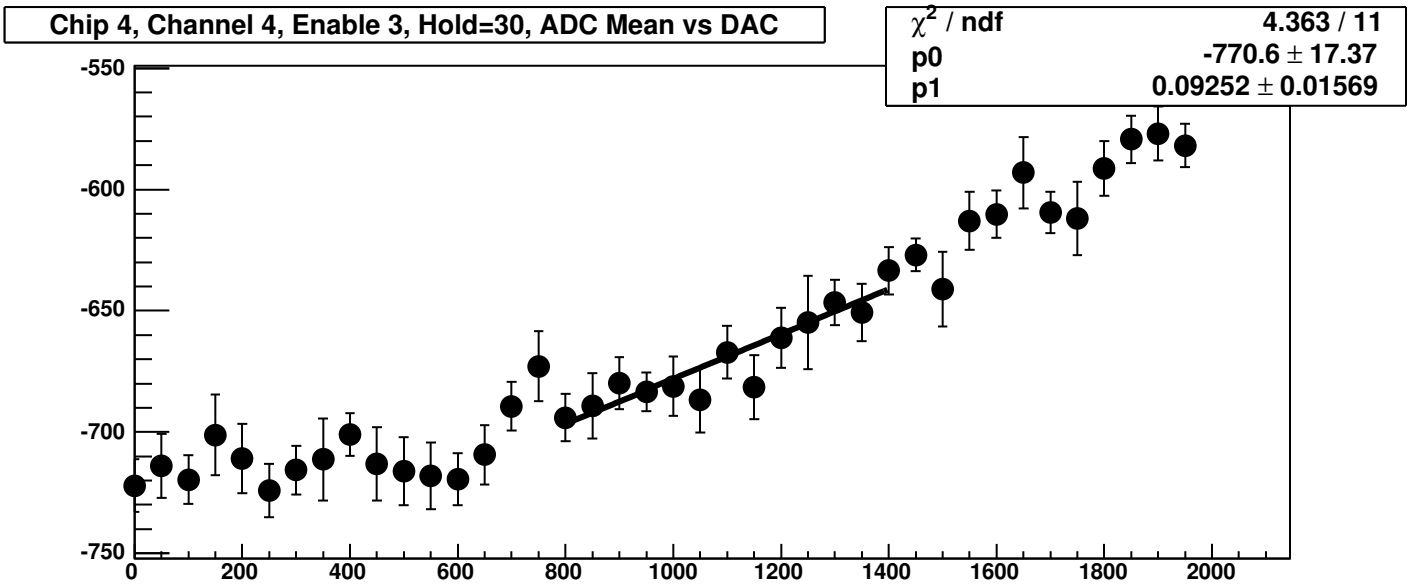

Chip 4, Channel 4, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

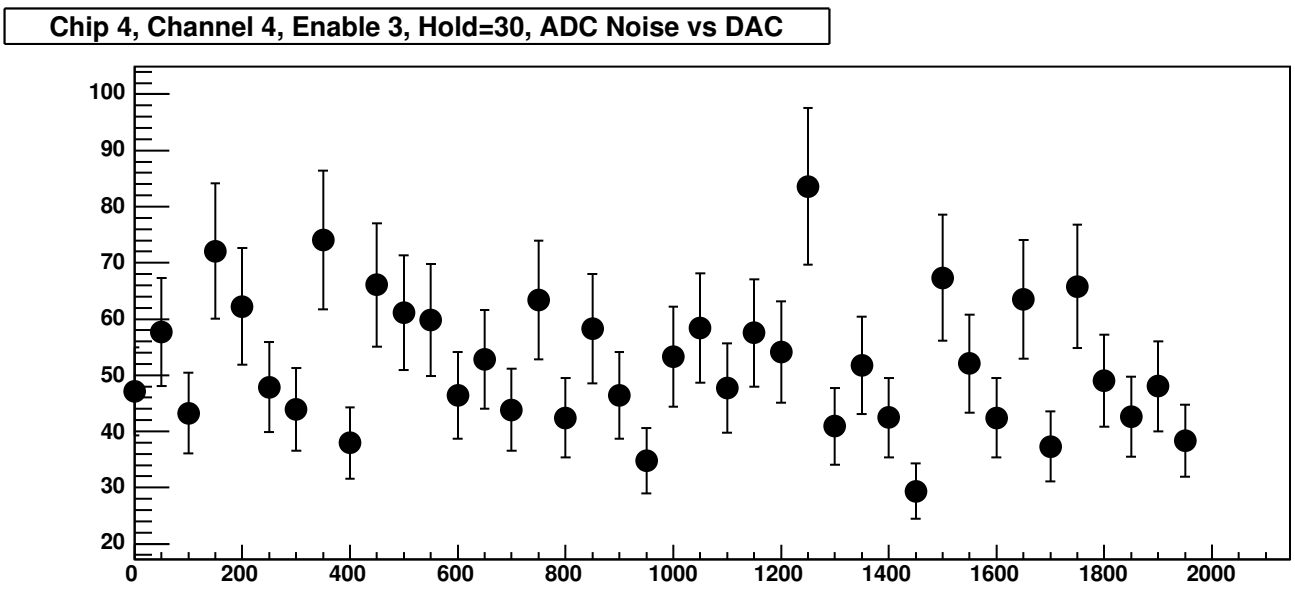

Chip 4, Channel 4, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

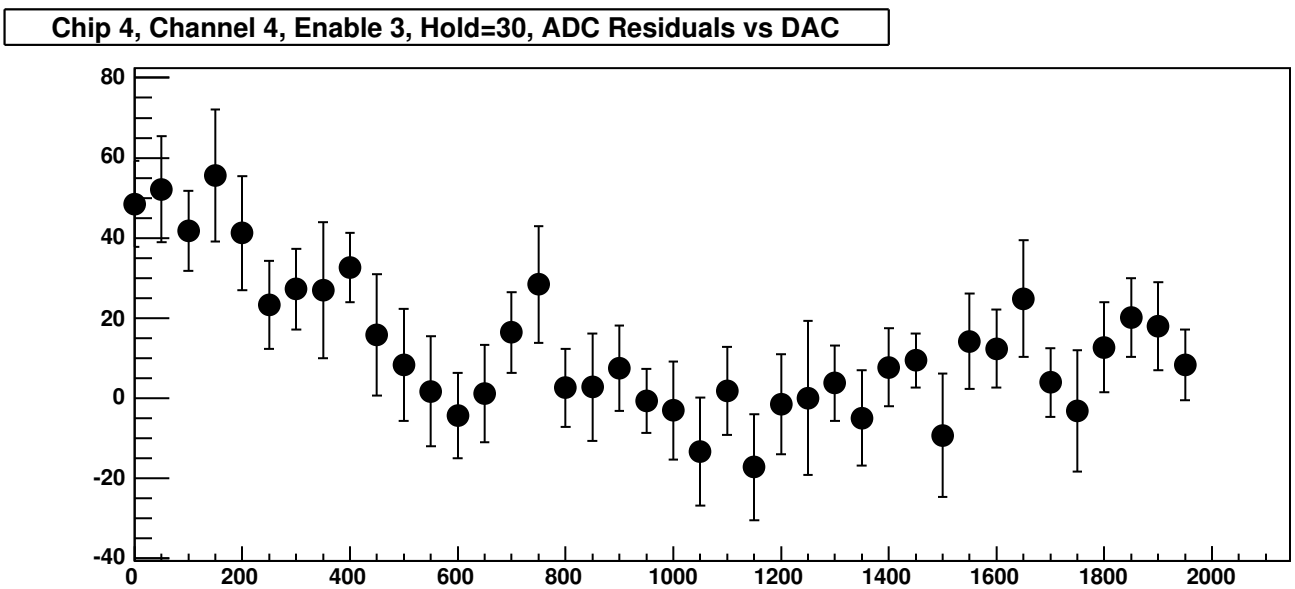

Chip 4, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 13.79 / 11
p0 -800.5 ± 19.88
p1 0.1508 ± 0.01801
-500
-550
-600
-650
-700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

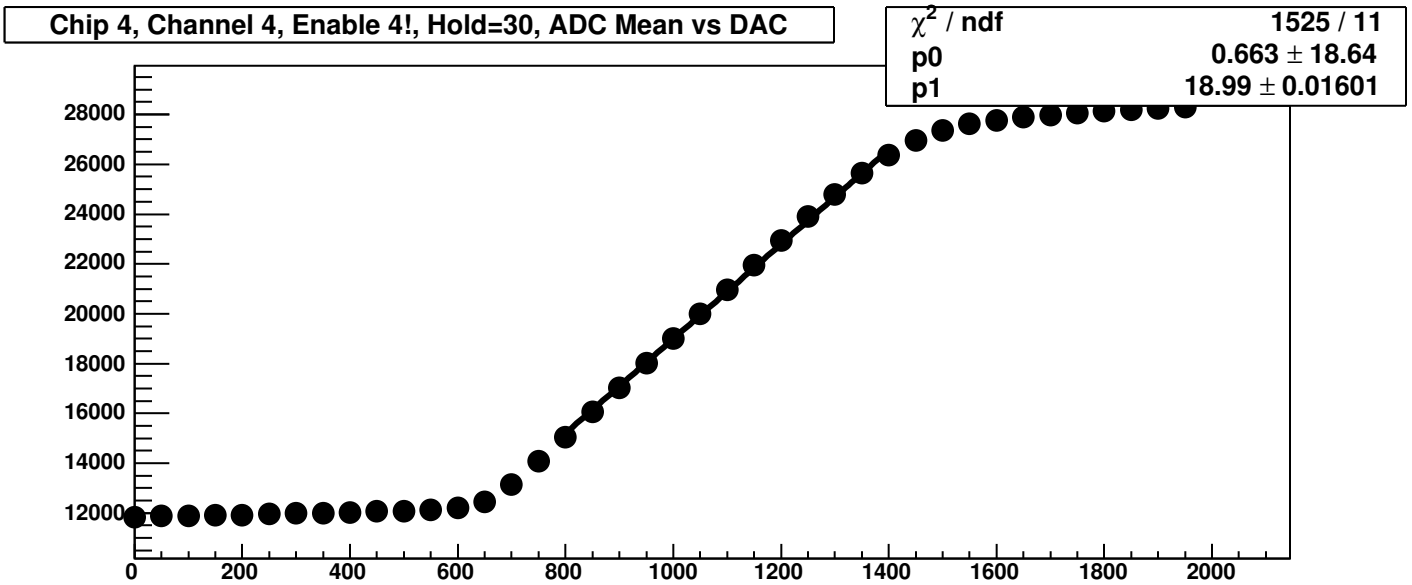

Chip 4, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

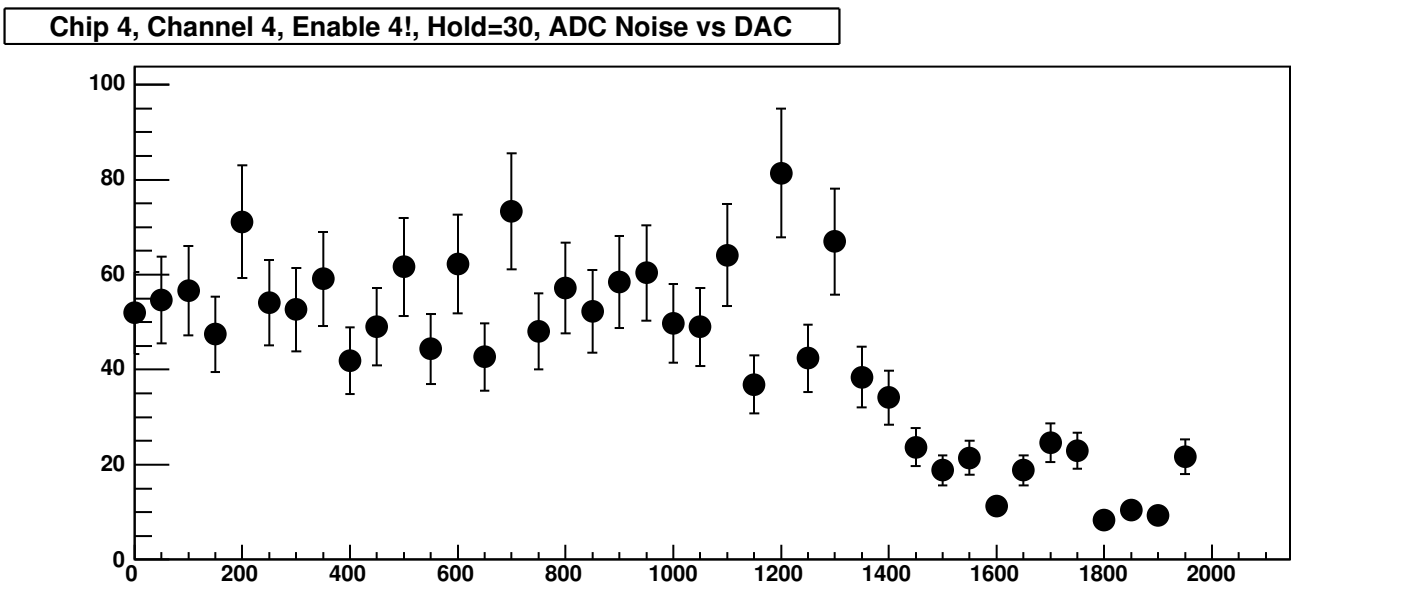

Chip 4, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

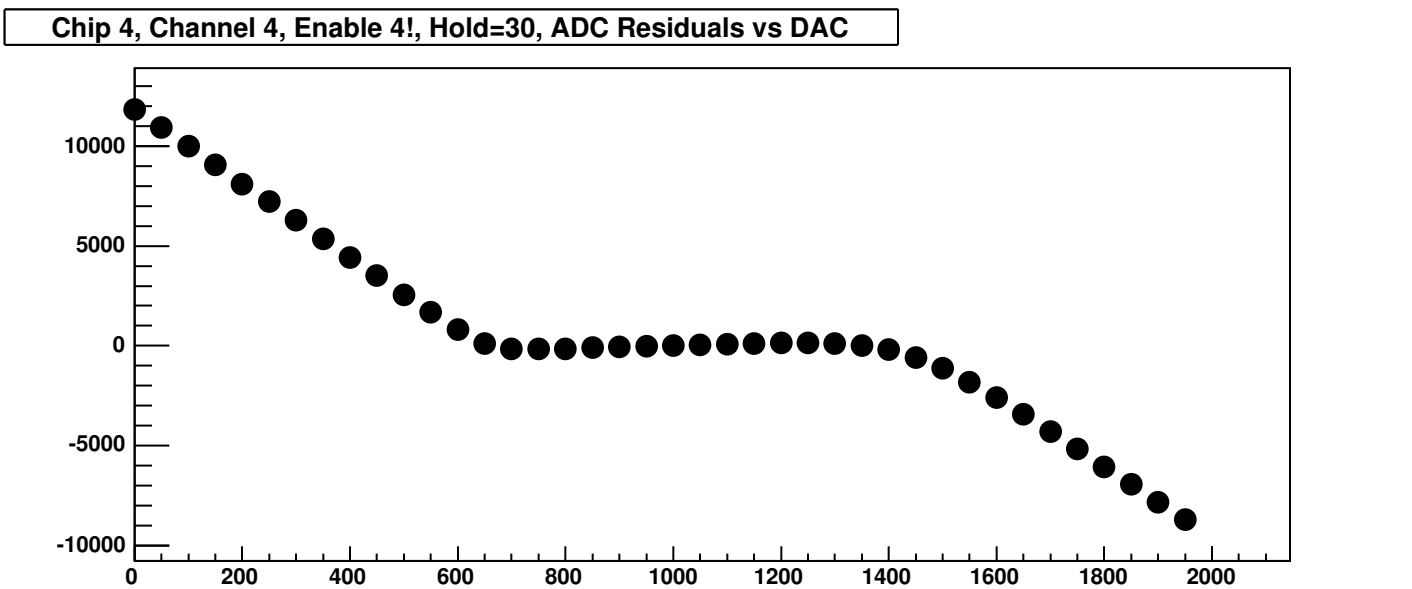

Chip 4, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4.363 / 11
p0 -770.6 ± 17.37
p1 0.09252 ± 0.01569
-550
-600
-650
-700
-750
0
200
400
600
800
1000
1200
1400
1600
1800
2000

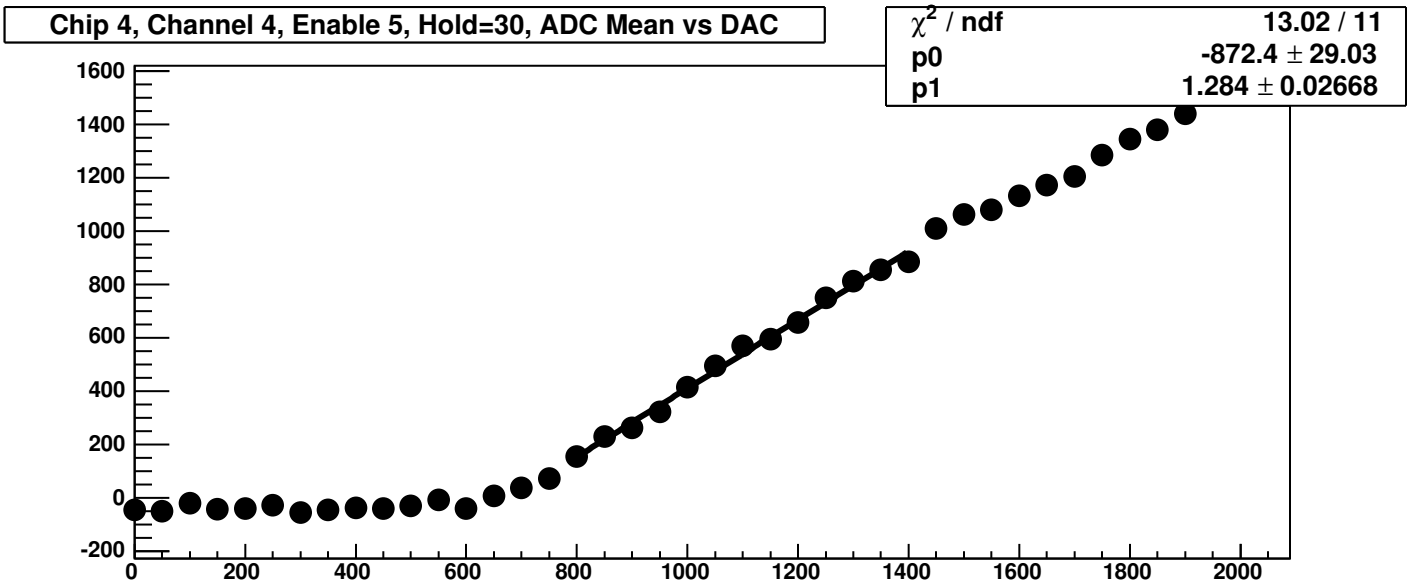

Chip 4, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

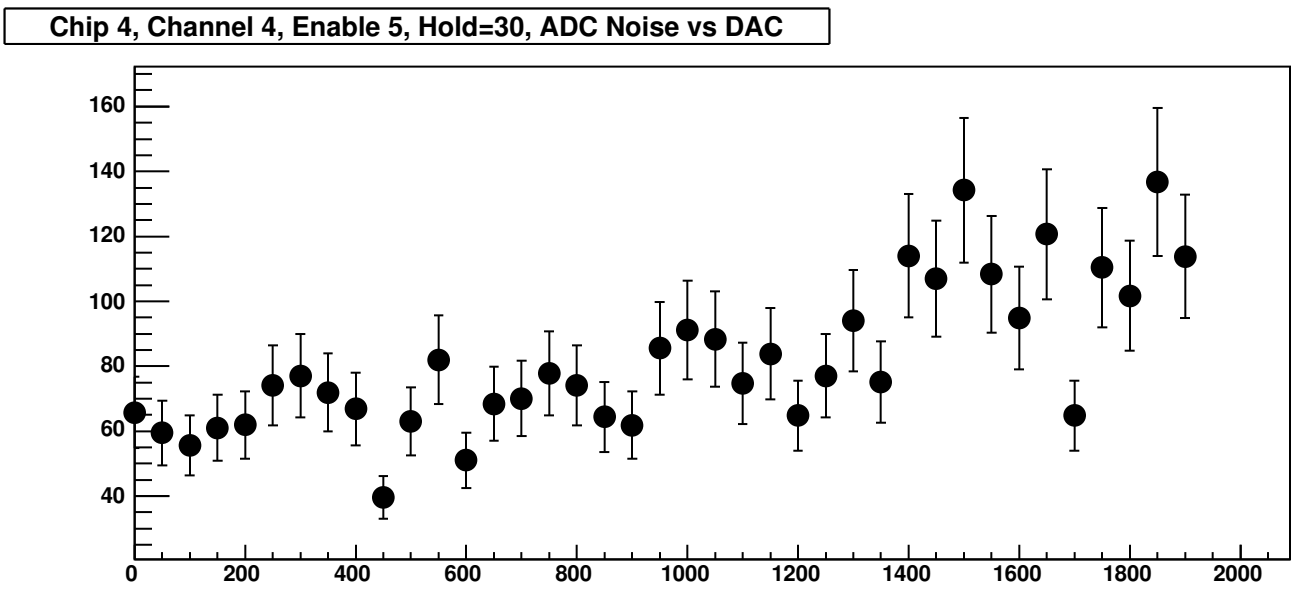

Chip 4, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

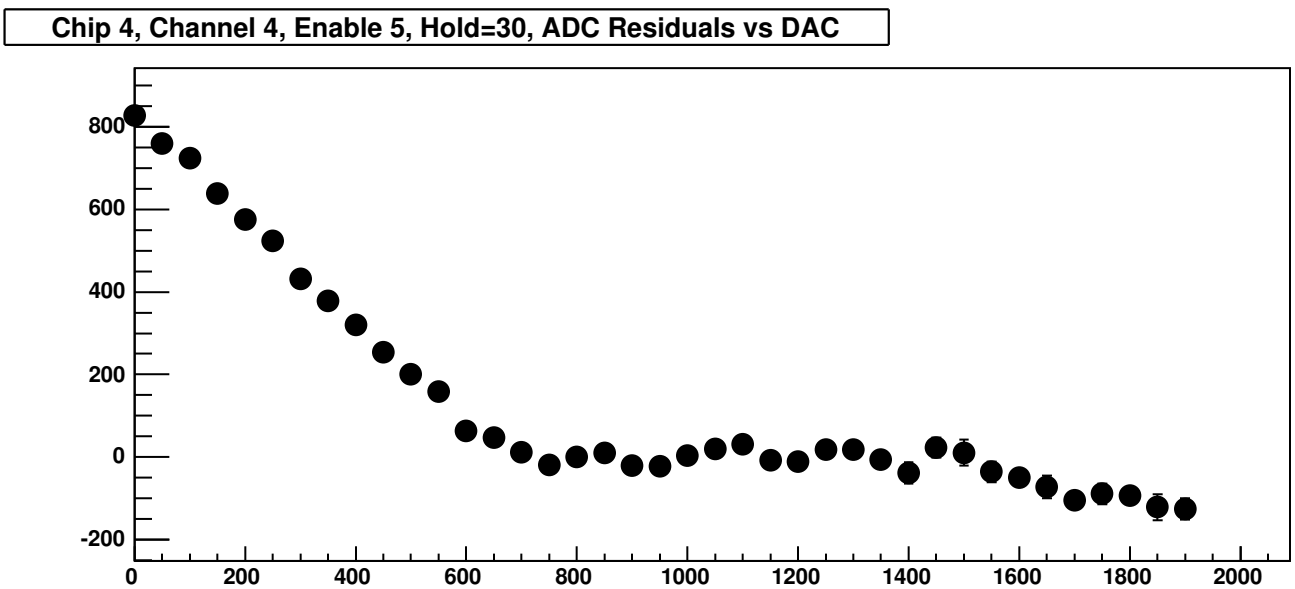

Chip 4, Channel 4, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 1525 / 11
p0 0.663 ± 18.64
p1 18.99 ± 0.01601
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

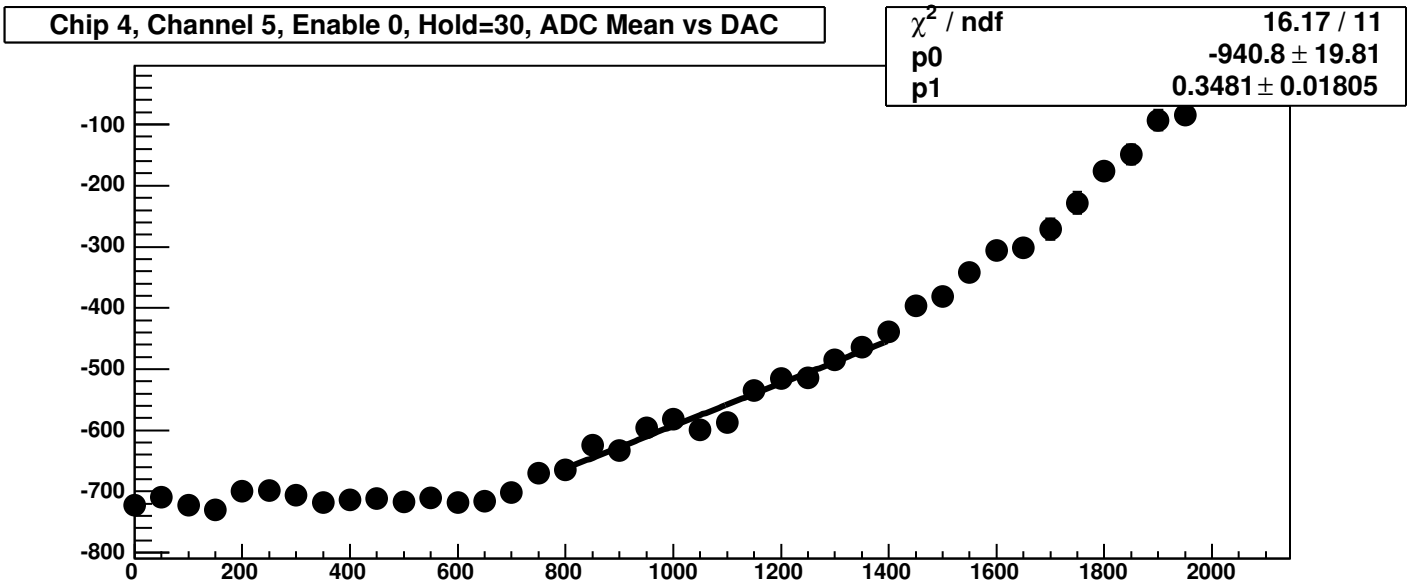

Chip 4, Channel 4, Enable 4!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

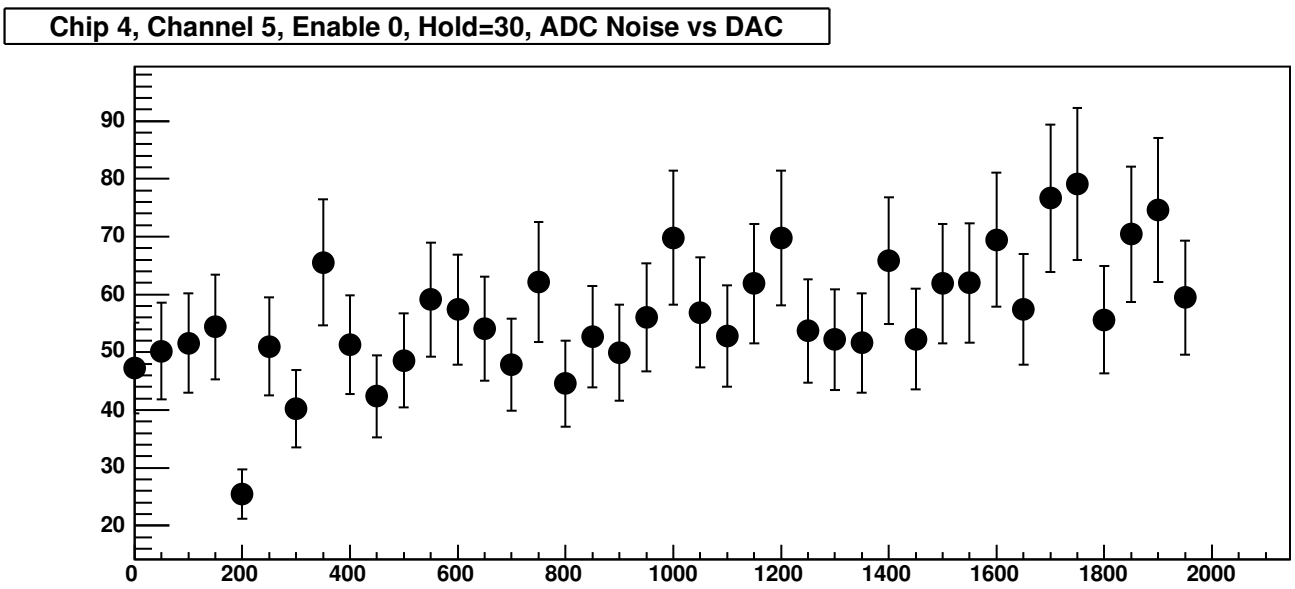

Chip 4, Channel 4, Enable 4!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

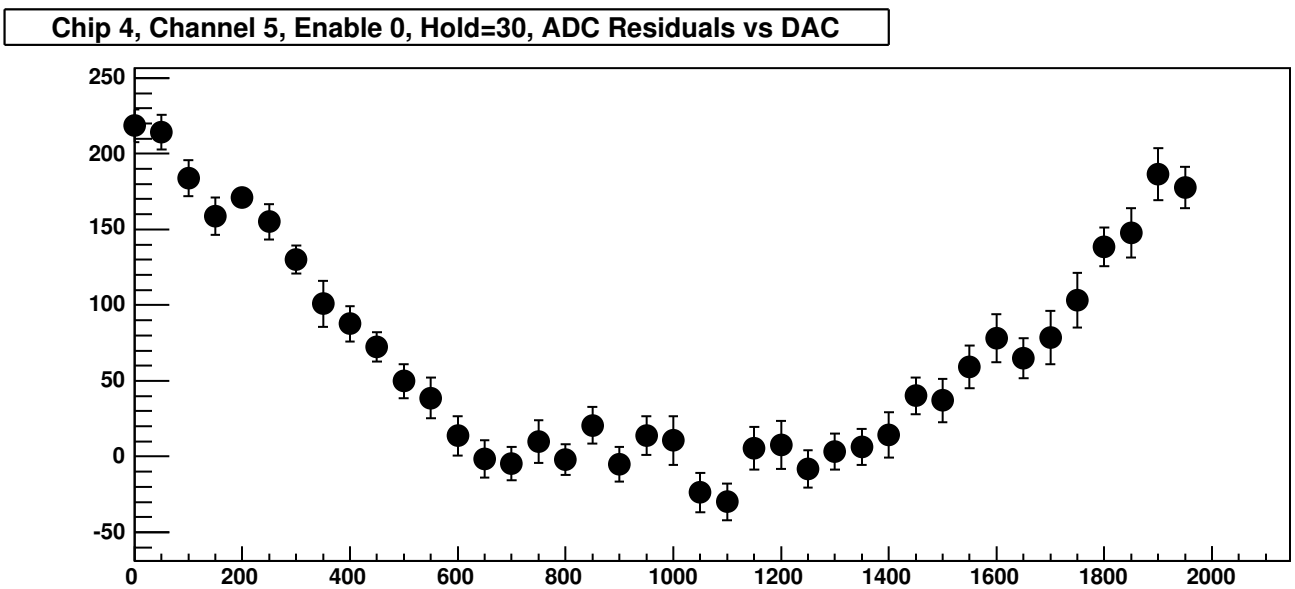

Chip 4, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 13.02 / 11
p0 -872.4 ± 29.03
p1 1.284 ± 0.02668
1600
1400
1200
1000
800
600
400
200
0
-200
0 200 400 600 800 1000 1200 1400 1600 1800 2000

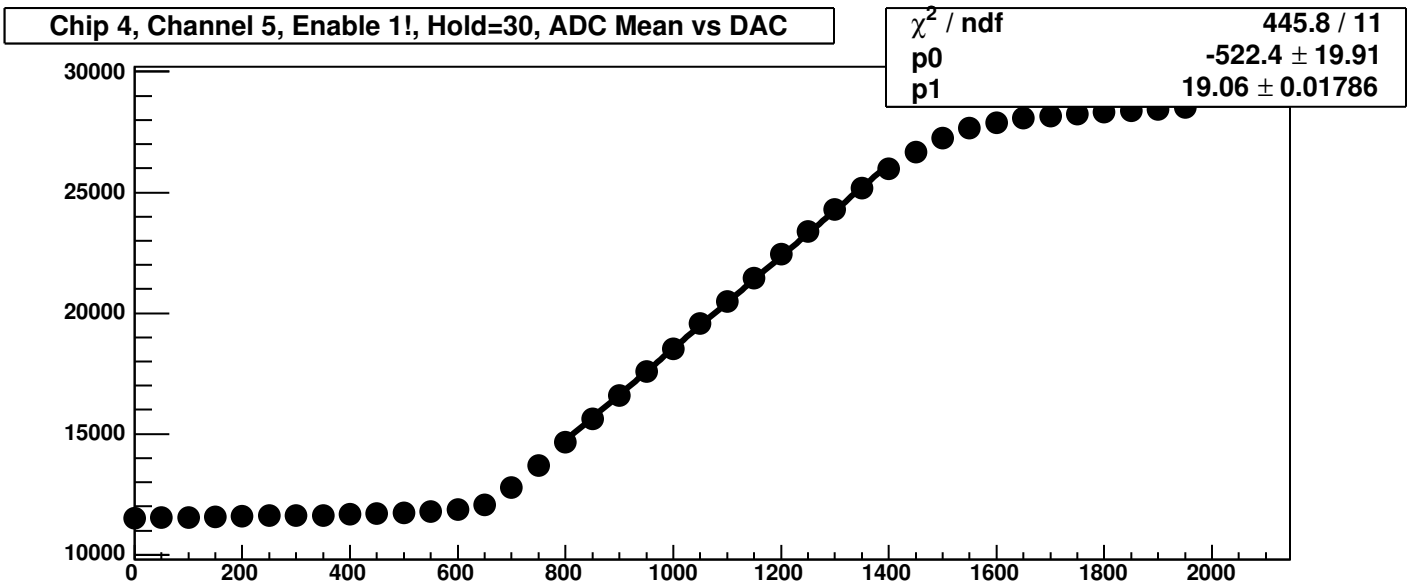

Chip 4, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0 200 400 600 800 1000 1200 1400 1600 1800 2000

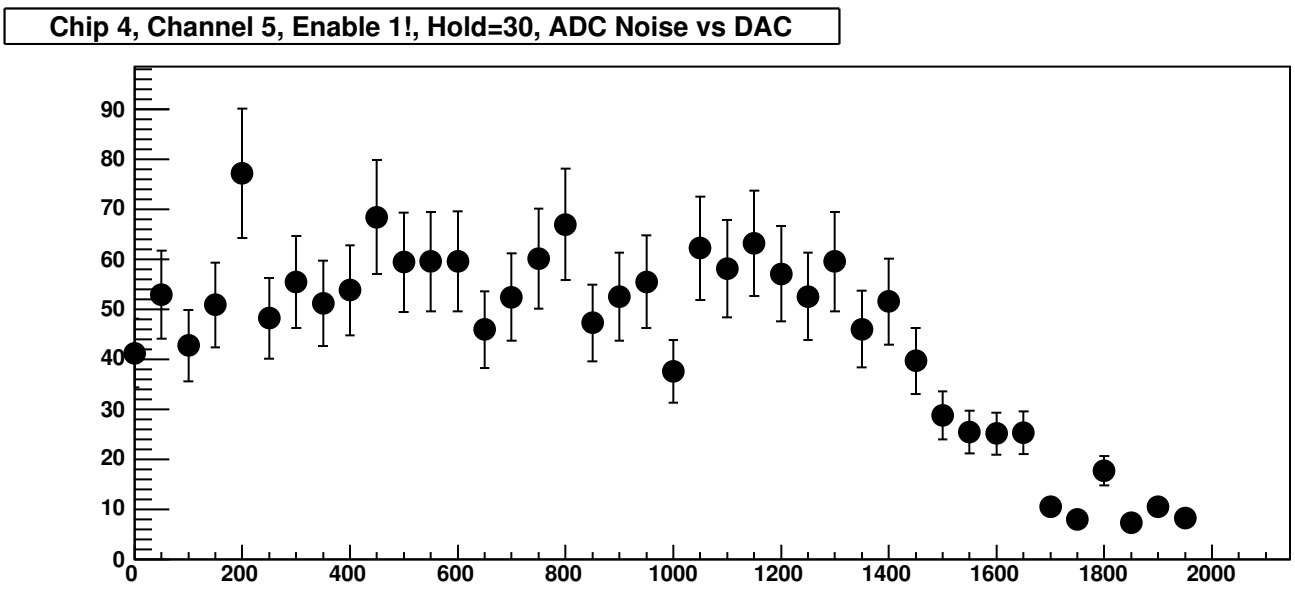

Chip 4, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC
800
600
400
200
0
-200
0 200 400 600 800 1000 1200 1400 1600 1800 2000

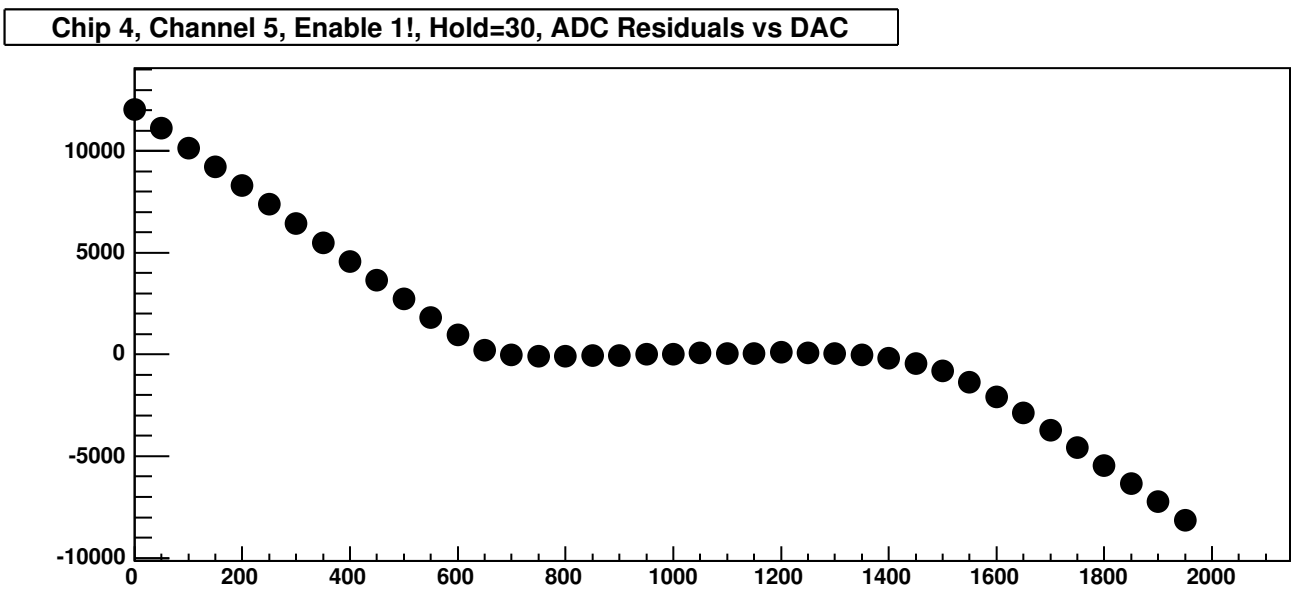

Chip 4, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 16.17 / 11
p0 -940.8 ± 19.81
p1 0.3481 ± 0.01805
-100
-200
-300
-400
-500
-600
-700
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

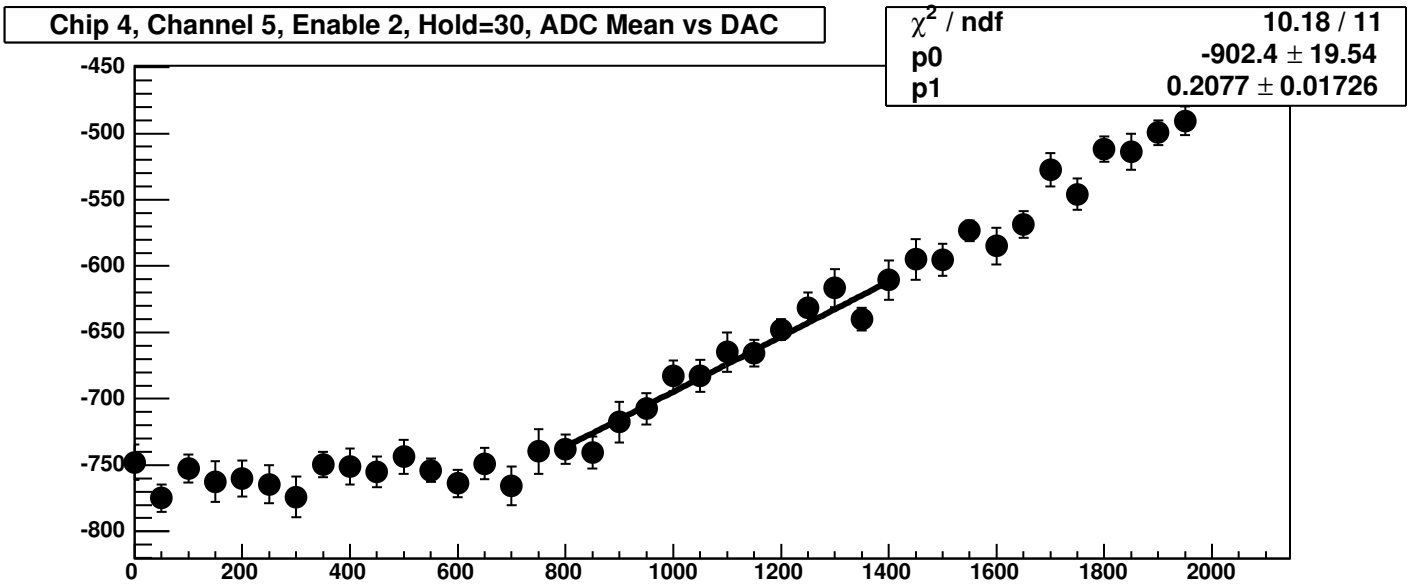

Chip 4, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

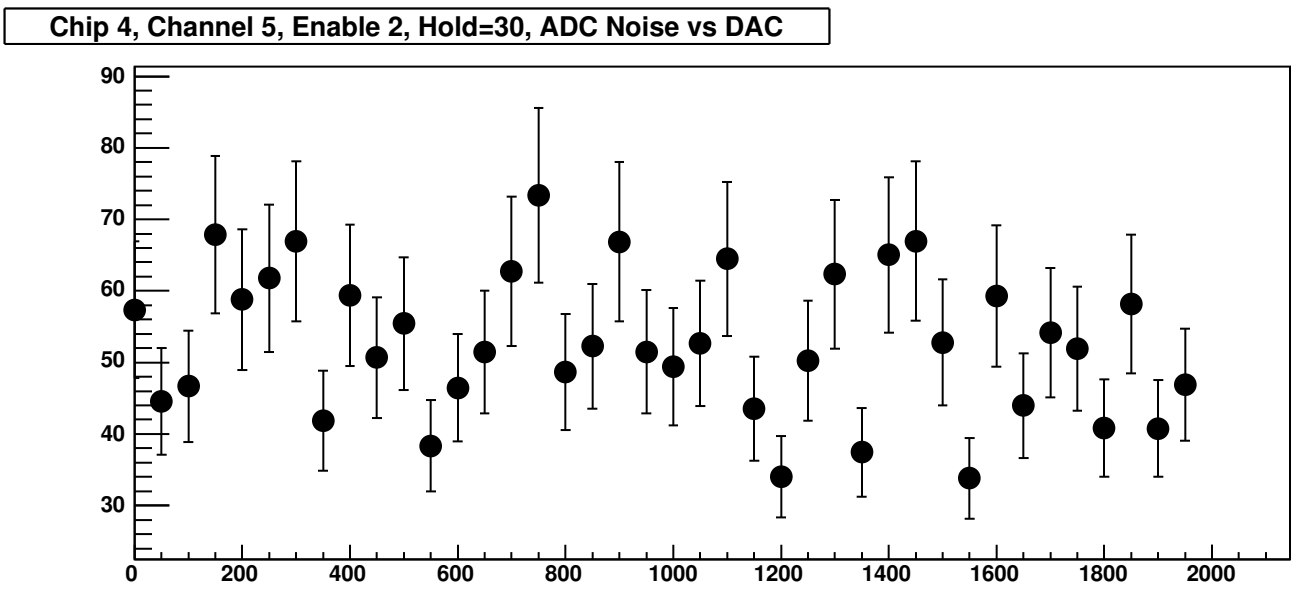

Chip 4, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

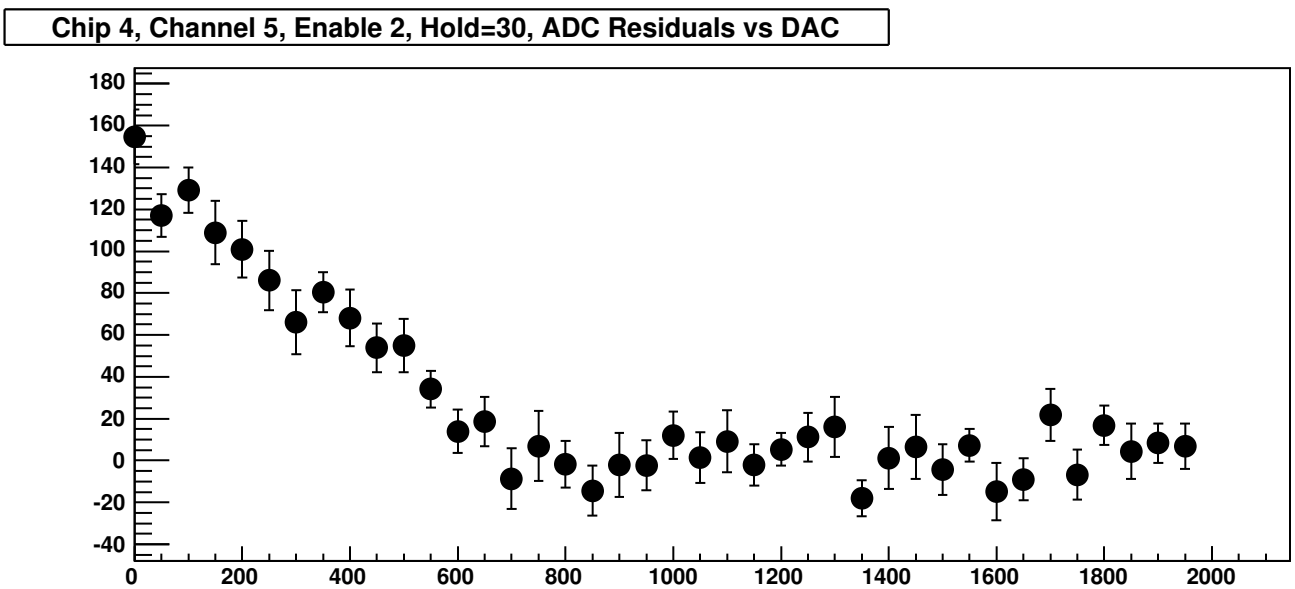

Chip 4, Channel 5, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 445.8 / 11
p0 -522.4 ± 19.91
p1 19.06 ± 0.01786
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

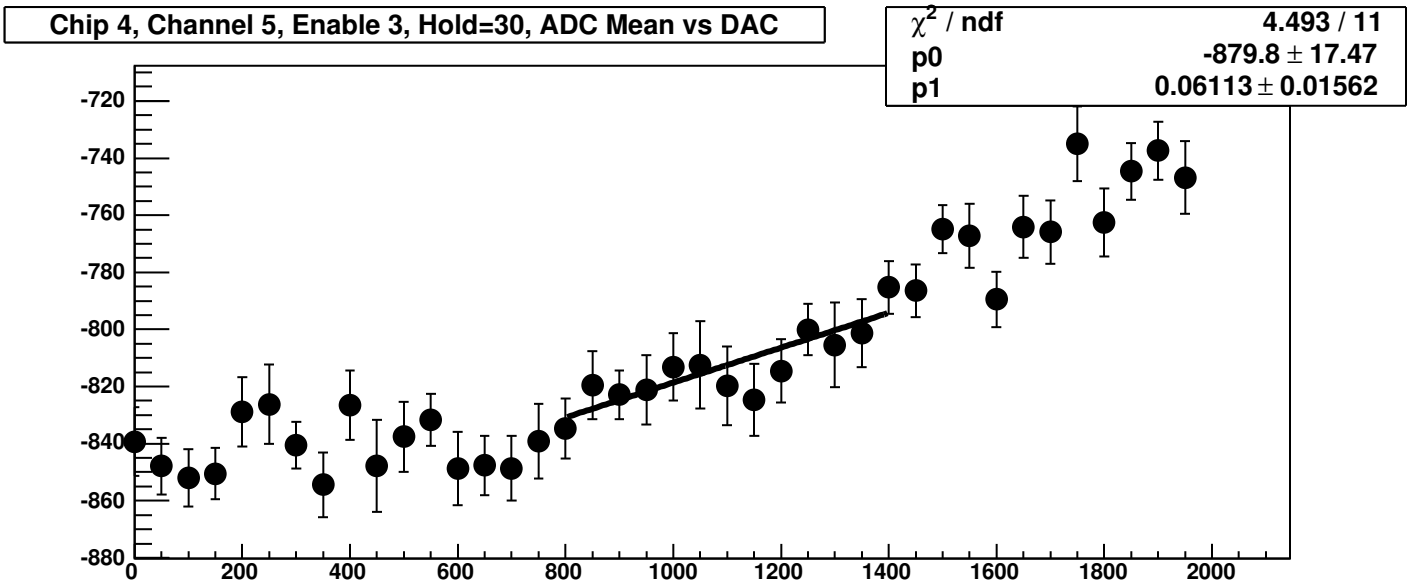

Chip 4, Channel 5, Enable 1!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

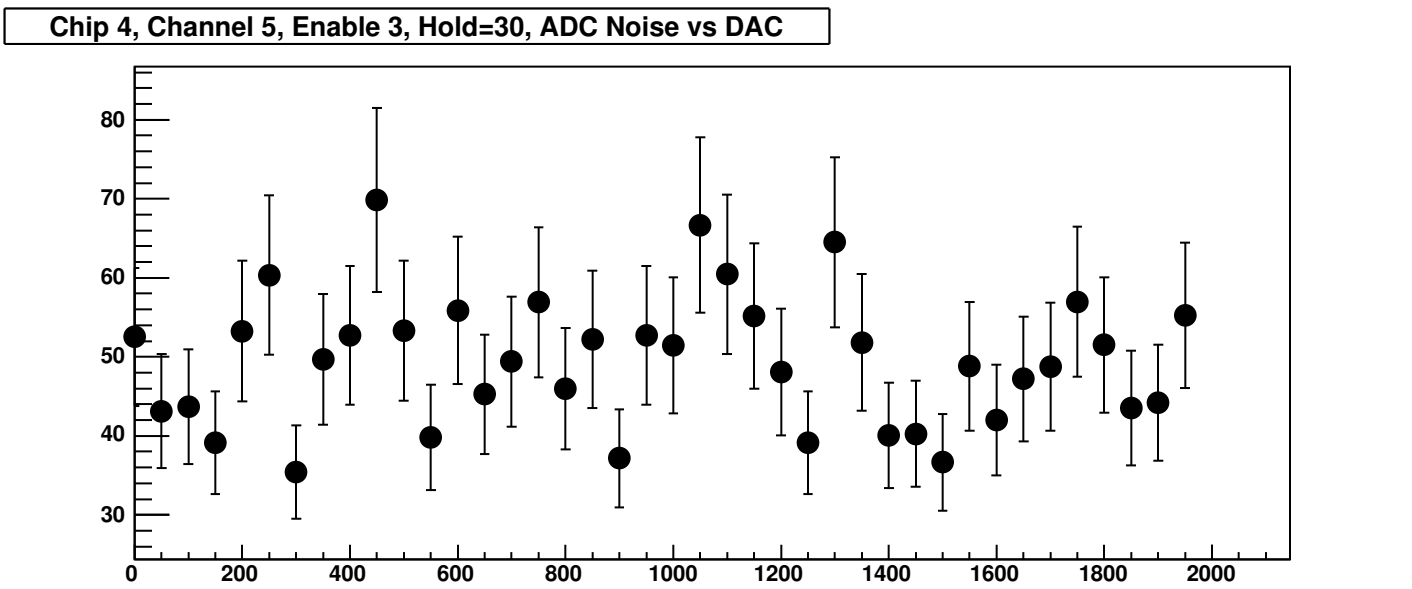

Chip 4, Channel 5, Enable 1!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

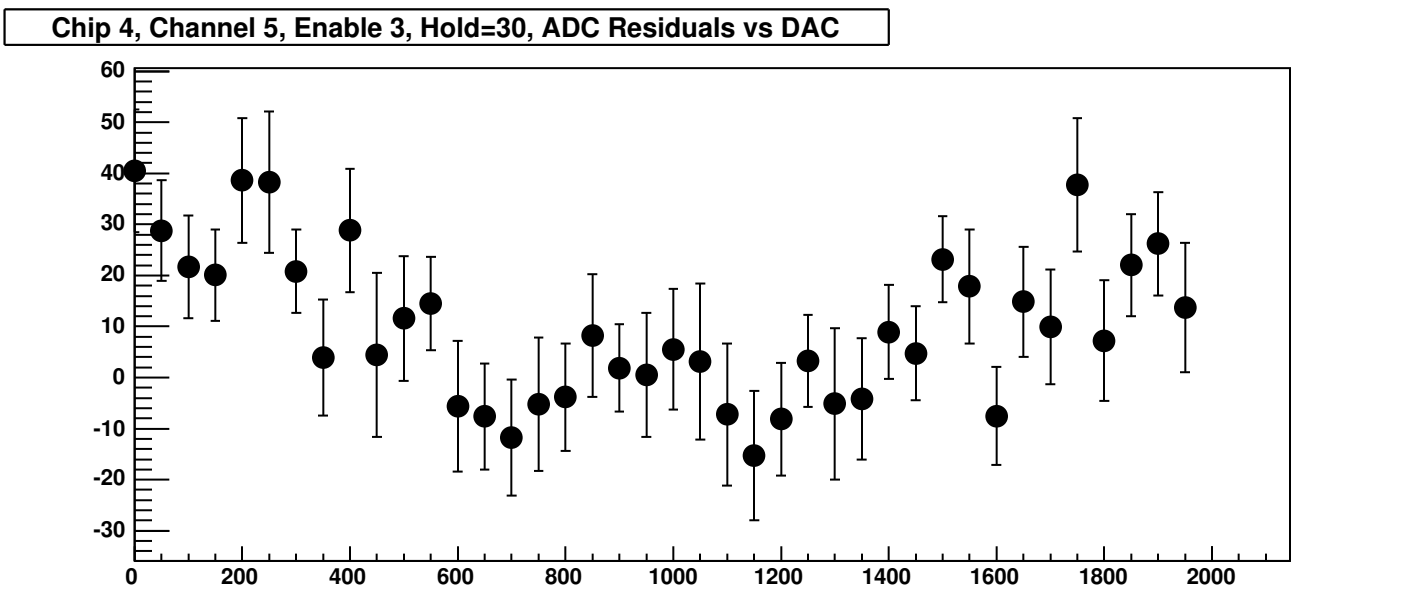

Chip 4, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 10.18 / 11
p0 -902.4 ± 19.54
p1 0.2077 ± 0.01726
-450
-500
-550
-600
-650
-700
-750
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

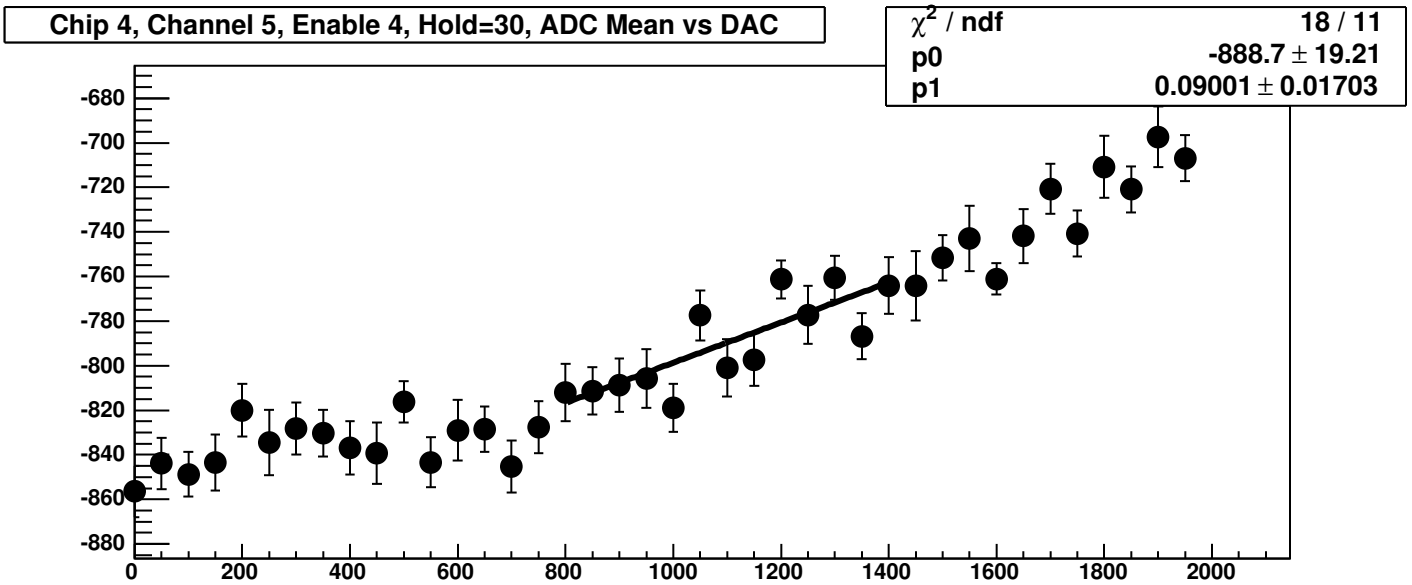

Chip 4, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

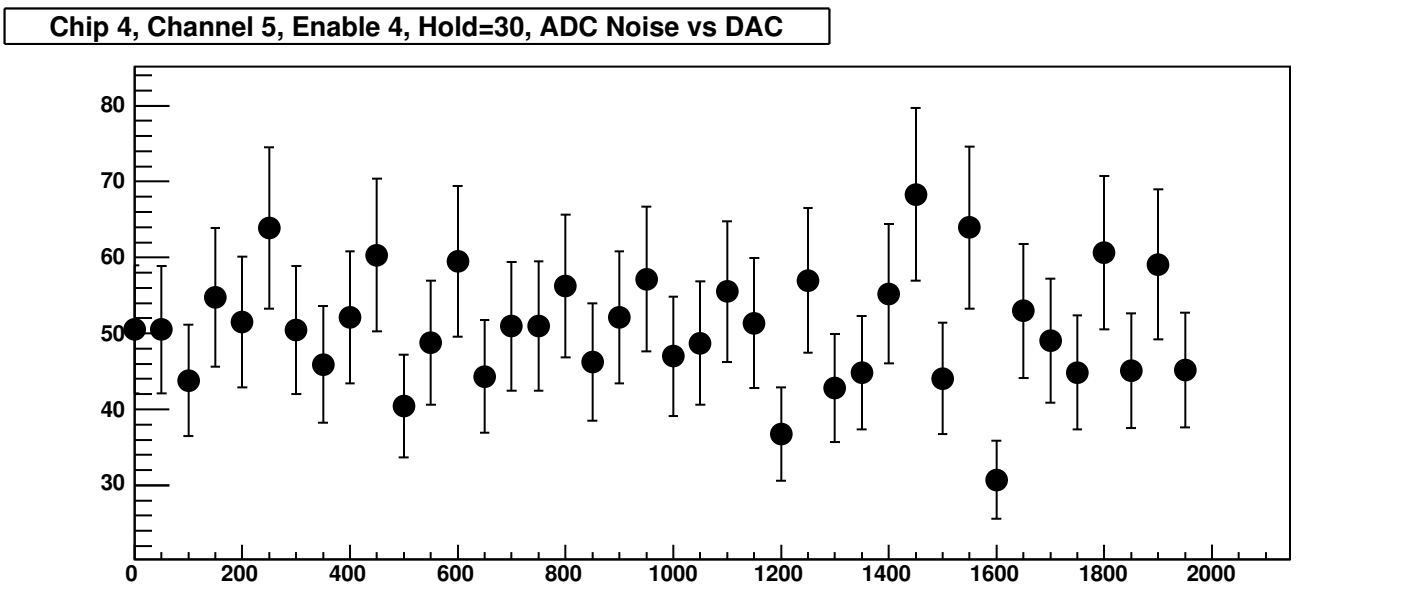

Chip 4, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
180
160
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

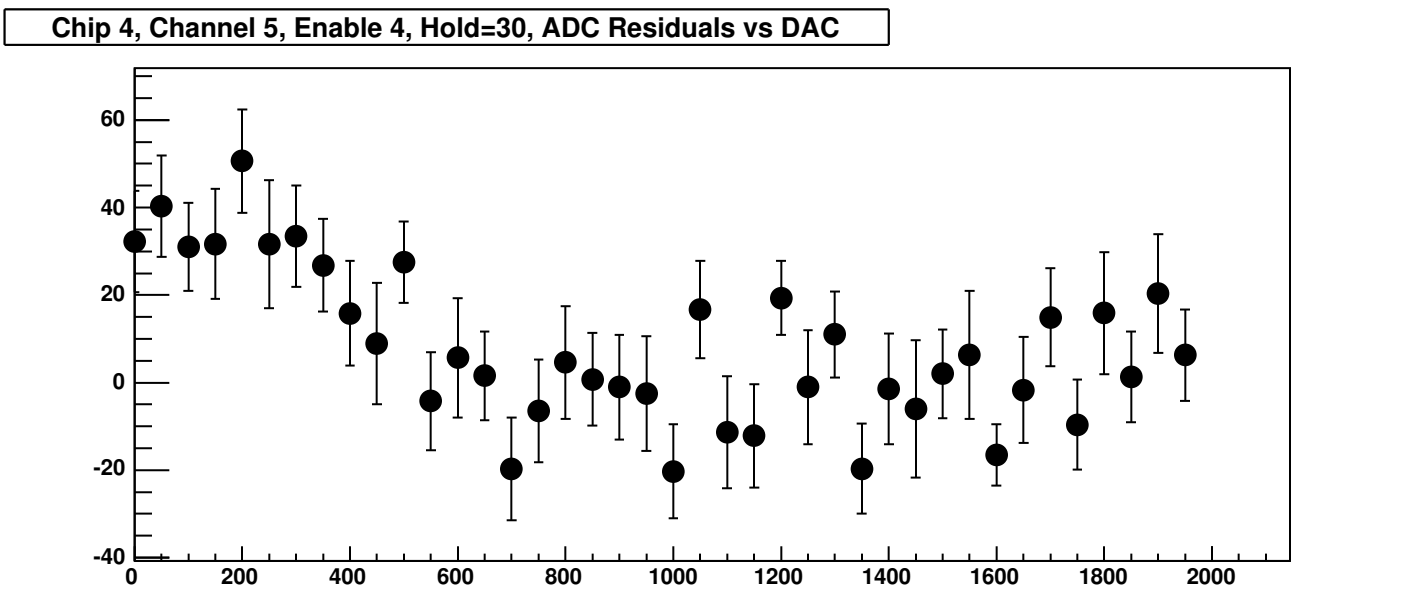

Chip 4, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4.493 / 11
p0 -879.8 ± 17.47
p1 0.06113 ± 0.01562
-720
-740
-760
-780
-800
-820
-840
-860
-880
0
200
400
600
800
1000
1200
1400
1600
1800
2000

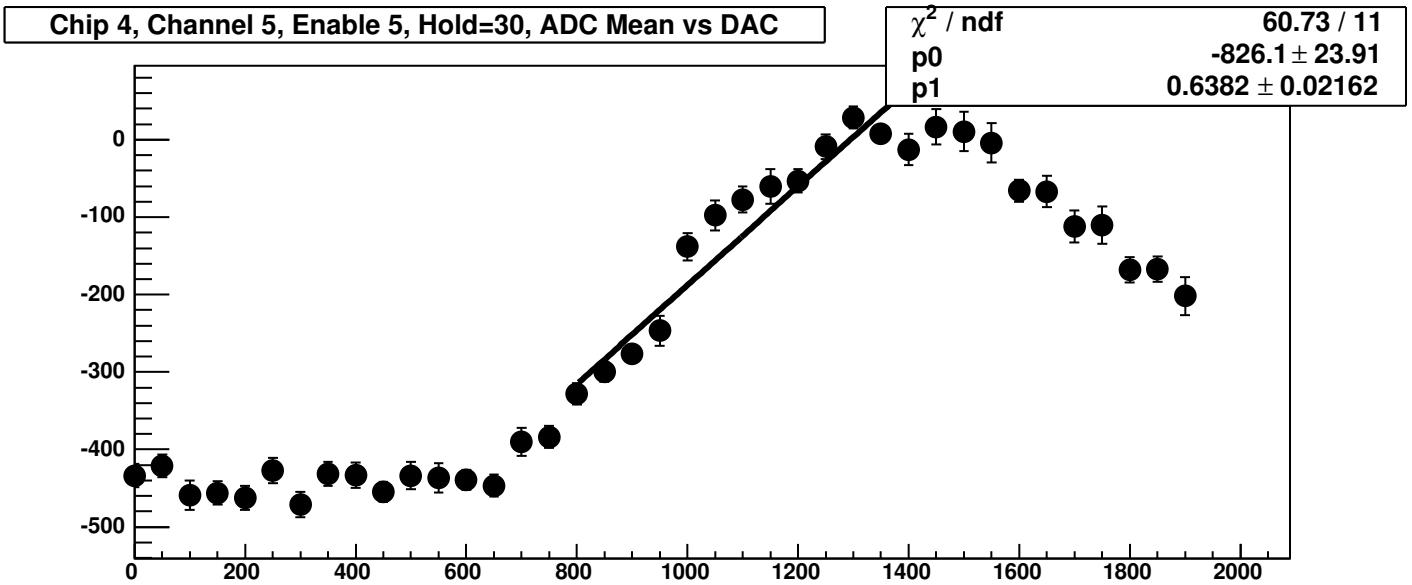

Chip 4, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

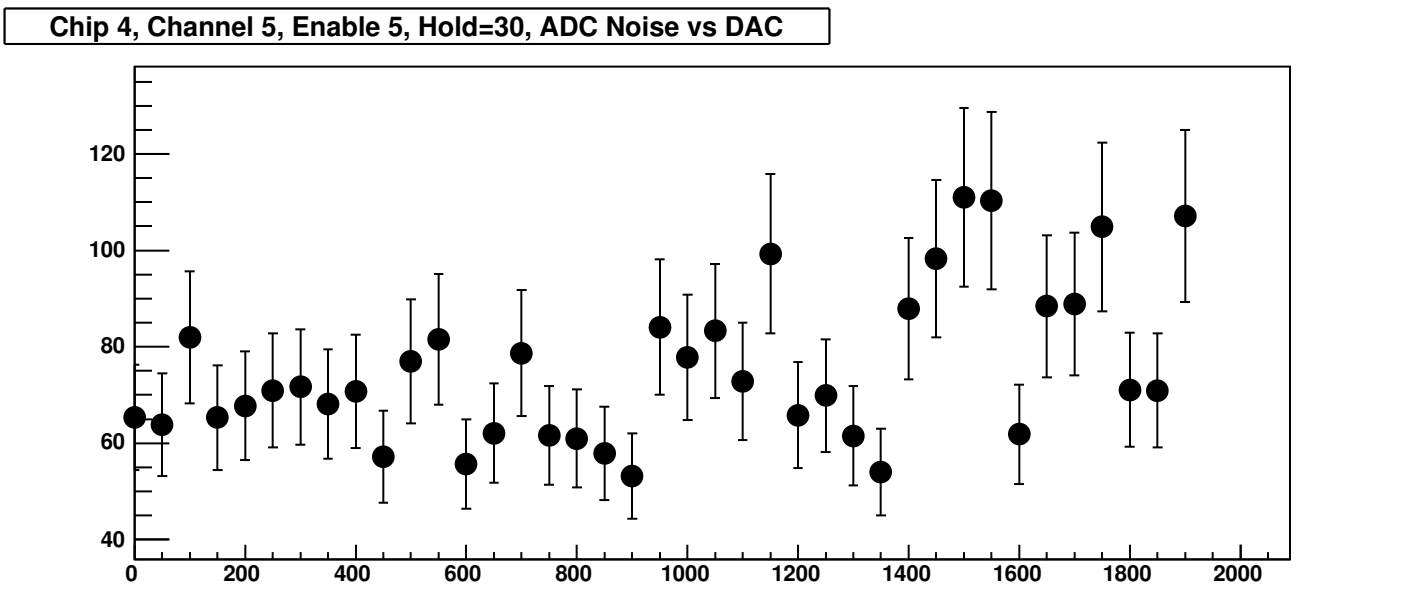

Chip 4, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC
60
50
40
30
20
10
0
-10
-20
-30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

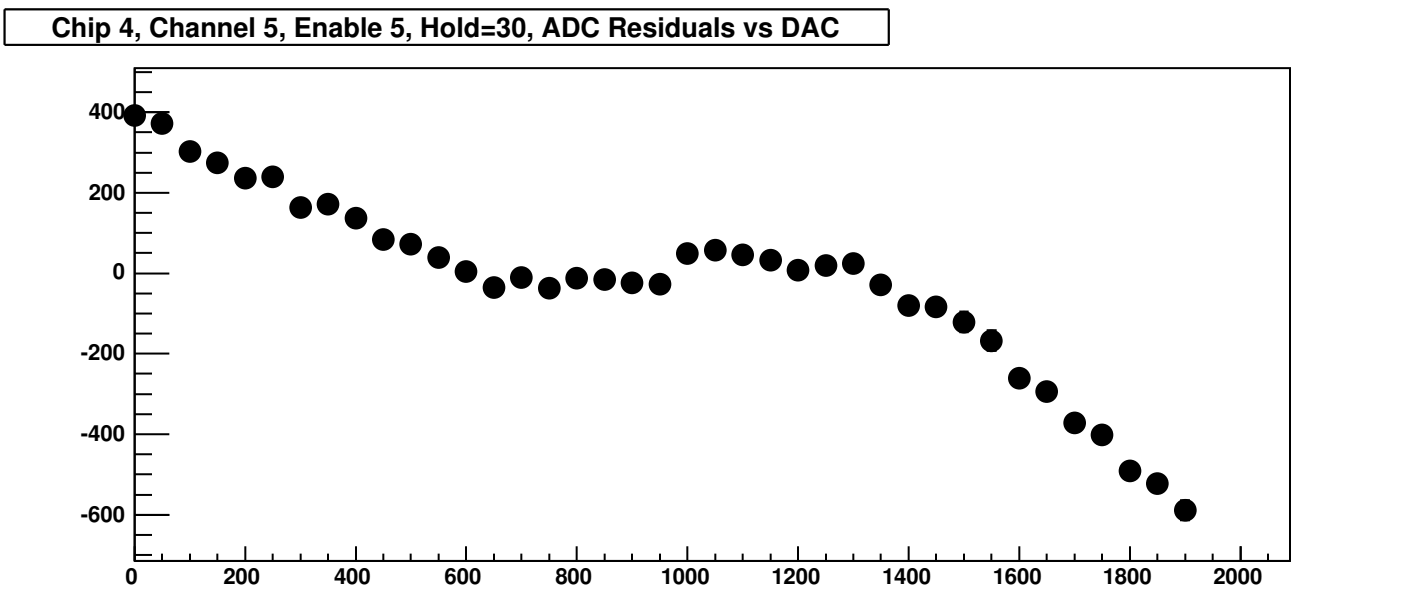

Chip 4, Channel 5, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 18 / 11
p0 -888.7 ± 19.21
p1 0.09001 ± 0.01703
-680
-700
-720
-740
-760
-780
-800
-820
-840
-860
-880
0
200
400
600
800
1000
1200
1400
1600
1800
2000

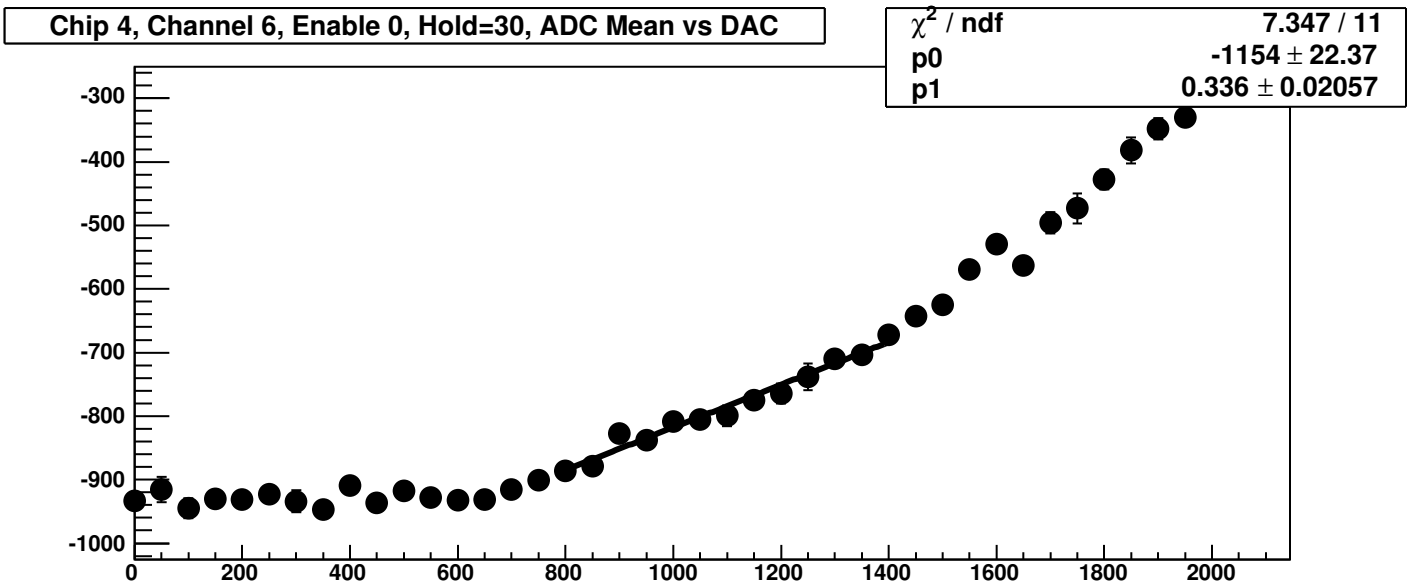

Chip 4, Channel 5, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

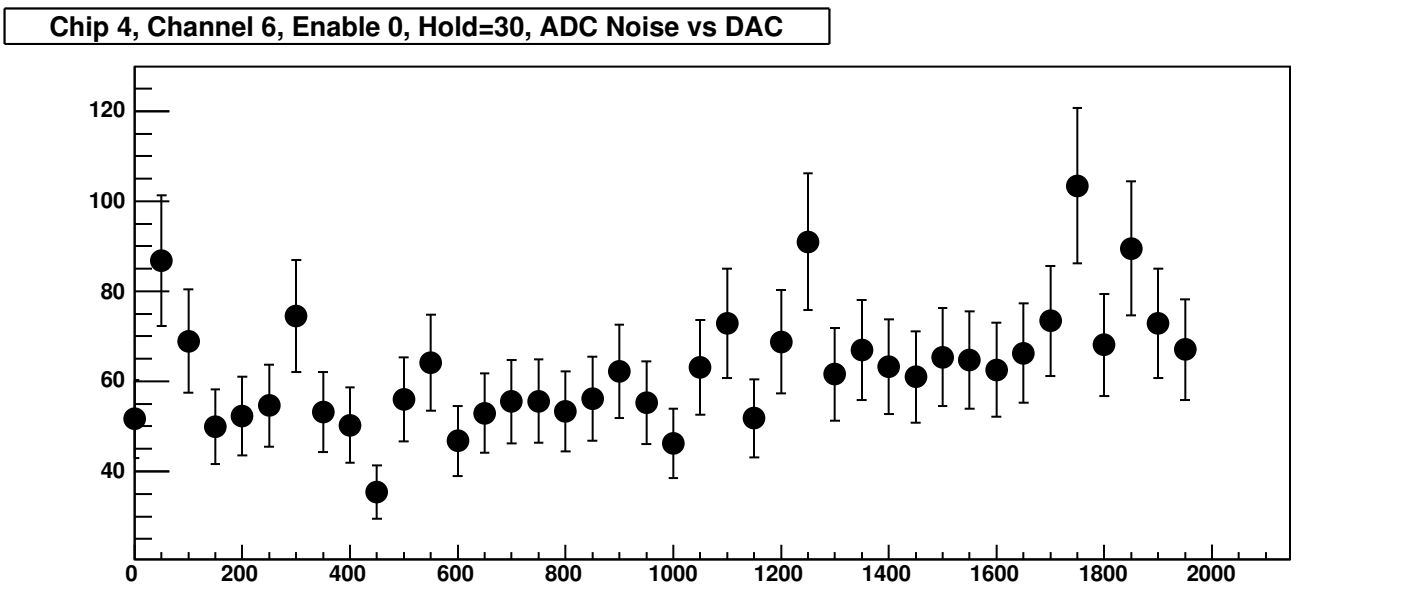

Chip 4, Channel 5, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

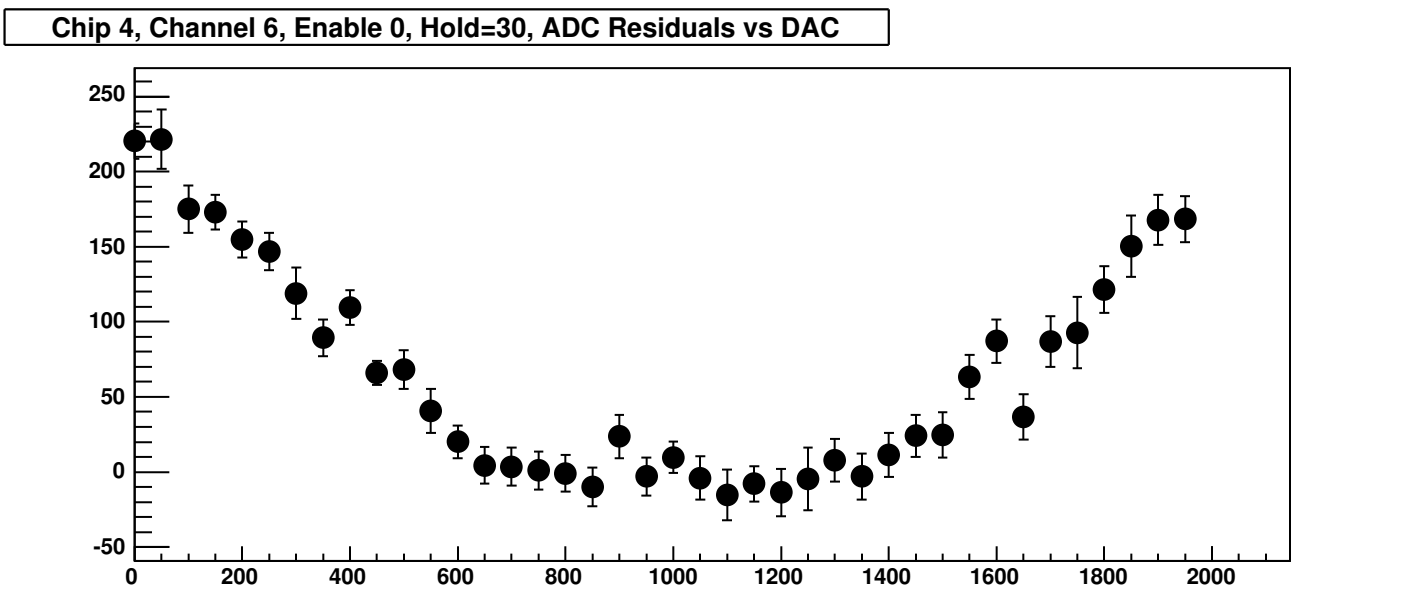

Chip 4, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 60.73 / 11
p0 -826.1 ± 23.91
p1 0.6382 ± 0.02162
0
-100
-200
-300
-400
-500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

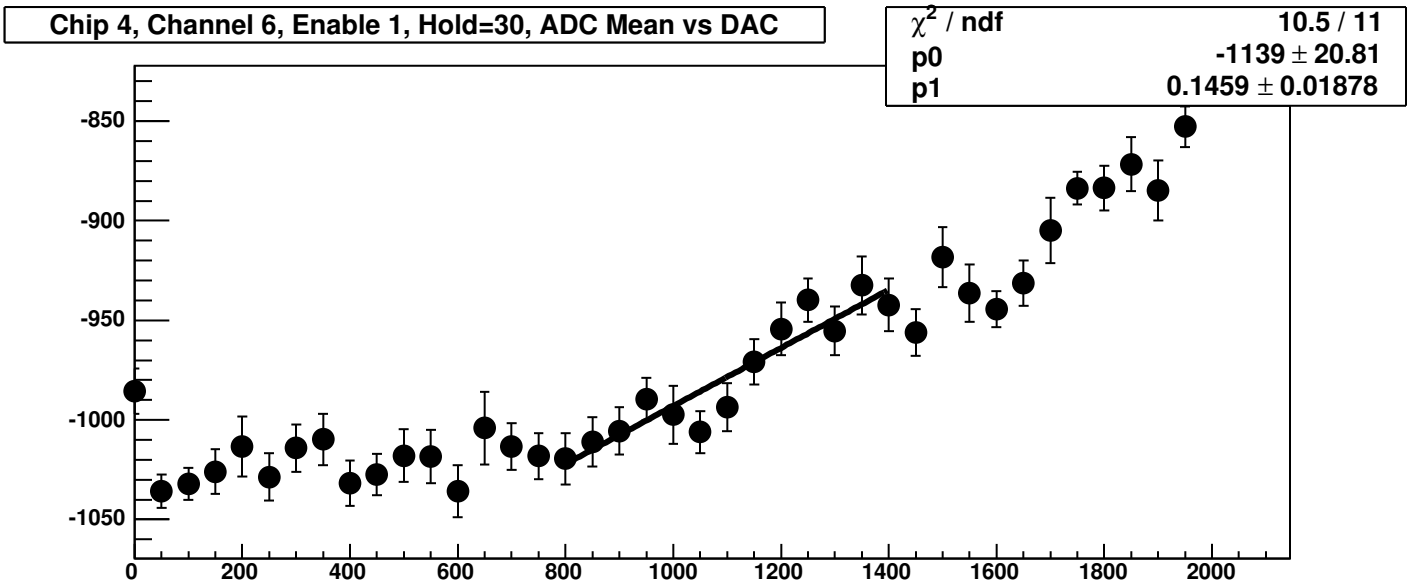

Chip 4, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

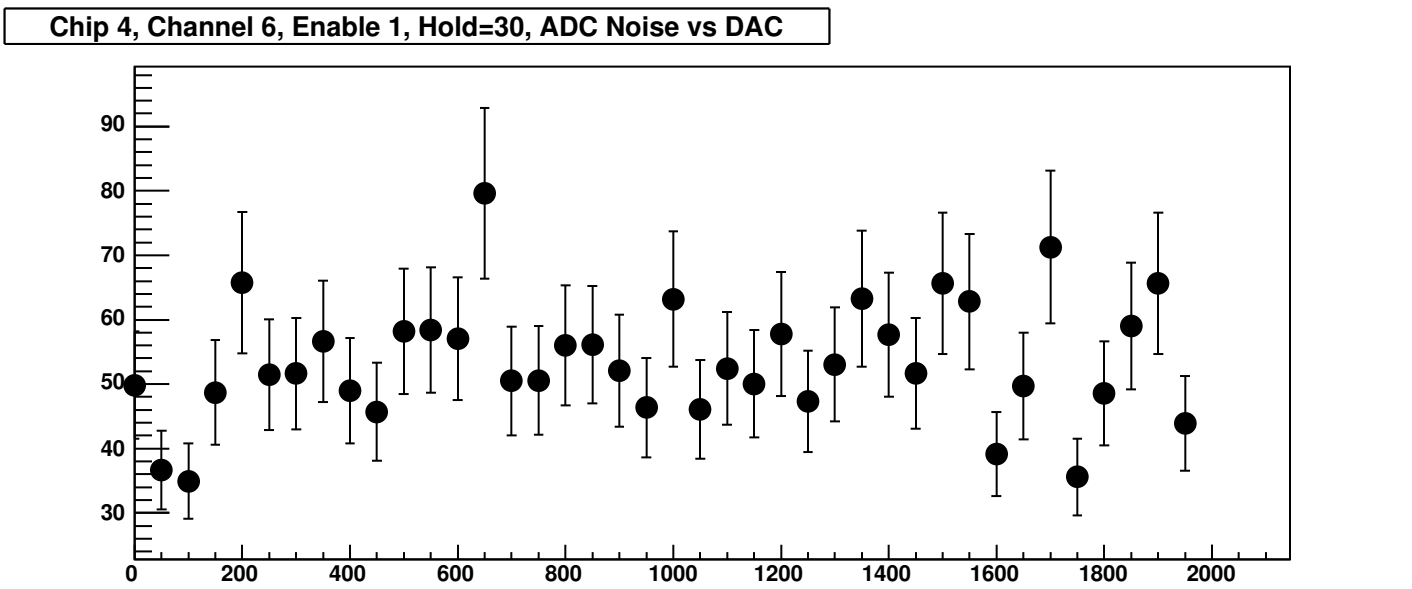

Chip 4, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

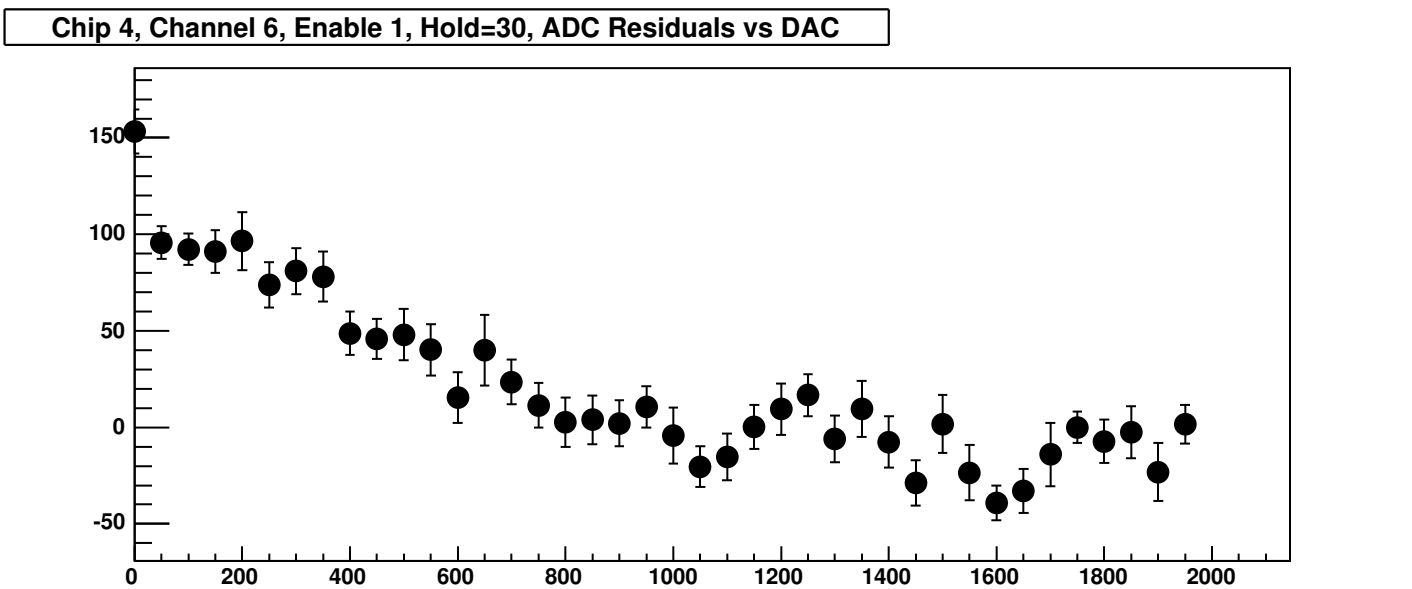

Chip 4, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 7.347 / 11
p0 -1154 ± 22.37
p1 0.336 ± 0.02057
-300
-400
-500
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

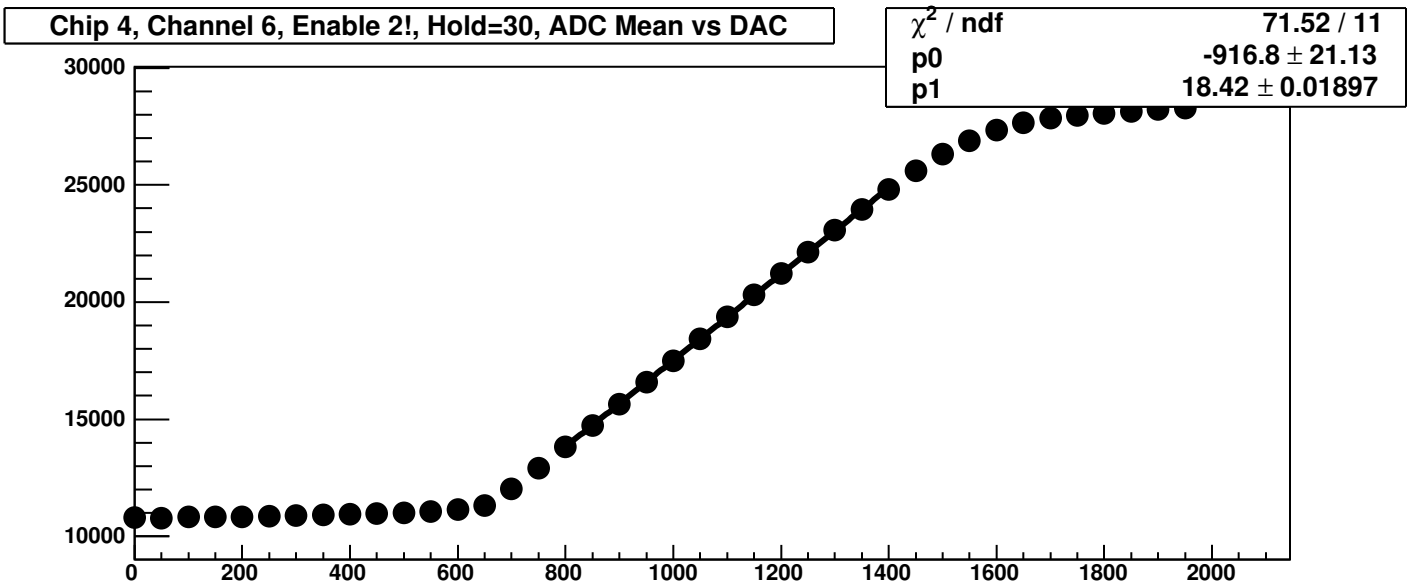

Chip 4, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

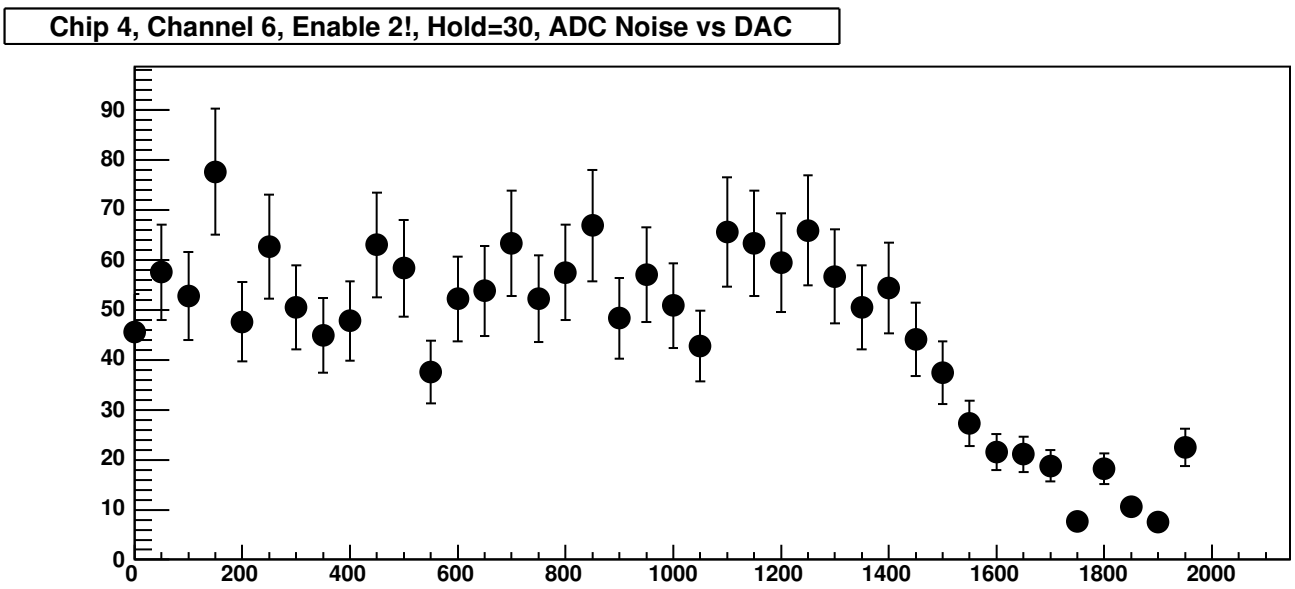

Chip 4, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

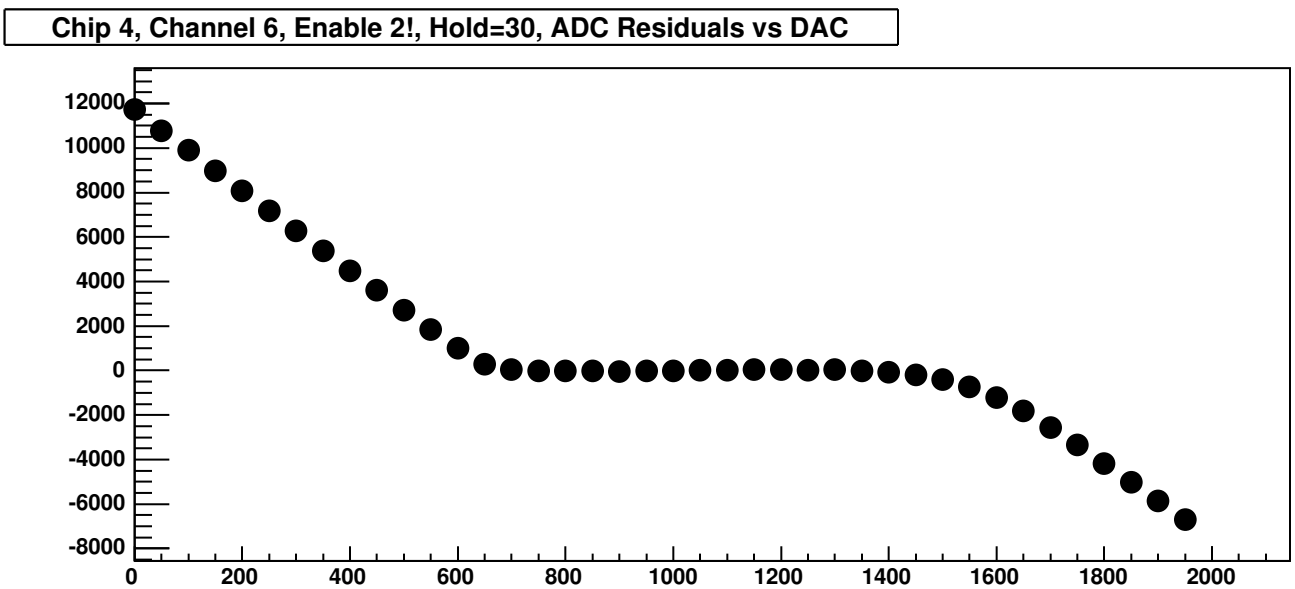

Chip 4, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 10.5 / 11
p0 -1139 ± 20.81
p1 0.1459 ± 0.01878
-850
-900
-950
-1000
-1050
0
200
400
600
800
1000
1200
1400
1600
1800
2000

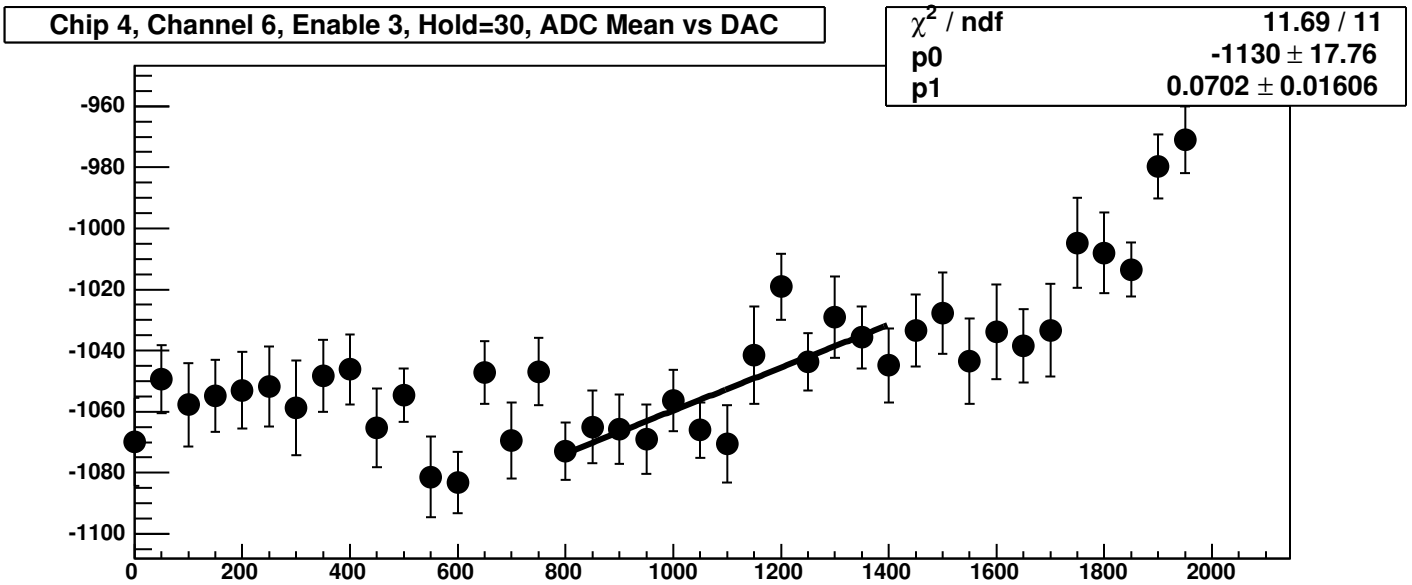

Chip 4, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

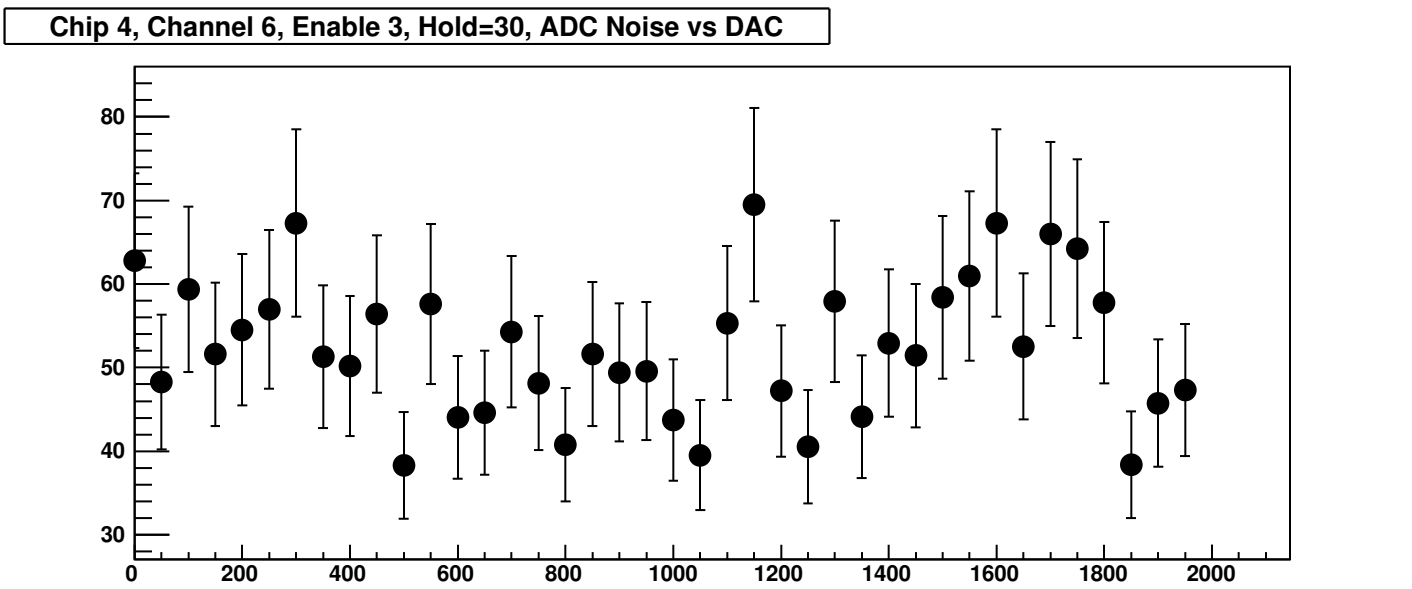

Chip 4, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

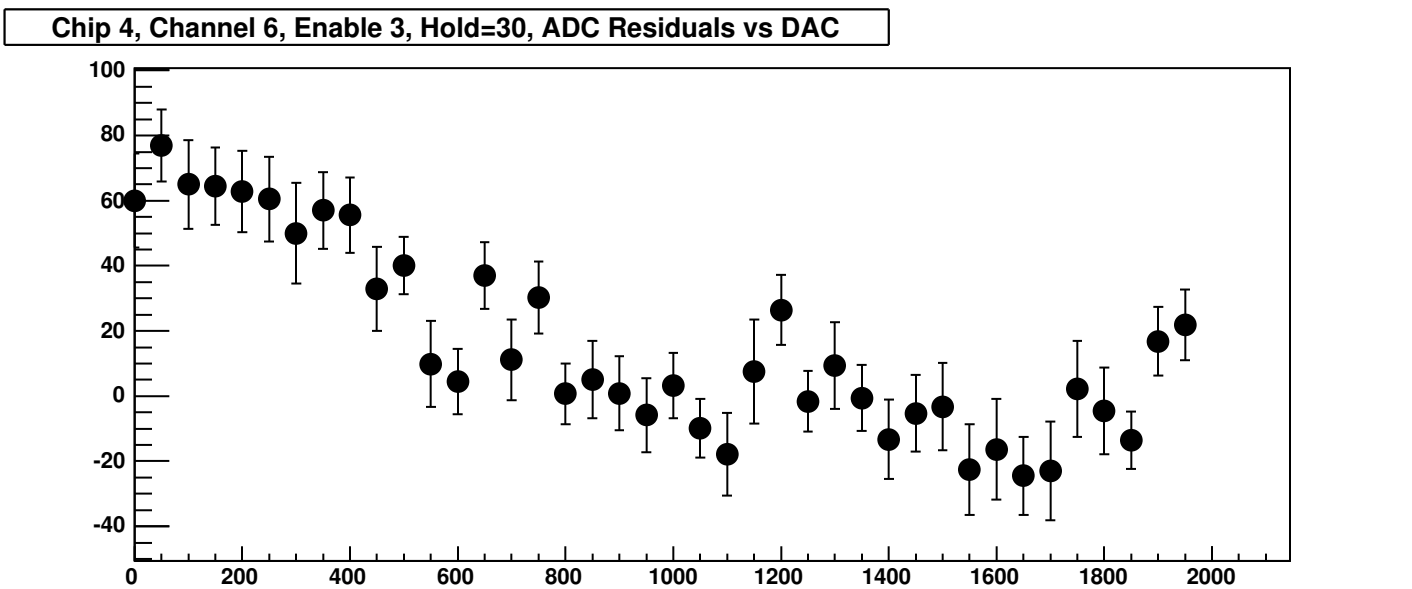

Chip 4, Channel 6, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 71.52 / 11
p0 -916.8 ± 21.13
p1 18.42 ± 0.01897
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

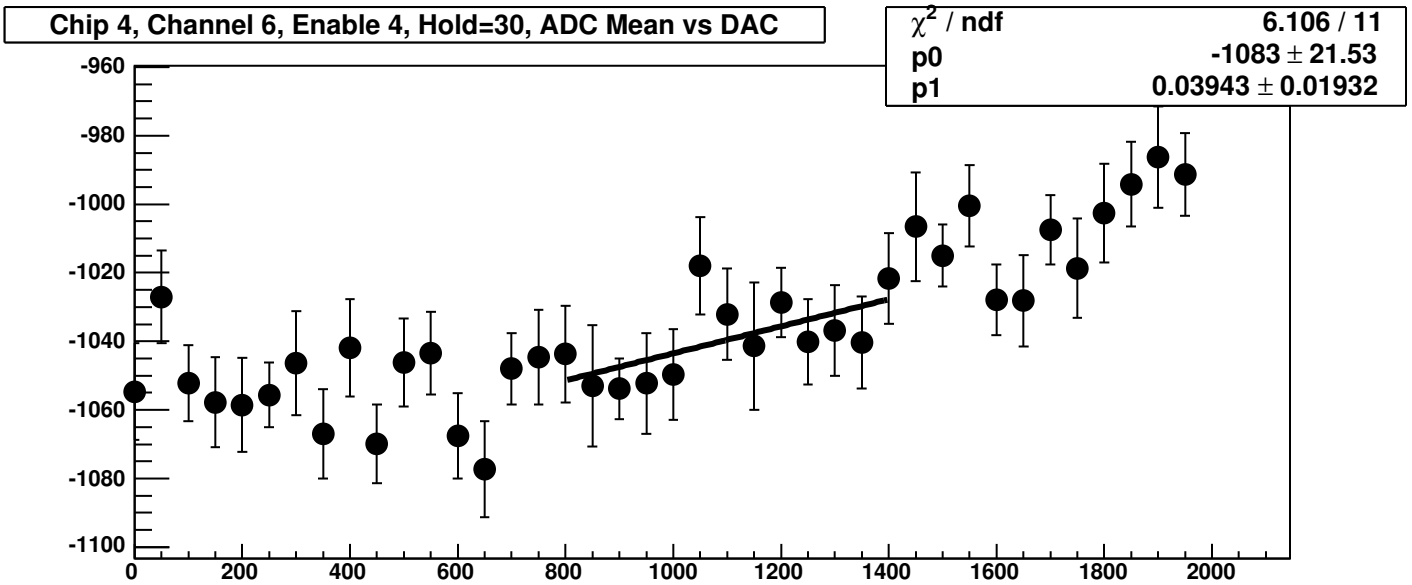

Chip 4, Channel 6, Enable 2!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

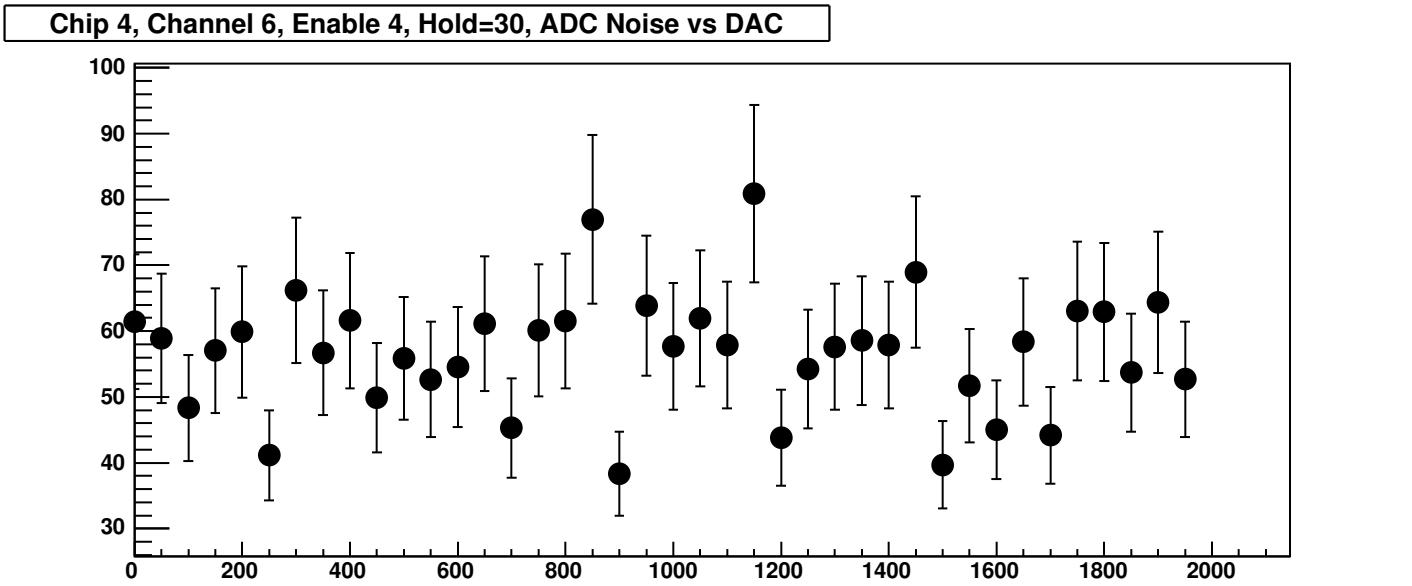

Chip 4, Channel 6, Enable 2!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

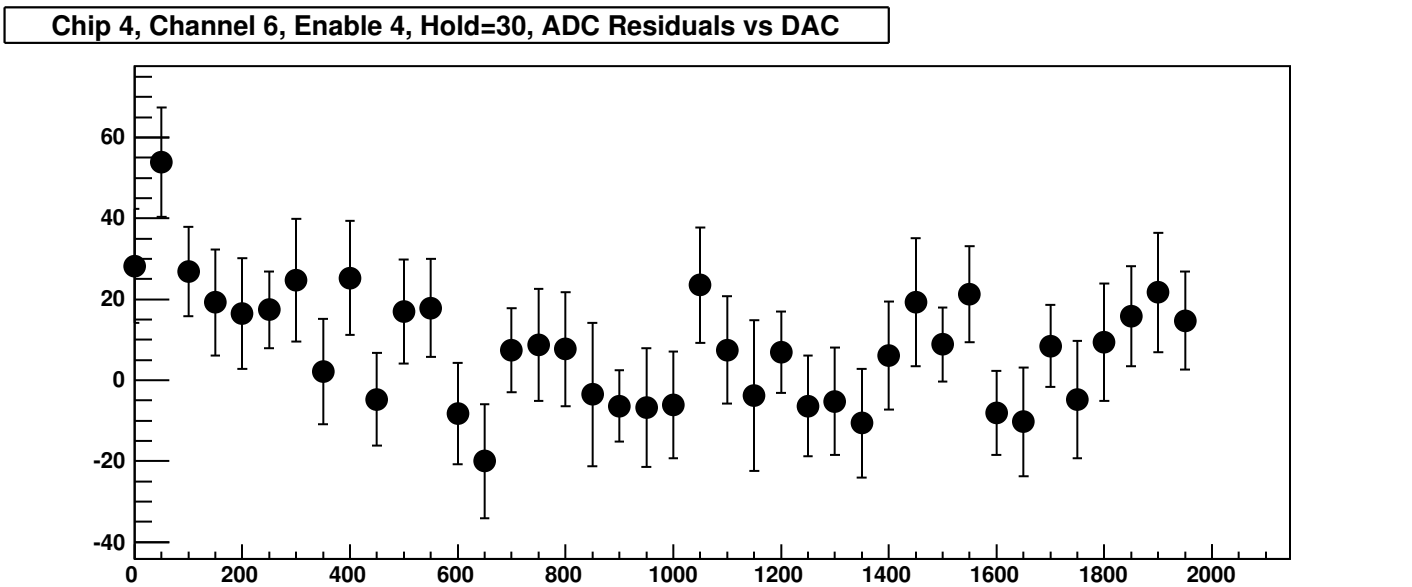

Chip 4, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 11.69 / 11
p0 -1130 ± 17.76
p1 0.0702 ± 0.01606
-960
-980
-1000
-1020
-1040
-1060
-1080
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

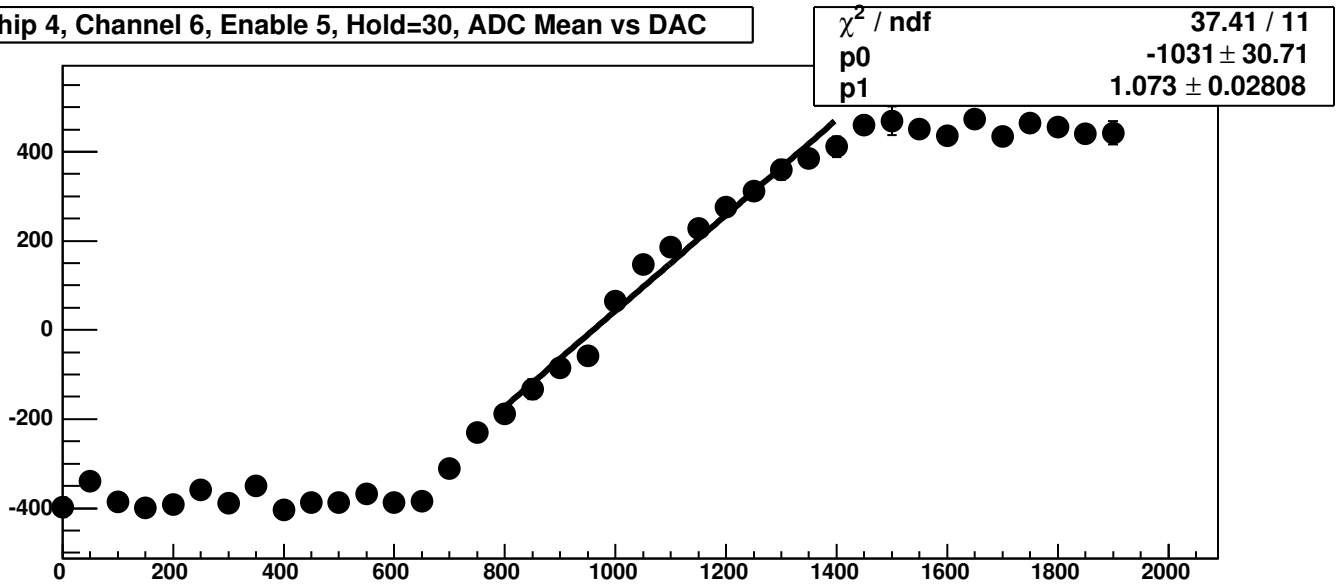

Chip 4, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6.106 / 11
p0 -1083 ± 21.53
p1 0.03943 ± 0.01932
-960
-980
-1000
-1020
-1040
-1060
-1080
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

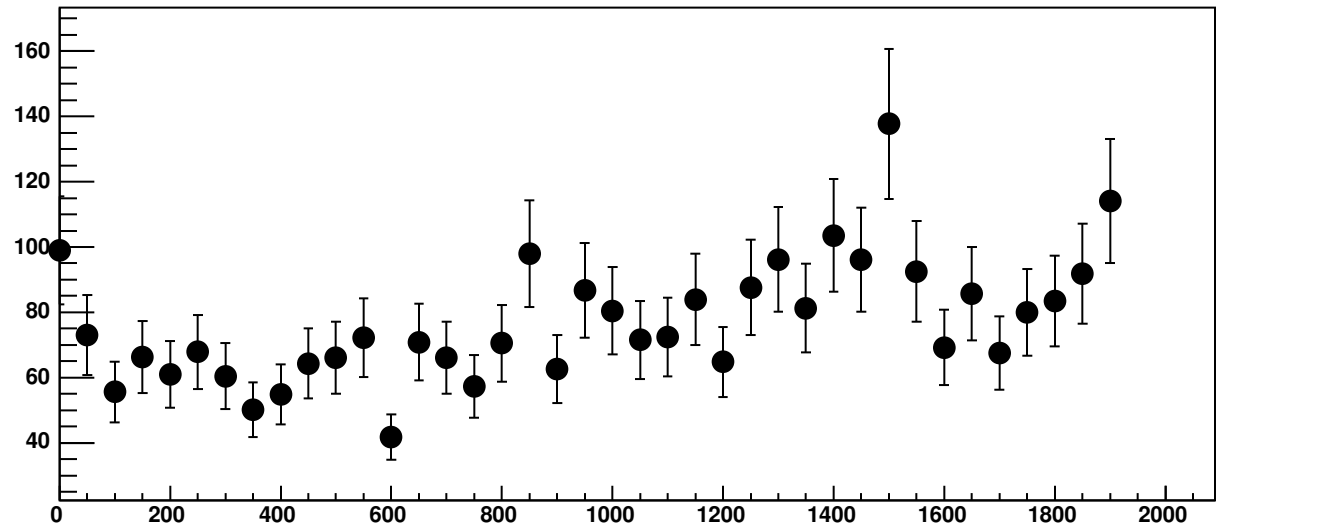

Chip 4, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

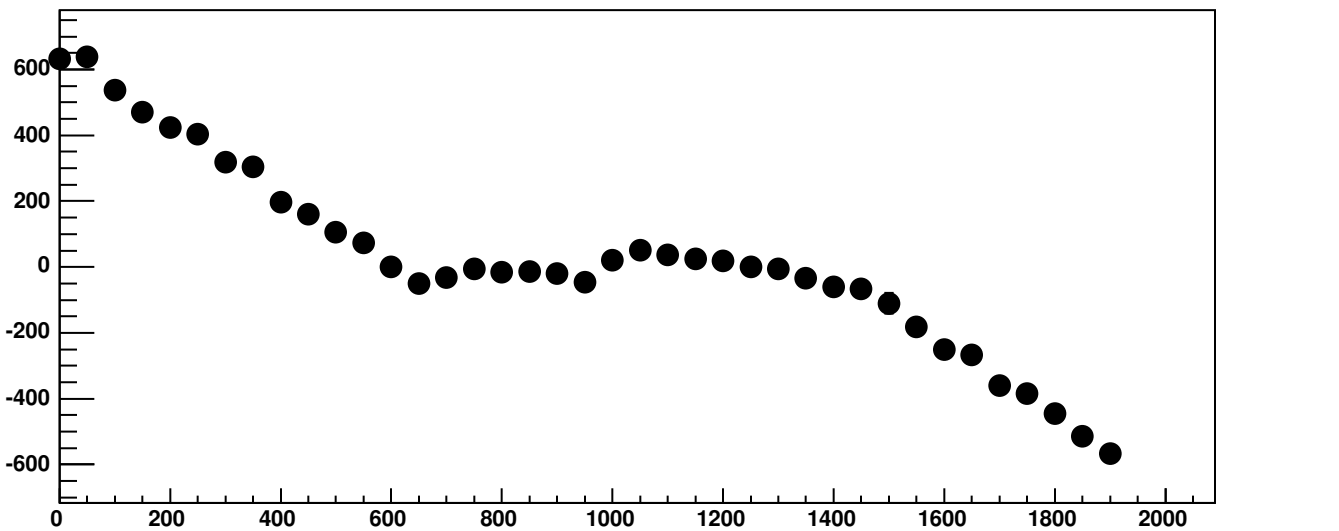
Chip 4, Channel 6, Enable 5, Hold=30, ADC Mean vs DAC

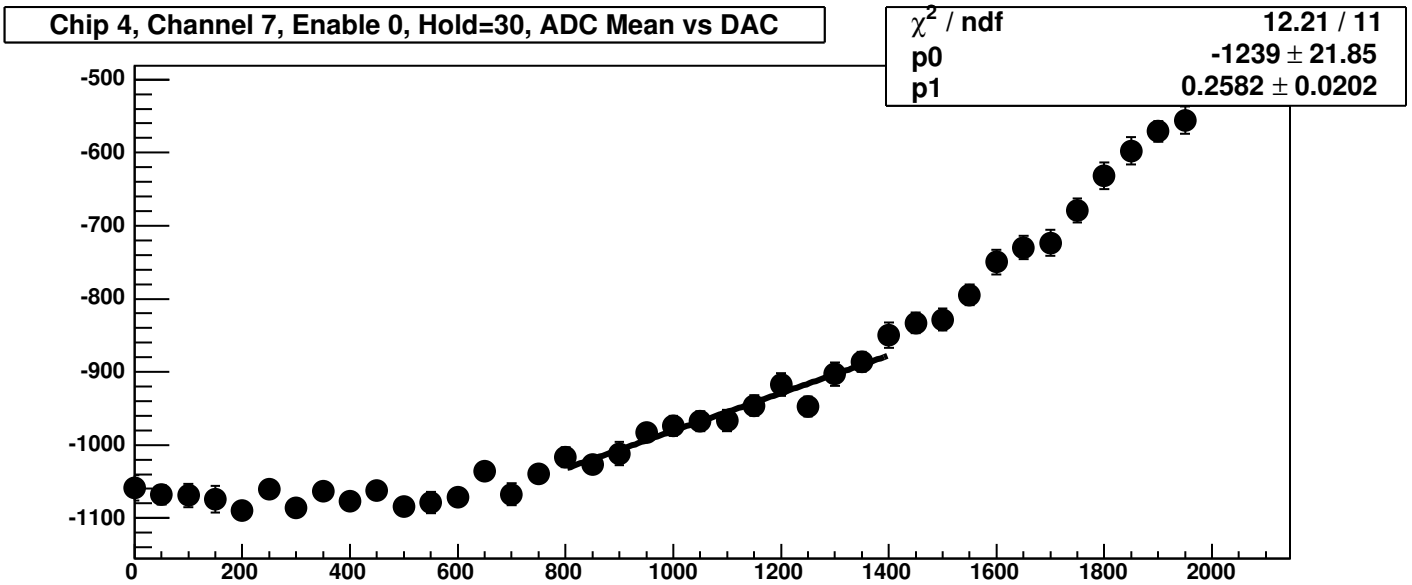


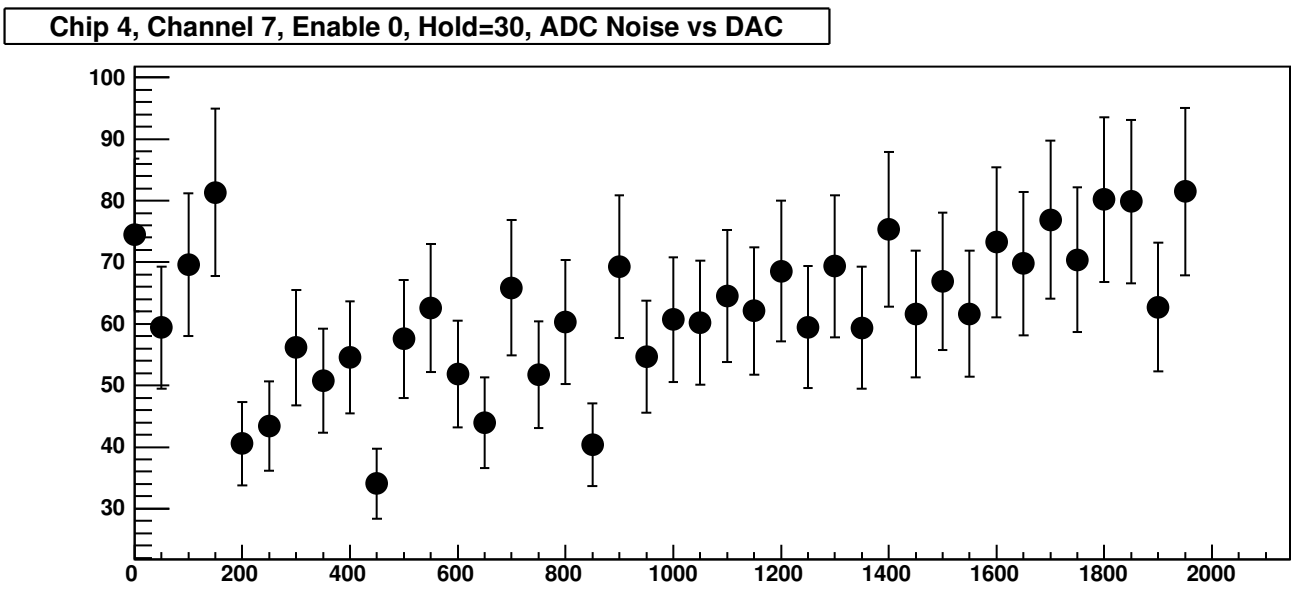
Chip 4, Channel 6, Enable 5, Hold=30, ADC Noise vs DAC

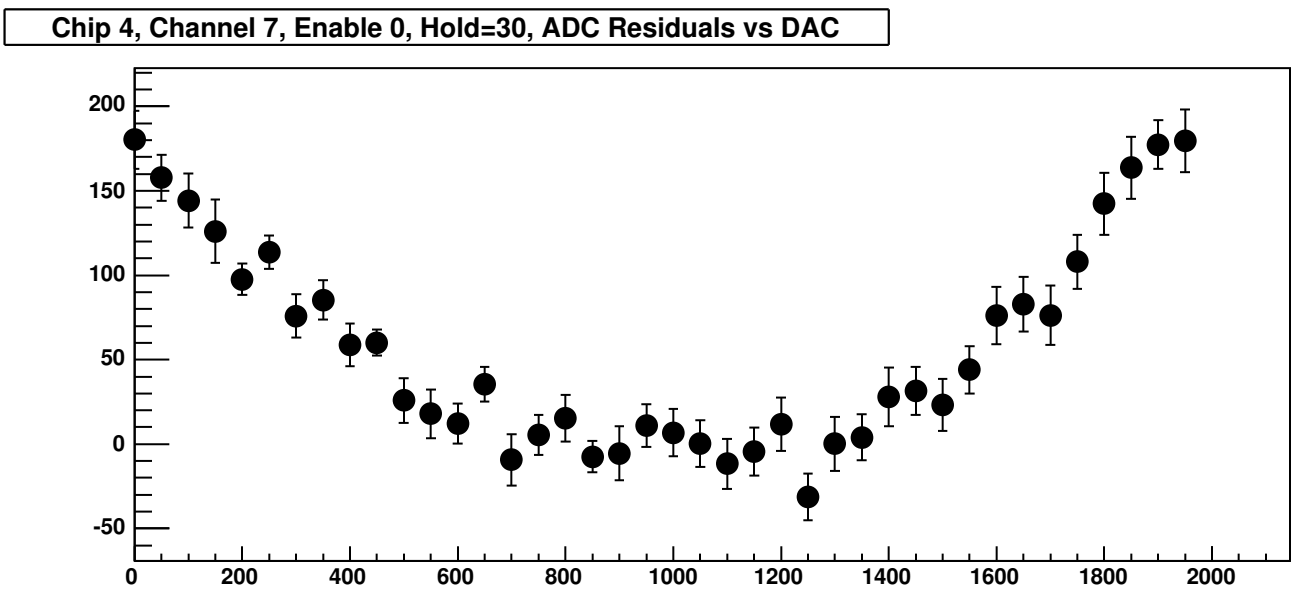


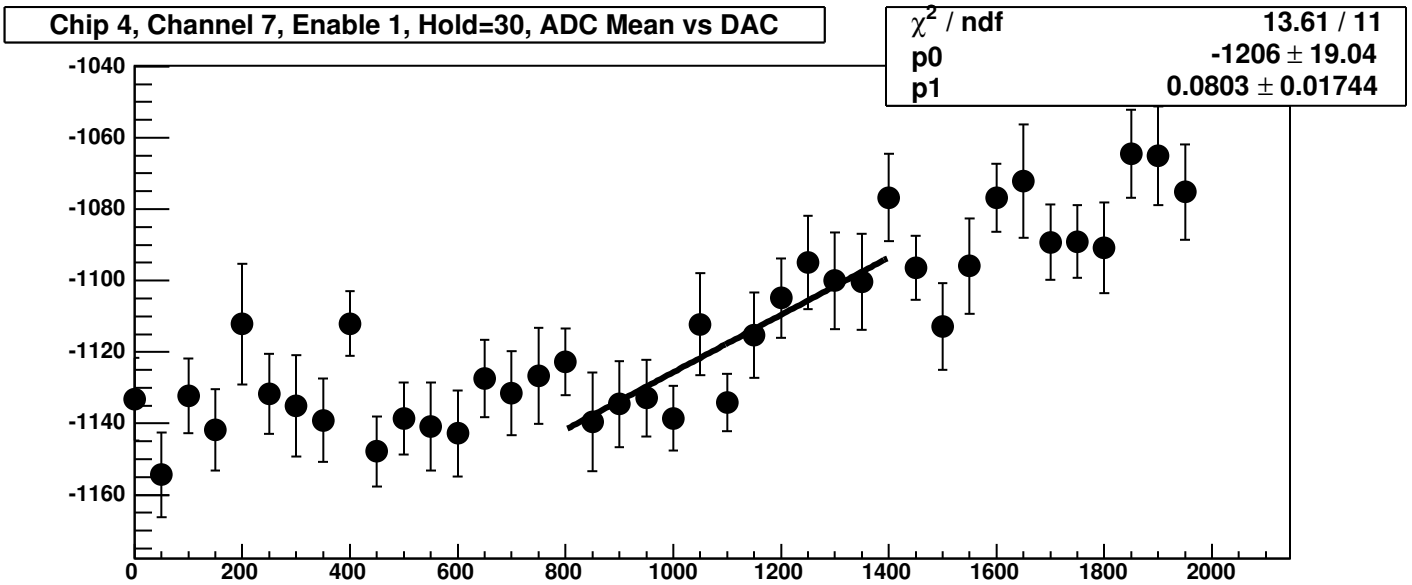
Chip 4, Channel 6, Enable 5, Hold=30, ADC Residuals vs DAC

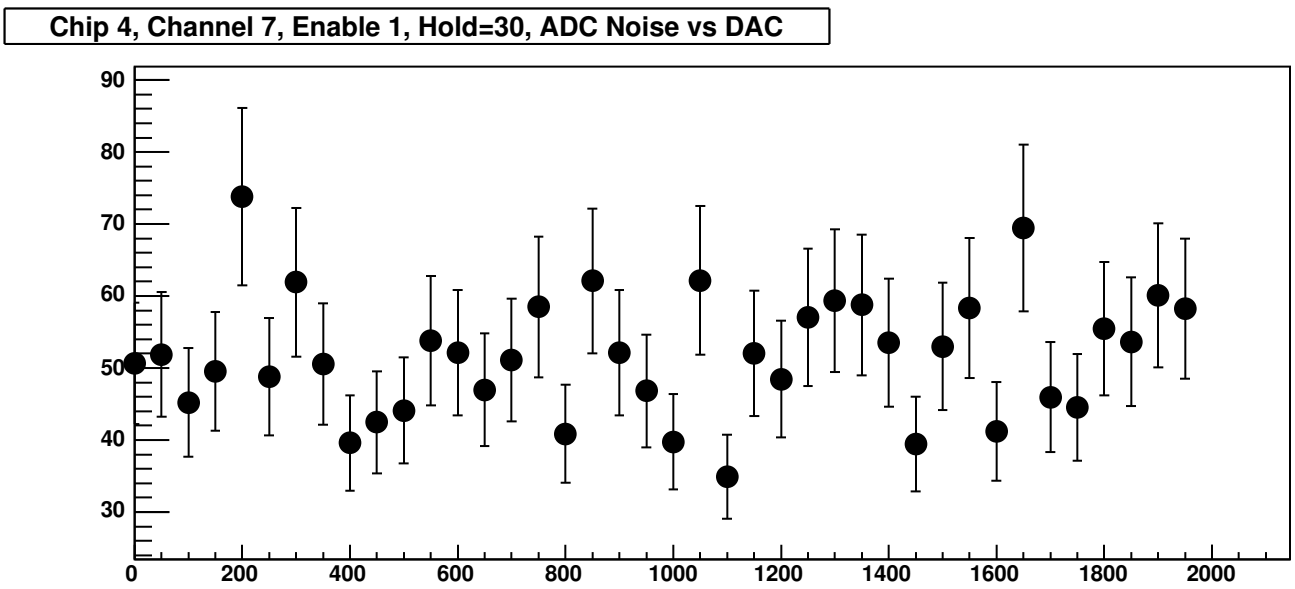


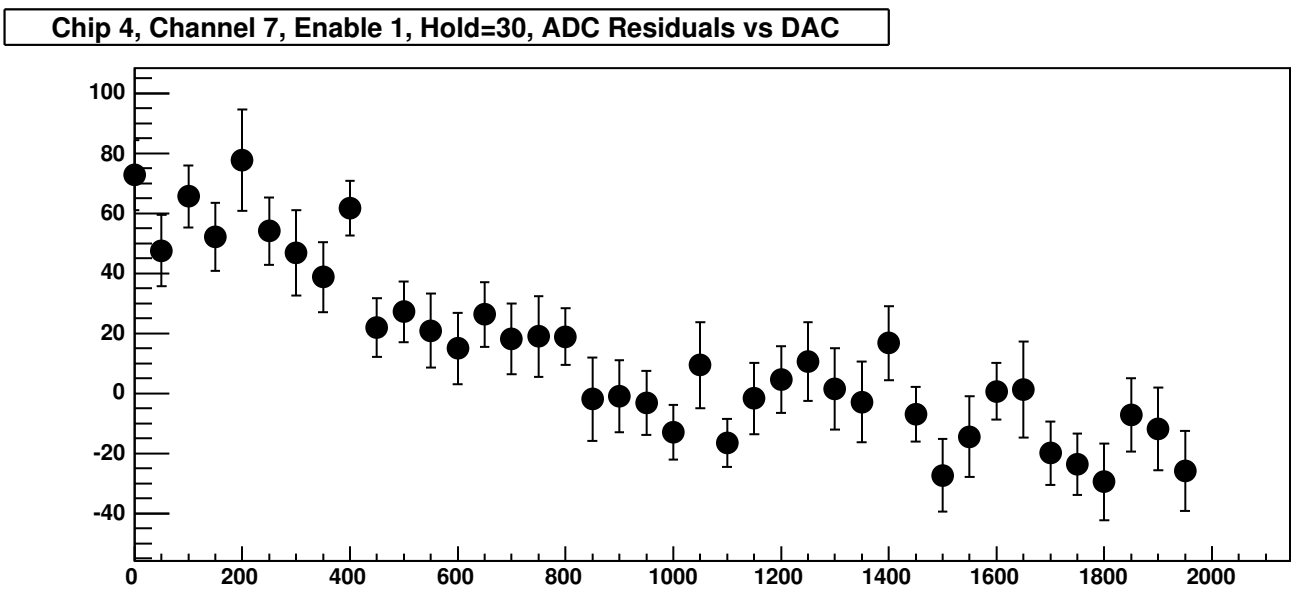

Chip 4, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 12.21 / 11
p0 -1239 ± 21.85
p1 0.2582 ± 0.0202
-500
-600
-700
-800
-900
-1000
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

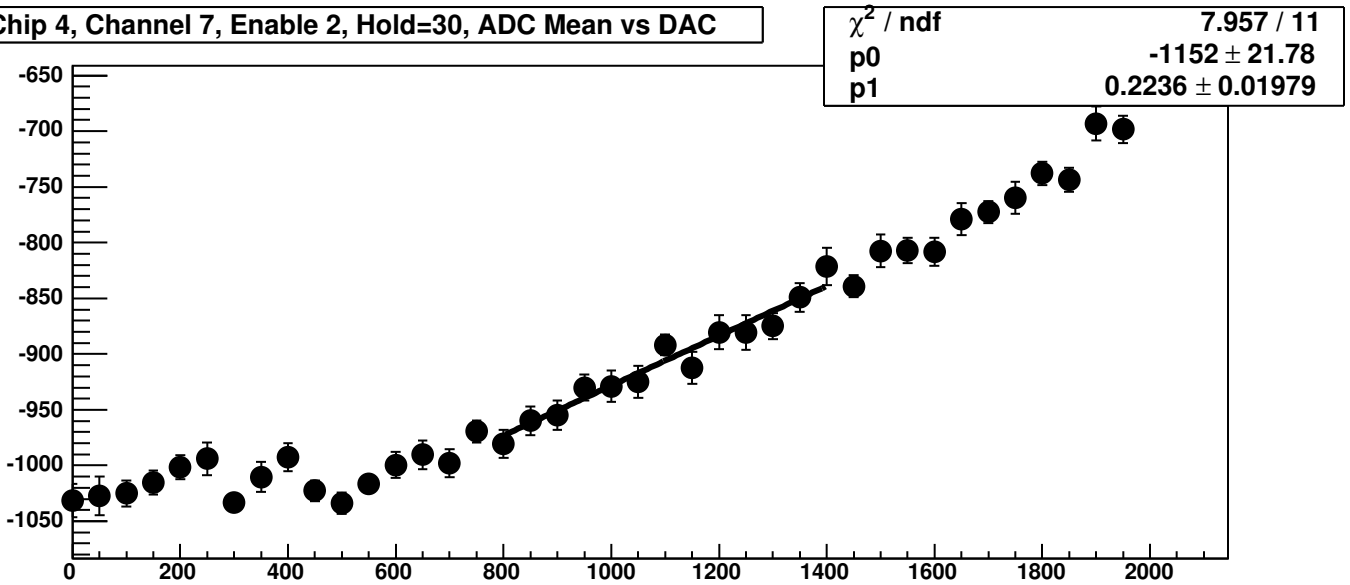

Chip 4, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 13.61 / 11
p0 -1206 ± 19.04
p1 0.0803 ± 0.01744
-1040
-1060
-1080
-1100
-1120
-1140
-1160
0
200
400
600
800
1000
1200
1400
1600
1800
2000

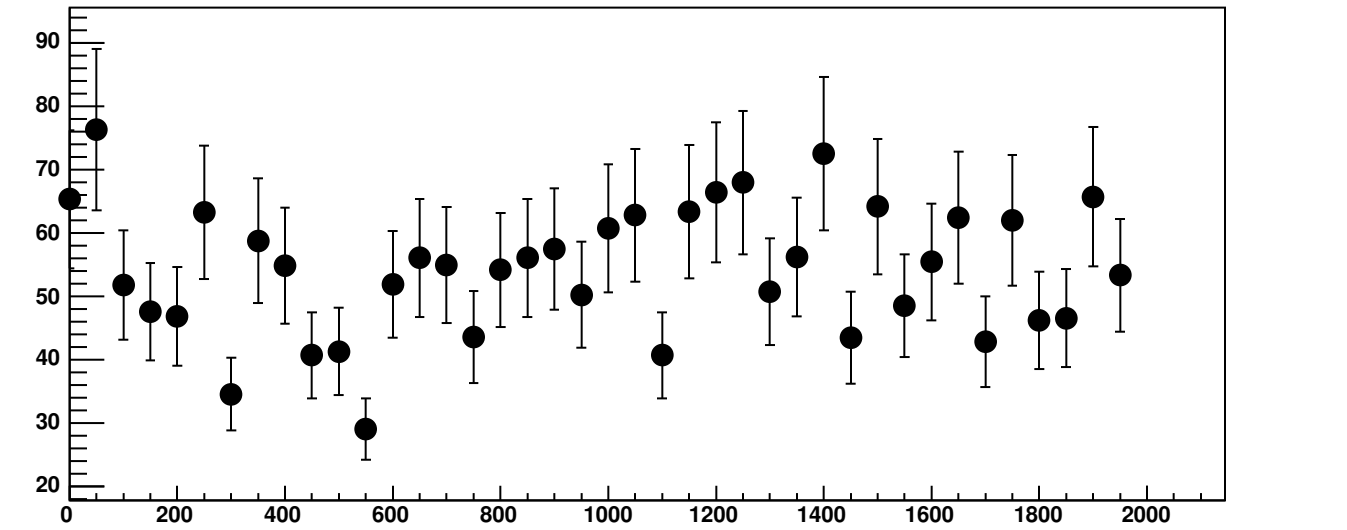

Chip 4, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

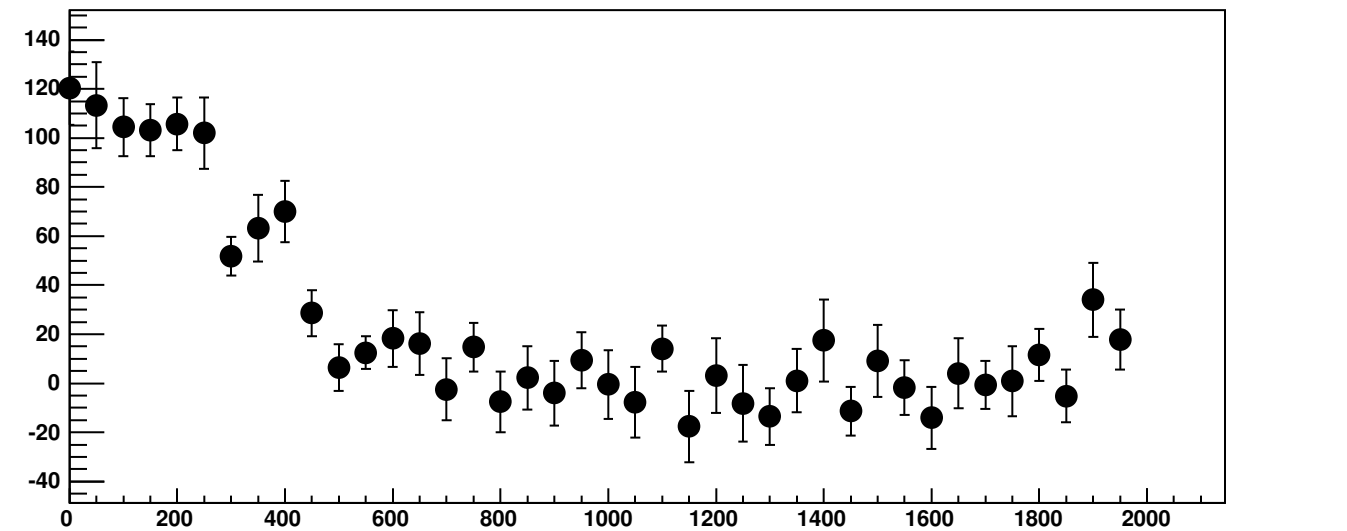
Chip 4, Channel 7, Enable 2, Hold=30, ADC Mean vs DAC

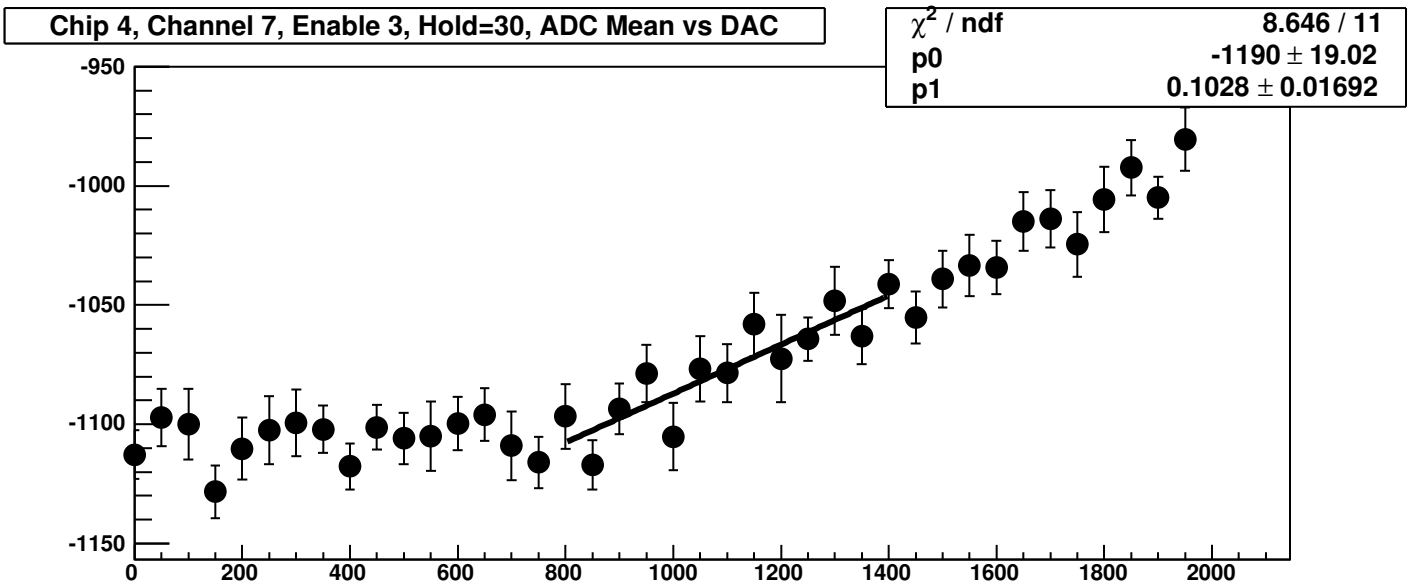


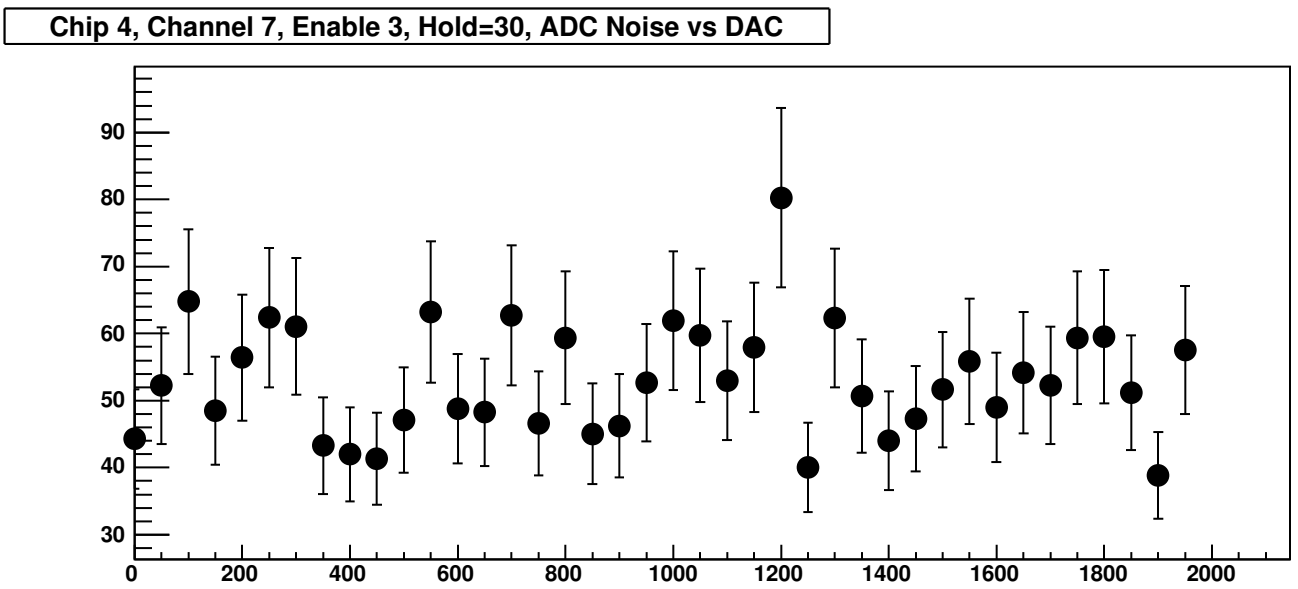
Chip 4, Channel 7, Enable 2, Hold=30, ADC Noise vs DAC

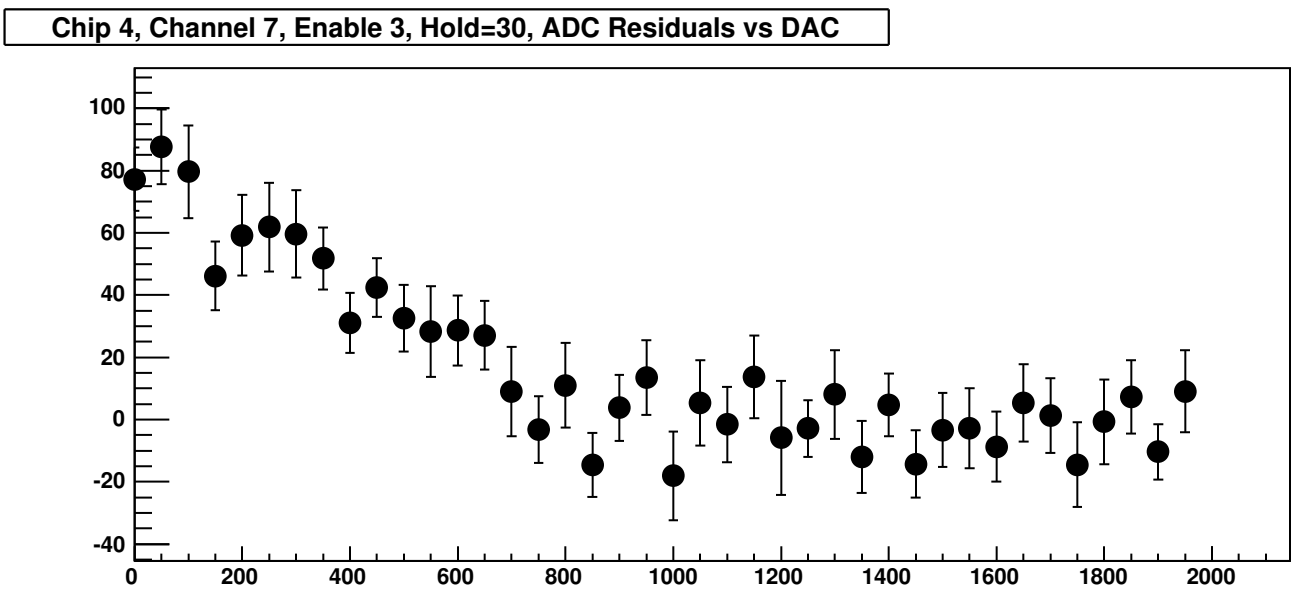


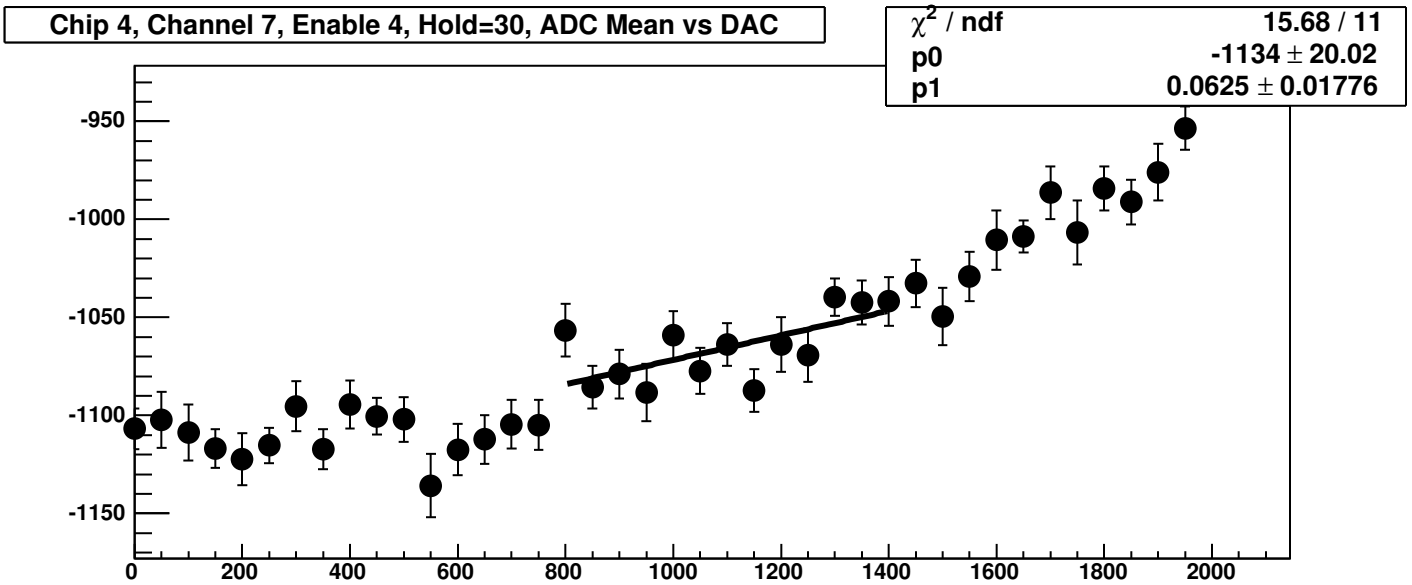
Chip 4, Channel 7, Enable 2, Hold=30, ADC Residuals vs DAC

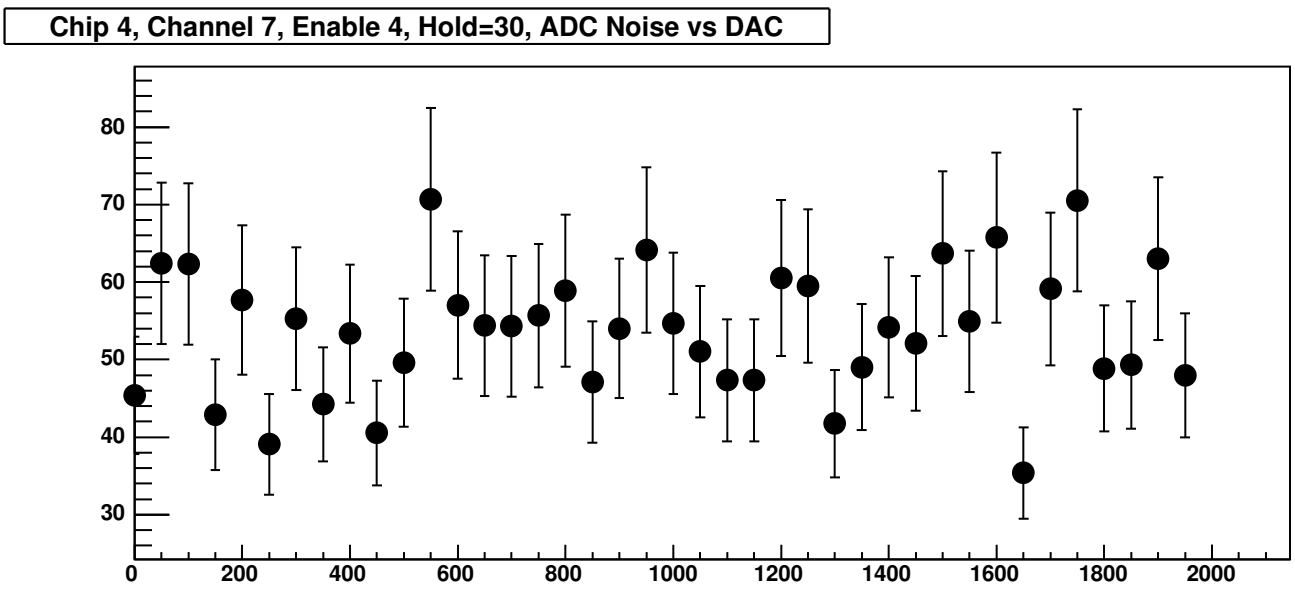


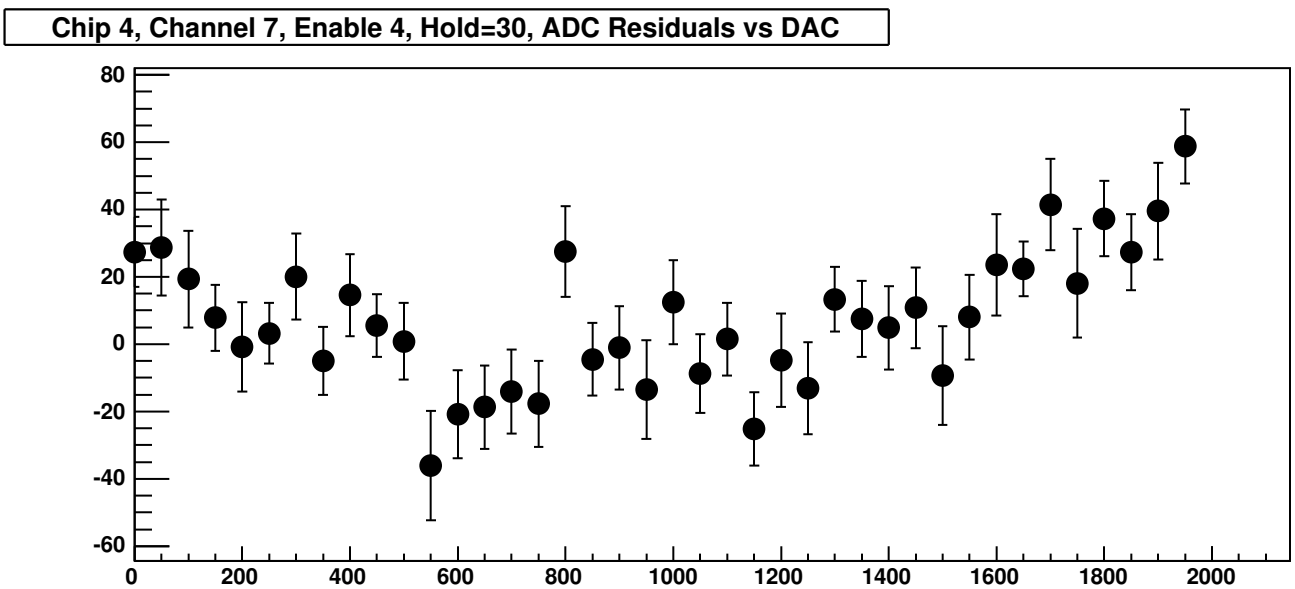

Chip 4, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 8.646 / 11
p0 -1190 ± 19.02
p1 0.1028 ± 0.01692
-950
-1000
-1050
-1100
-1150
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

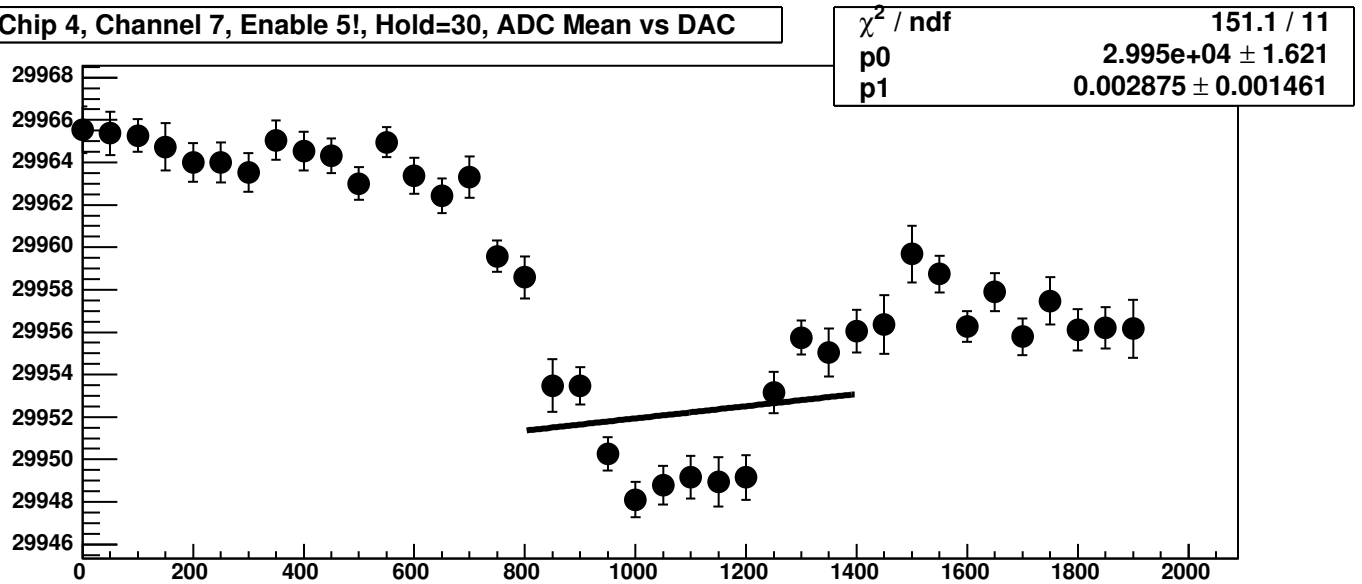

Chip 4, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 15.68 / 11
p0 -1134 ± 20.02
p1 0.0625 ± 0.01776
-950
-1000
-1050
-1100
-1150
0
200
400
600
800
1000
1200
1400
1600
1800
2000

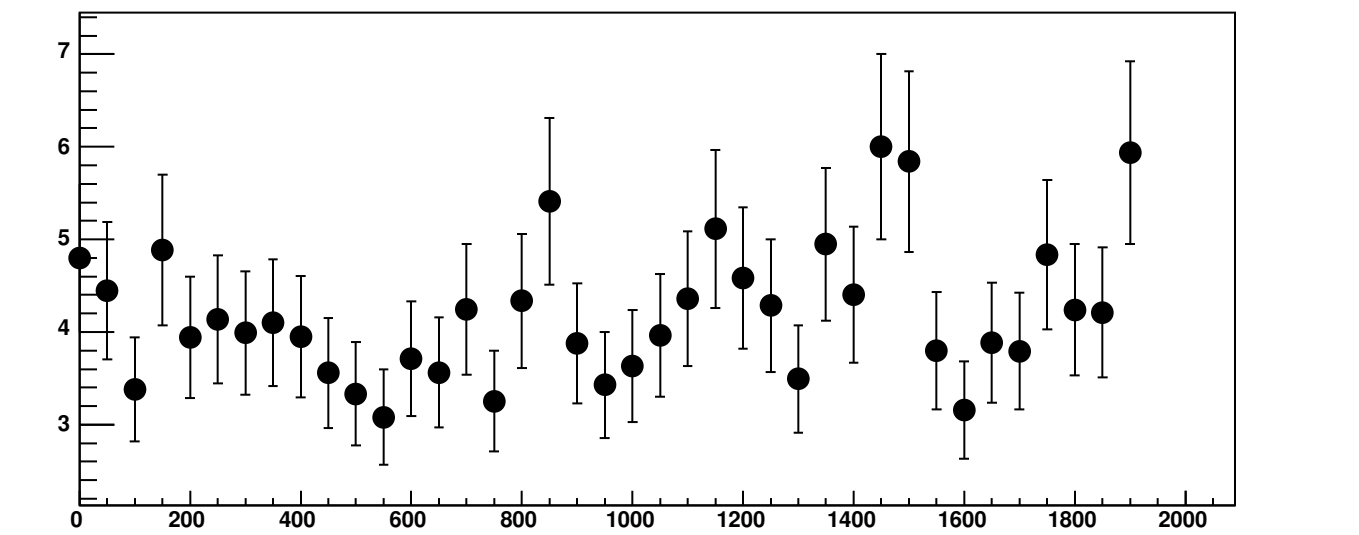

Chip 4, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

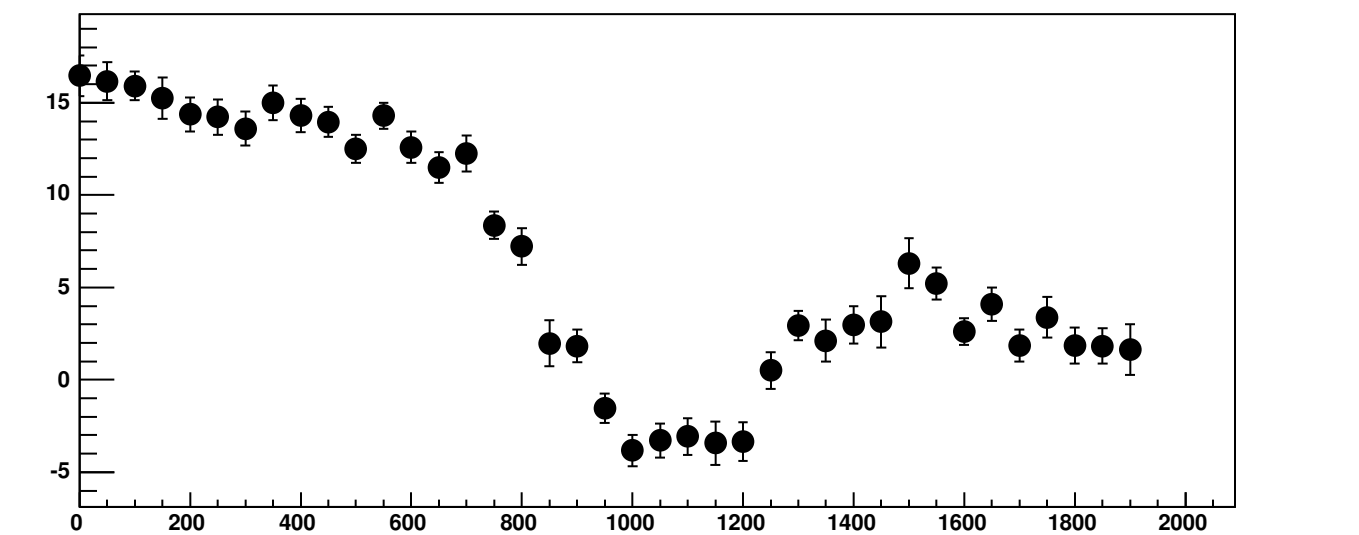
Chip 4, Channel 7, Enable 5!, Hold=30, ADC Mean vs DAC

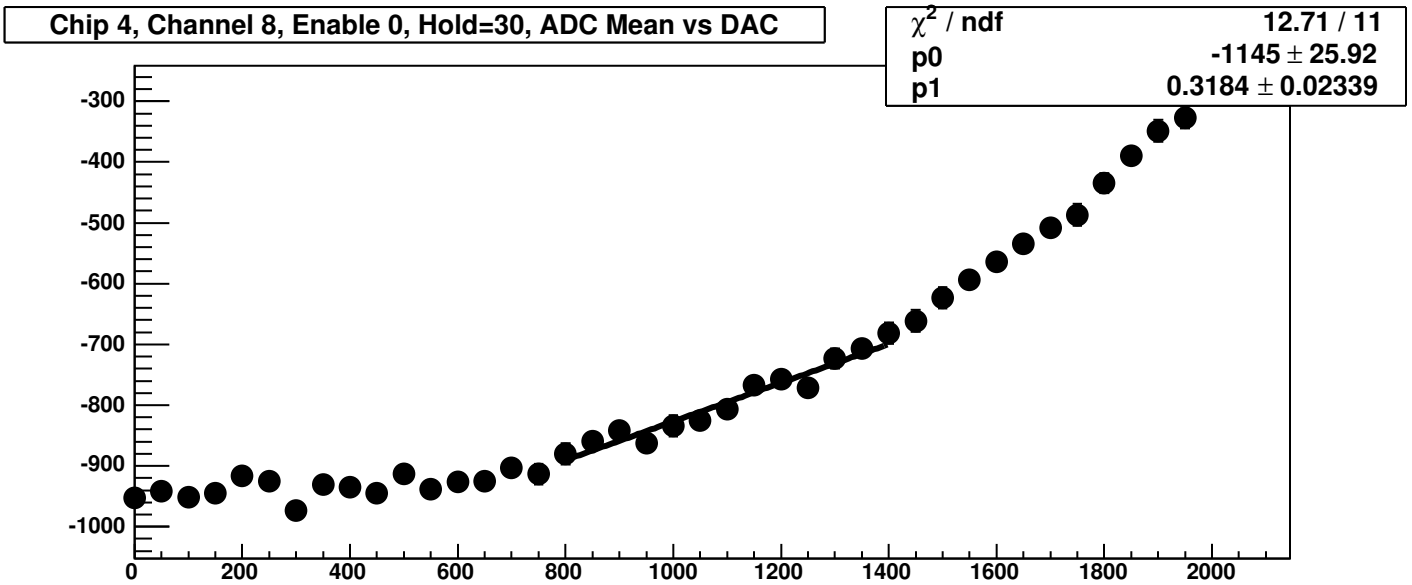


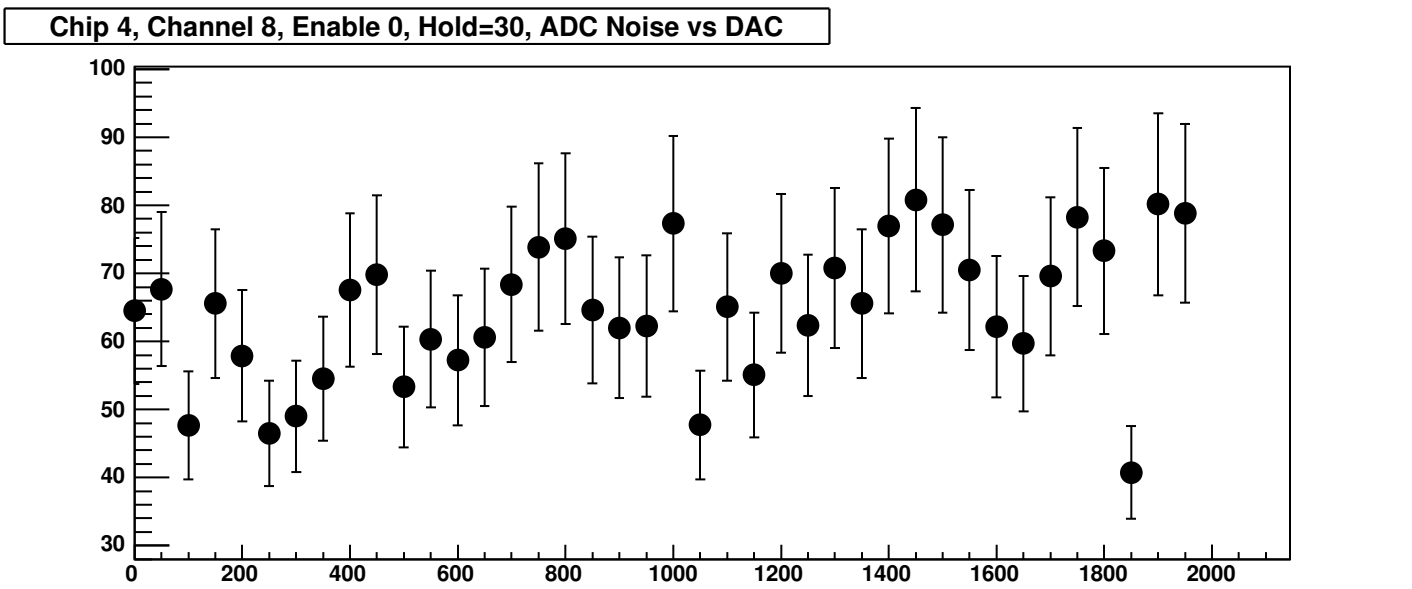
Chip 4, Channel 7, Enable 5!, Hold=30, ADC Noise vs DAC

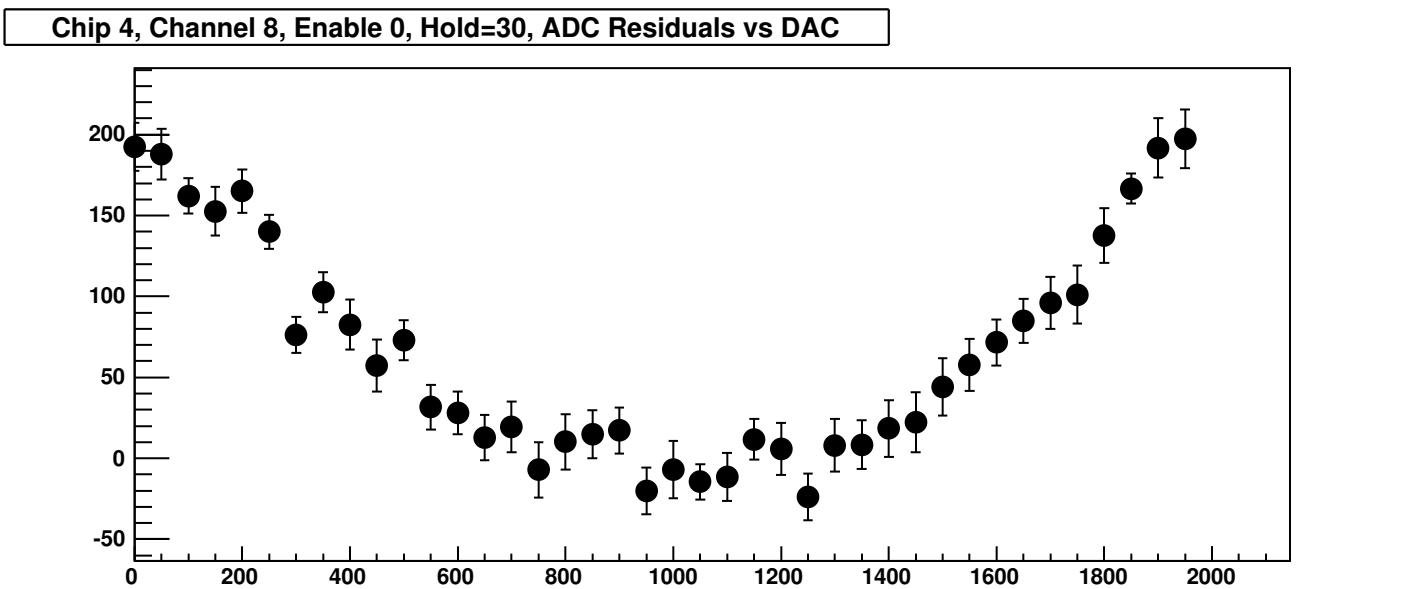


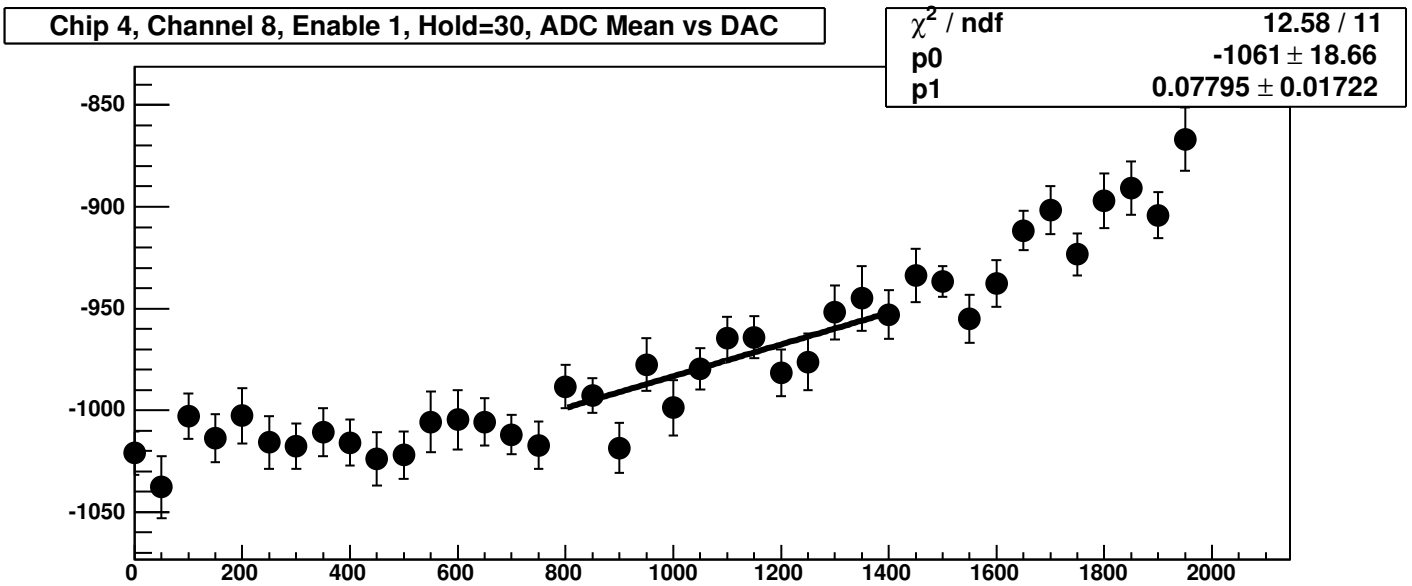
Chip 4, Channel 7, Enable 5!, Hold=30, ADC Residuals vs DAC

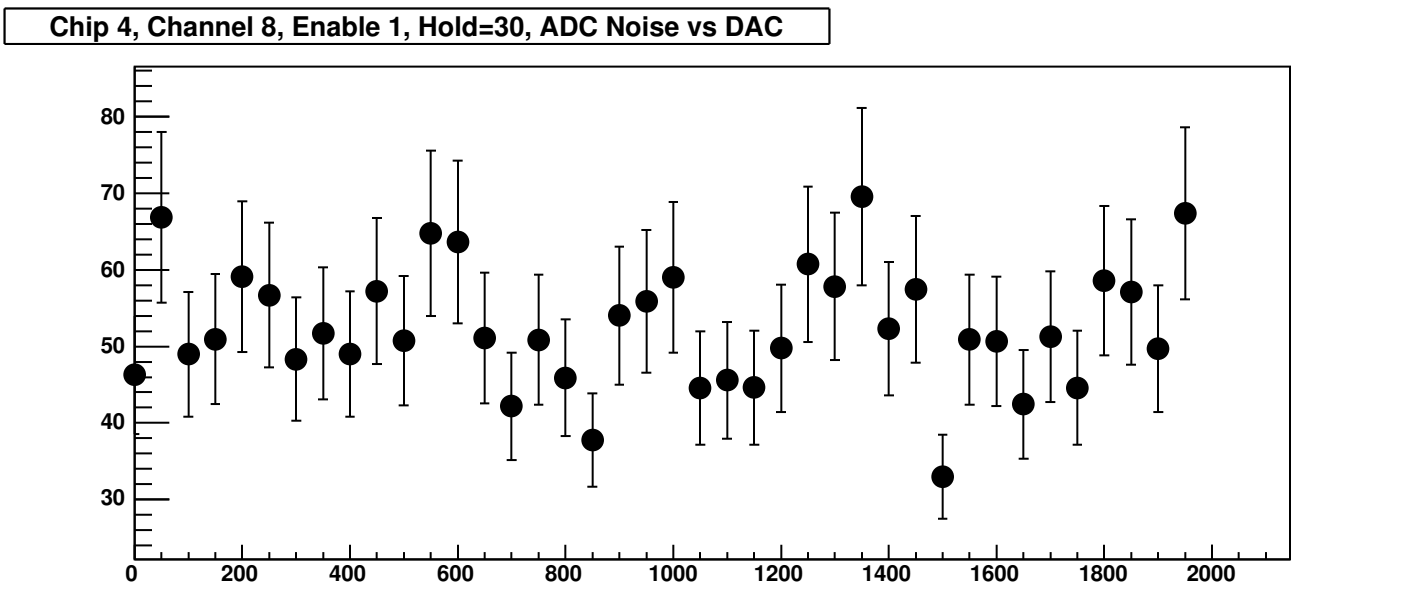


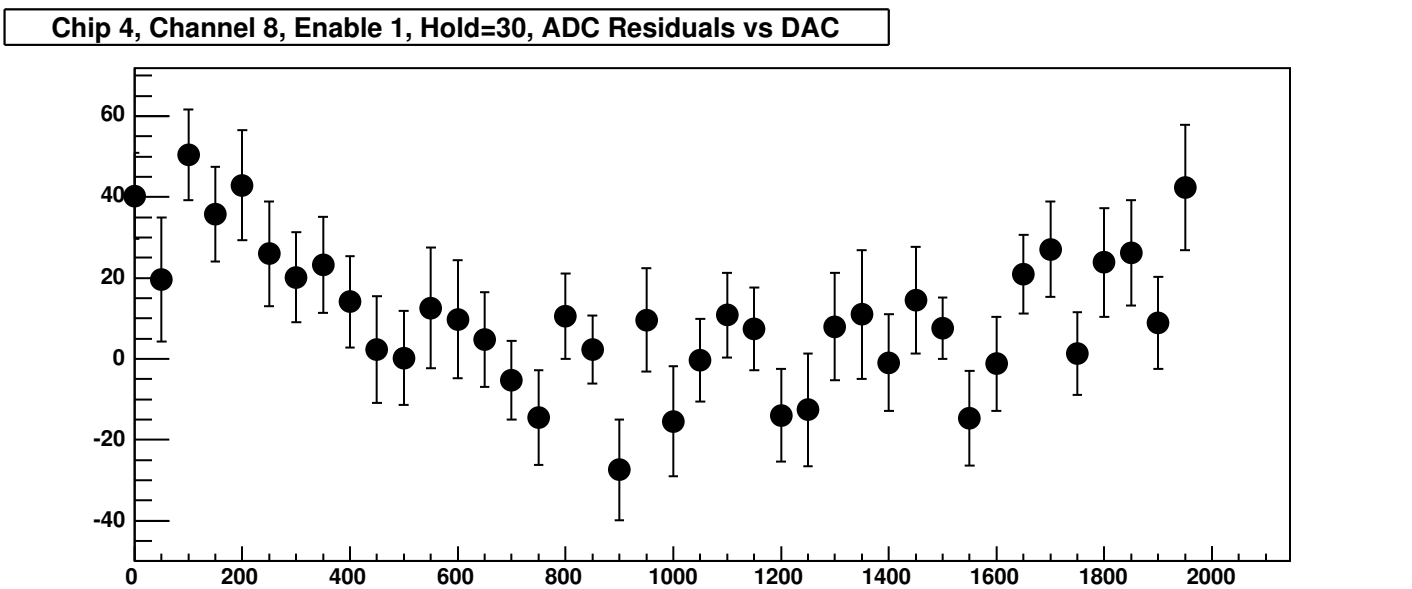

Chip 4, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 12.71 / 11
p0 -1145 ± 25.92
p1 0.3184 ± 0.02339
-300
-400
-500
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

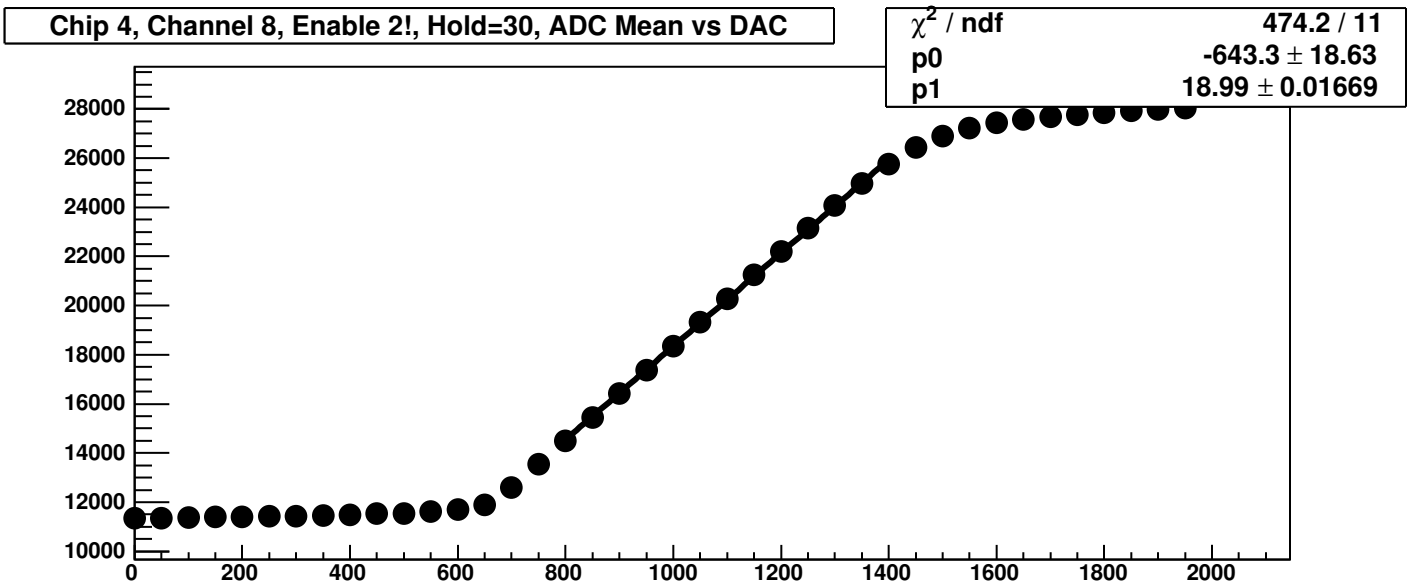

Chip 4, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

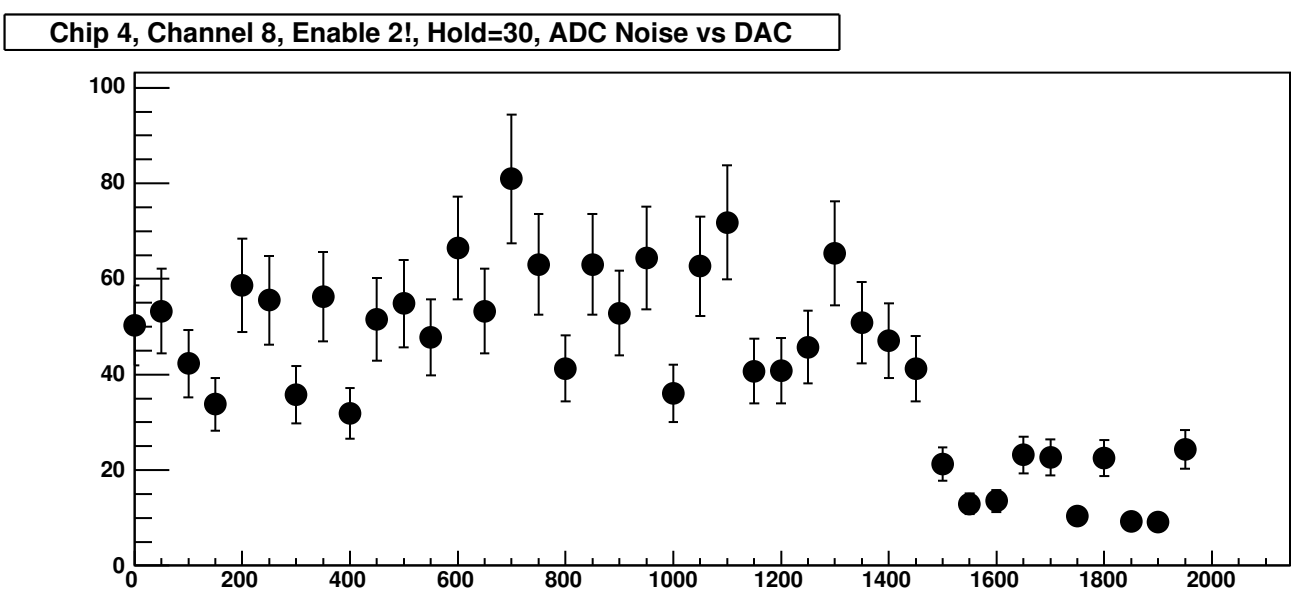

Chip 4, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

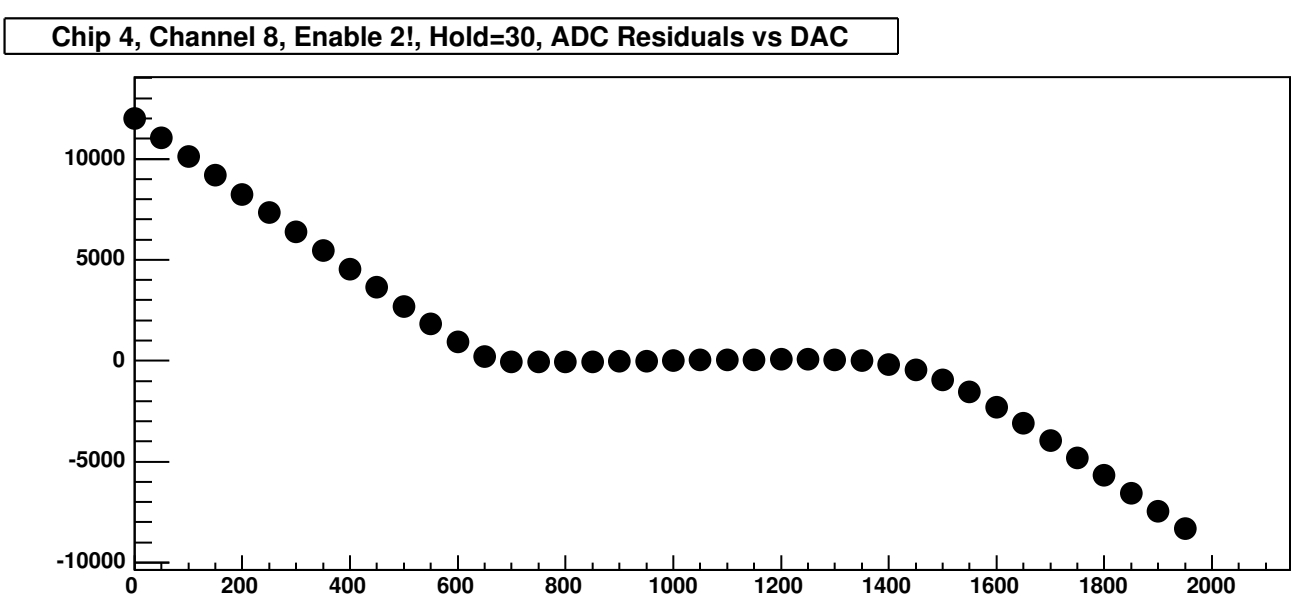

Chip 4, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 12.58 / 11
p0 -1061 ± 18.66
p1 0.07795 ± 0.01722
-850
-900
-950
-1000
-1050
0
200
400
600
800
1000
1200
1400
1600
1800
2000

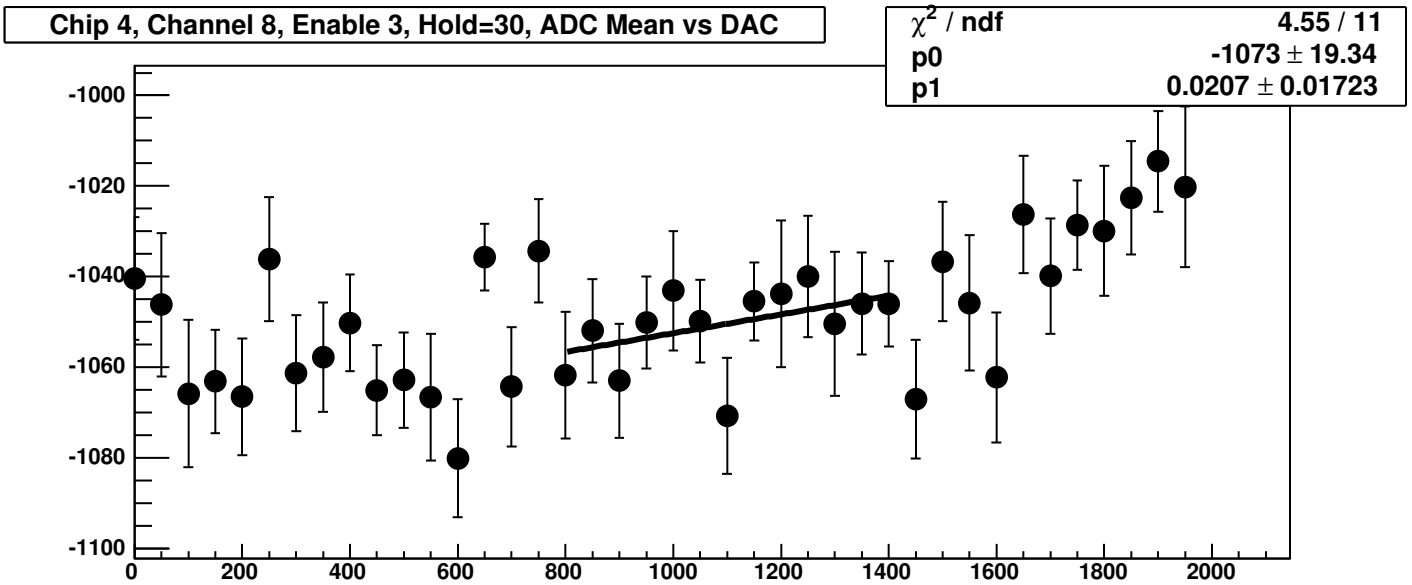

Chip 4, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

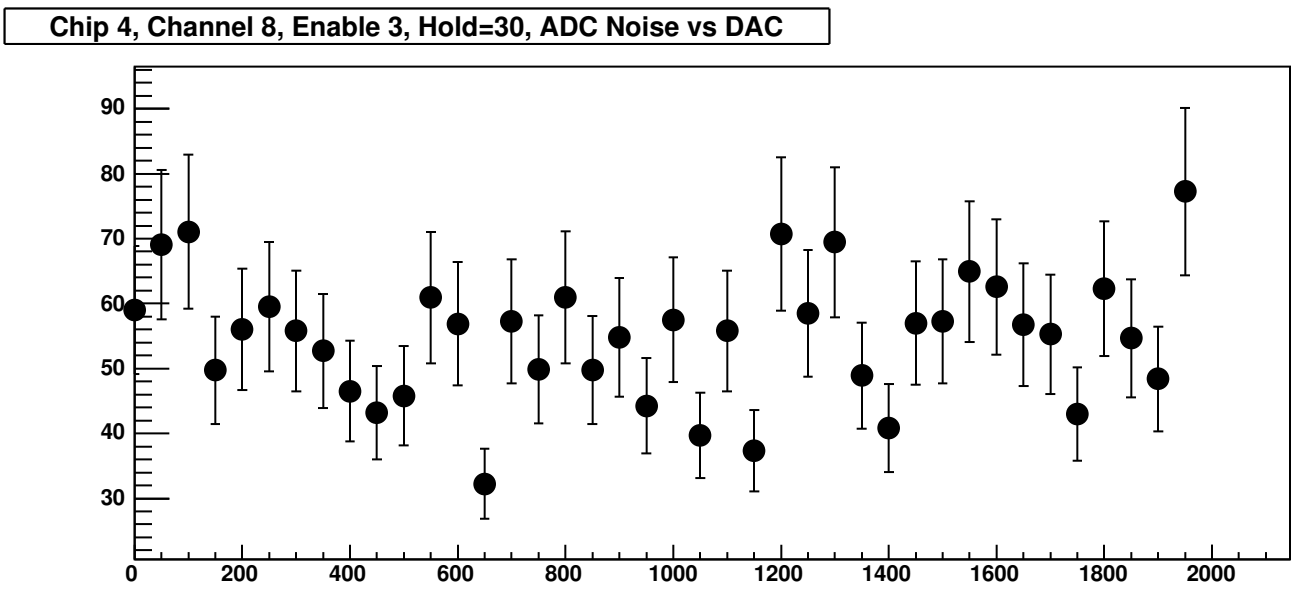

Chip 4, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

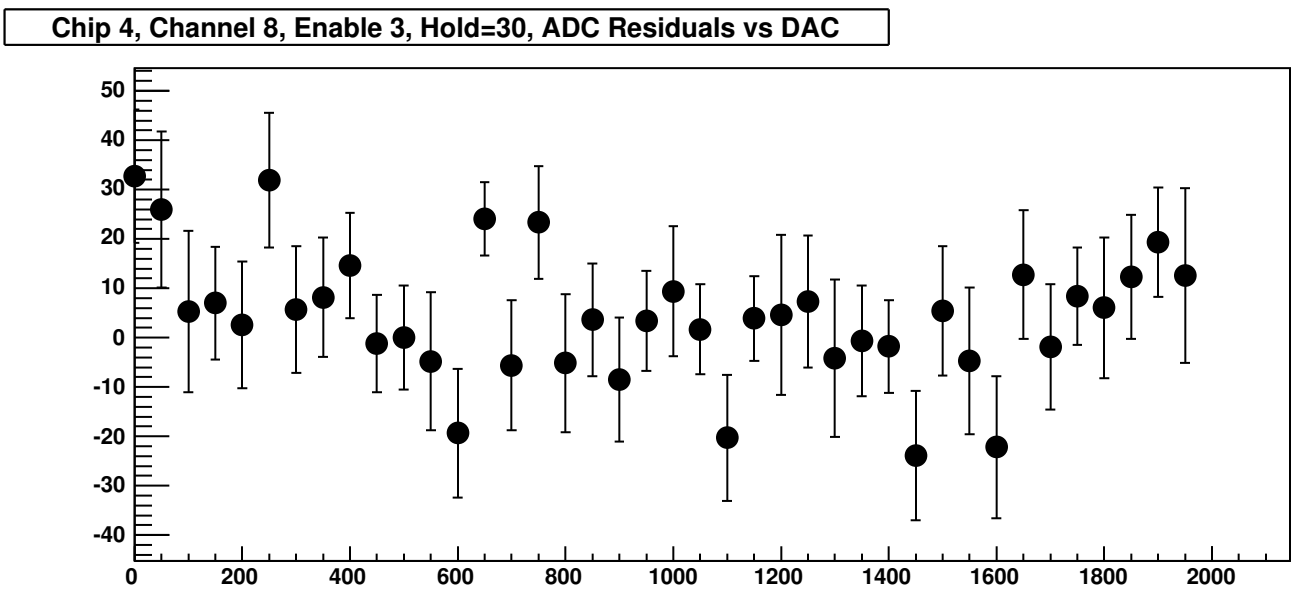

Chip 4, Channel 8, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 474.2 / 11
p0 -643.3 ± 18.63
p1 18.99 ± 0.01669
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

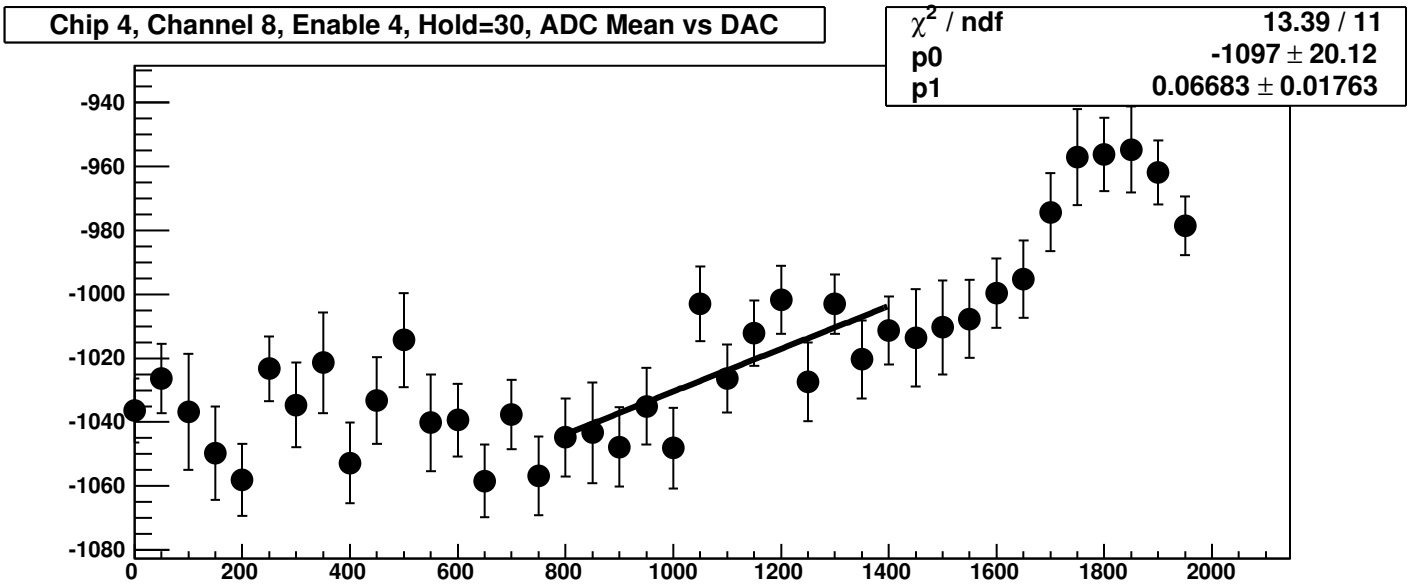

Chip 4, Channel 8, Enable 2!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

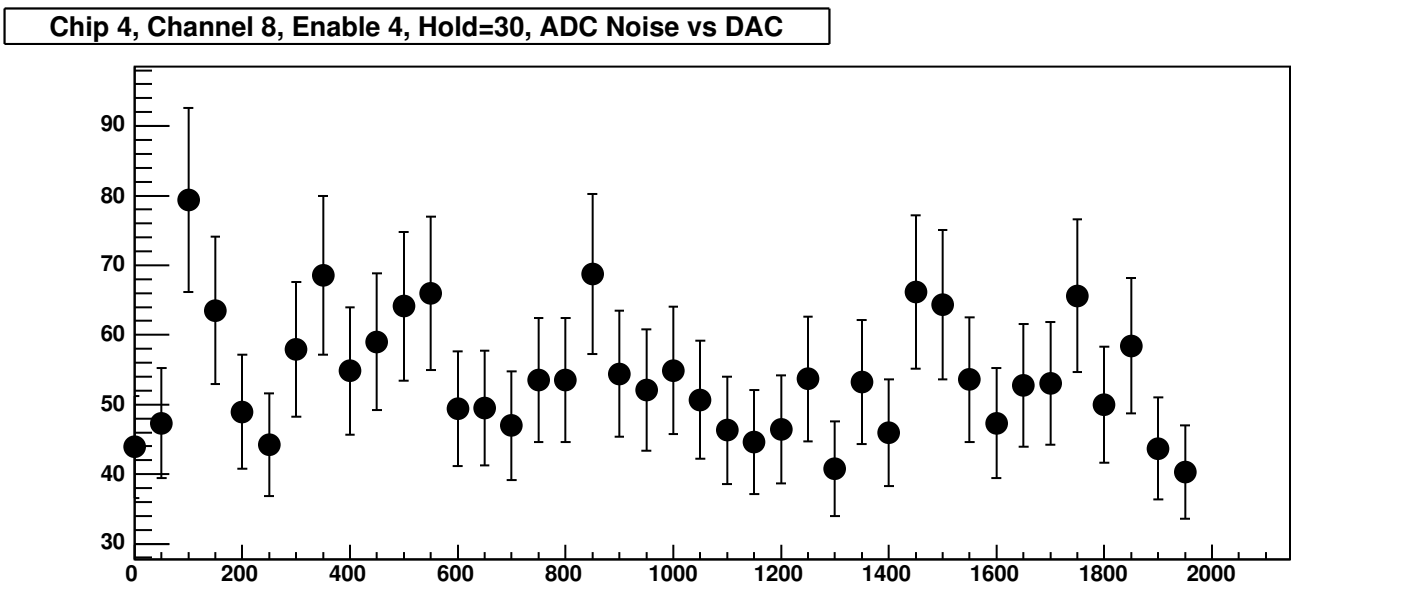

Chip 4, Channel 8, Enable 2!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

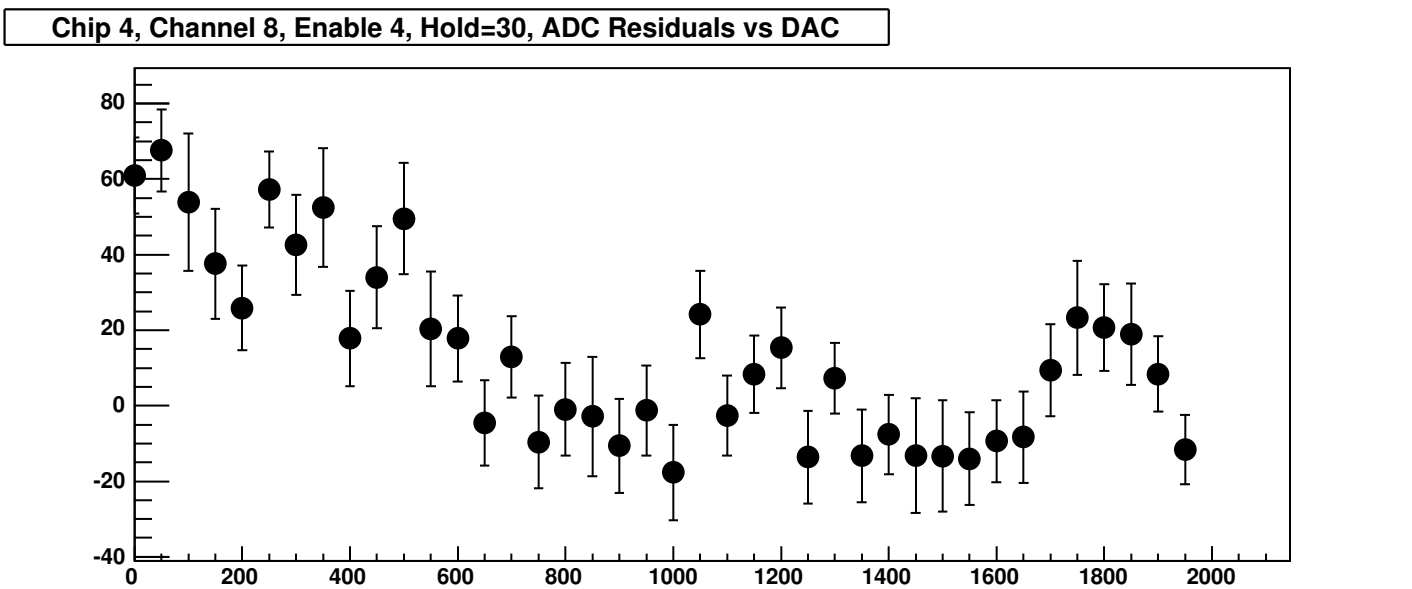

Chip 4, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4.55 / 11
p0 -1073 ± 19.34
p1 0.0207 ± 0.01723
-1000
-1020
-1040
-1060
-1080
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

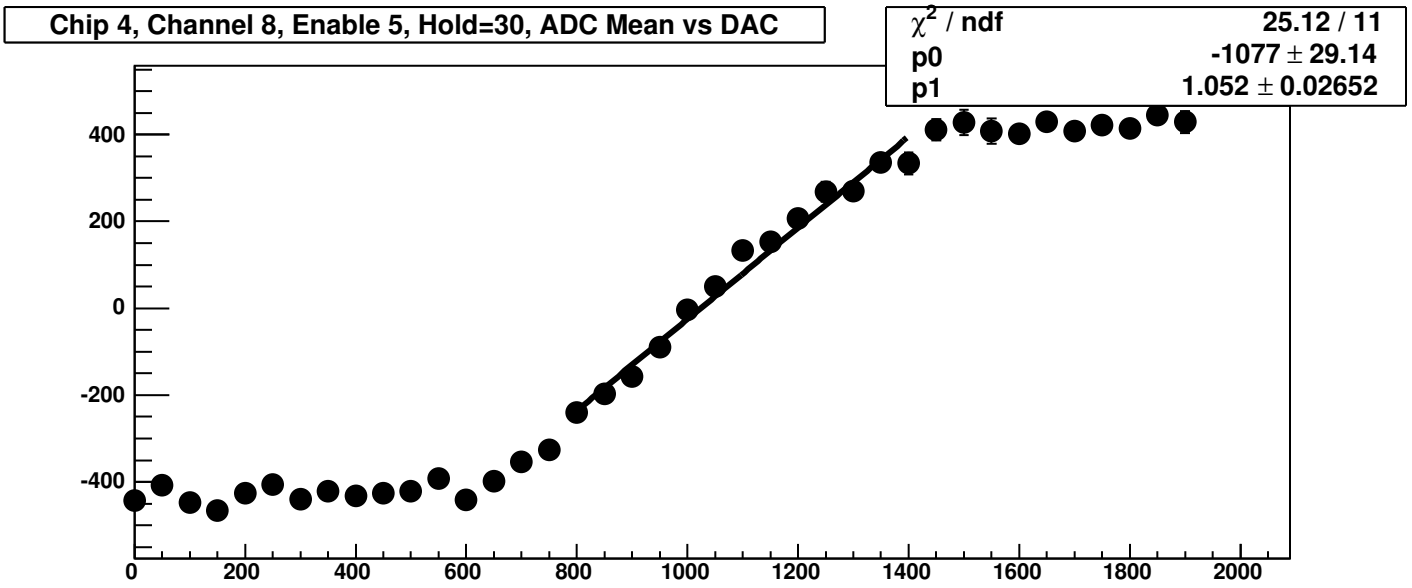

Chip 4, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

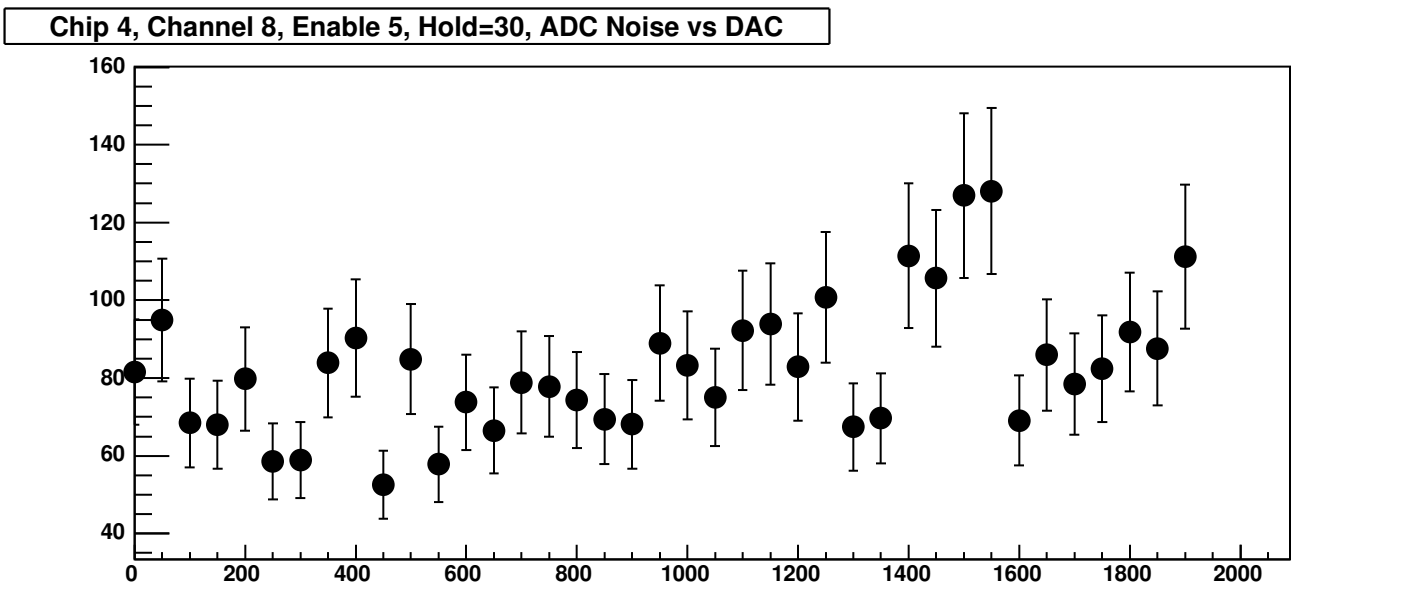

Chip 4, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
50
40
30
20
10
0
-10
-20
-30
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

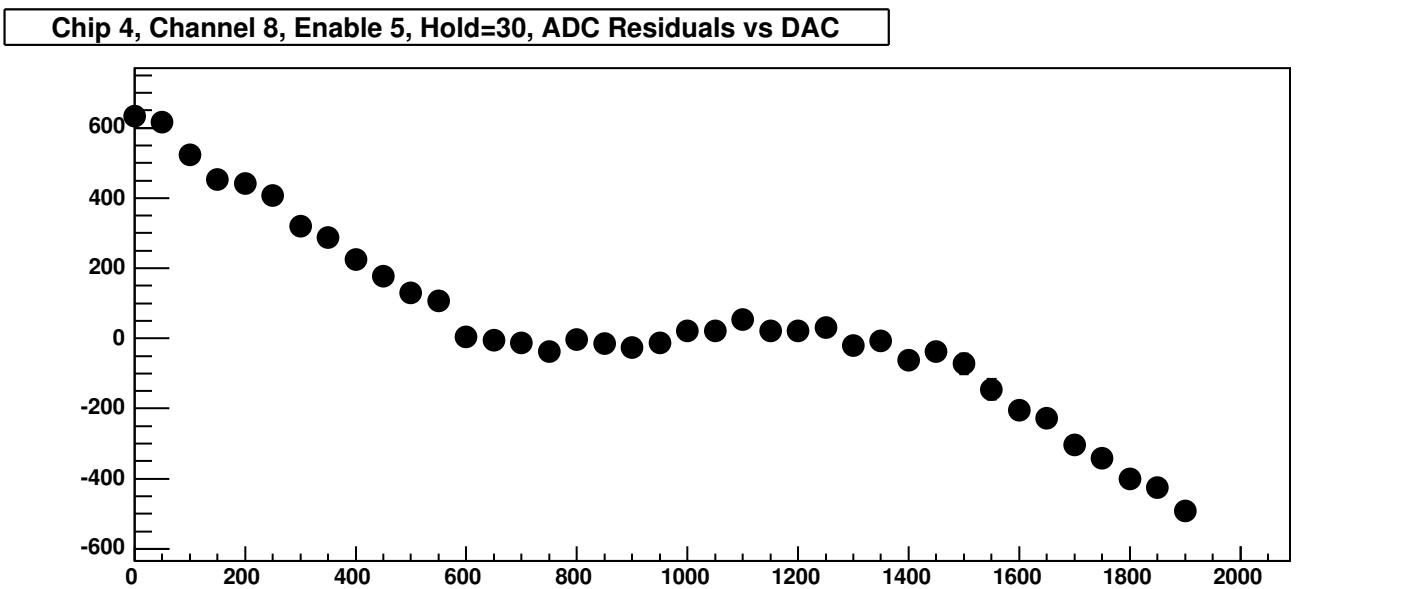

Chip 4, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 13.39 / 11
p0 -1097 ± 20.12
p1 0.06683 ± 0.01763
-940
-960
-980
-1000
-1020
-1040
-1060
-1080
0
200
400
600
800
1000
1200
1400
1600
1800
2000

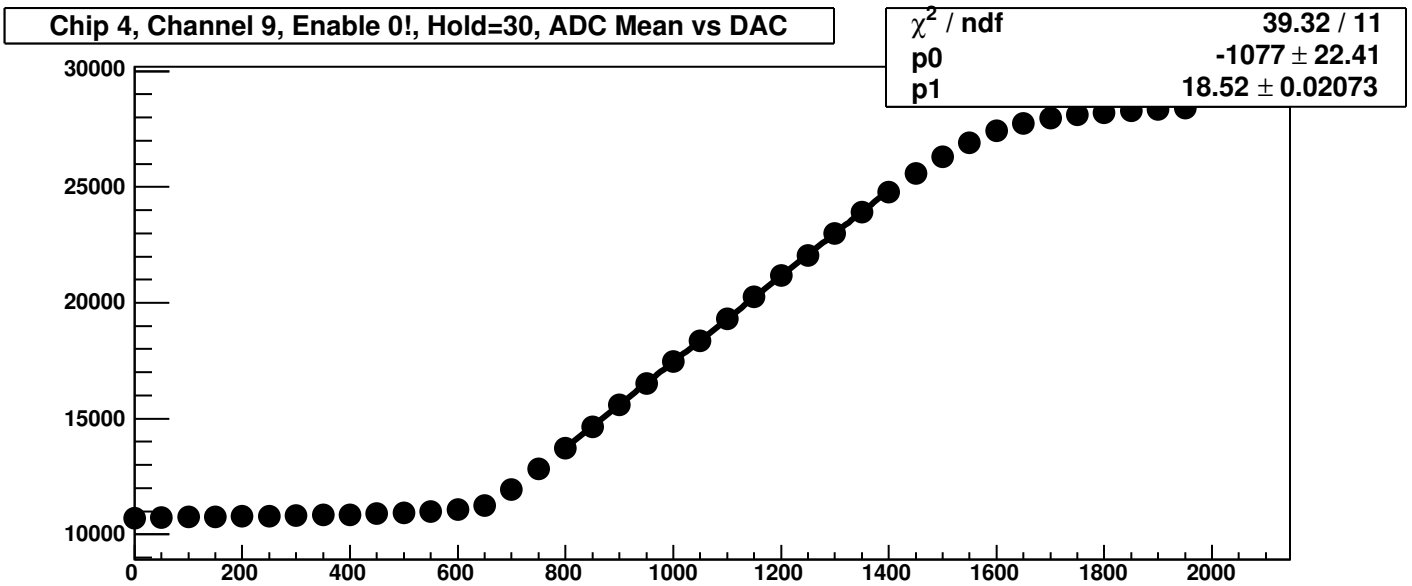

Chip 4, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

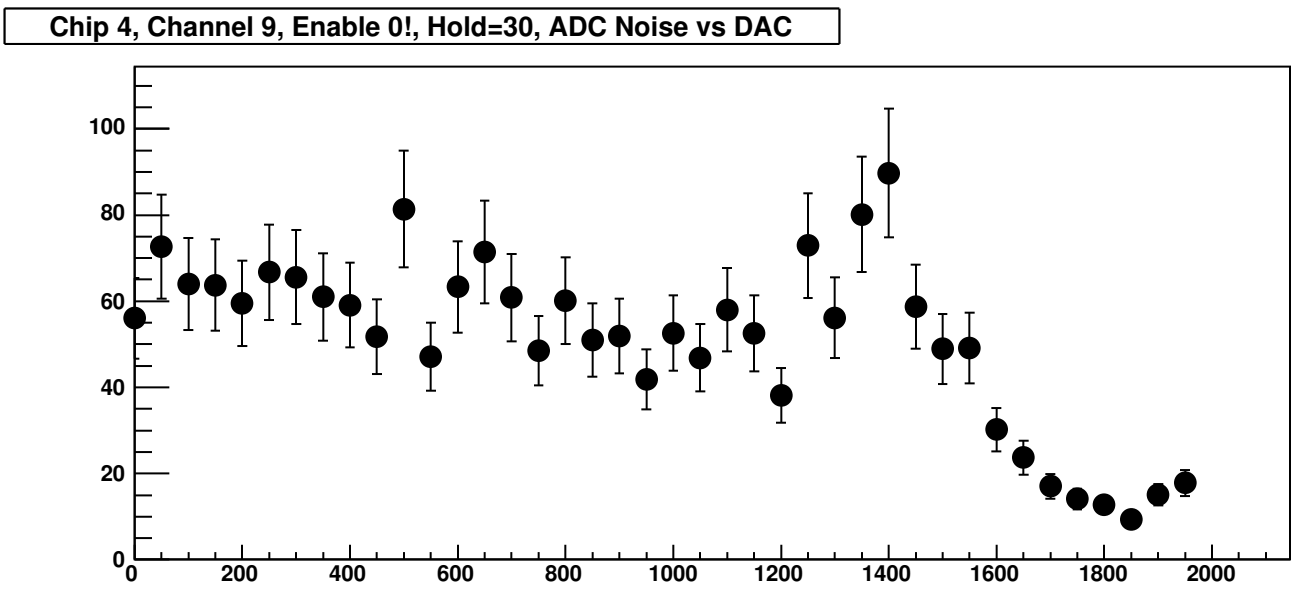

Chip 4, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

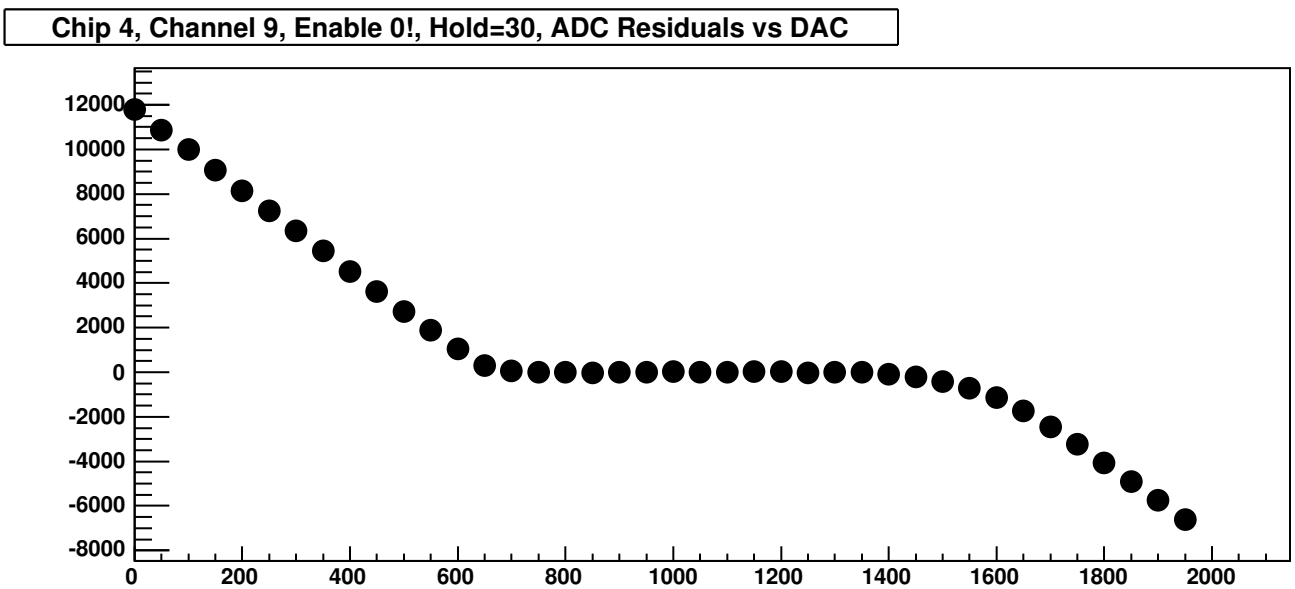

Chip 4, Channel 8, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 25.12 / 11
p0 -1077 ± 29.14
p1 1.052 ± 0.02652
400
200
0
-200
-400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

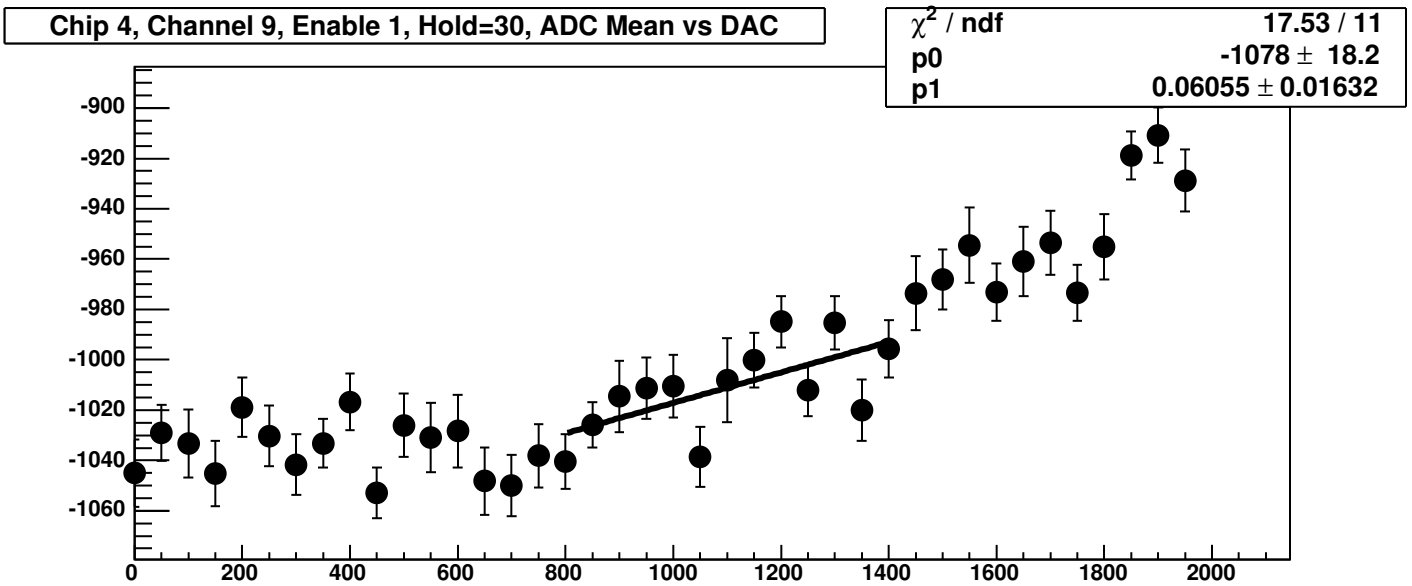

Chip 4, Channel 8, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

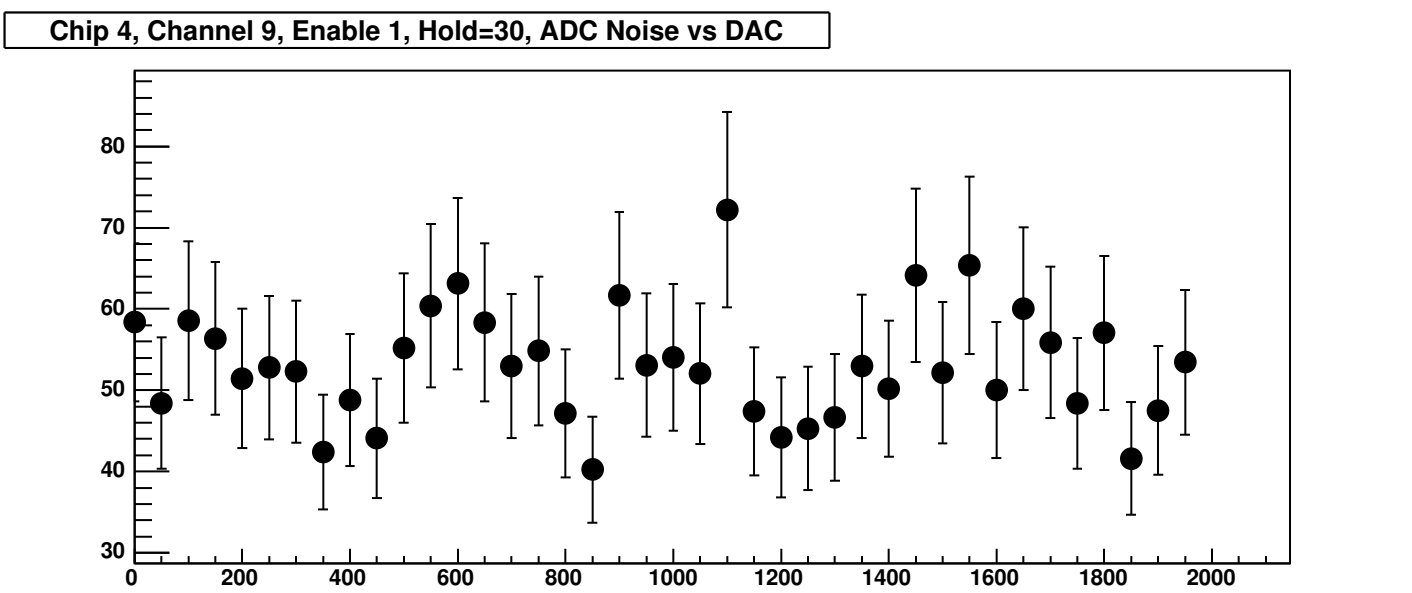

Chip 4, Channel 8, Enable 5, Hold=30, ADC Residuals vs DAC
600
400
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

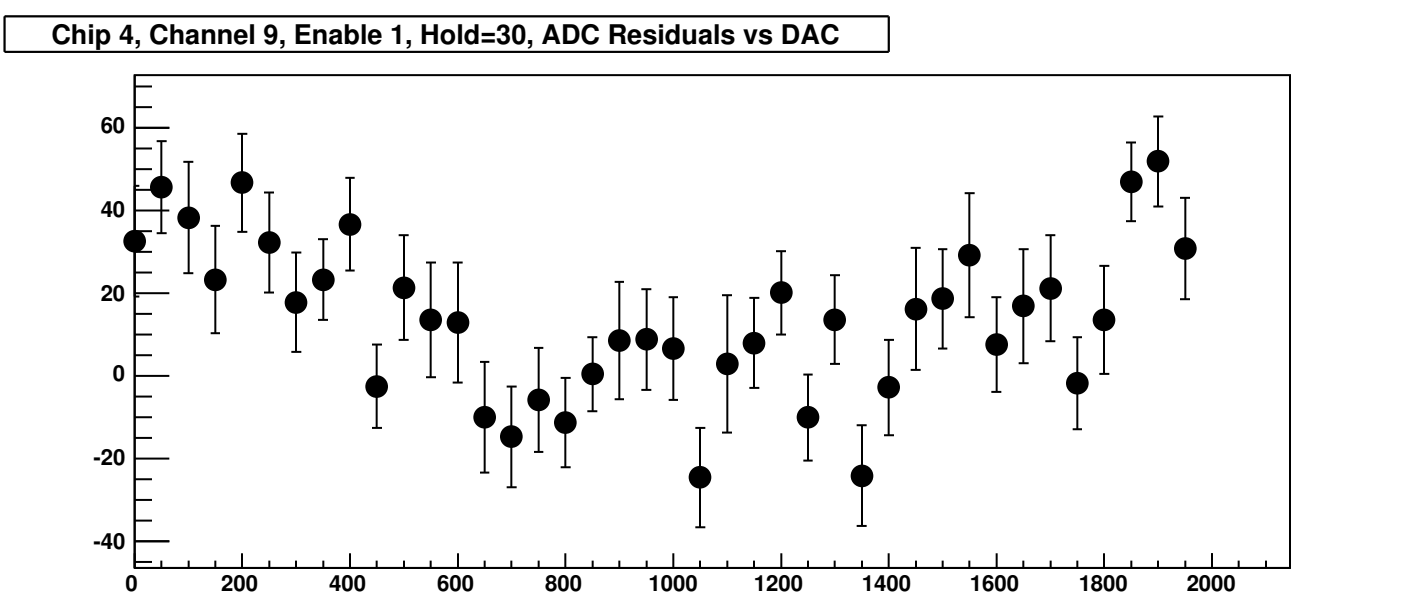

Chip 4, Channel 9, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 39.32 / 11
p0 -1077 ± 22.41
p1 18.52 ± 0.02073
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

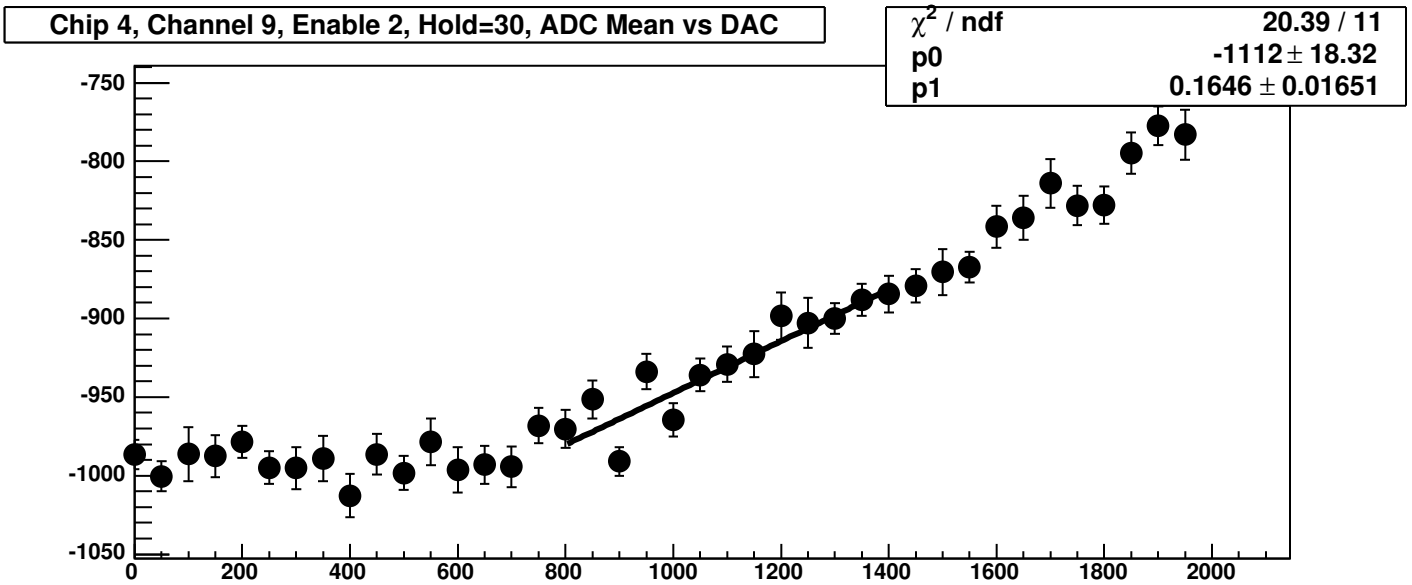

Chip 4, Channel 9, Enable 0!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

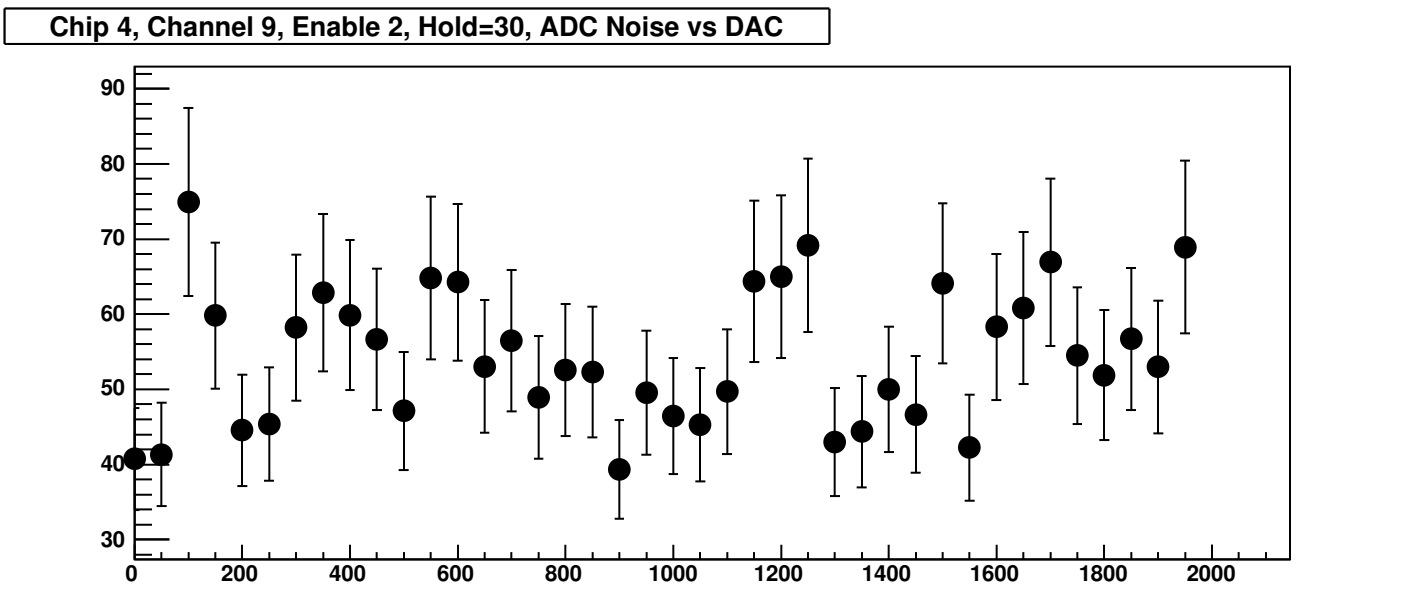

Chip 4, Channel 9, Enable 0!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

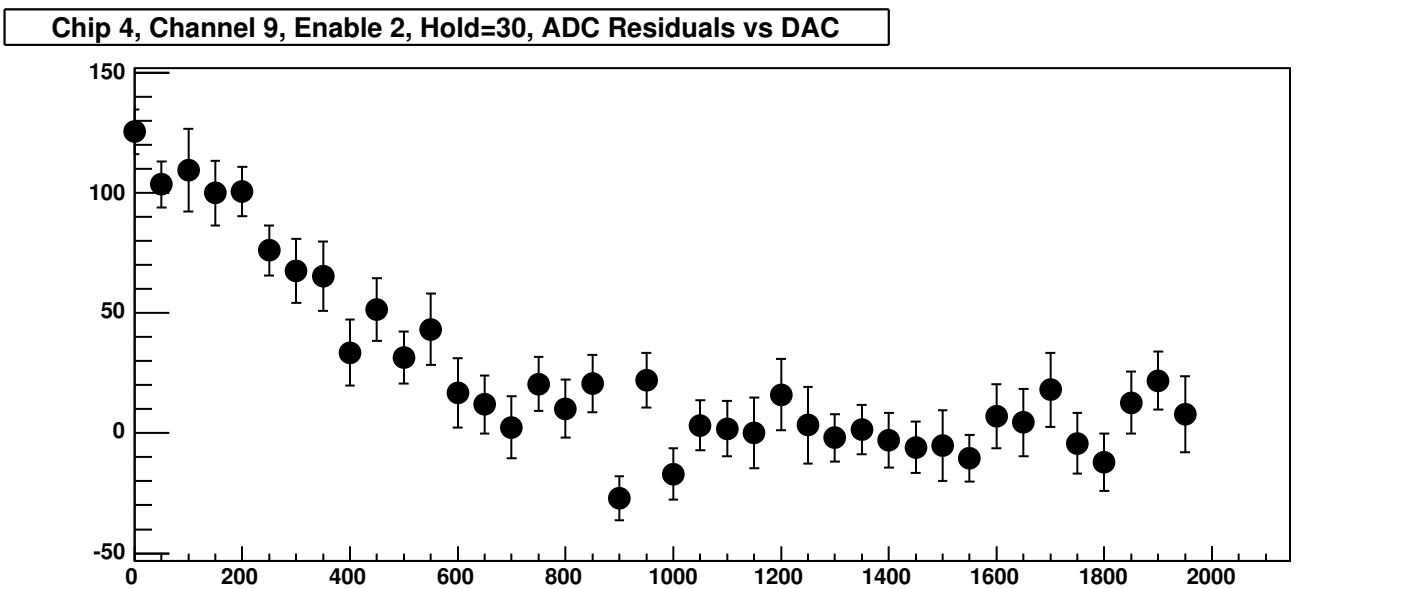

Chip 4, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 17.53 / 11
p0 -1078 ± 18.2
p1 0.06055 ± 0.01632
-900
-920
-940
-960
-980
-1000
-1020
-1040
-1060
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

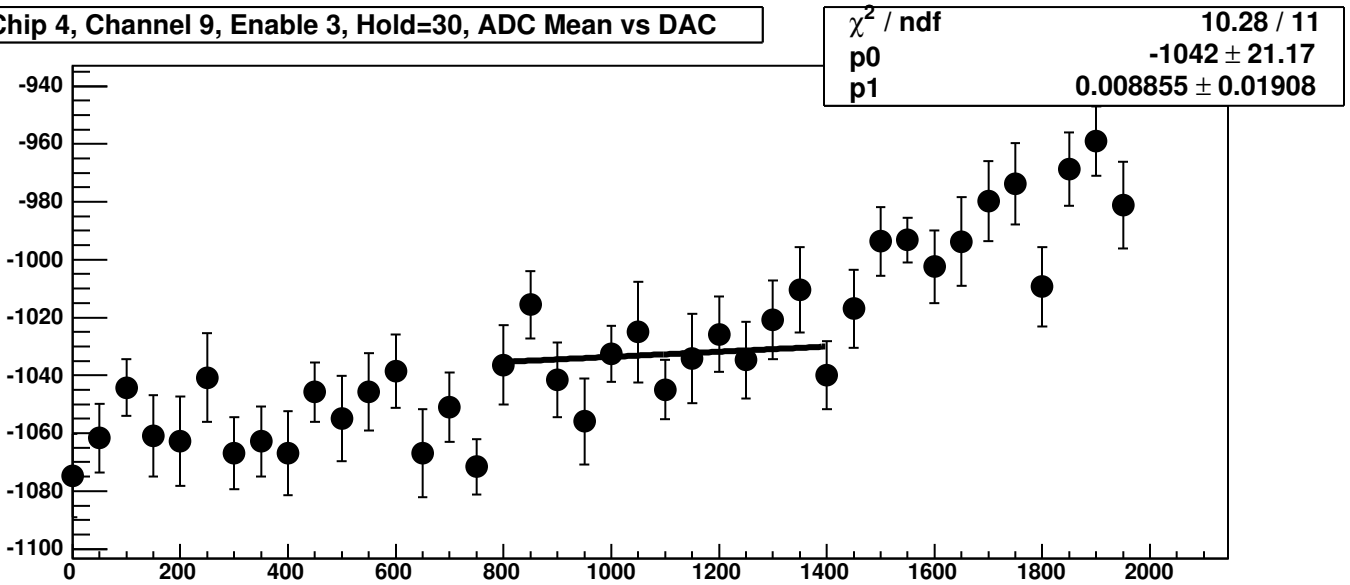

Chip 4, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 20.39 / 11
p0 -1112 ± 18.32
p1 0.1646 ± 0.01651
-750
-800
-850
-900
-950
-1000
-1050
0
200
400
600
800
1000
1200
1400
1600
1800
2000

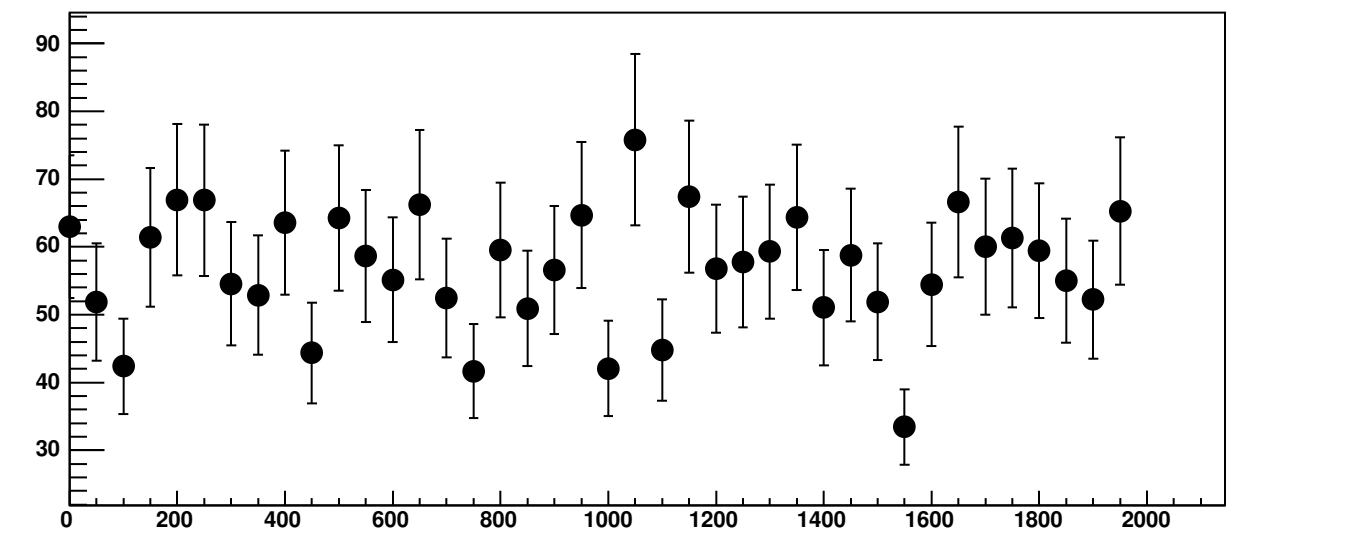

Chip 4, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

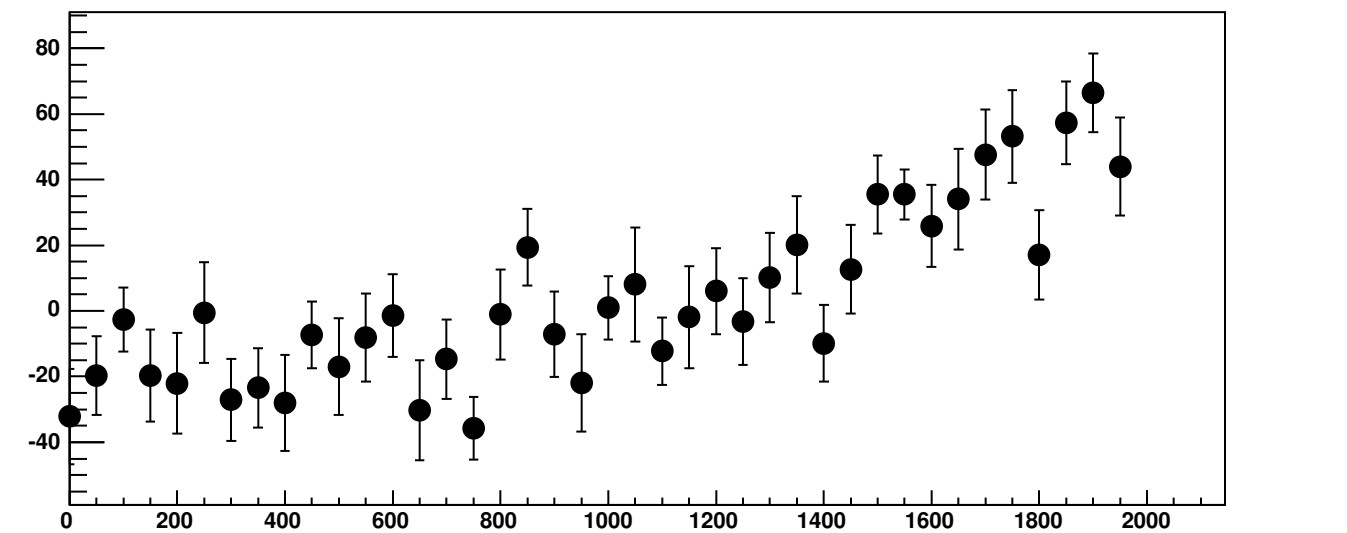
Chip 4, Channel 9, Enable 3, Hold=30, ADC Mean vs DAC

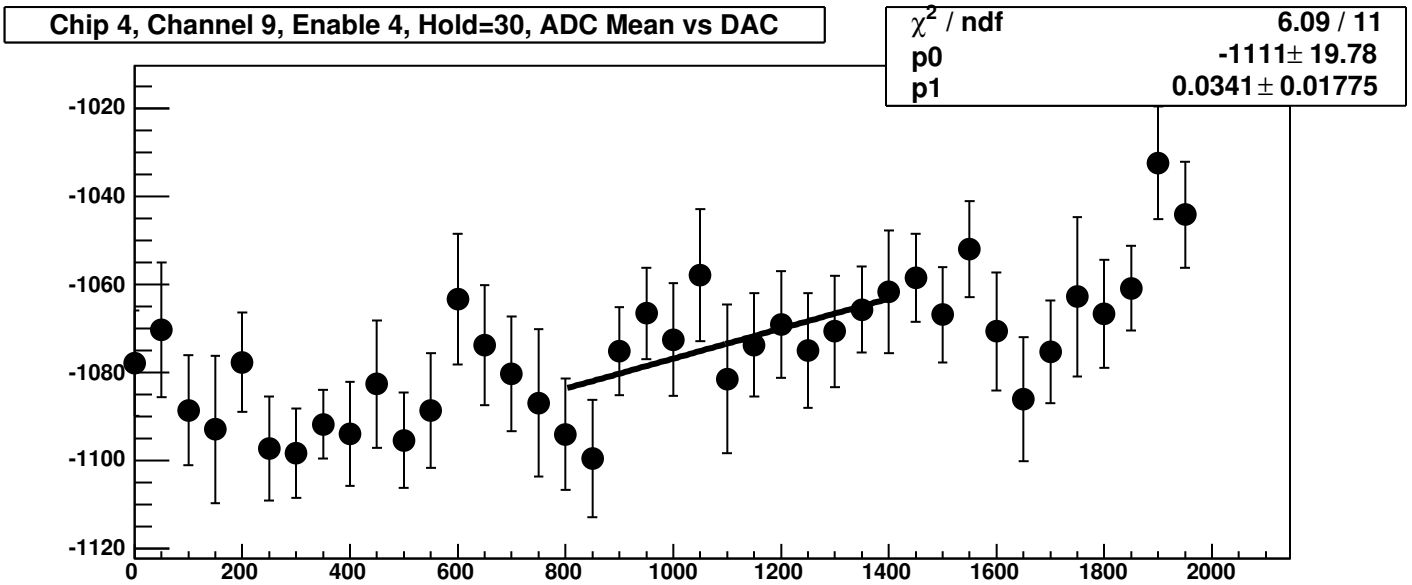


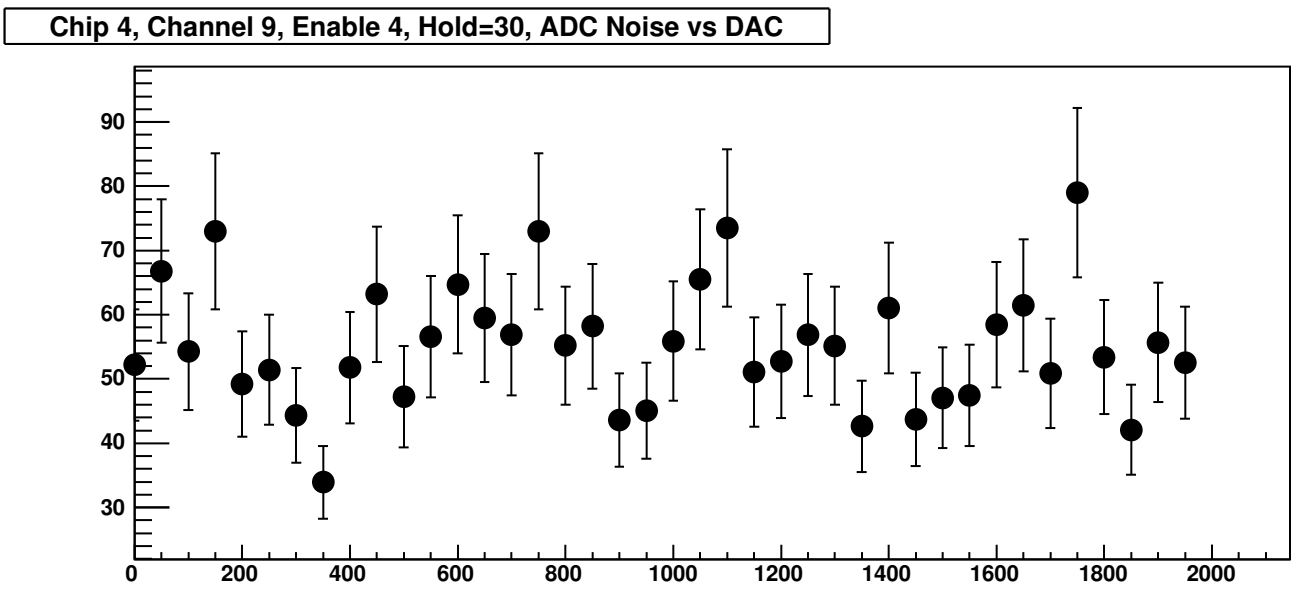
Chip 4, Channel 9, Enable 3, Hold=30, ADC Noise vs DAC

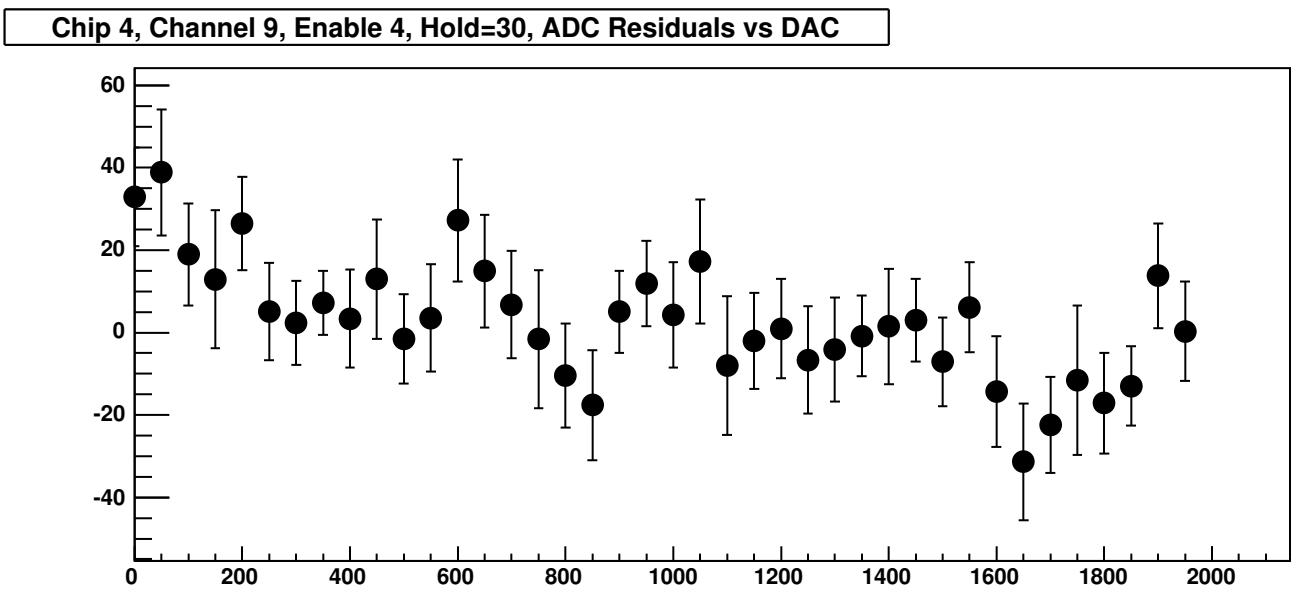


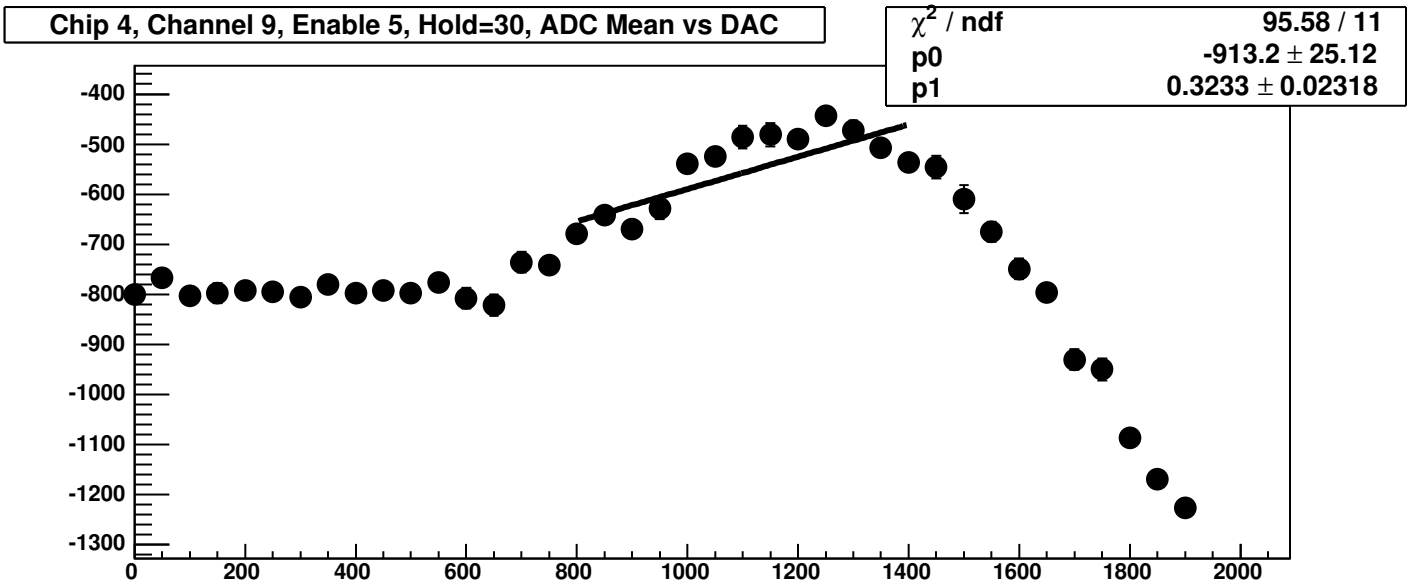
Chip 4, Channel 9, Enable 3, Hold=30, ADC Residuals vs DAC

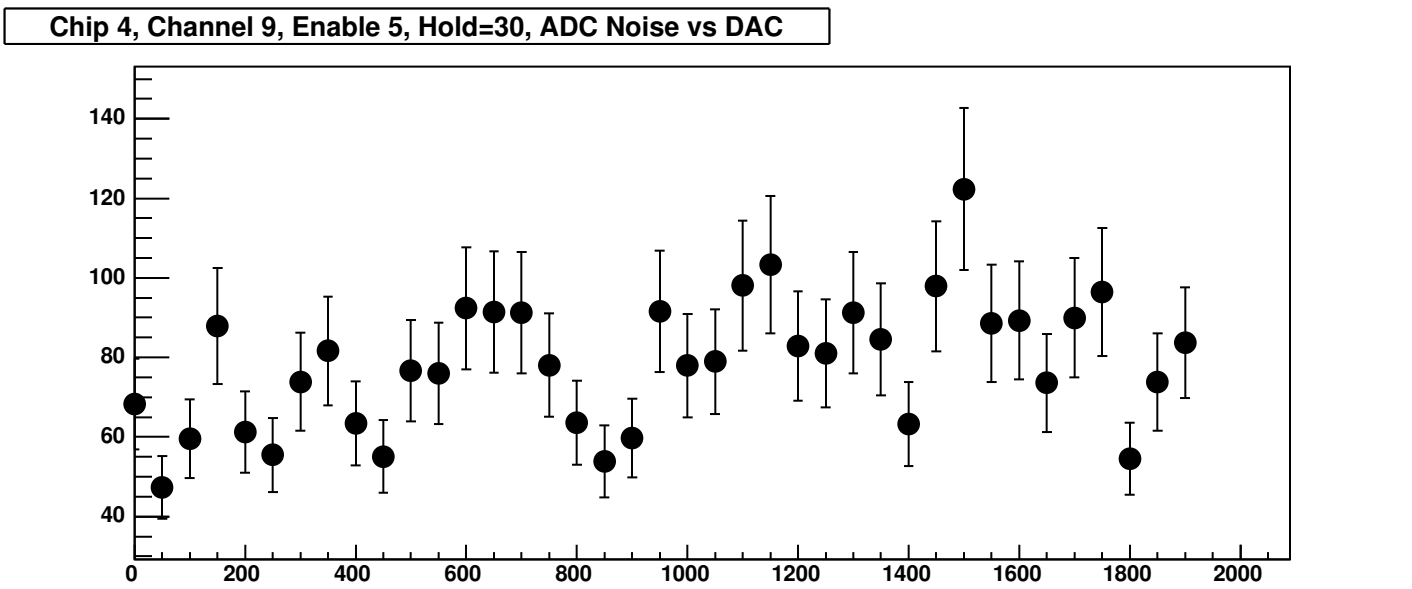


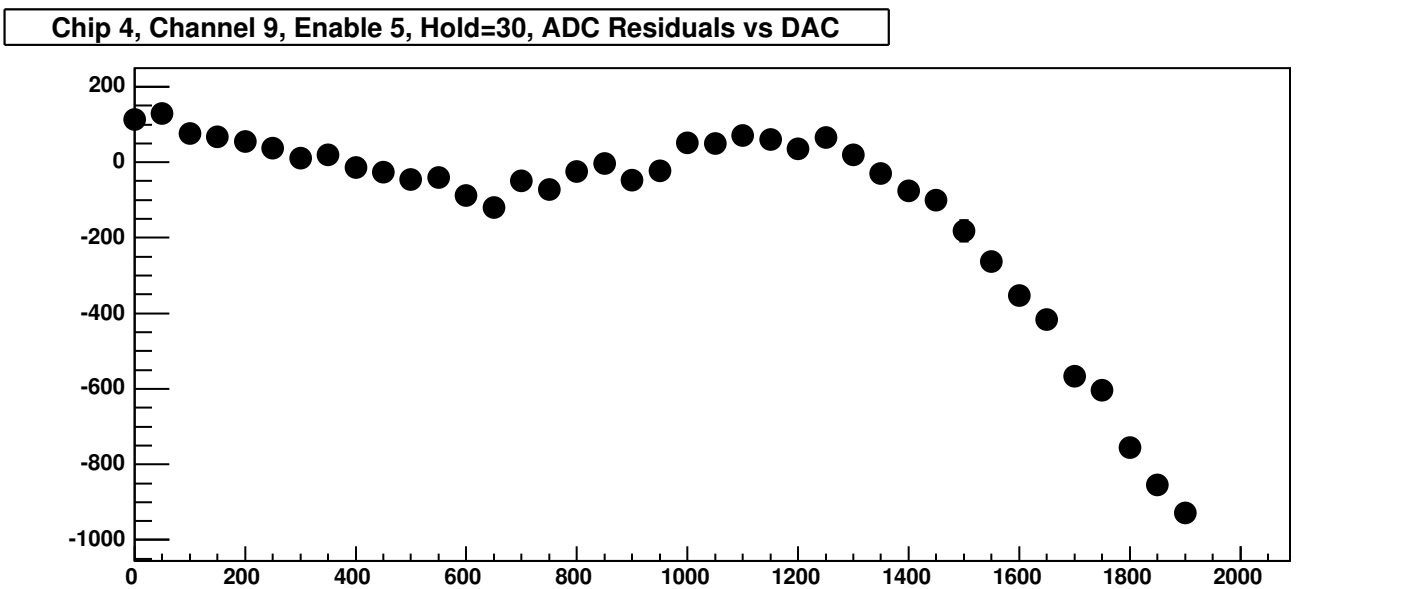

Chip 4, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6.09 / 11
p0 -1111 ± 19.78
p1 0.0341 ± 0.01775
-1020
-1040
-1060
-1080
-1100
-1120
0
200
400
600
800
1000
1200
1400
1600
1800
2000

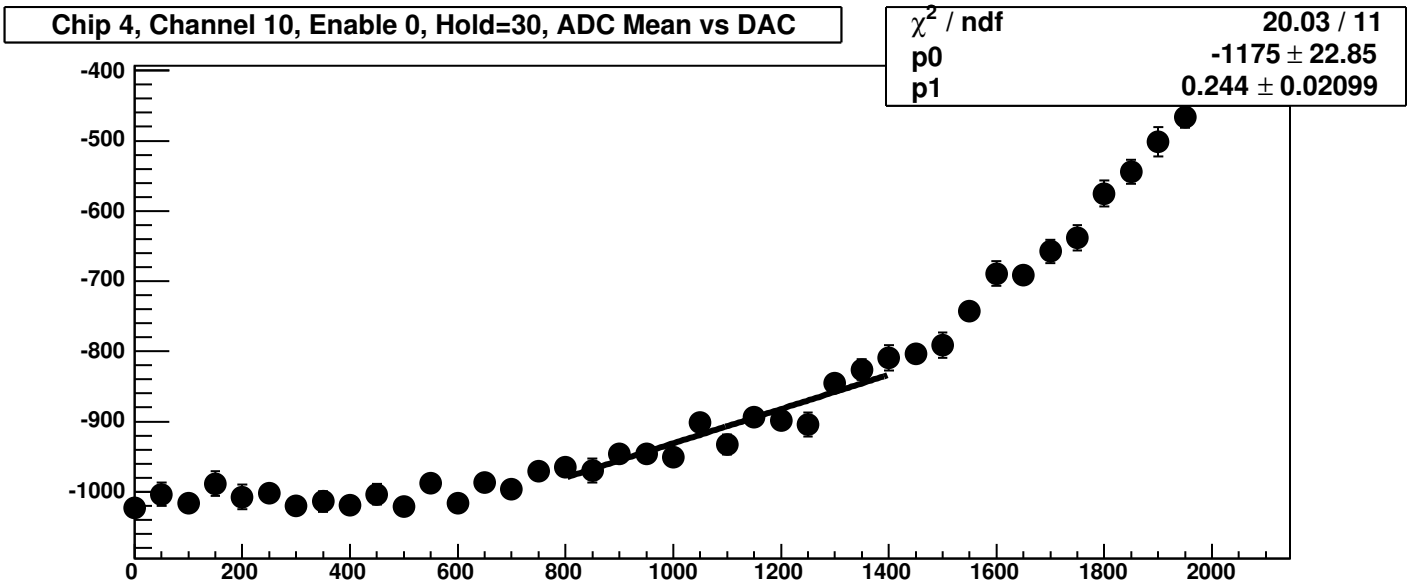

Chip 4, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

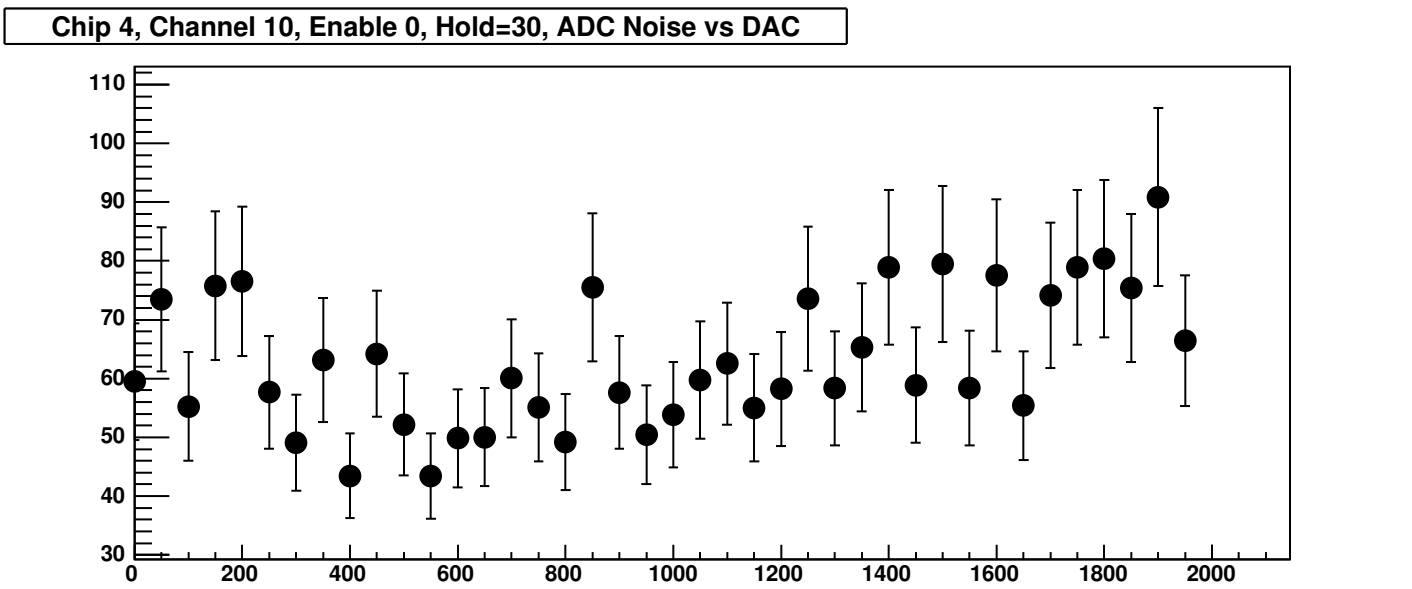

Chip 4, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

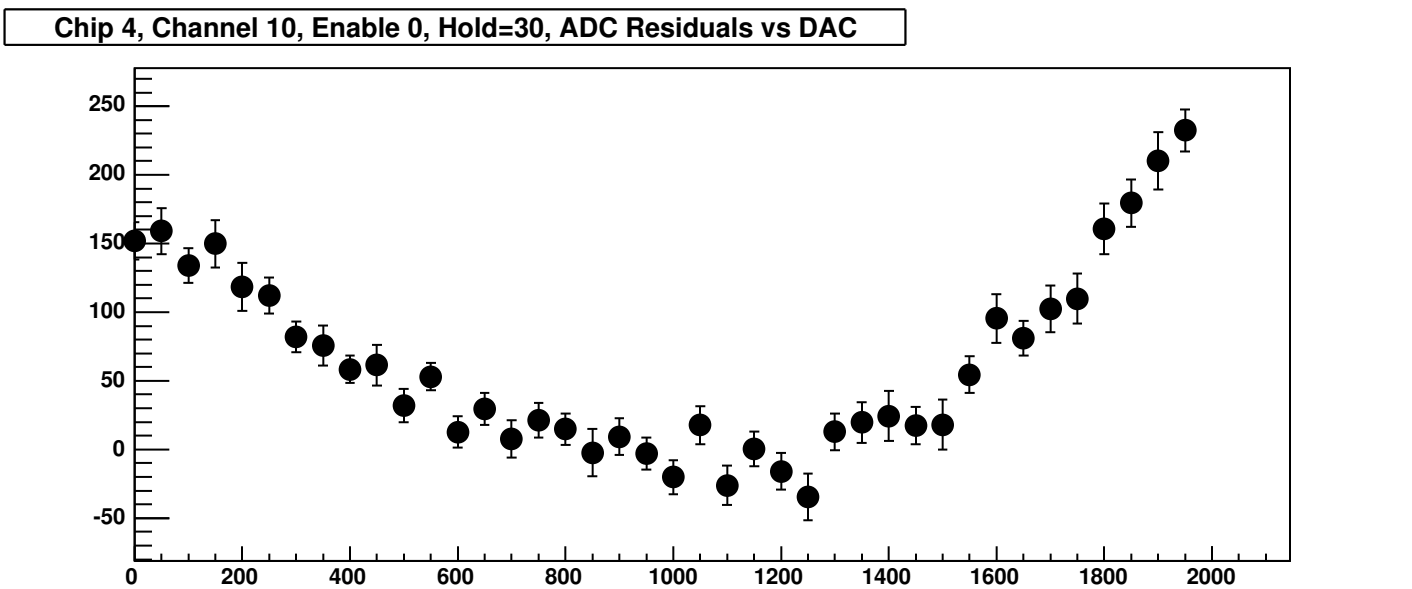

Chip 4, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 95.58 / 11
p0 -913.2 ± 25.12
p1 0.3233 ± 0.02318
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

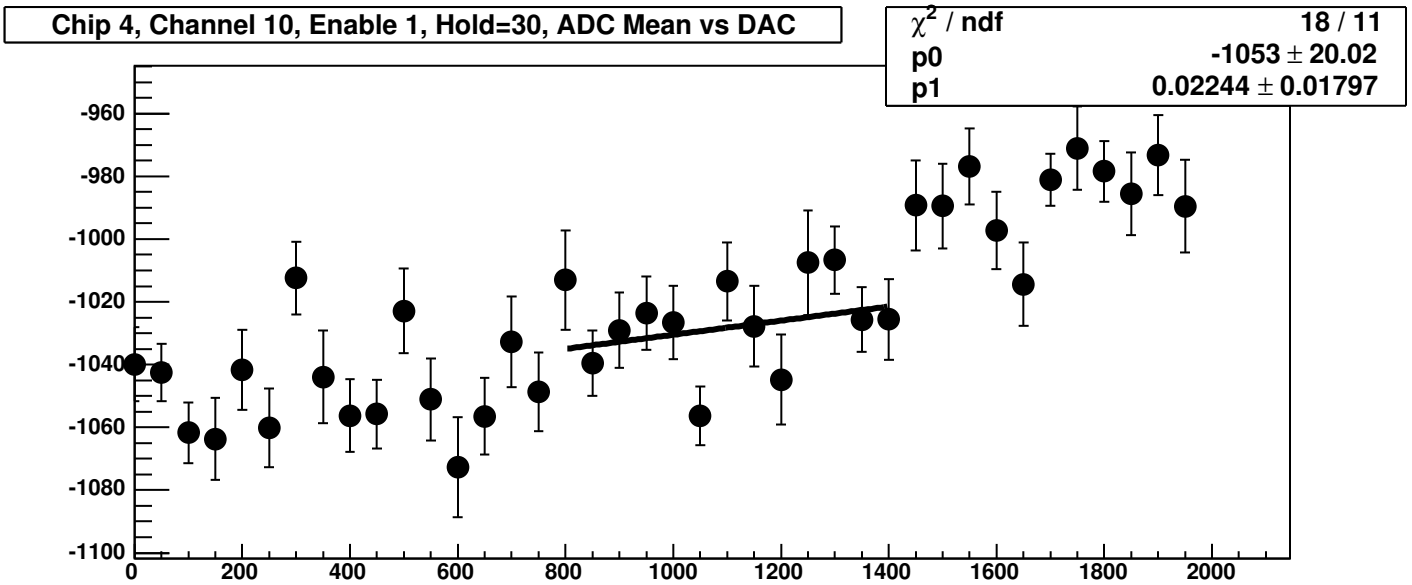

Chip 4, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

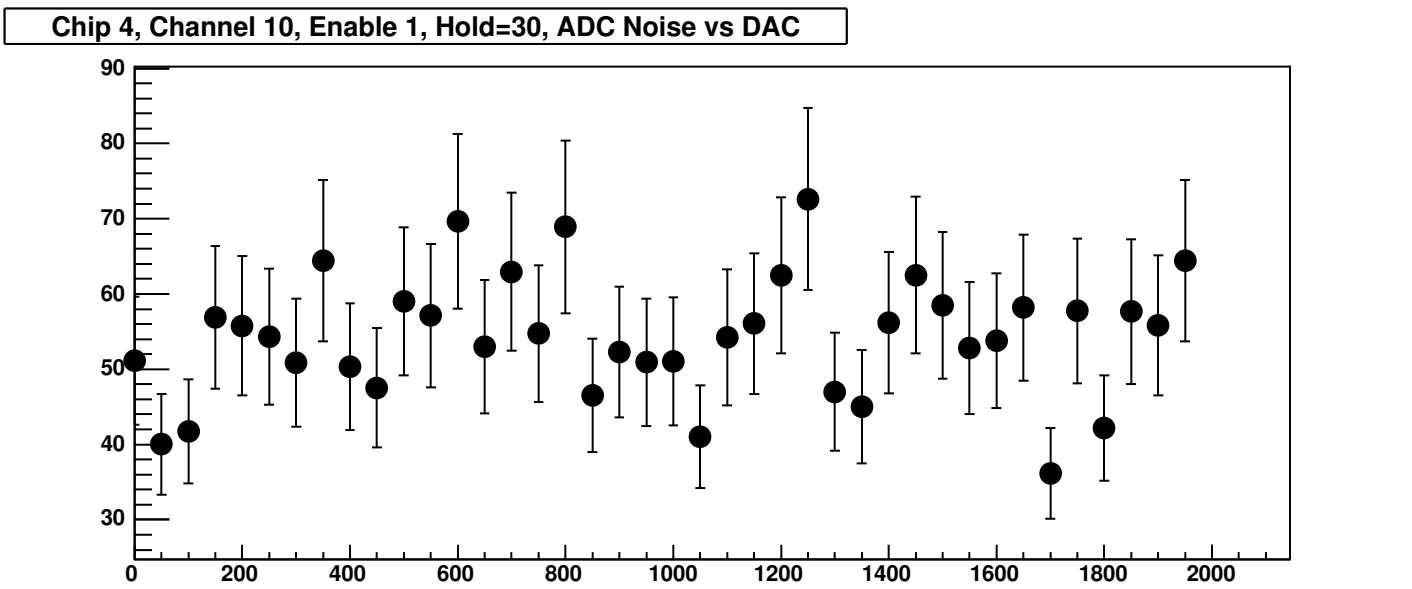

Chip 4, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

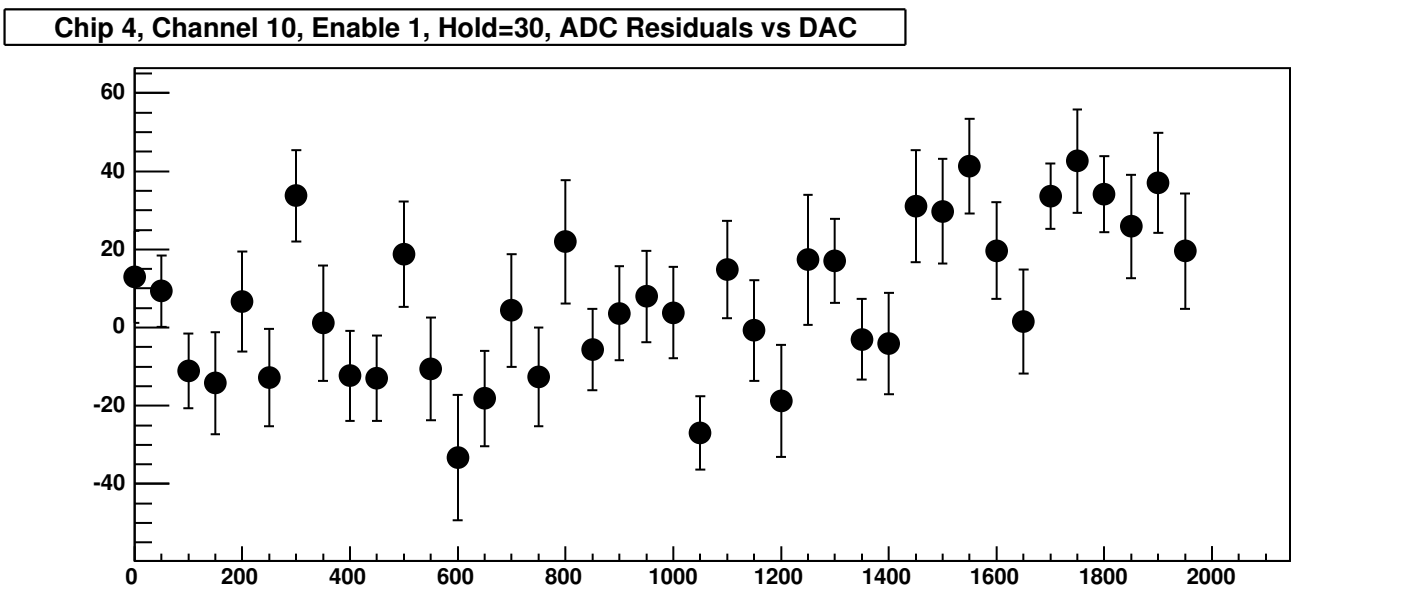

Chip 4, Channel 10, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 20.03 / 11
p0 -1175 ± 22.85
p1 0.244 ± 0.02099
-400
-500
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

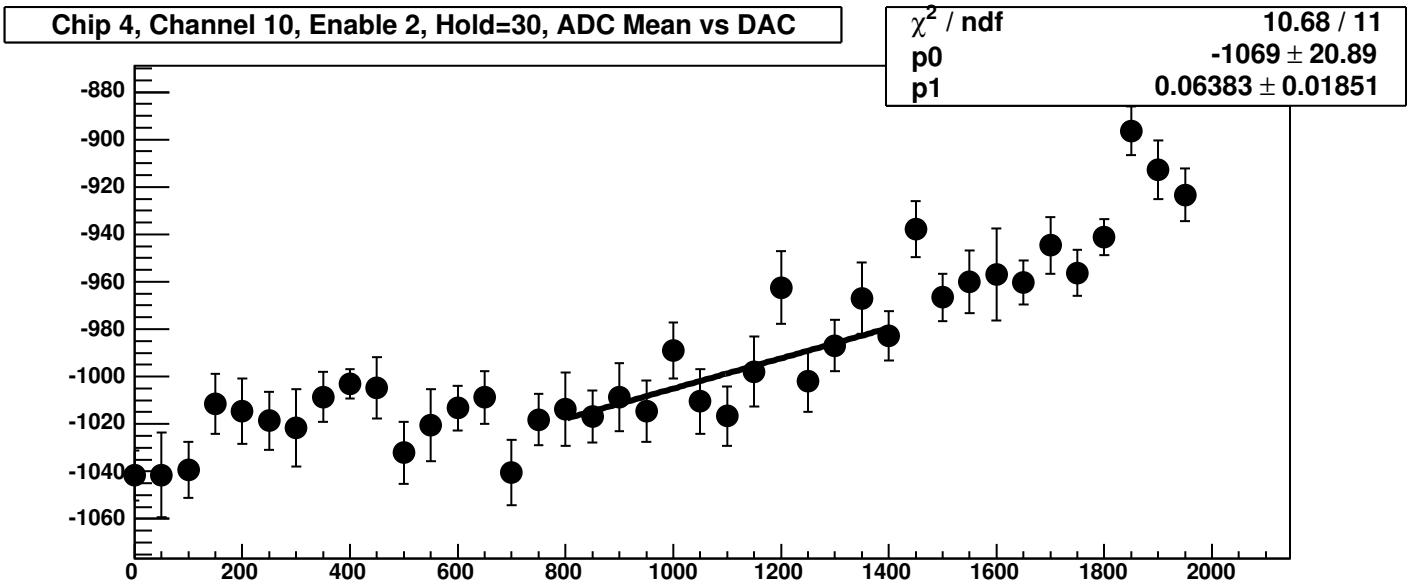

Chip 4, Channel 10, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

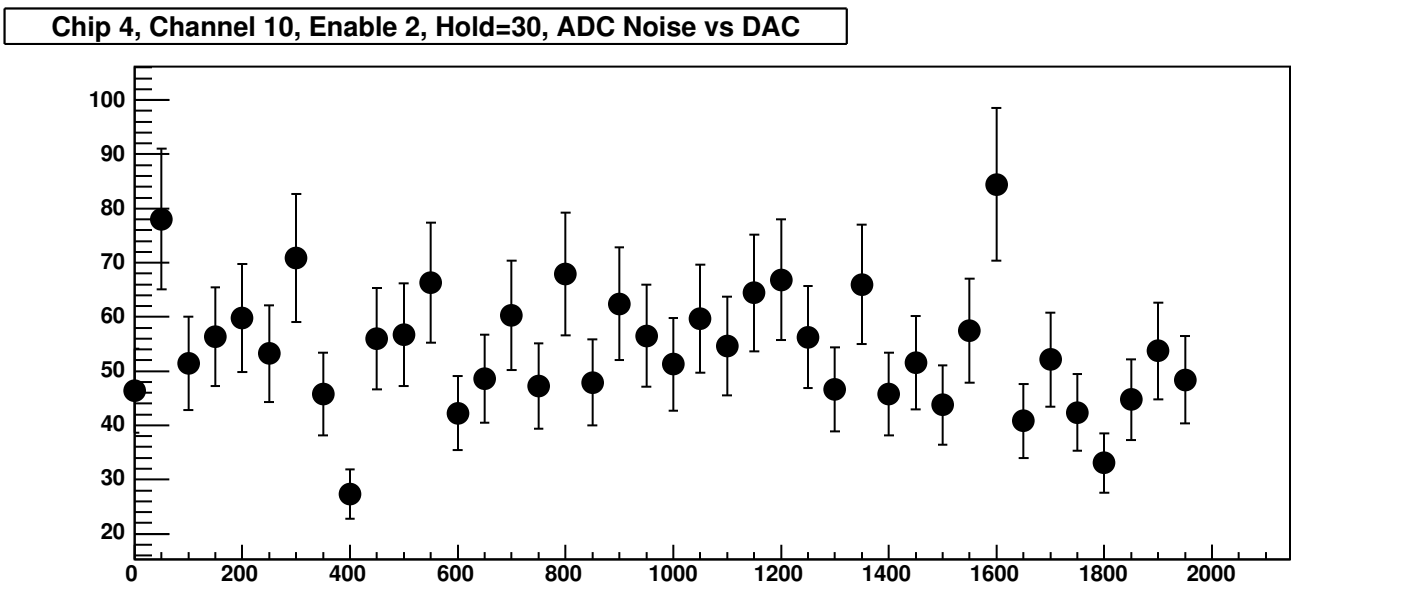

Chip 4, Channel 10, Enable 0, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

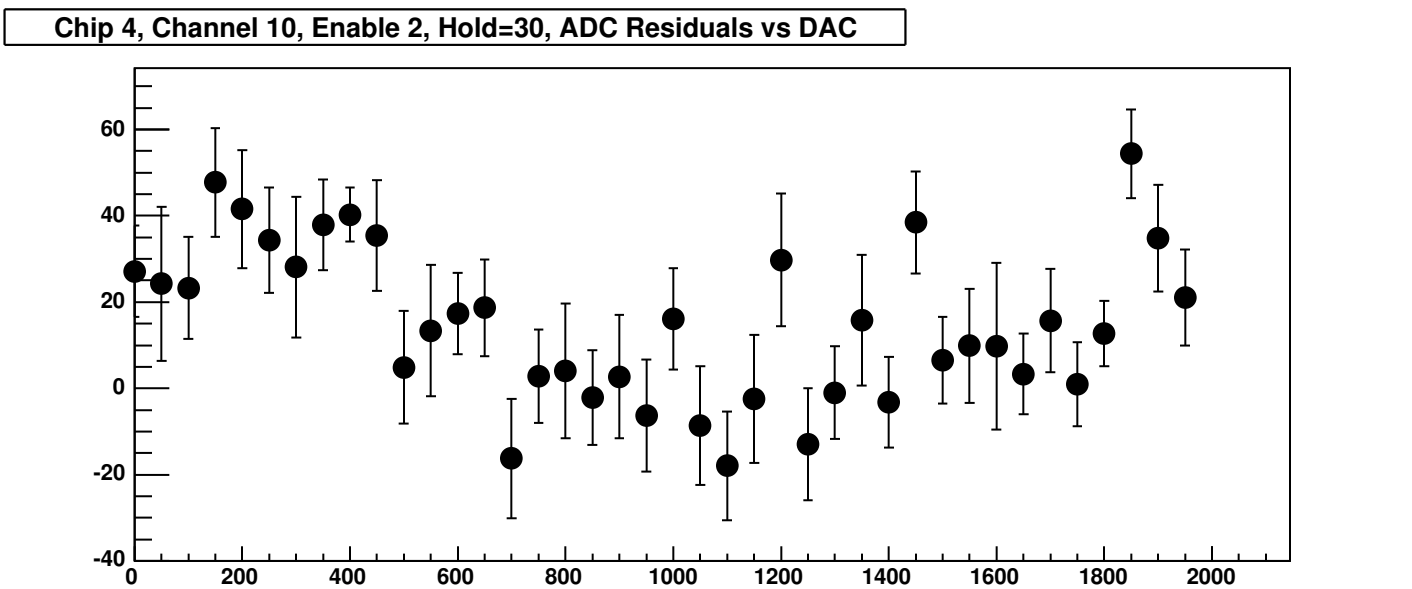

Chip 4, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 18 / 11
p0 -1053 ± 20.02
p1 0.02244 ± 0.01797
-960
-980
-1000
-1020
-1040
-1060
-1080
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

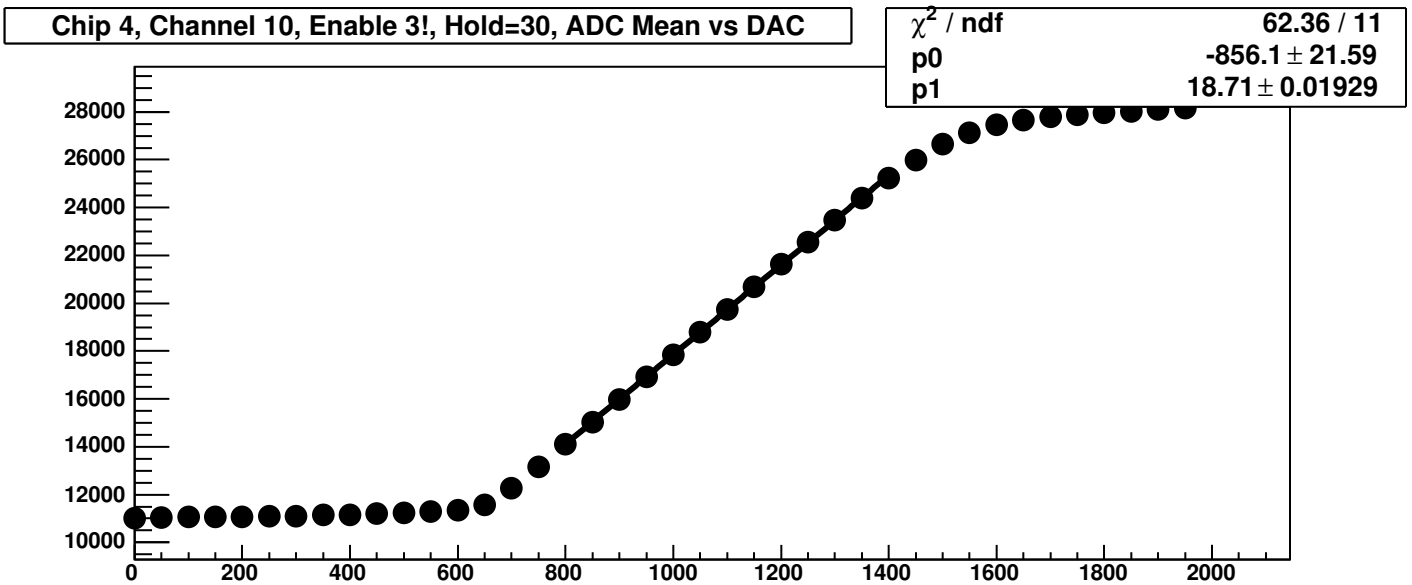

Chip 4, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

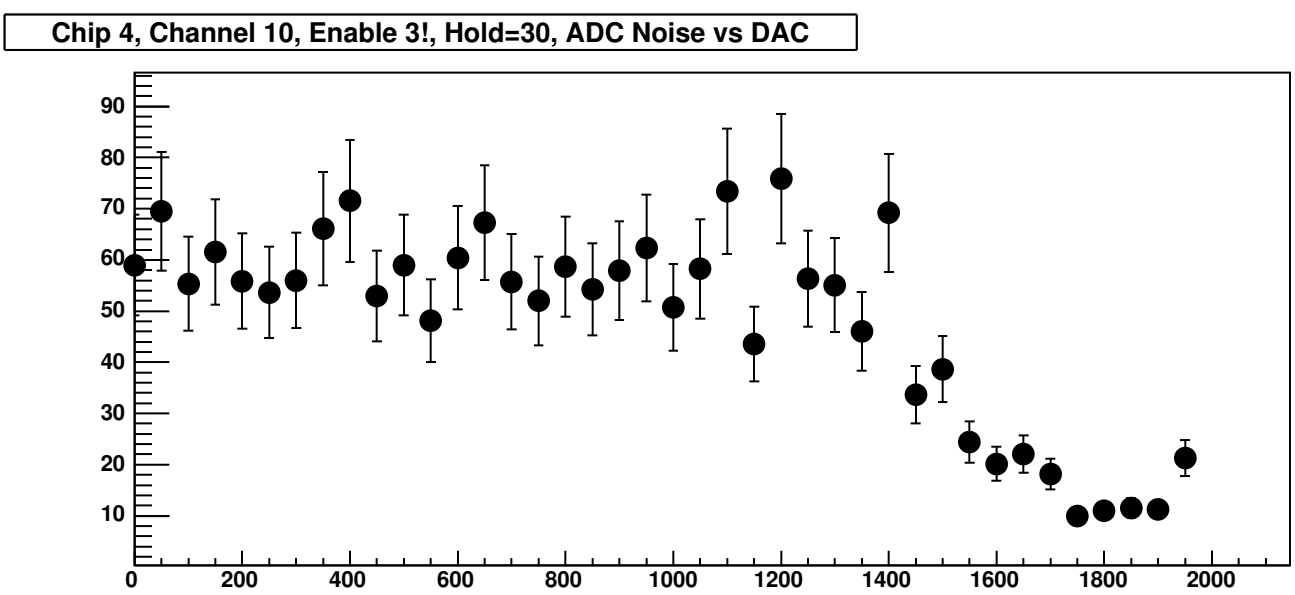

Chip 4, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

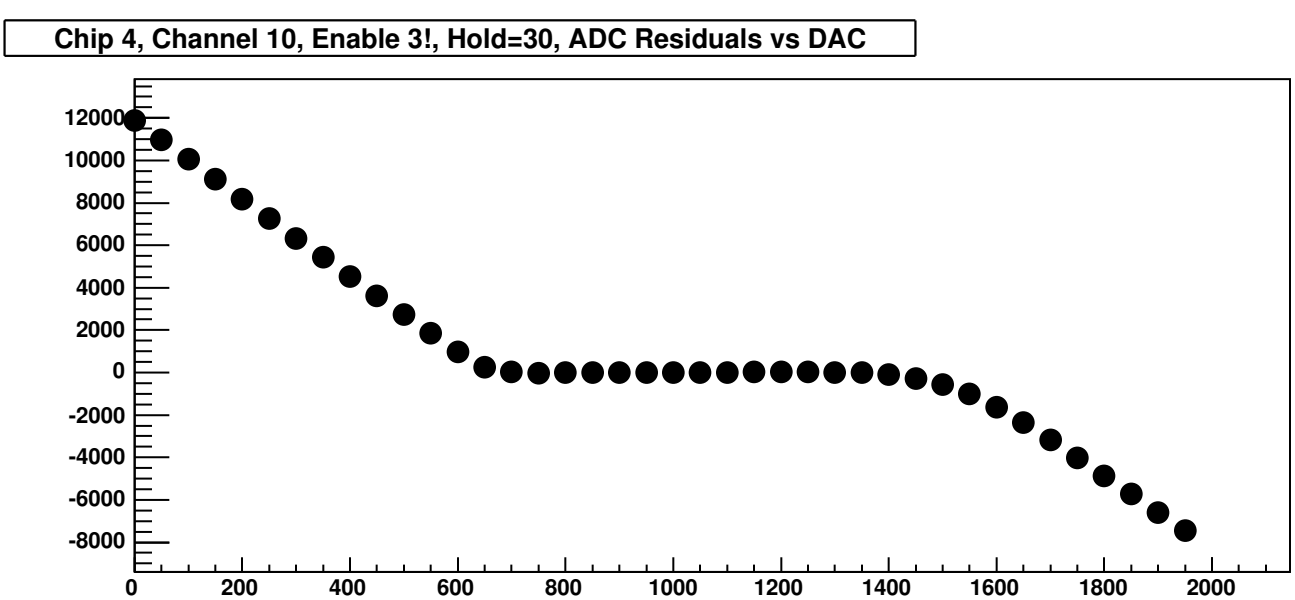

Chip 4, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 10.68 / 11
p0 -1069 ± 20.89
p1 0.06383 ± 0.01851
-880
-900
-920
-940
-960
-980
-1000
-1020
-1040
-1060
0
200
400
600
800
1000
1200
1400
1600
1800
2000

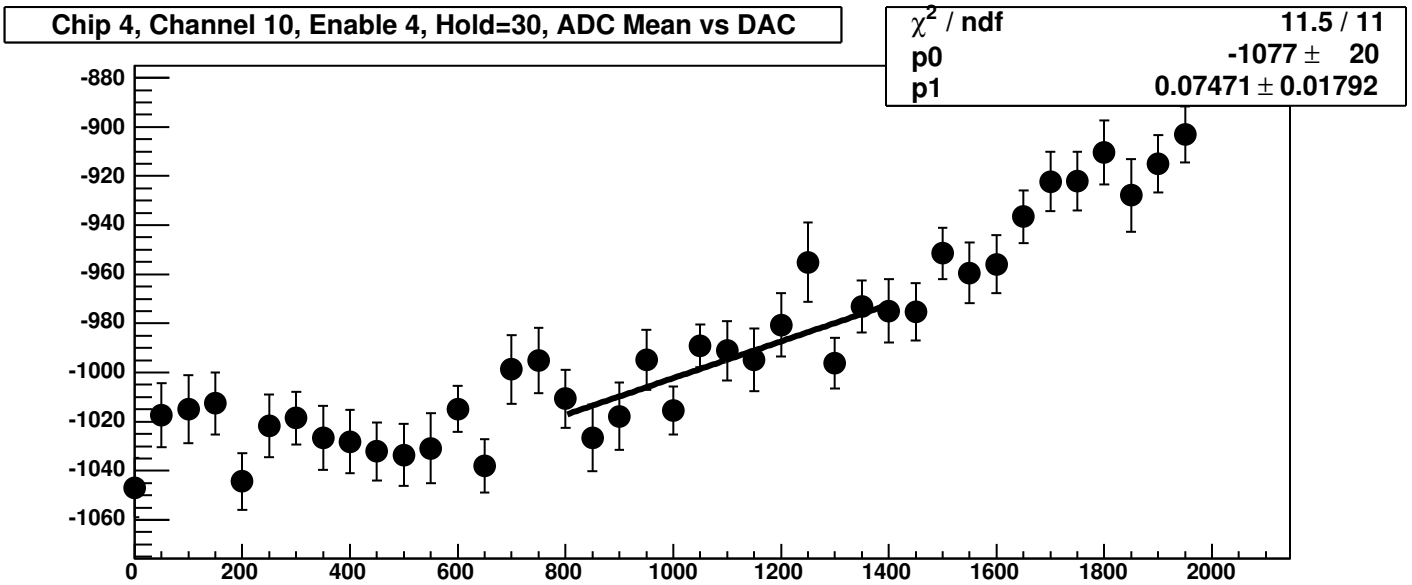

Chip 4, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

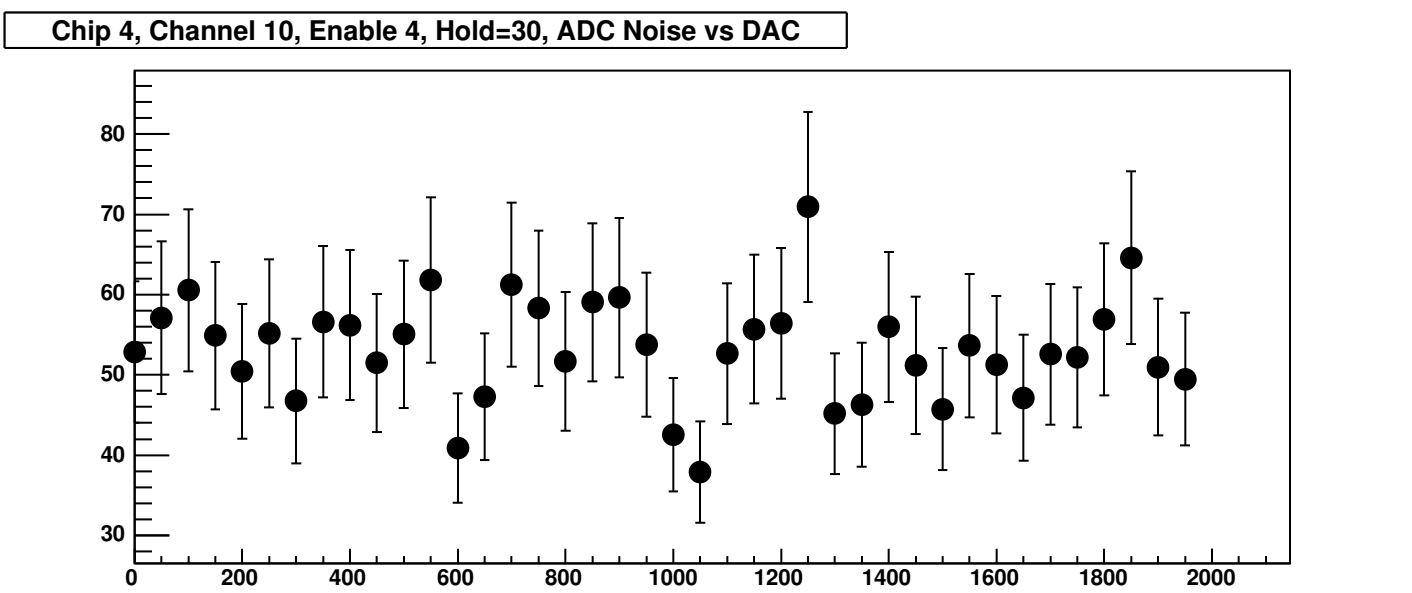

Chip 4, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

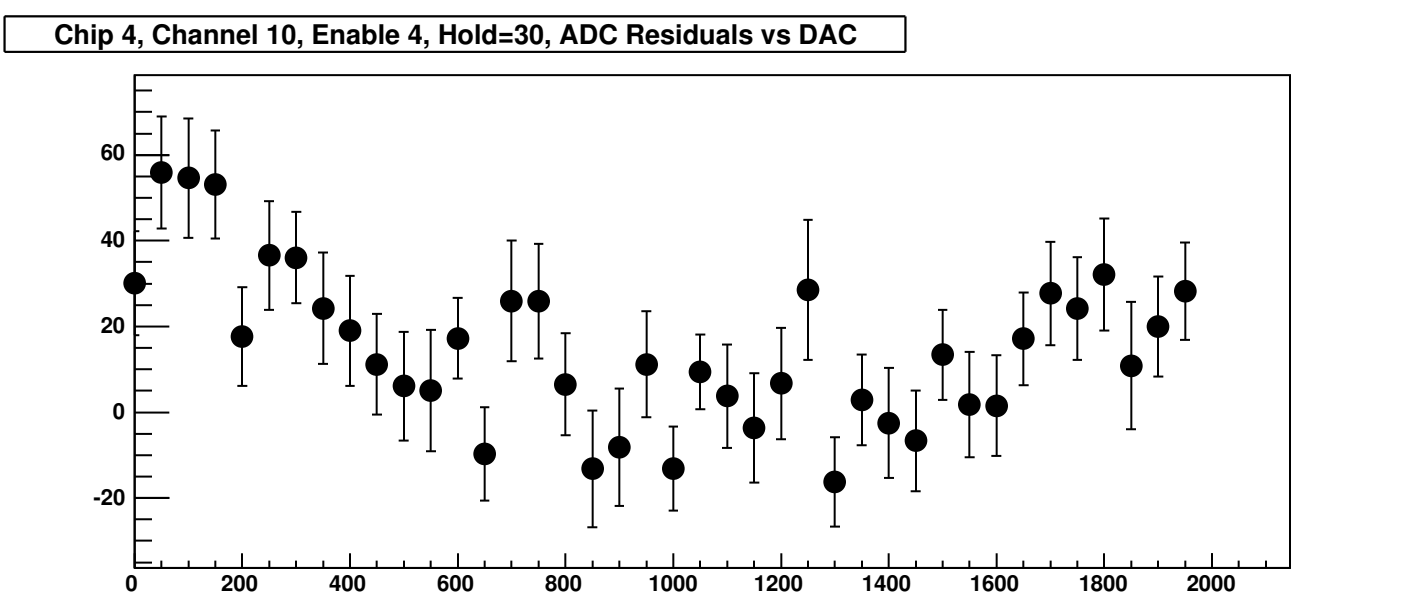

Chip 4, Channel 10, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 62.36 / 11
p0 -856.1 ± 21.59
p1 18.71 ± 0.01929
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 10, Enable 3!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

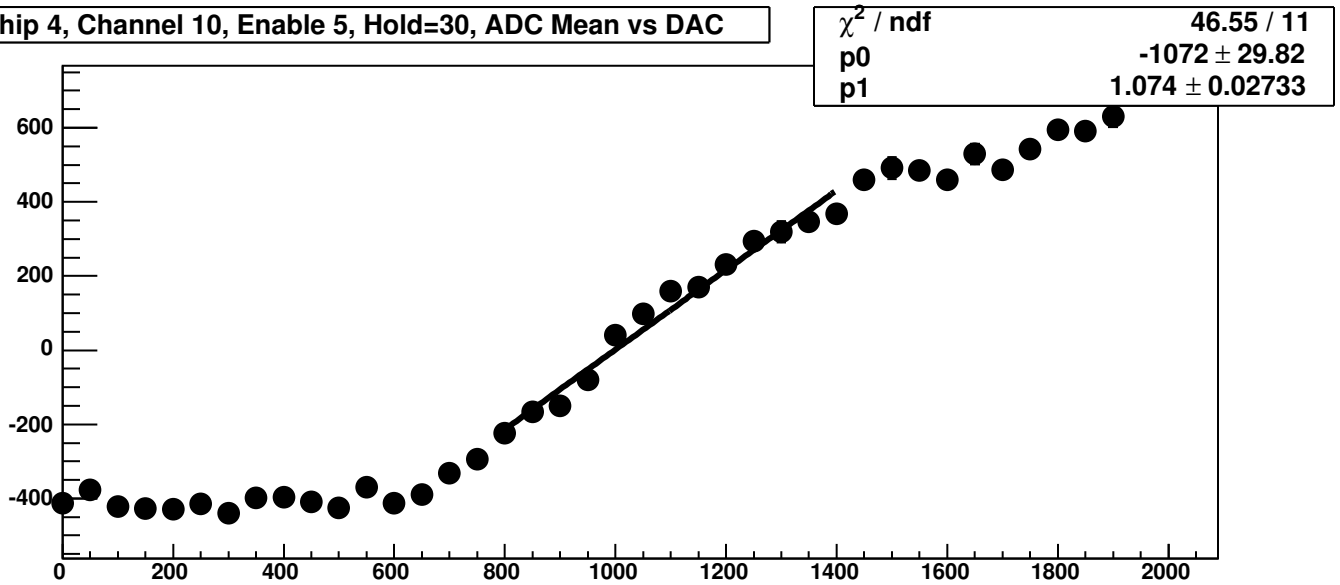

Chip 4, Channel 10, Enable 3!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 11.5 / 11
p0 -1077 ± 20
p1 0.07471 ± 0.01792
-880
-900
-920
-940
-960
-980
-1000
-1020
-1040
-1060
0
200
400
600
800
1000
1200
1400
1600
1800
2000

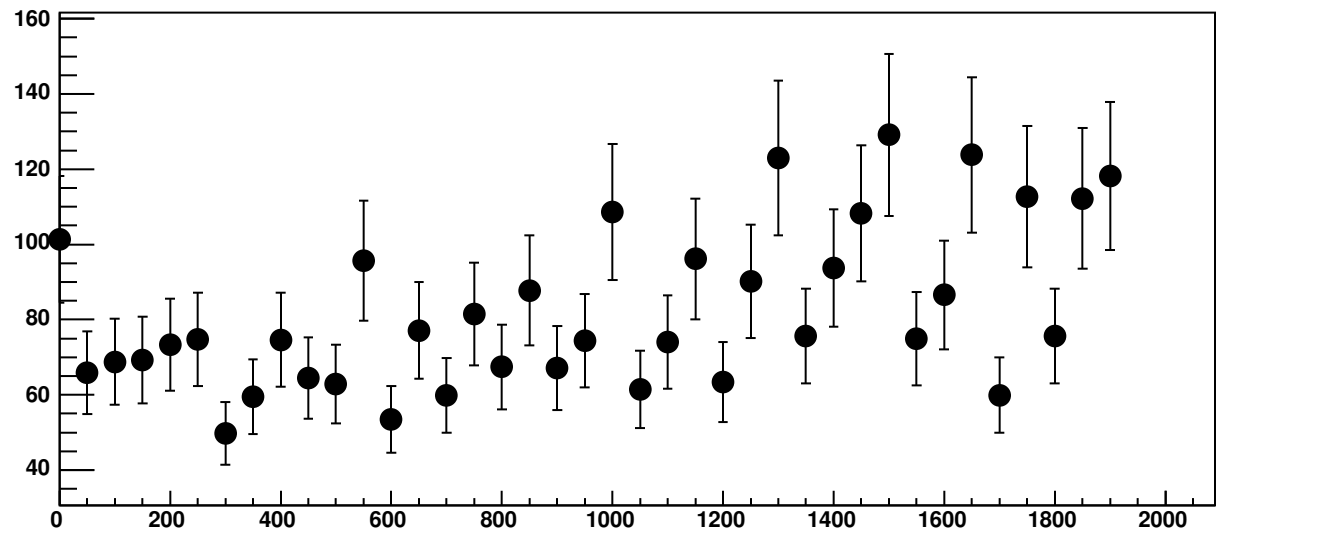

Chip 4, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

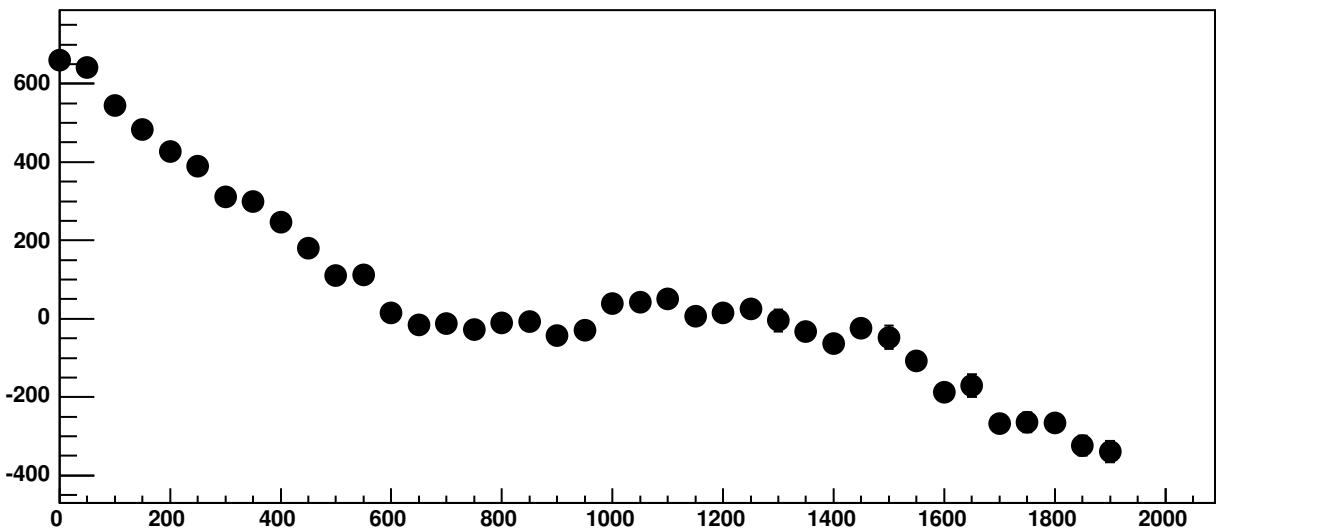
Chip 4, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC

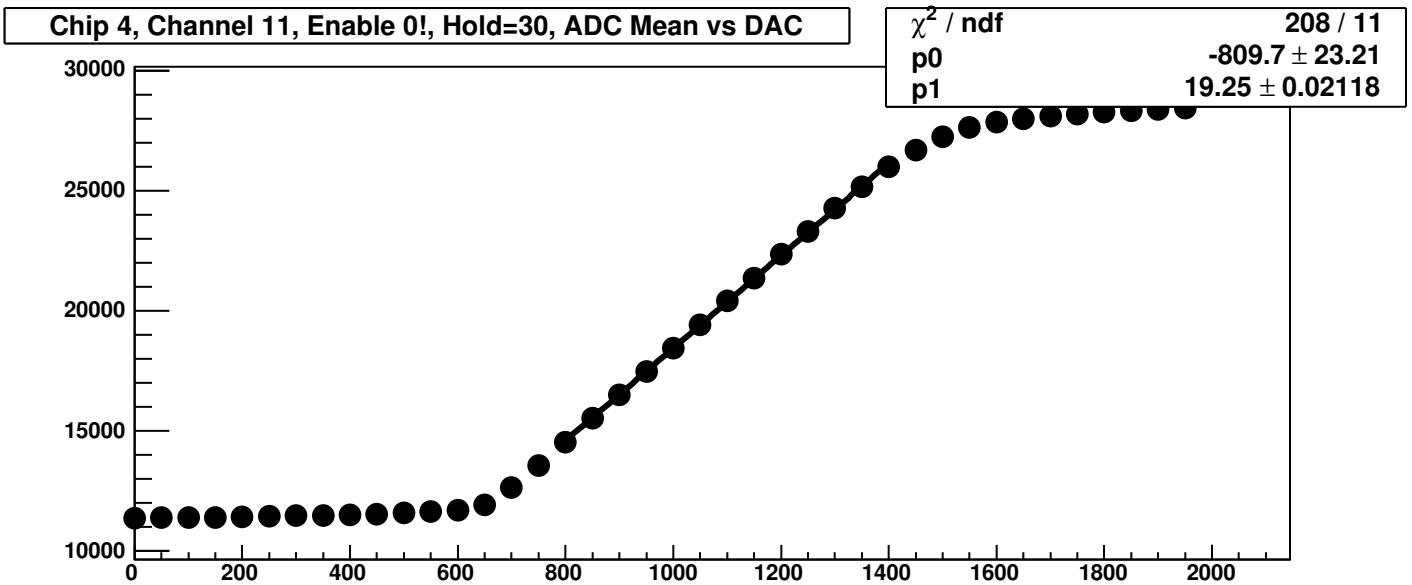


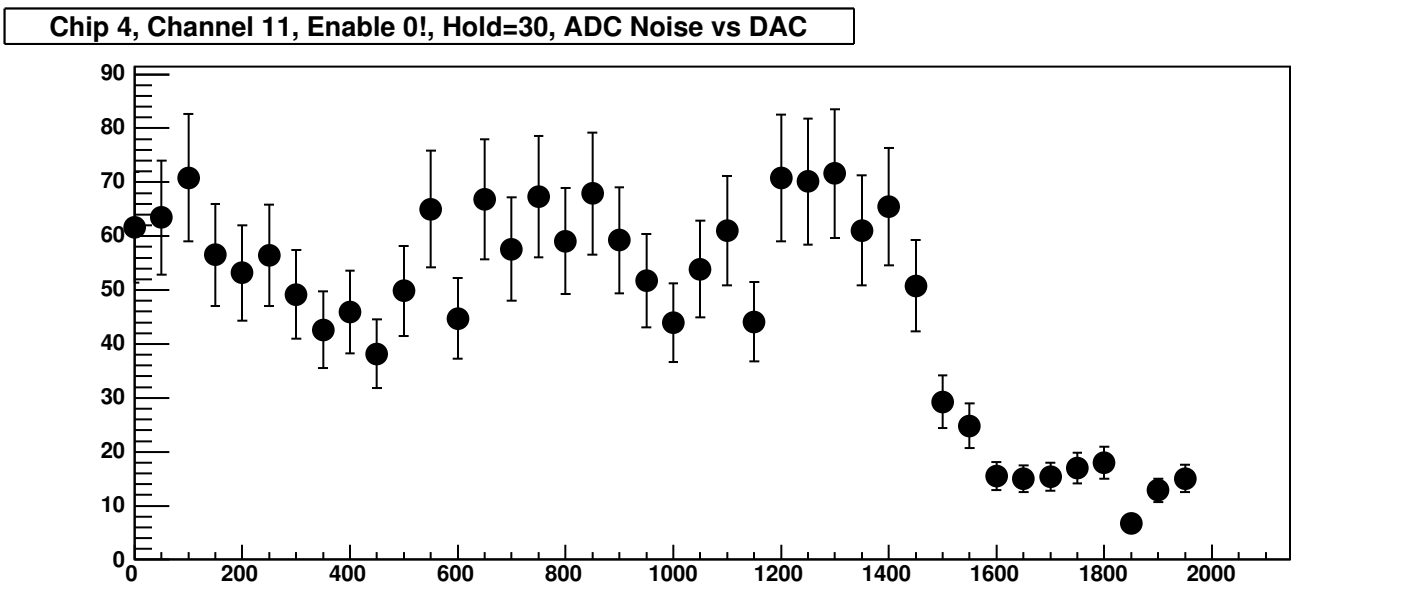
Chip 4, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC

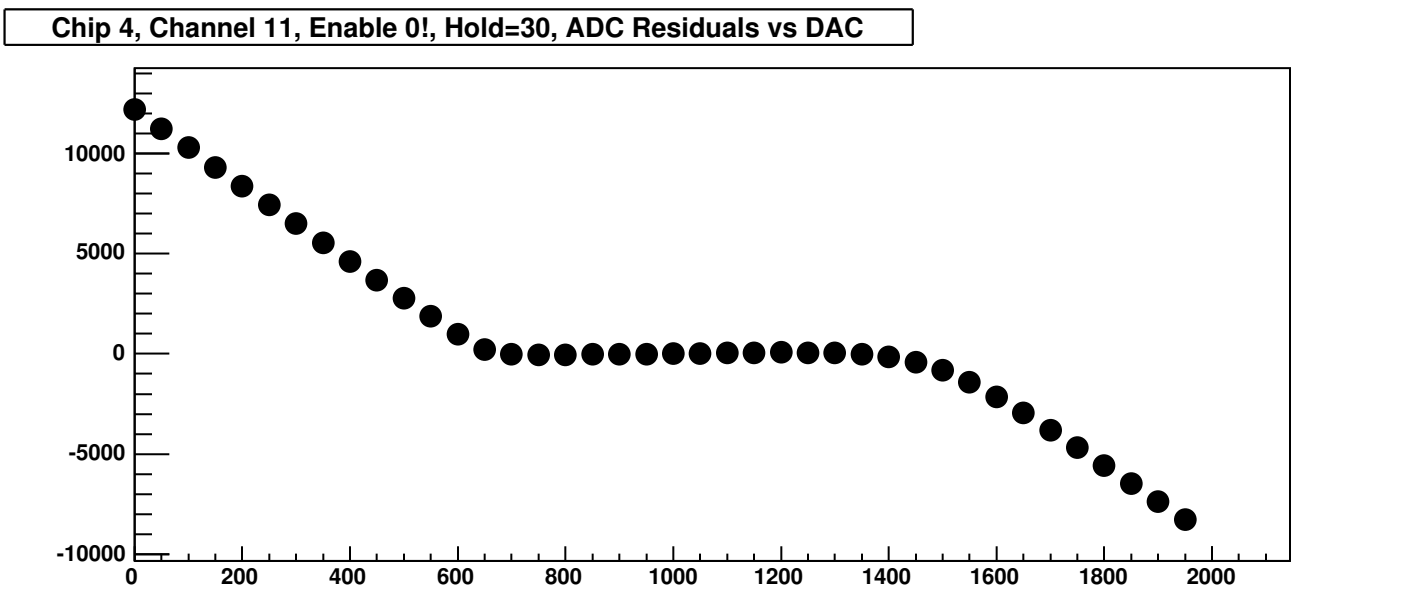


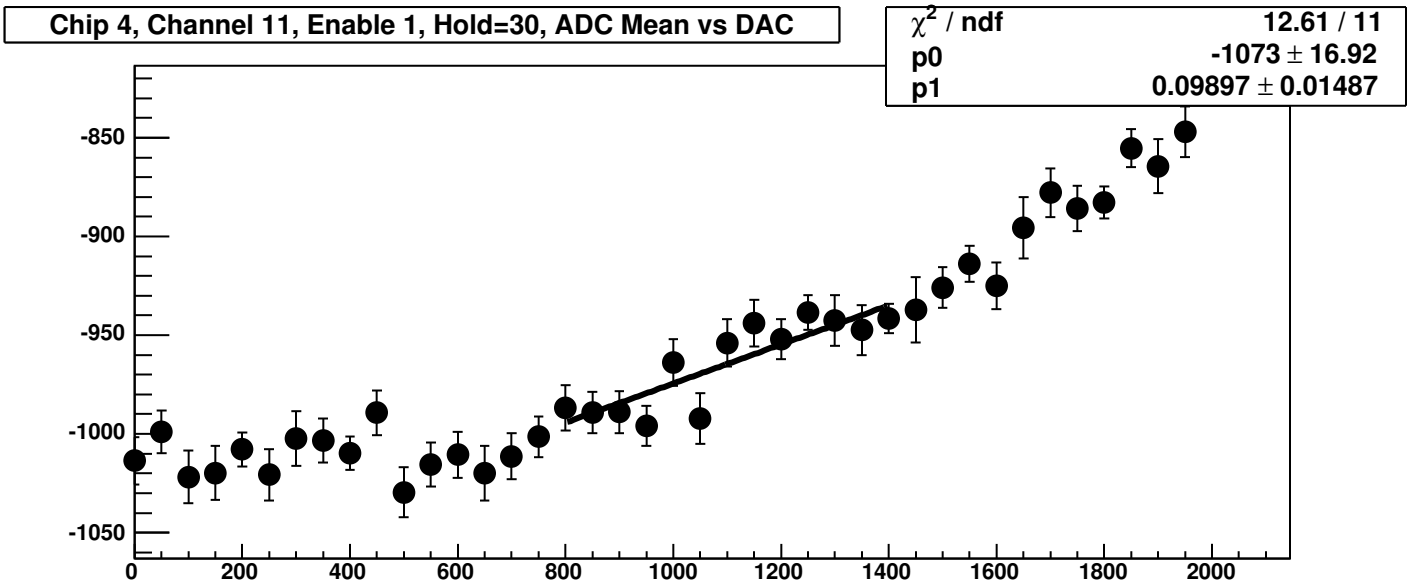
Chip 4, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC

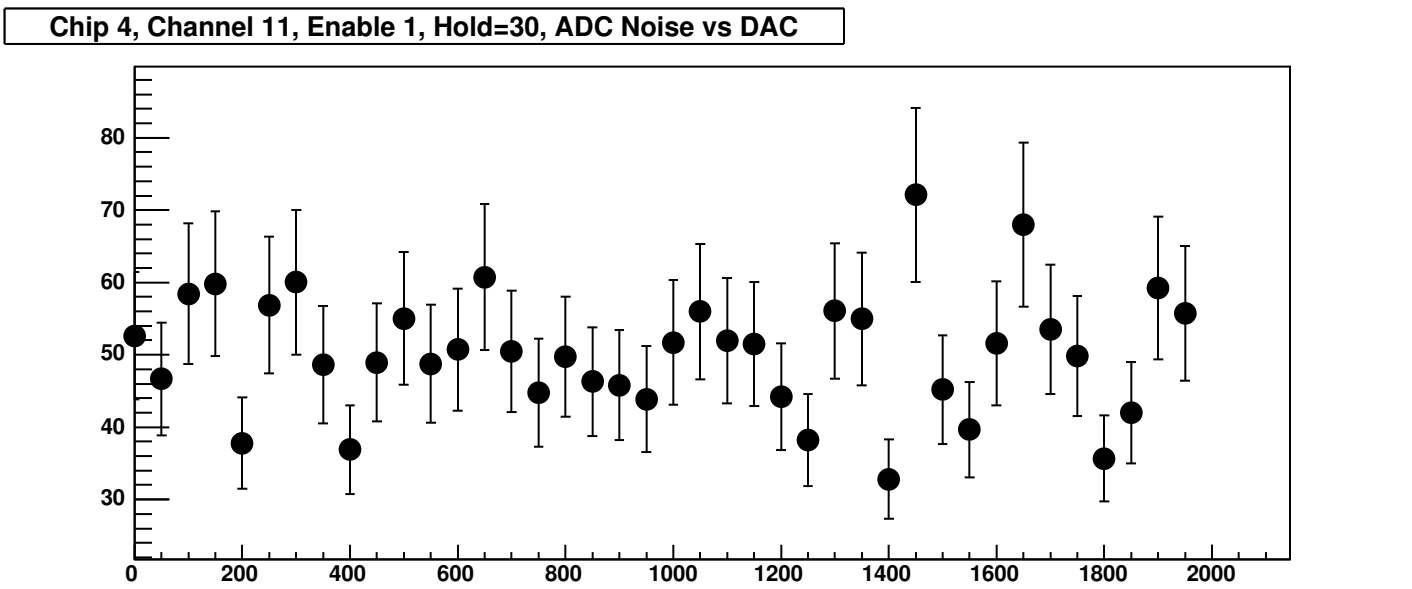


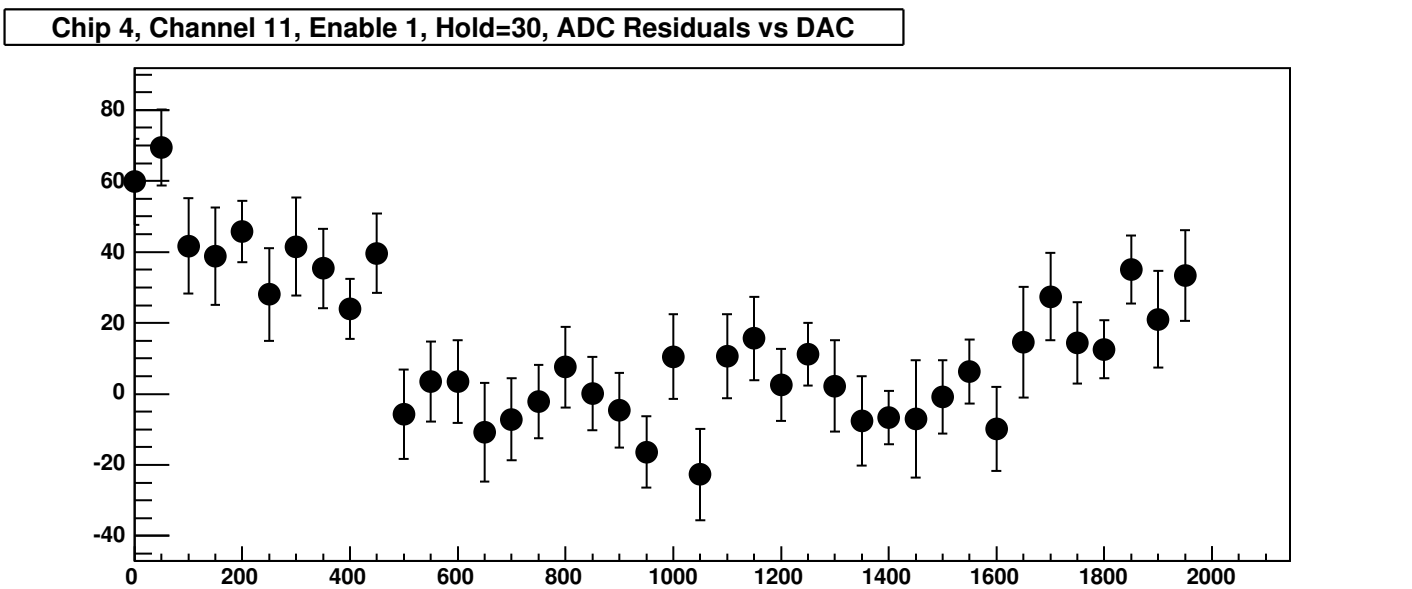

Chip 4, Channel 11, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 208 / 11
p0 -809.7 ± 23.21
p1 19.25 ± 0.02118
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

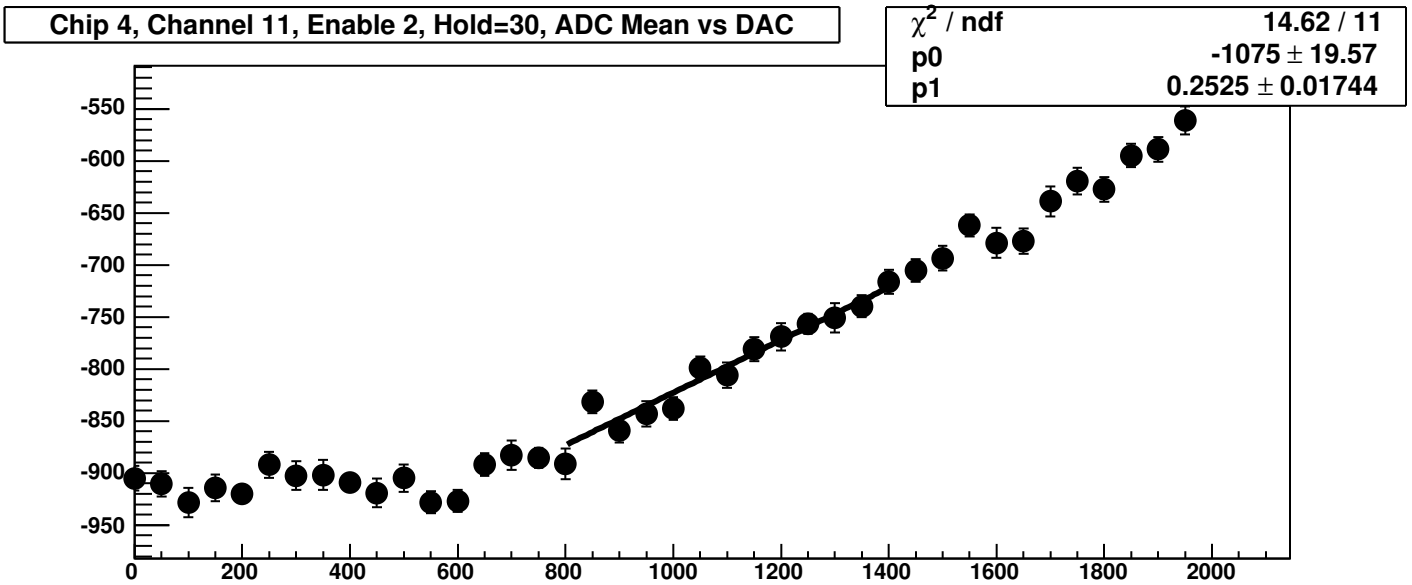

Chip 4, Channel 11, Enable 0!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

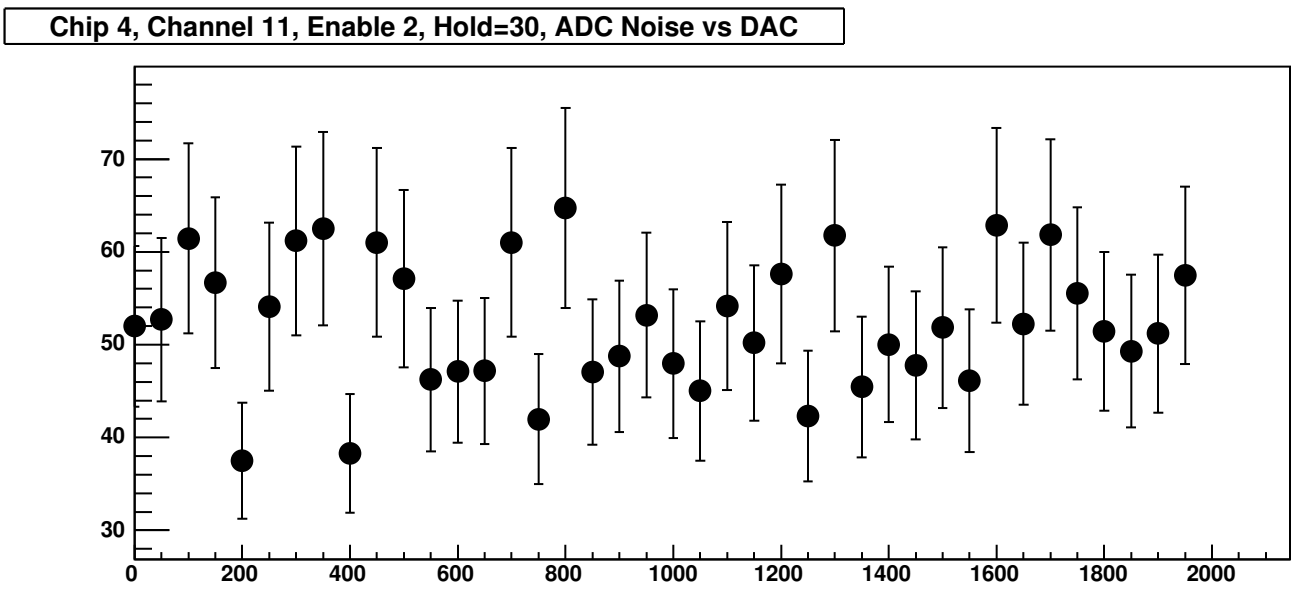

Chip 4, Channel 11, Enable 0!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

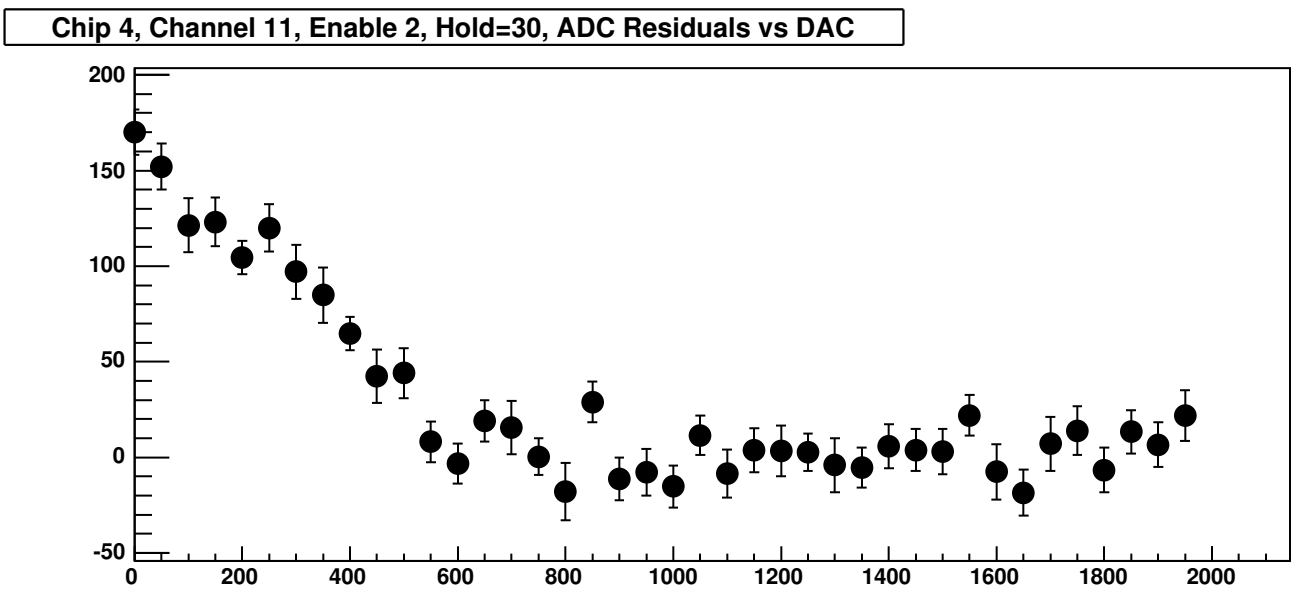

Chip 4, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 12.61 / 11
p0 -1073 ± 16.92
p1 0.09897 ± 0.01487
-850
-900
-950
-1000
-1050
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

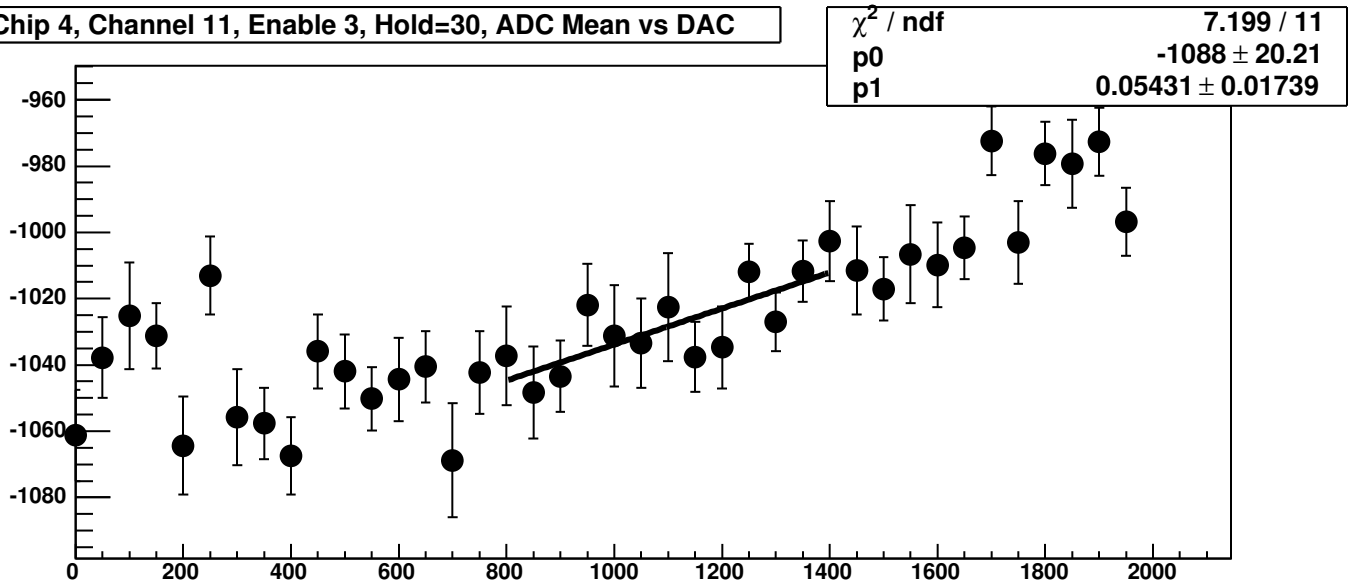

Chip 4, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 14.62 / 11
p0 -1075 ± 19.57
p1 0.2525 ± 0.01744
-550
-600
-650
-700
-750
-800
-850
-900
-950
0
200
400
600
800
1000
1200
1400
1600
1800
2000

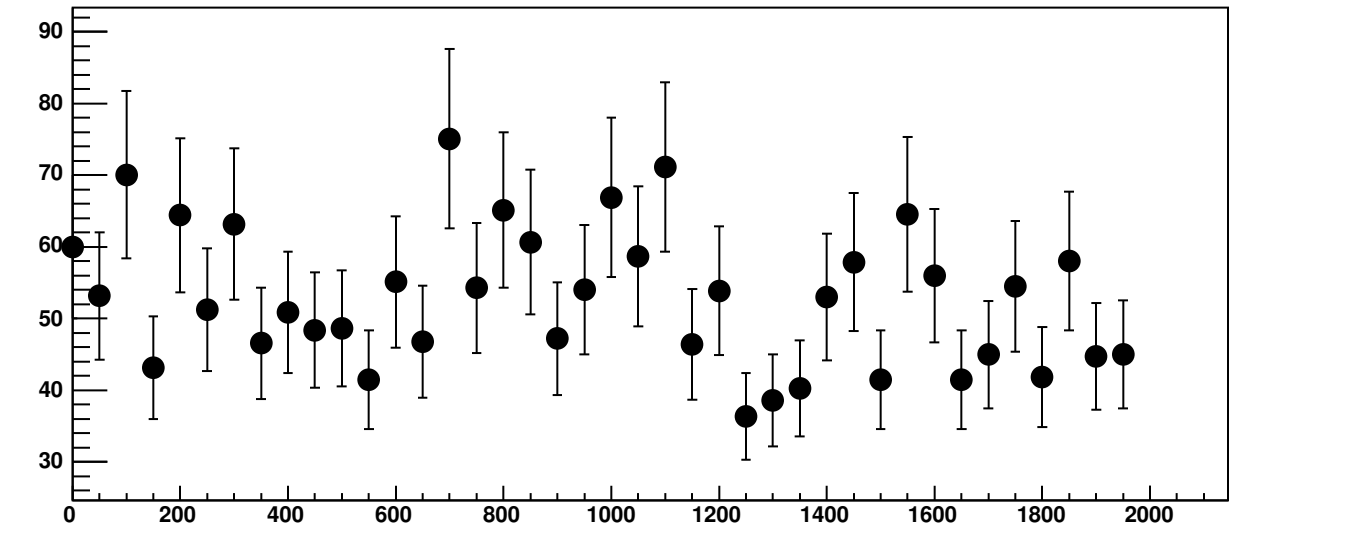

Chip 4, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

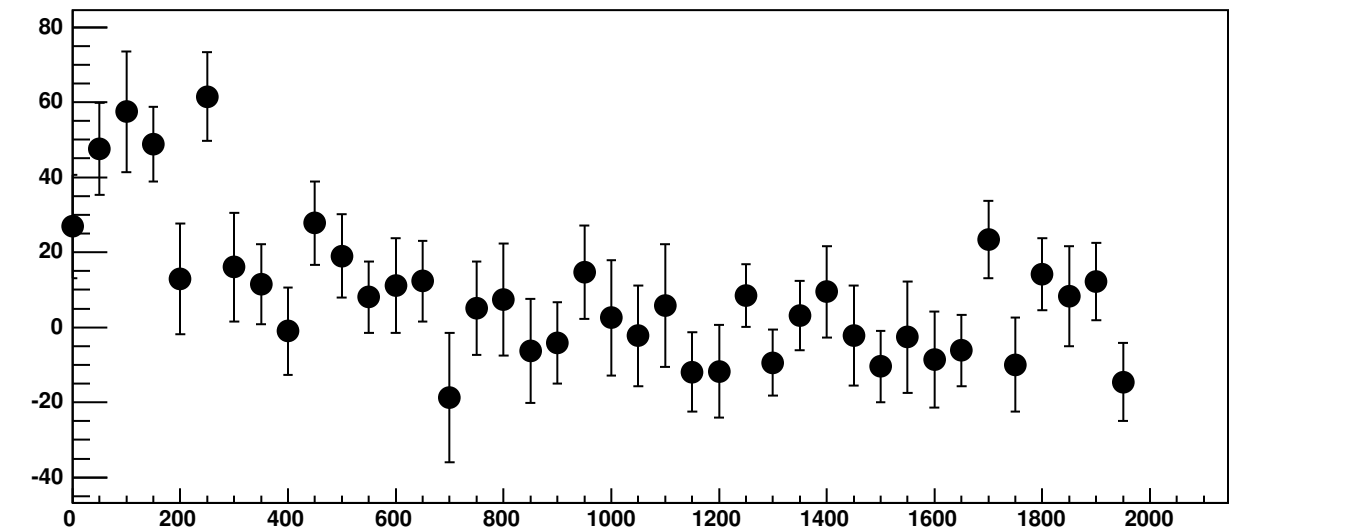
Chip 4, Channel 11, Enable 3, Hold=30, ADC Mean vs DAC



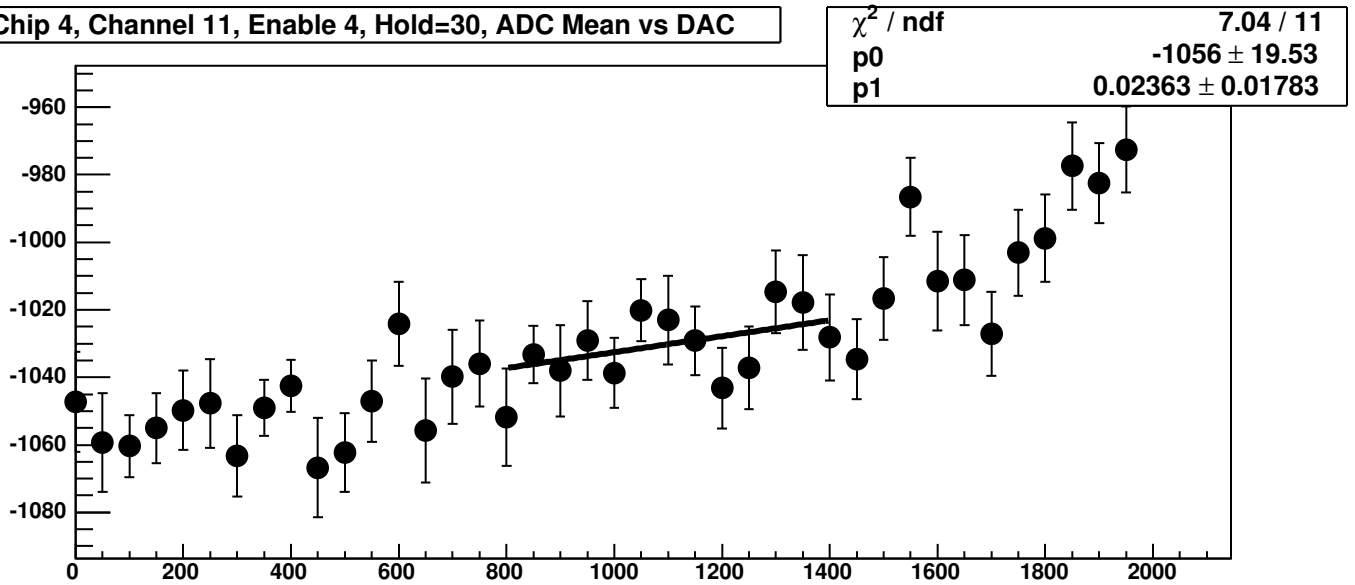
Chip 4, Channel 11, Enable 3, Hold=30, ADC Noise vs DAC



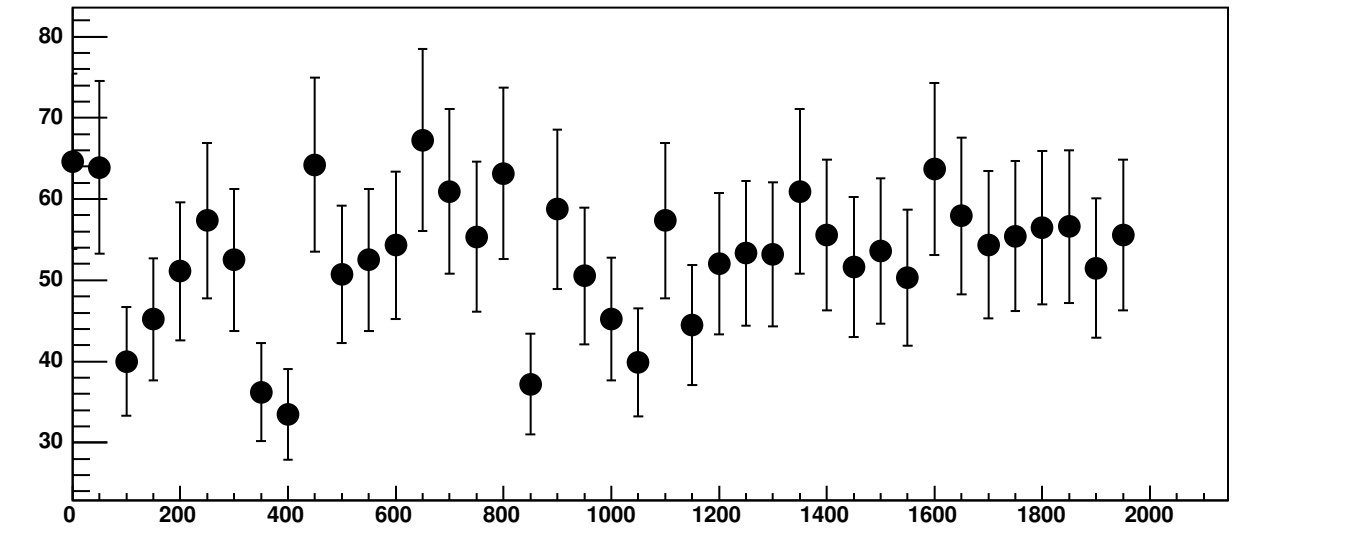
Chip 4, Channel 11, Enable 3, Hold=30, ADC Residuals vs DAC



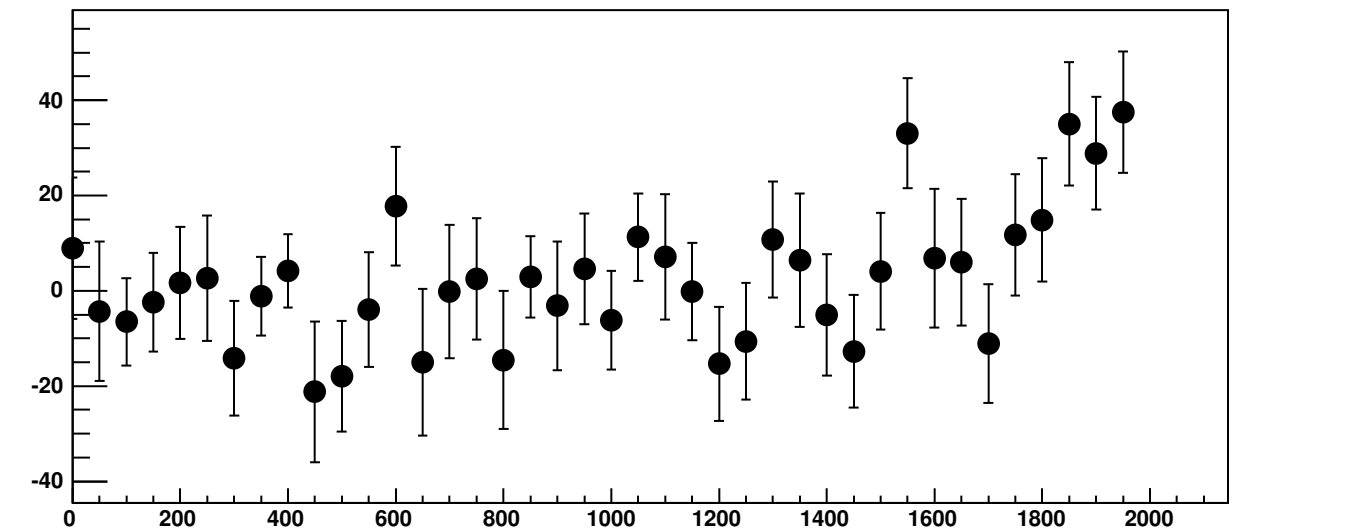
Chip 4, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC

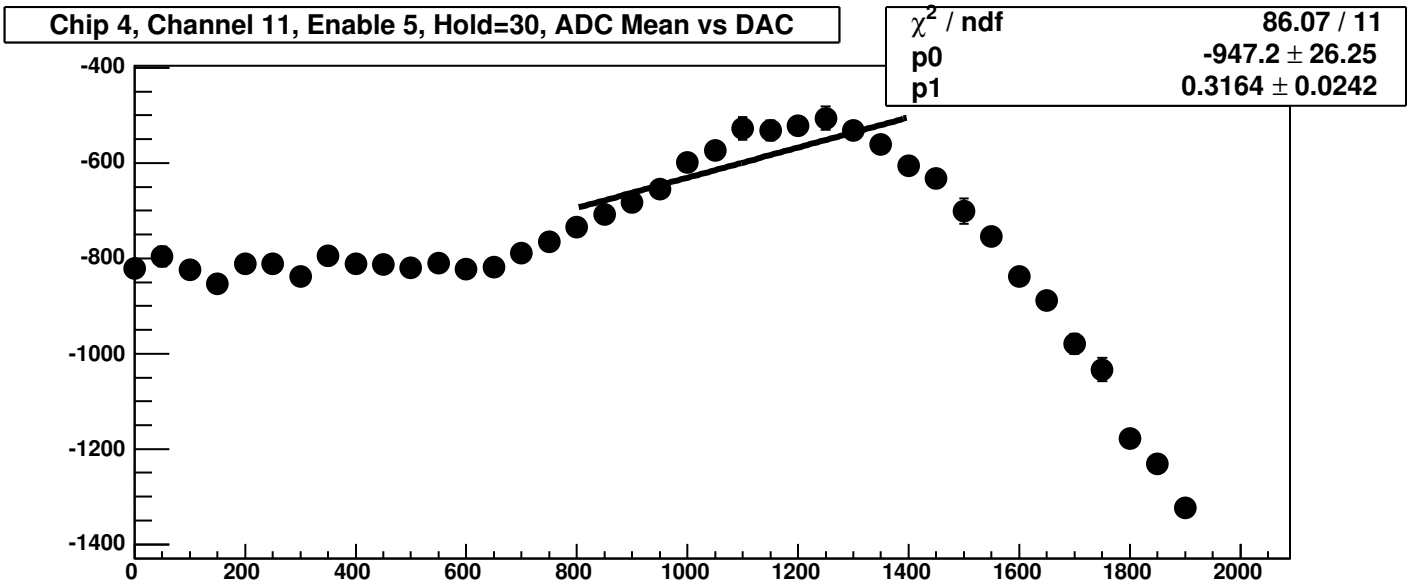


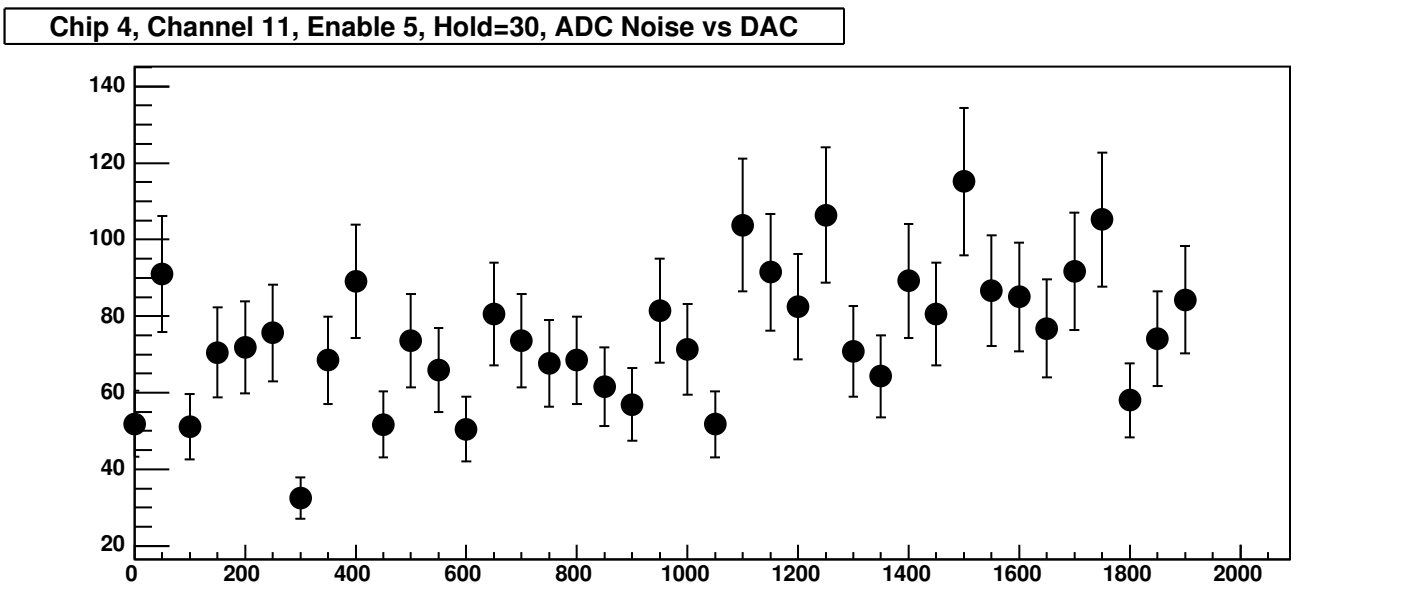
Chip 4, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC

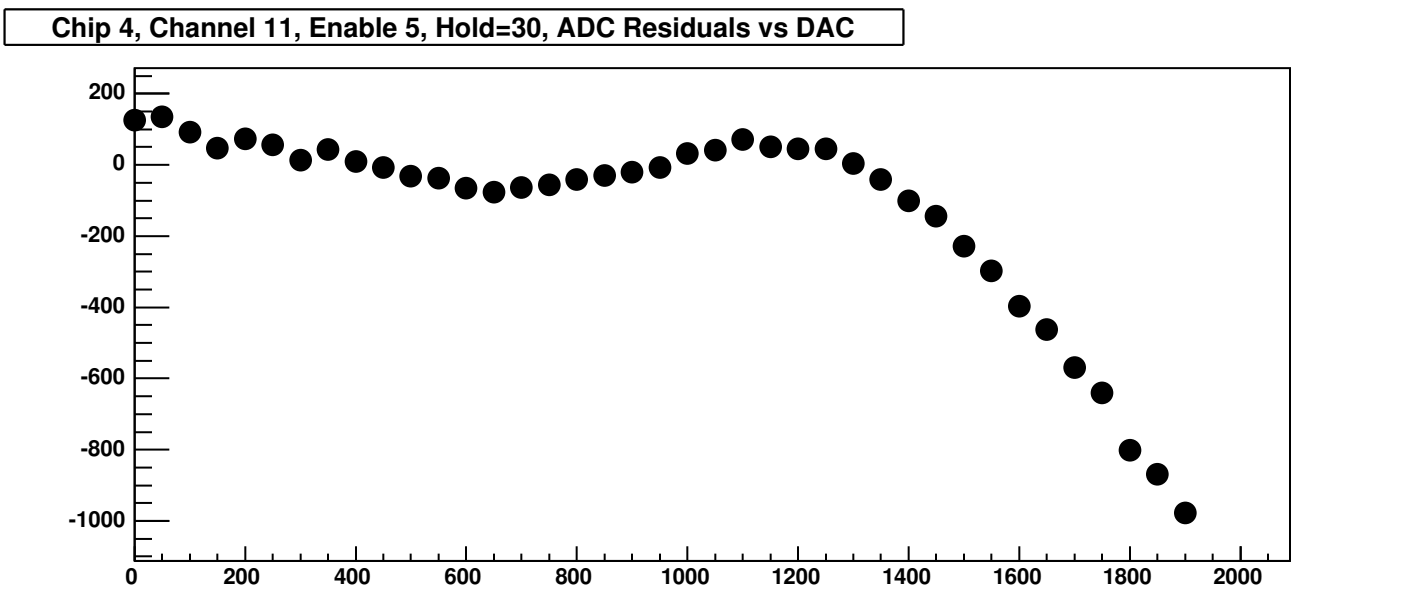


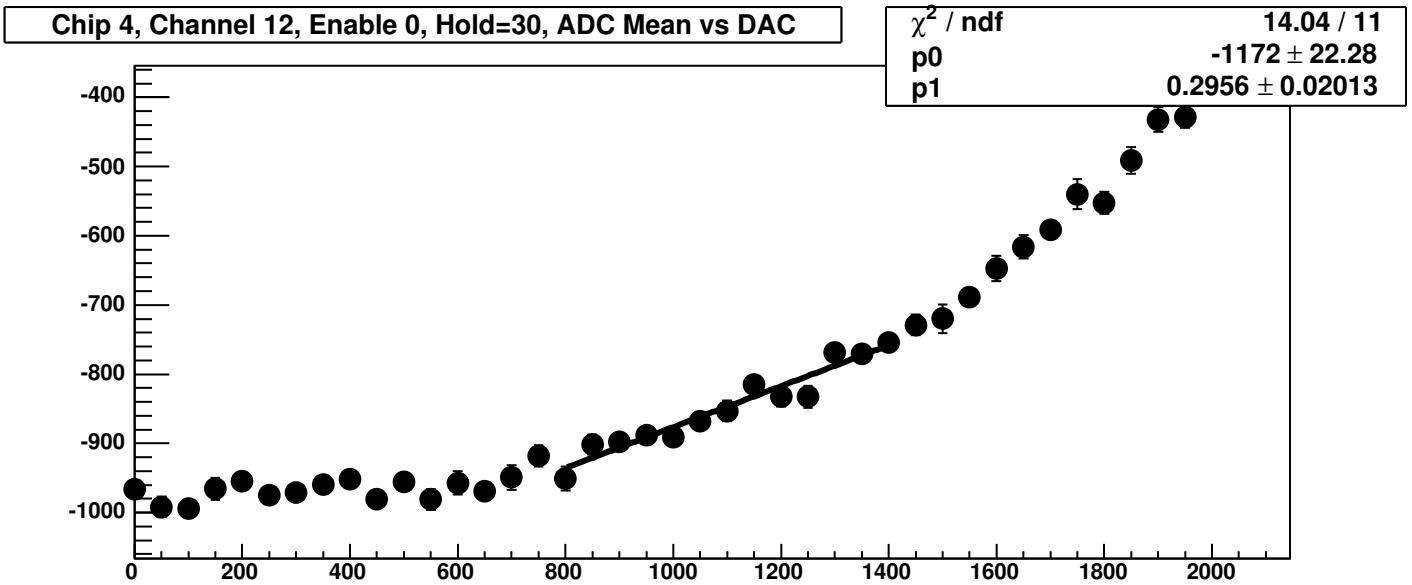
Chip 4, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC

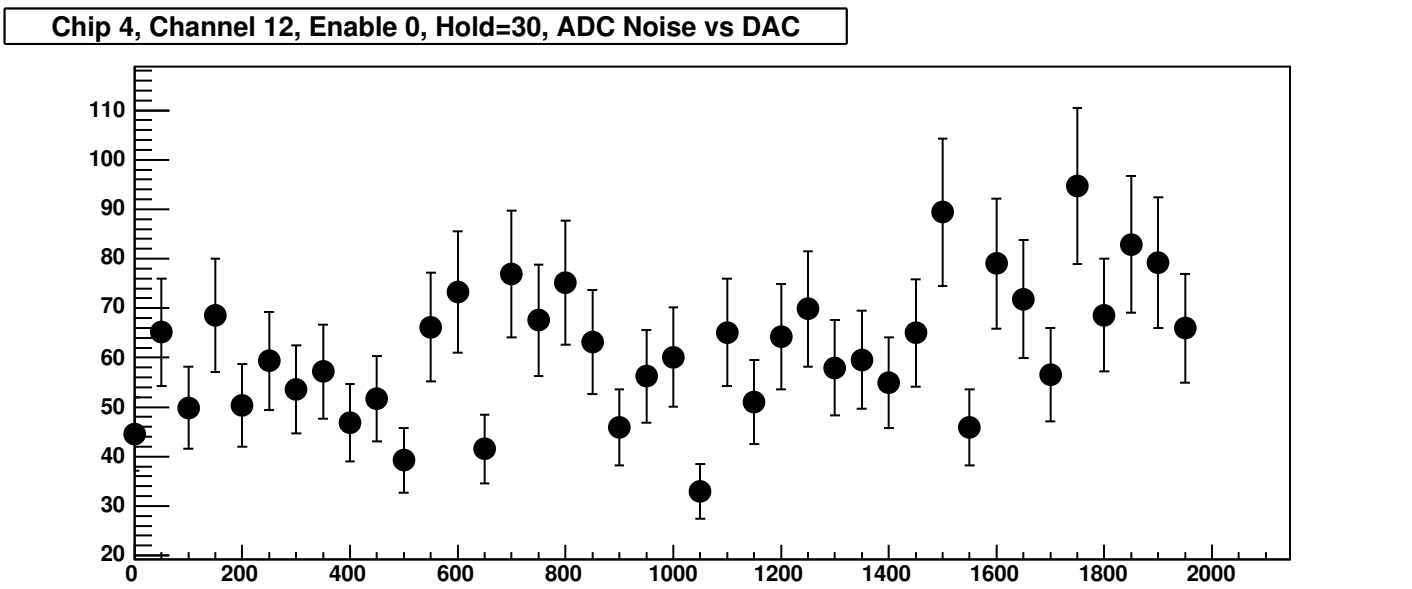


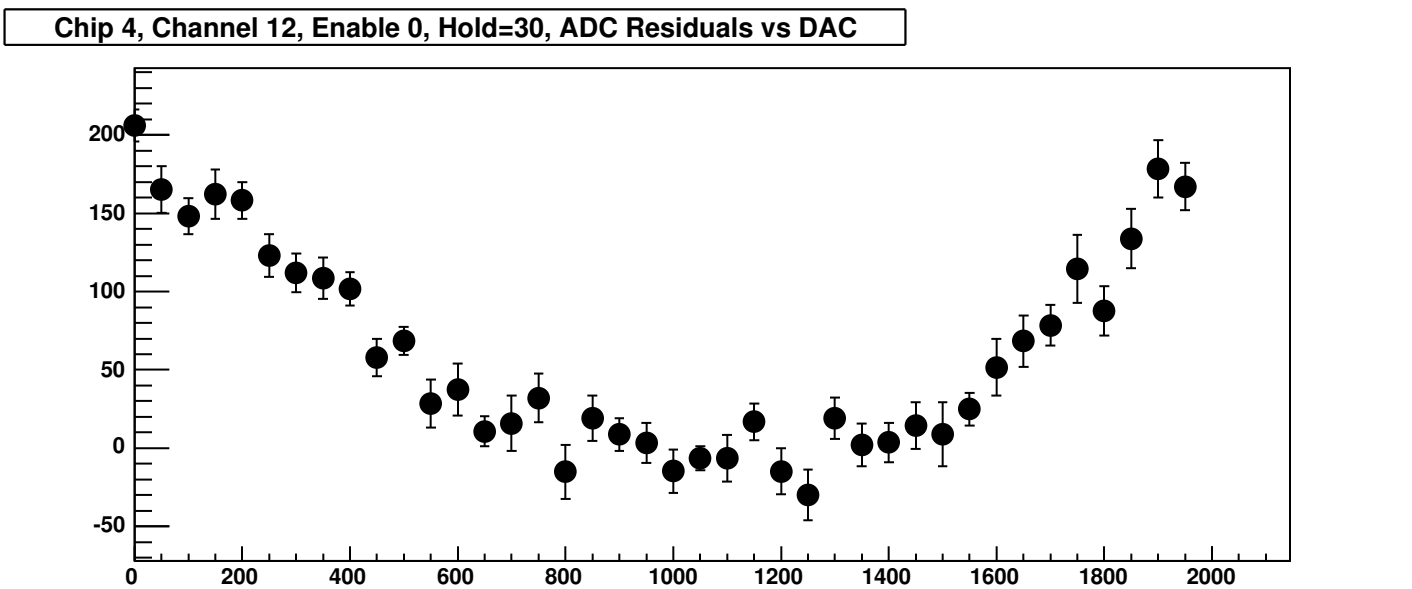

Chip 4, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 86.07 / 11
p0 -947.2 ± 26.25
p1 0.3164 ± 0.0242
-400
-600
-800
-1000
-1200
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

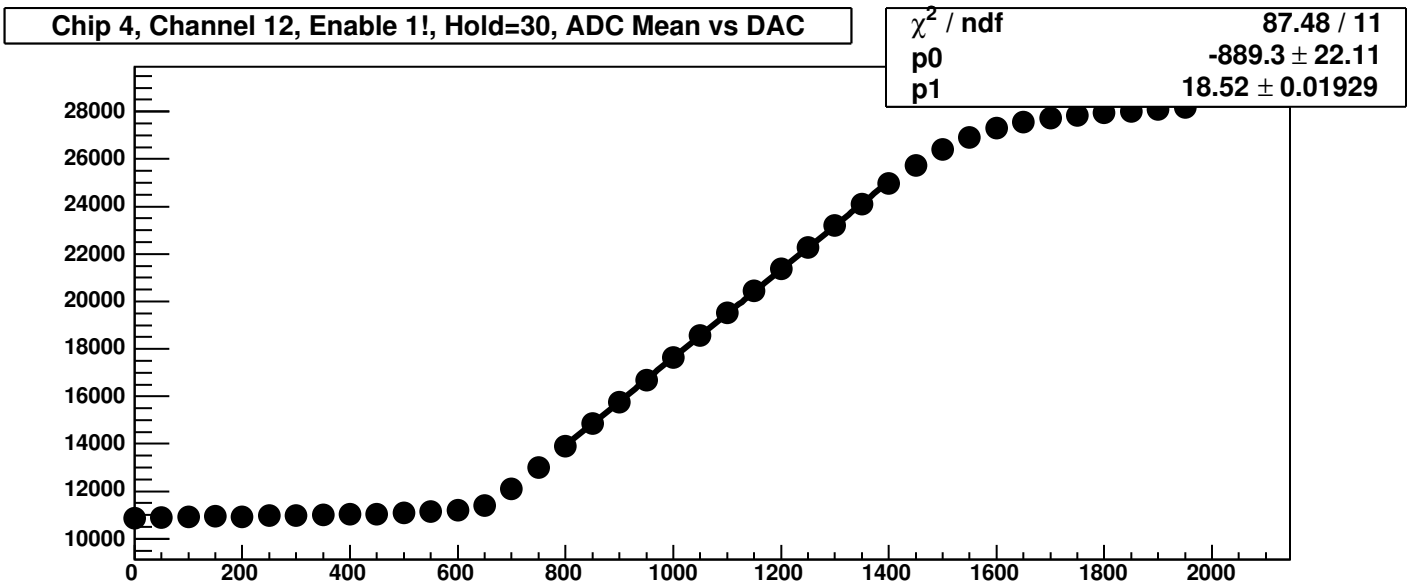

Chip 4, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

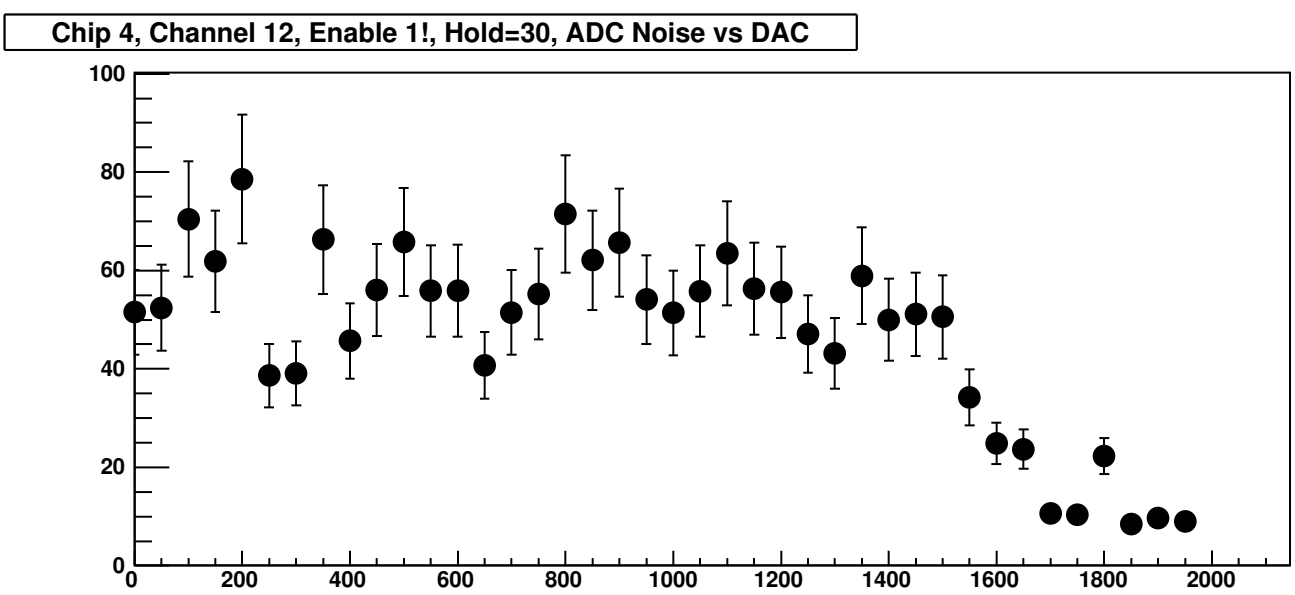

Chip 4, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

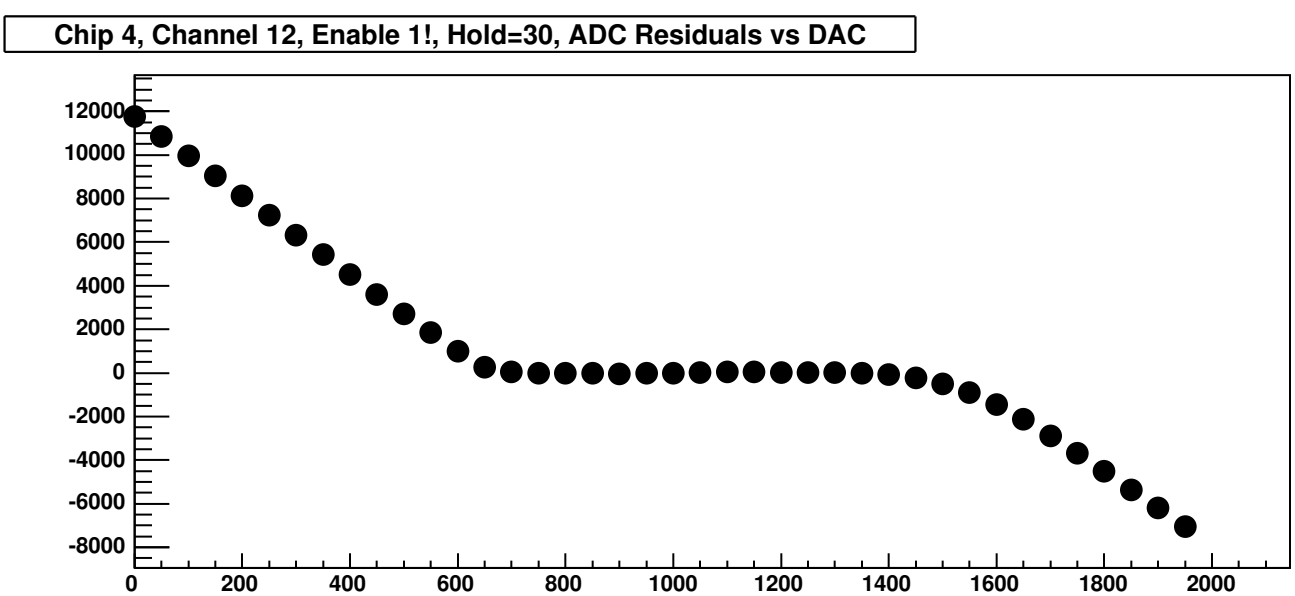

Chip 4, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 14.04 / 11
p0 -1172 ± 22.28
p1 0.2956 ± 0.02013
-400
-500
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

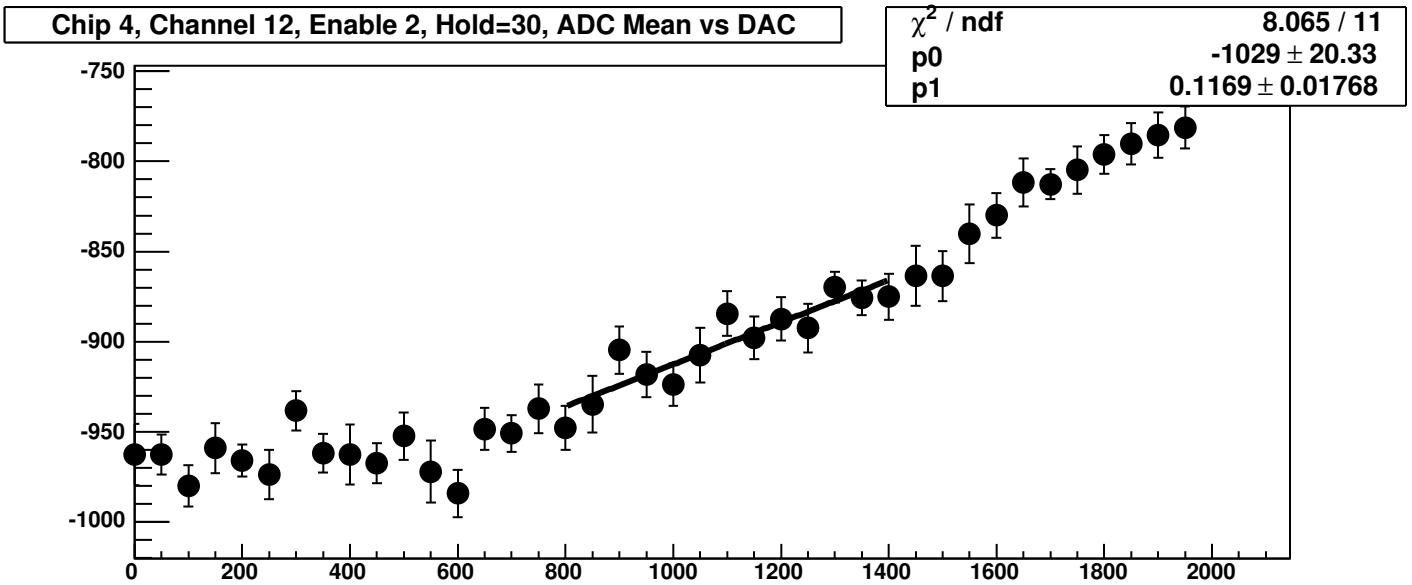

Chip 4, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

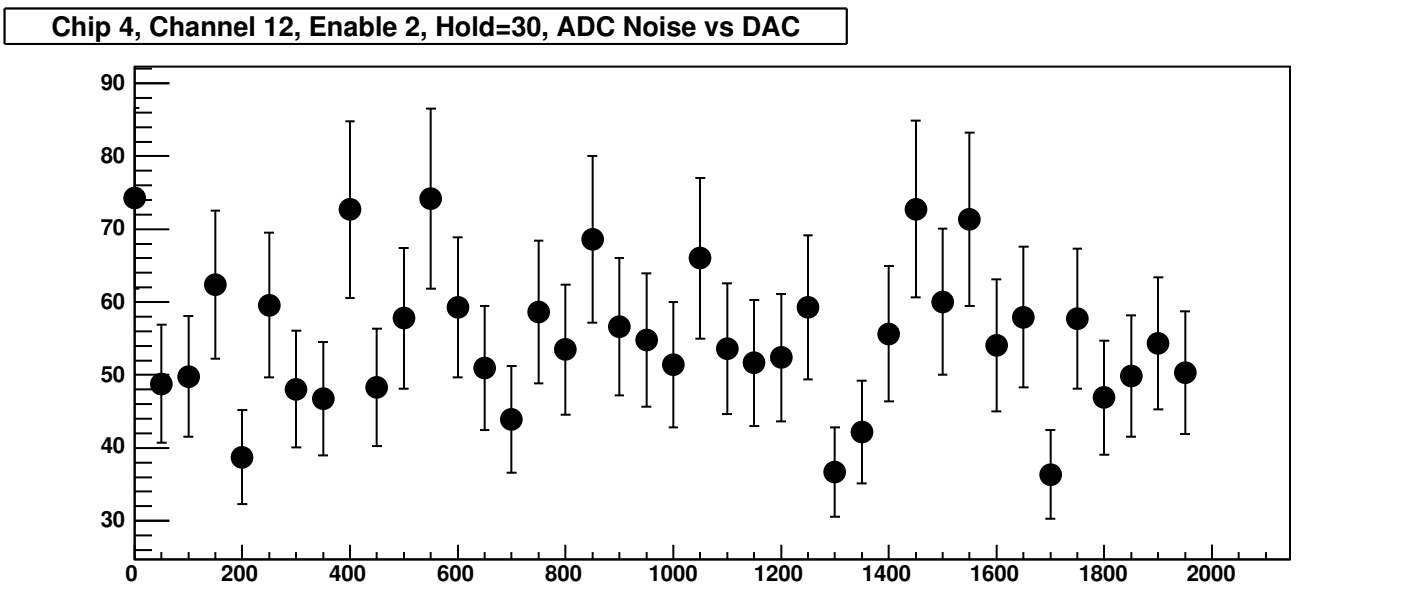

Chip 4, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

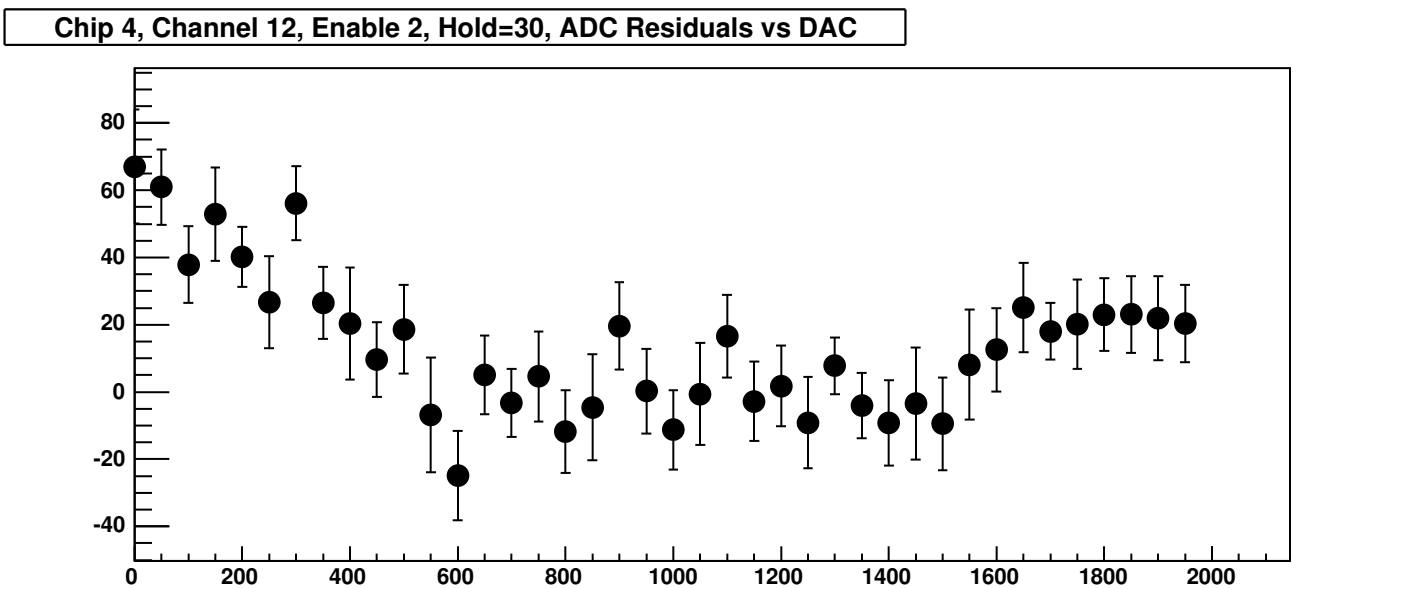

Chip 4, Channel 12, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 87.48 / 11
p0 -889.3 ± 22.11
p1 18.52 ± 0.01929
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

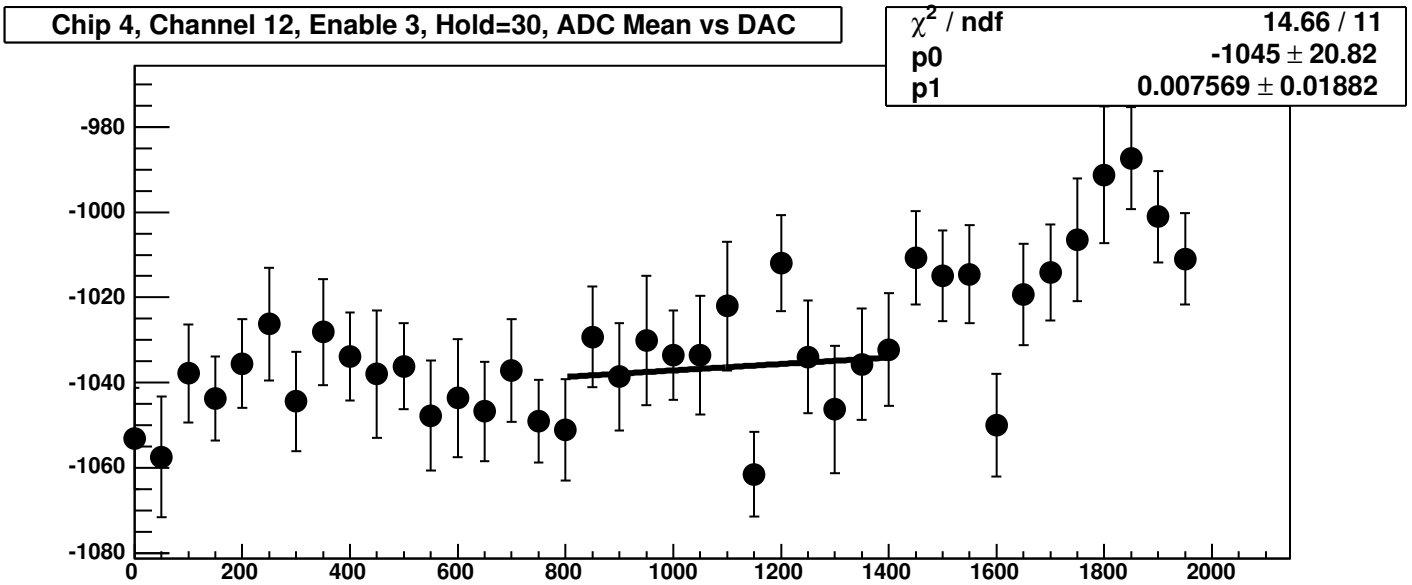

Chip 4, Channel 12, Enable 1!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

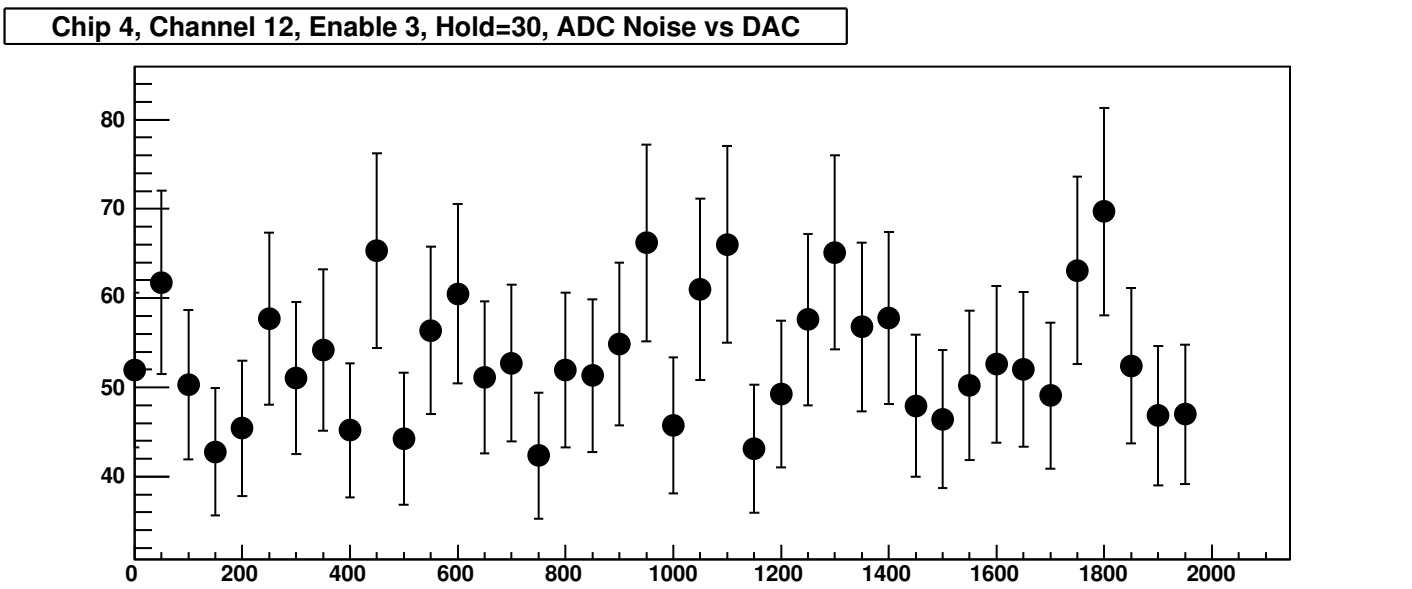

Chip 4, Channel 12, Enable 1!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

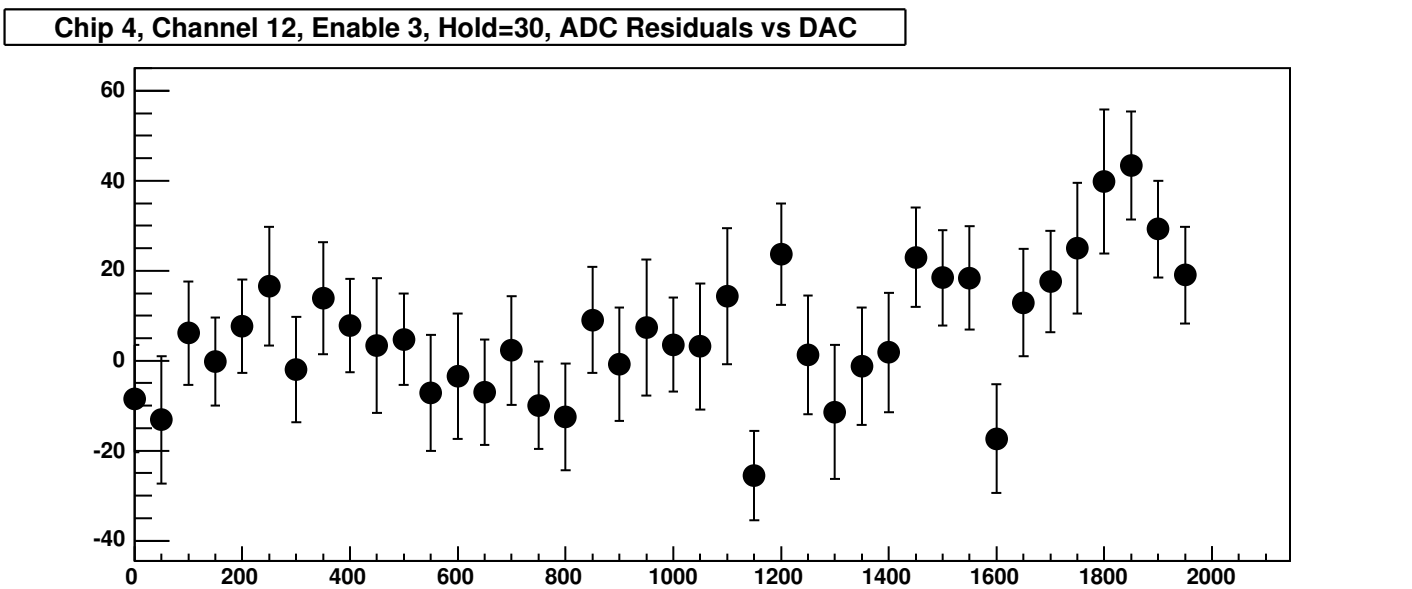

Chip 4, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 8.065 / 11
p0 -1029 ± 20.33
p1 0.1169 ± 0.01768
-750
-800
-850
-900
-950
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

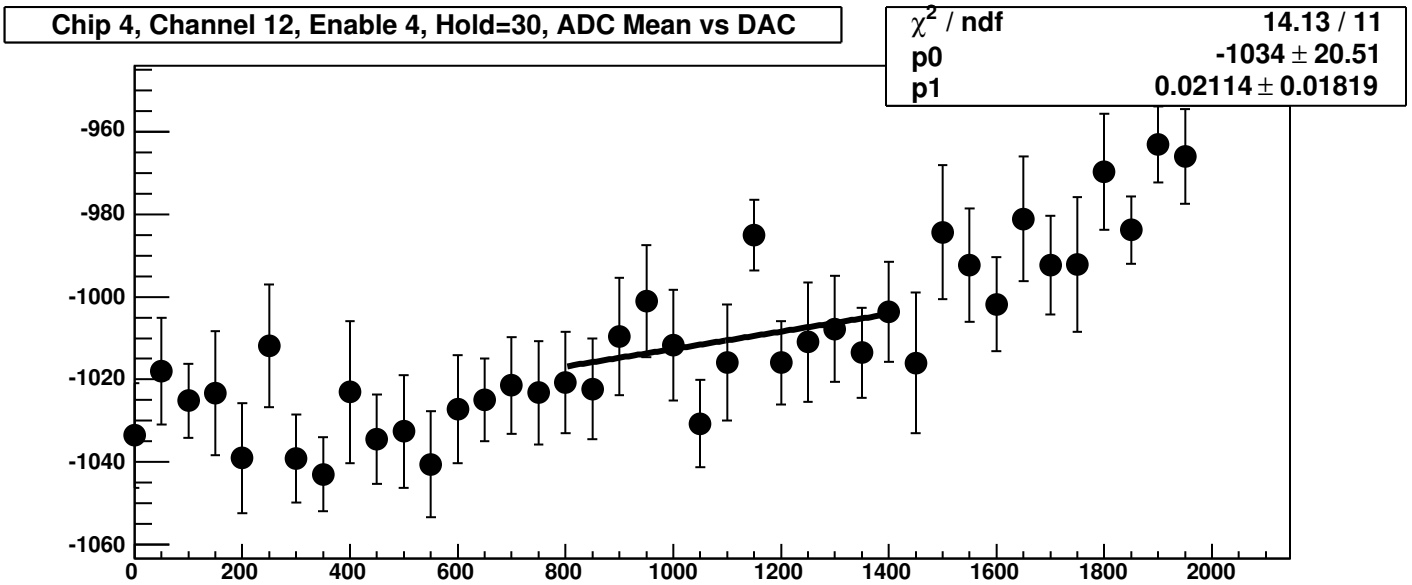

Chip 4, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

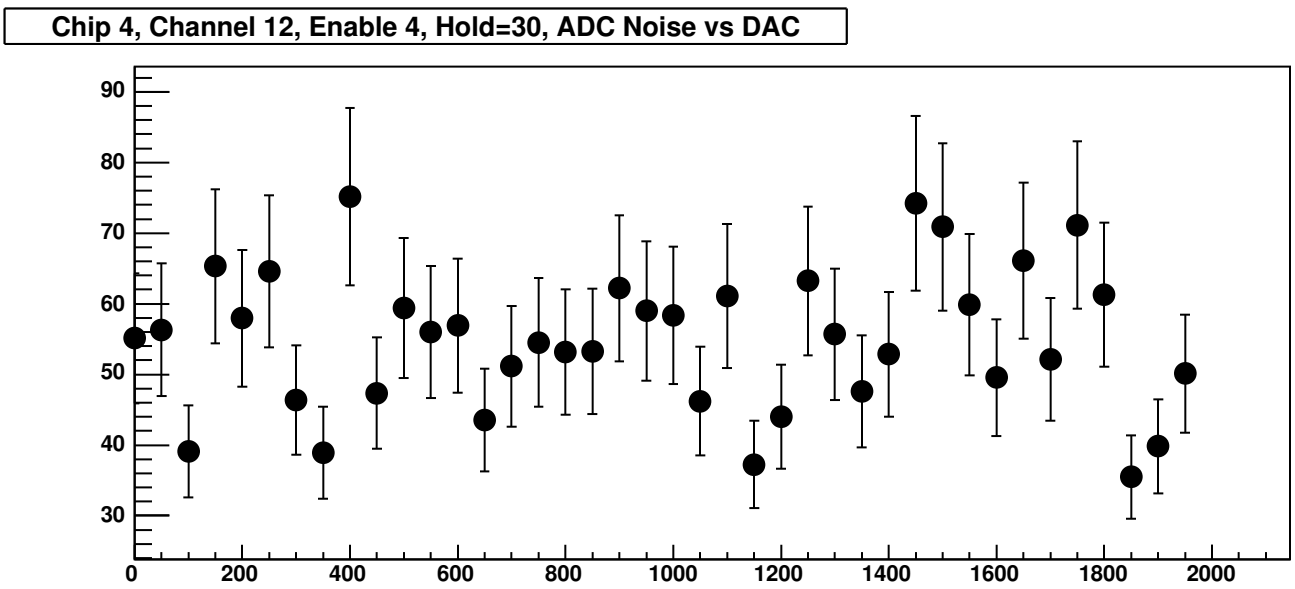

Chip 4, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

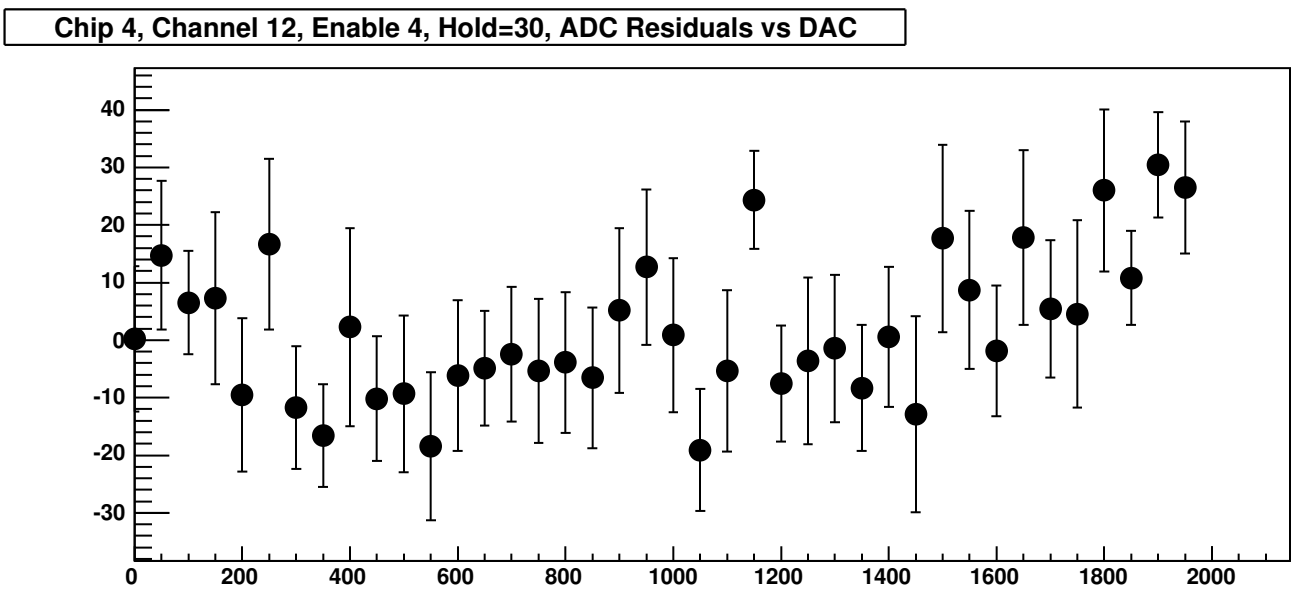

Chip 4, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 14.66 / 11
p0 -1045 ± 20.82
p1 0.007569 ± 0.01882
-980
-1000
-1020
-1040
-1060
-1080
0
200
400
600
800
1000
1200
1400
1600
1800
2000

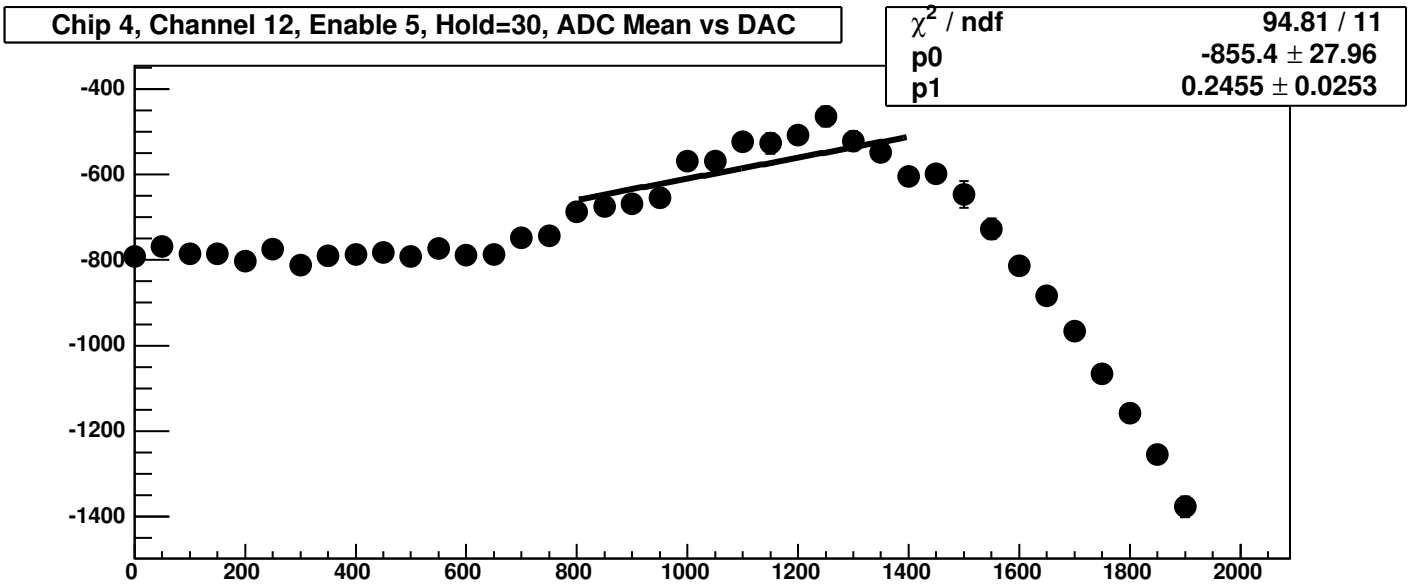

Chip 4, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

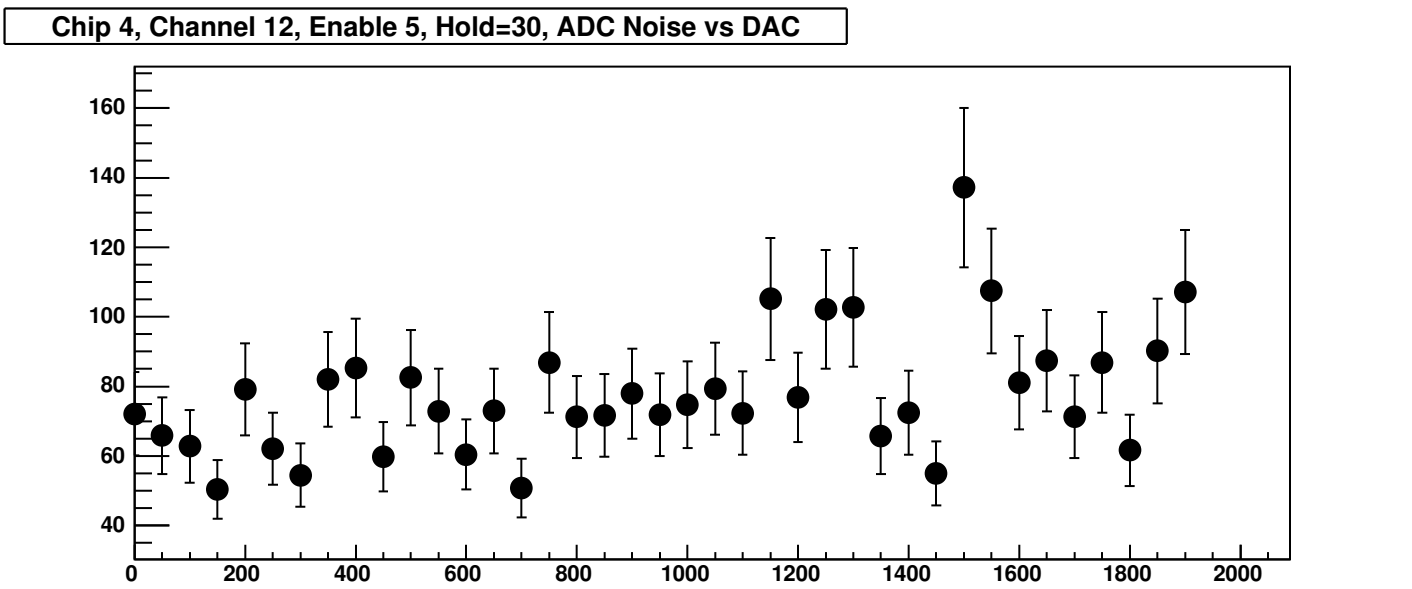

Chip 4, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

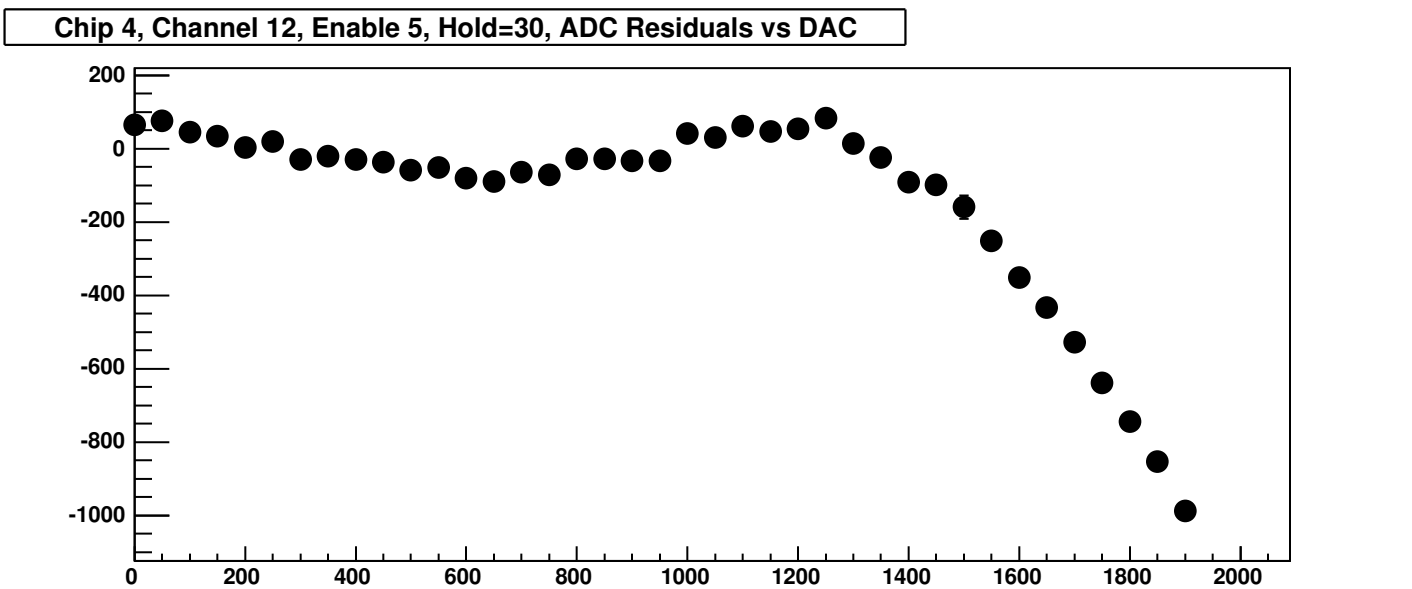

Chip 4, Channel 12, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 14.13 / 11
p0 -1034 ± 20.51
p1 0.02114 ± 0.01819
-960
-980
-1000
-1020
-1040
-1060
0
200
400
600
800
1000
1200
1400
1600
1800
2000

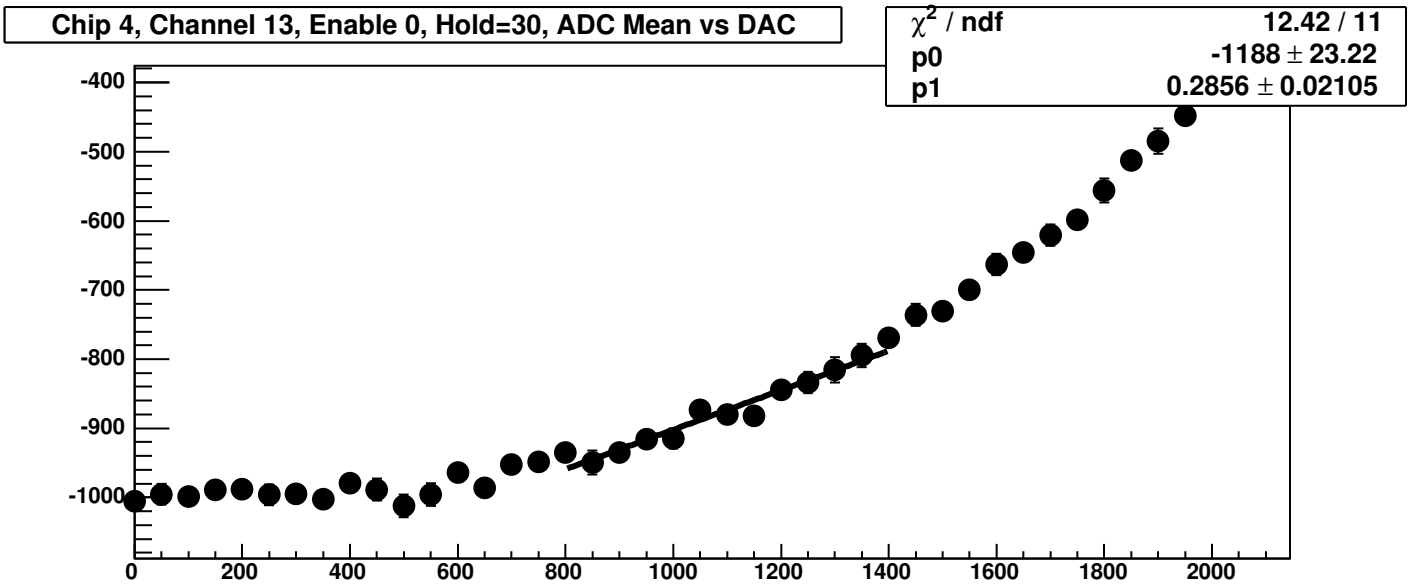

Chip 4, Channel 12, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

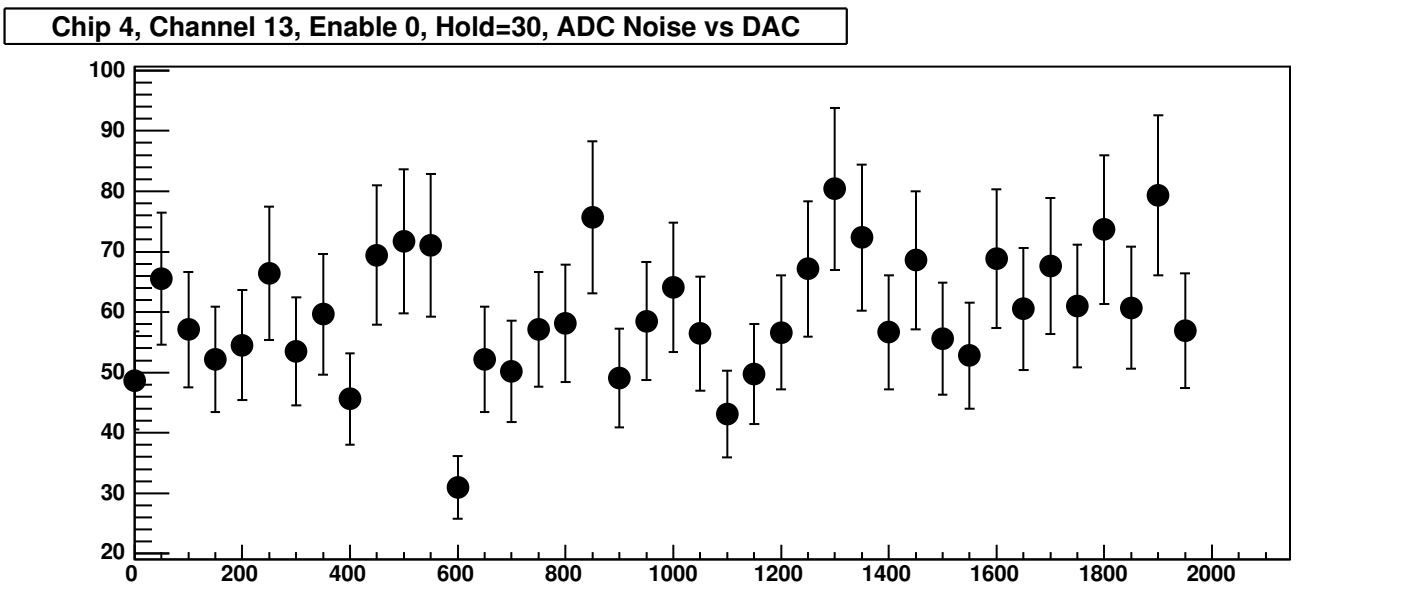

Chip 4, Channel 12, Enable 4, Hold=30, ADC Residuals vs DAC
40
30
20
10
0
-10
-20
-30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

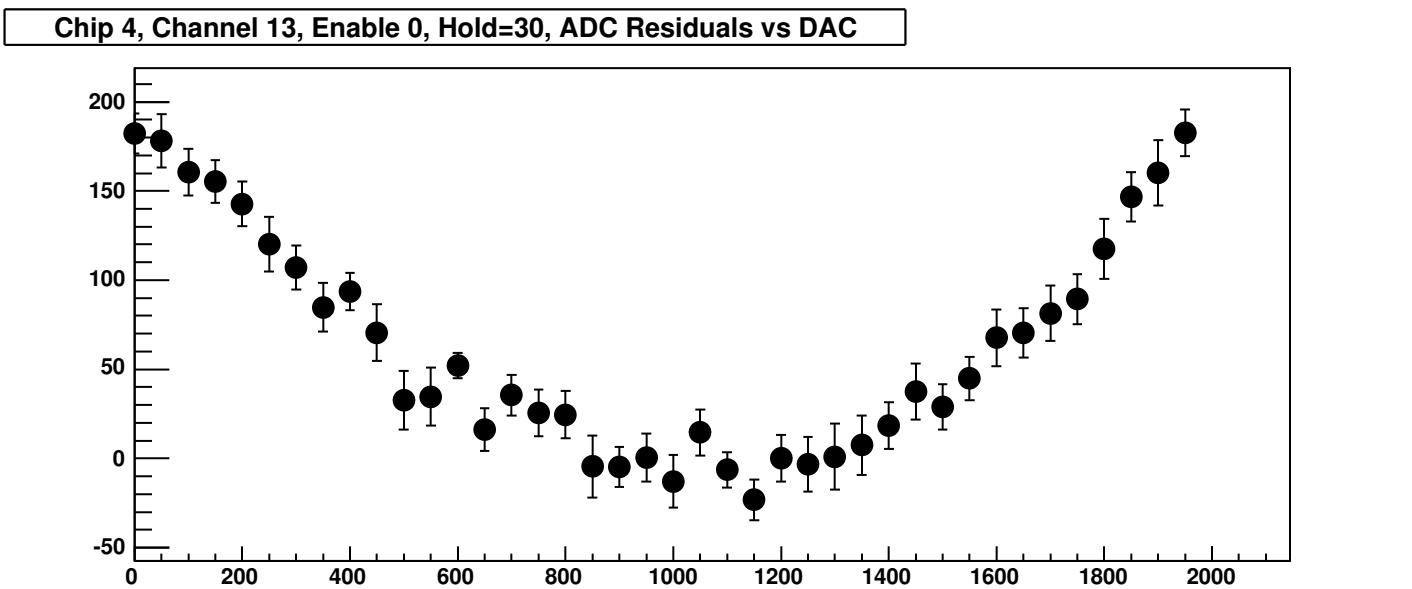

Chip 4, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 94.81 / 11
p0 -855.4 ± 27.96
p1 0.2455 ± 0.0253
-400
-600
-800
-1000
-1200
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

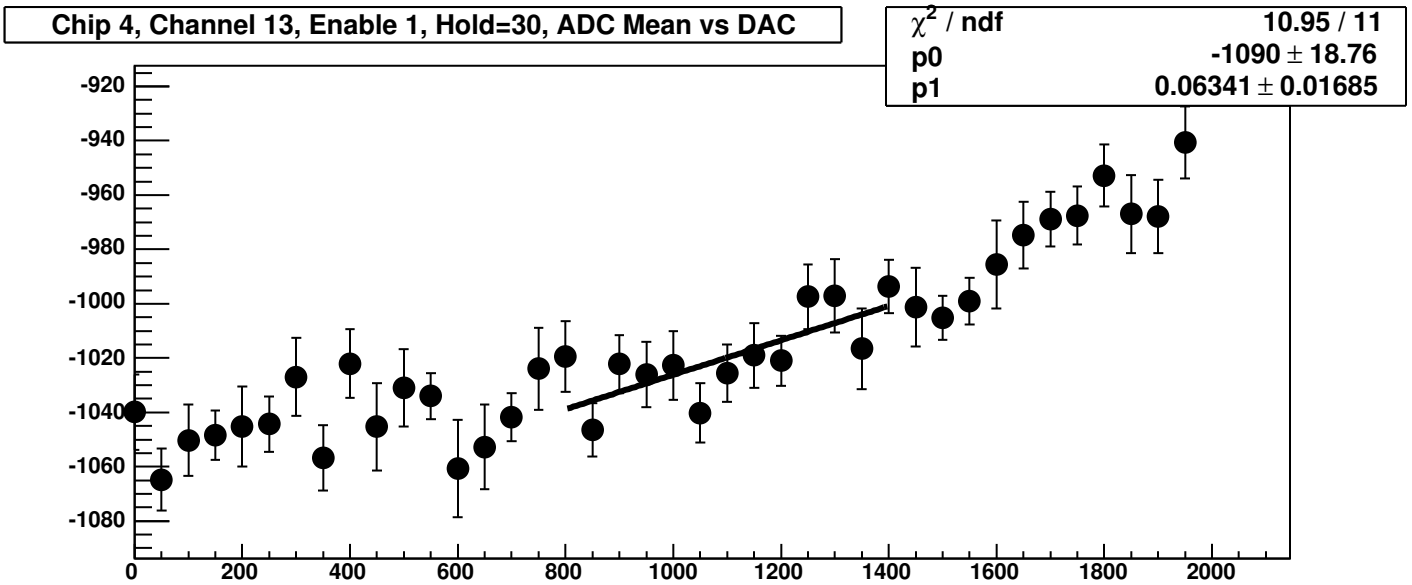

Chip 4, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

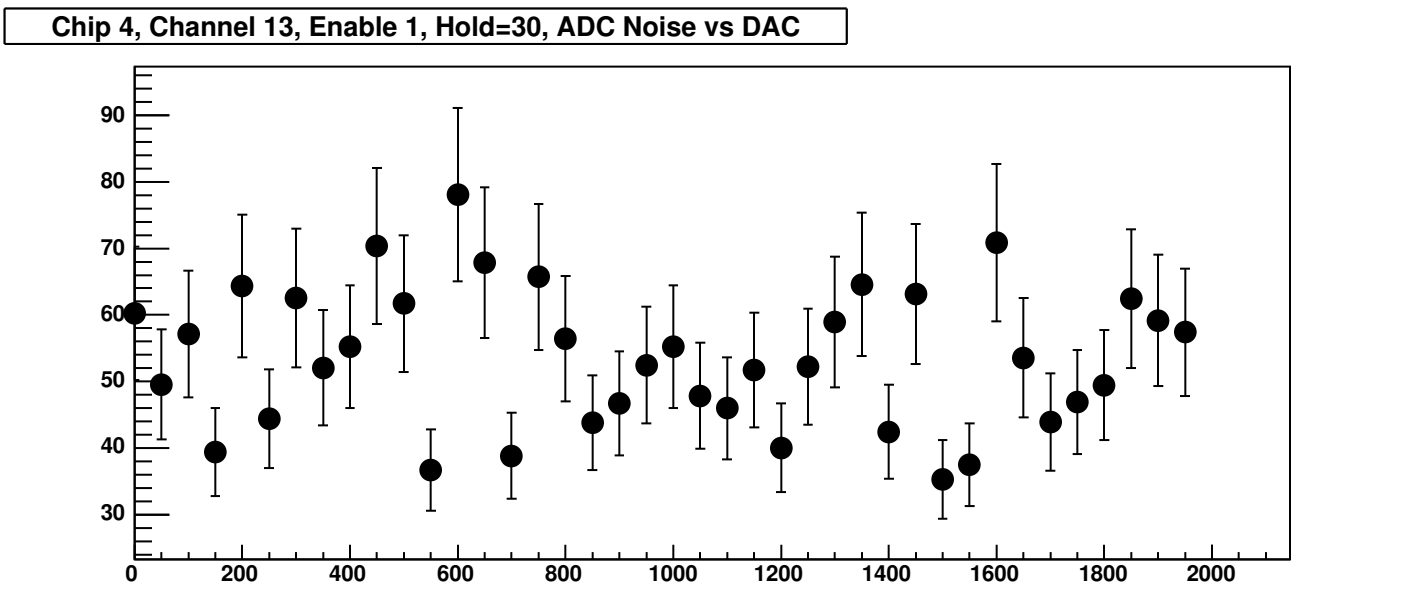

Chip 4, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

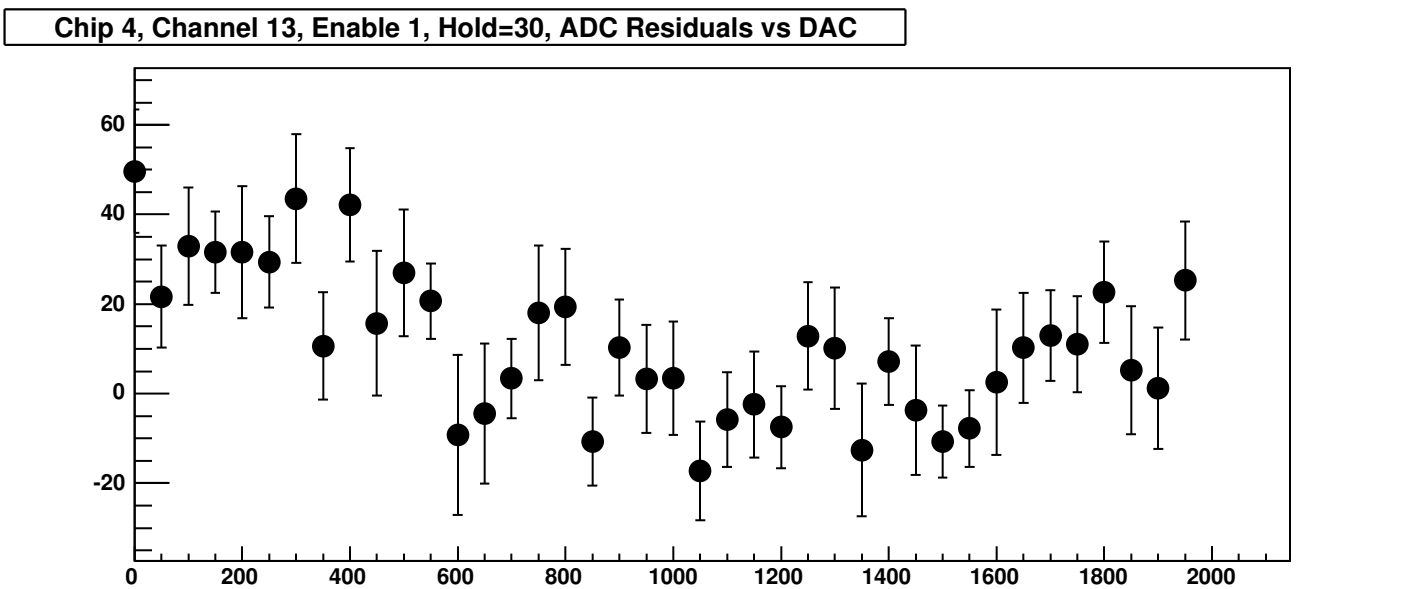

Chip 4, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 12.42 / 11
p0 -1188 ± 23.22
p1 0.2856 ± 0.02105
-400
-500
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

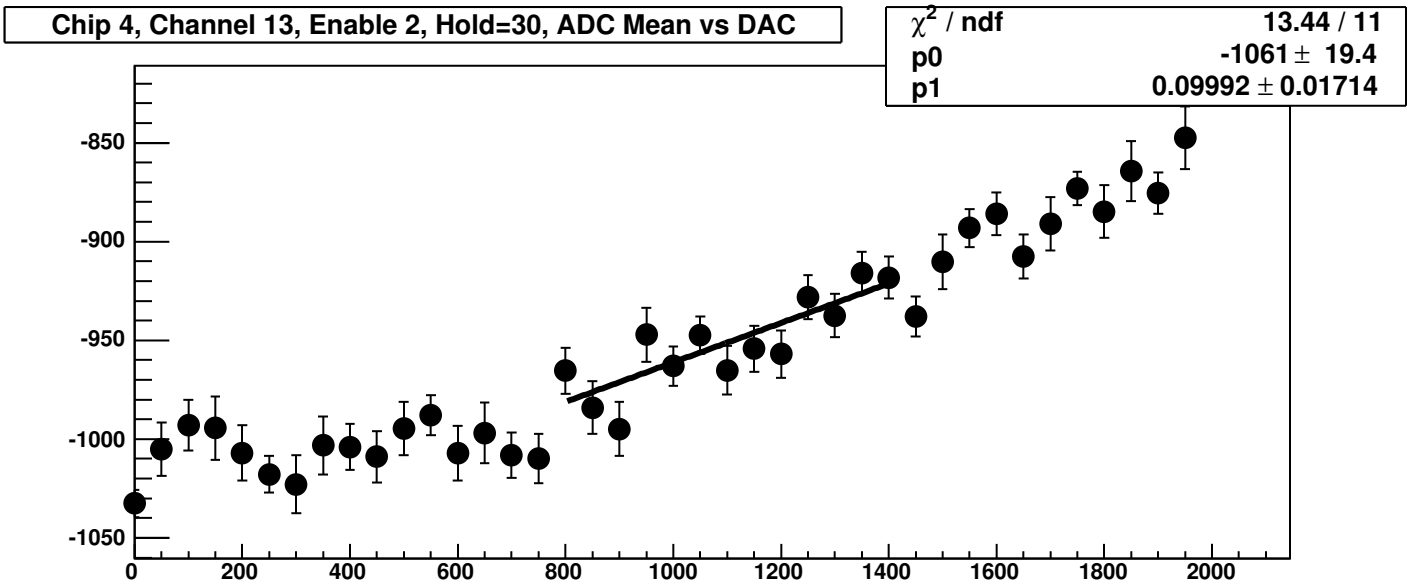

Chip 4, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

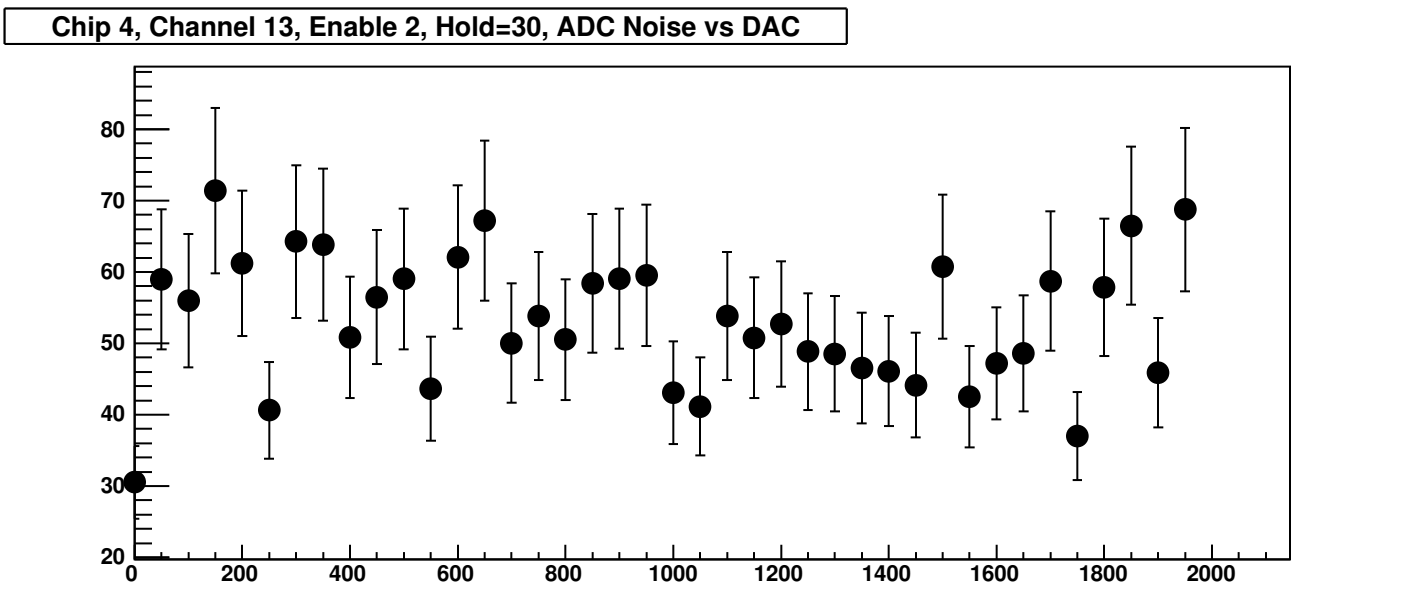

Chip 4, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

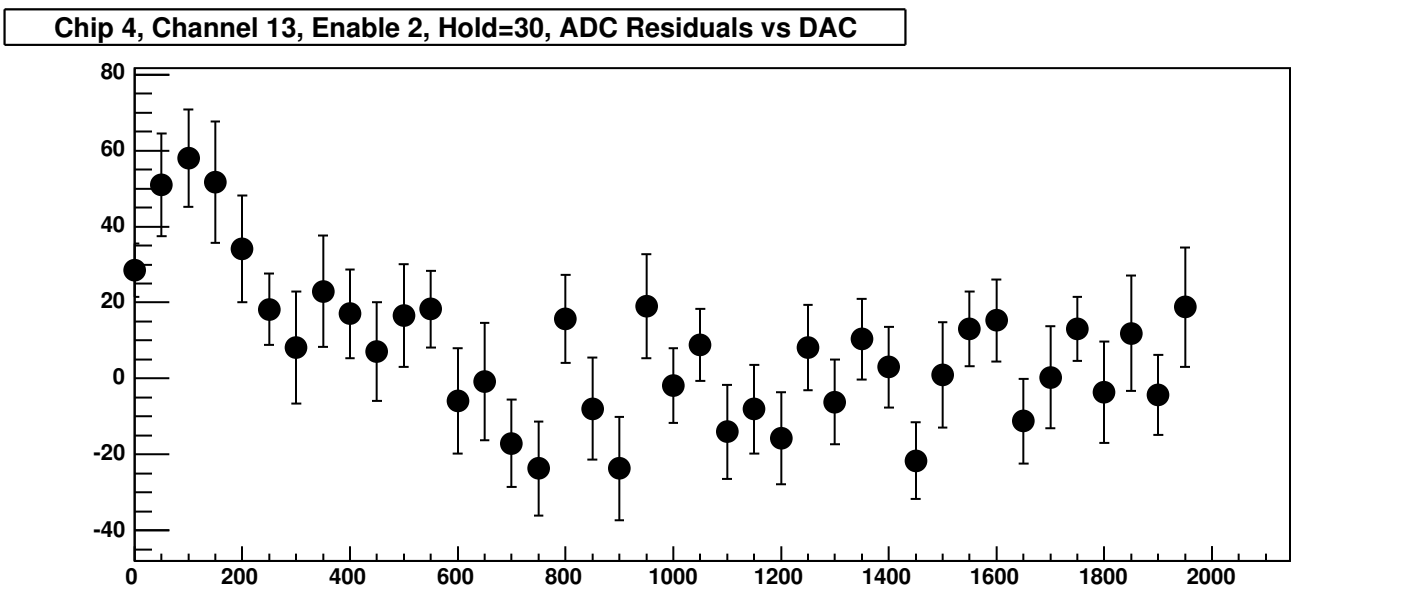

Chip 4, Channel 13, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 10.95 / 11
p0 -1090 ± 18.76
p1 0.06341 ± 0.01685
-920
-940
-960
-980
-1000
-1020
-1040
-1060
-1080
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 13, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

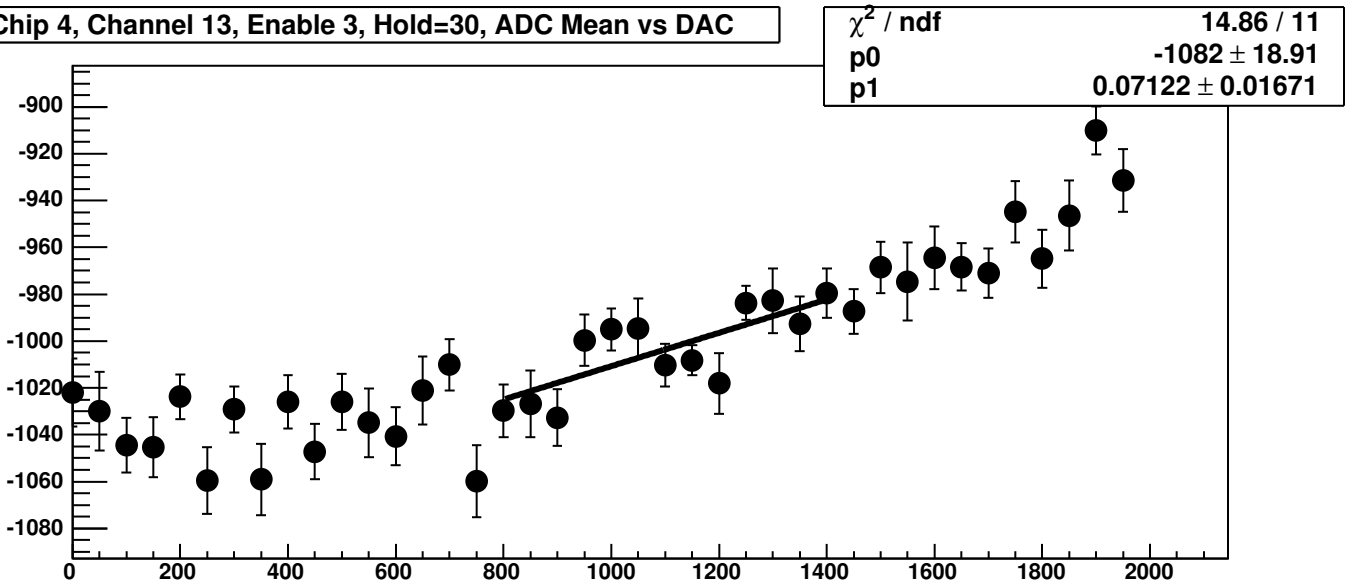

Chip 4, Channel 13, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 13.44 / 11
p0 -1061 ± 19.4
p1 0.09992 ± 0.01714
-850
-900
-950
-1000
-1050
0
200
400
600
800
1000
1200
1400
1600
1800
2000

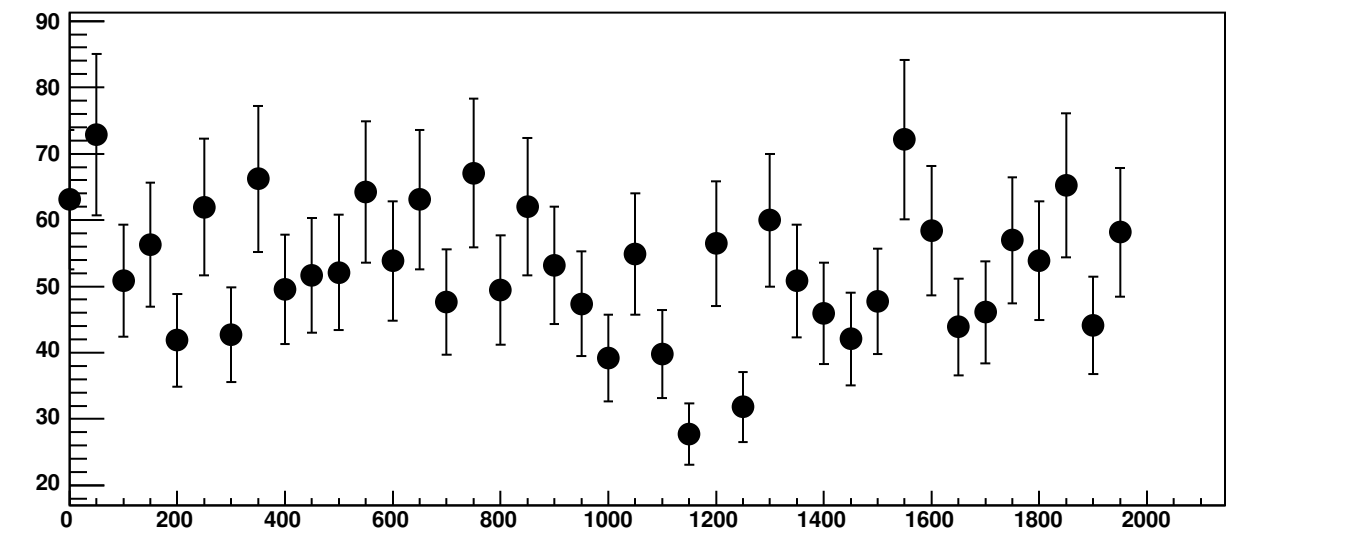

Chip 4, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

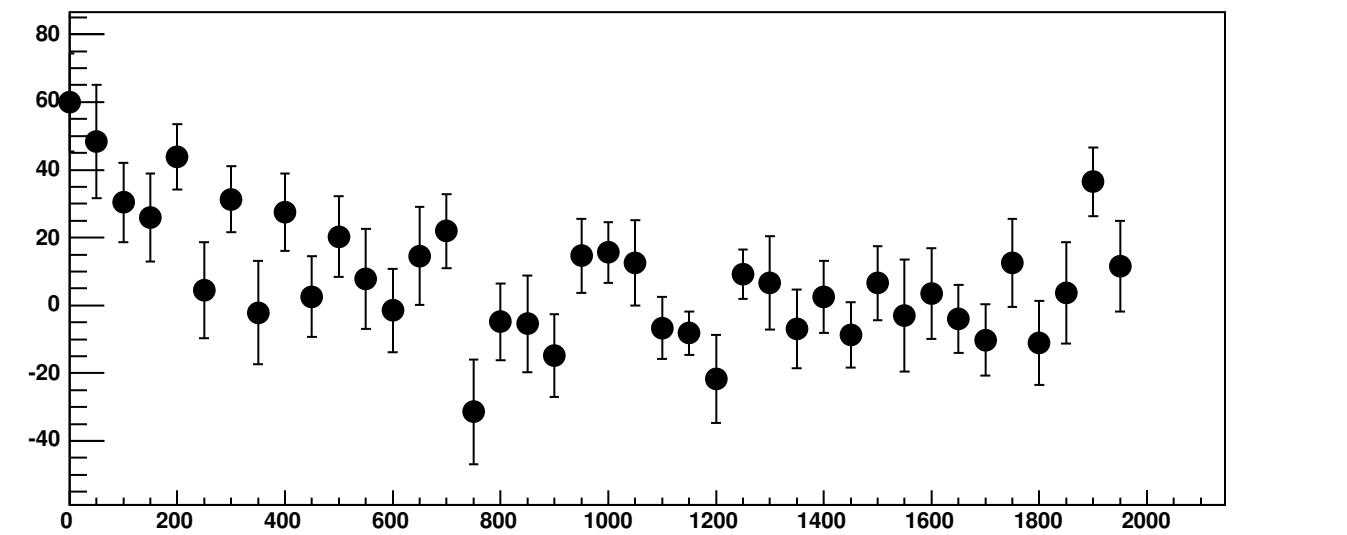
Chip 4, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC

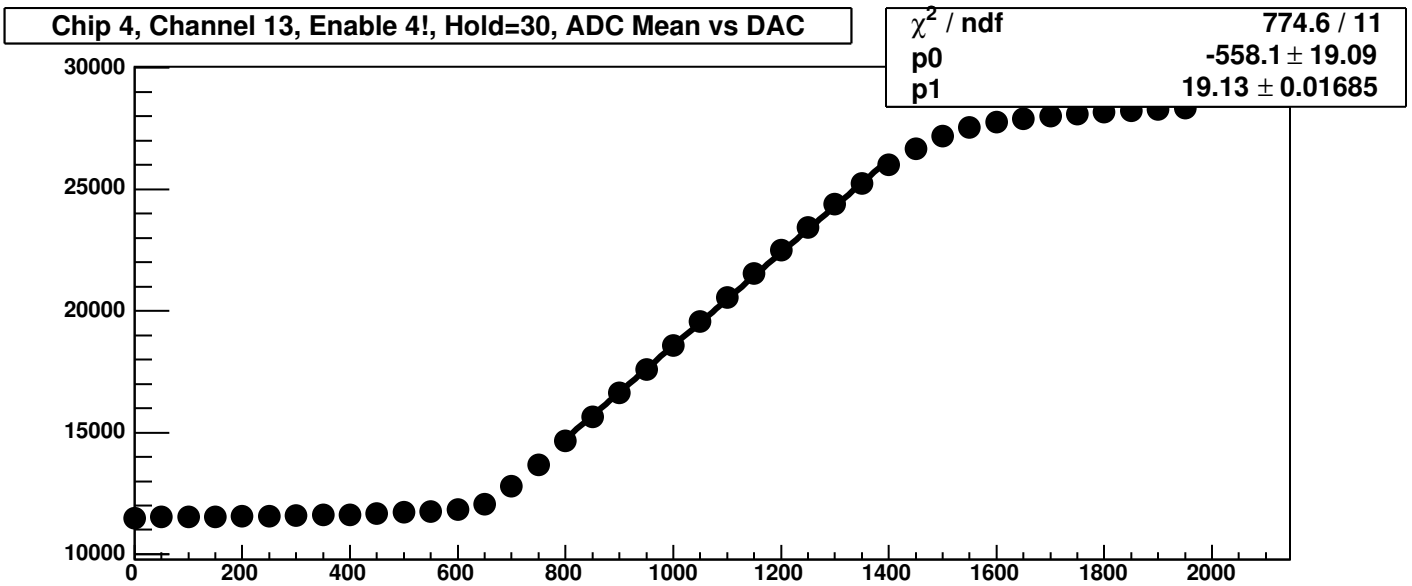


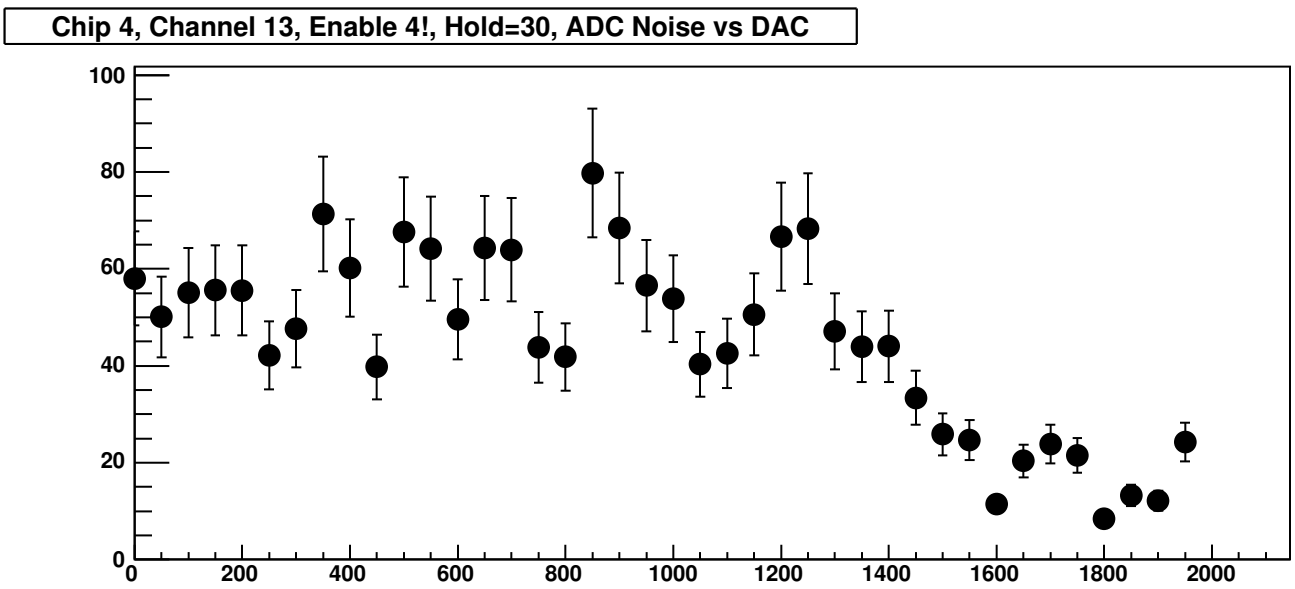
Chip 4, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC

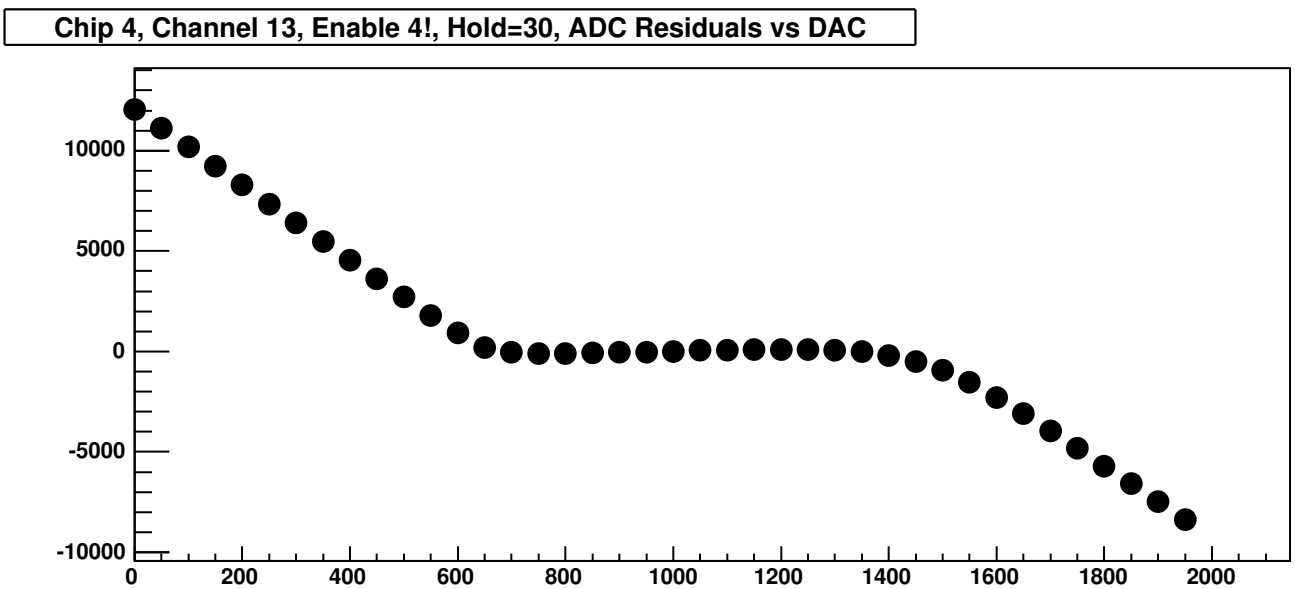


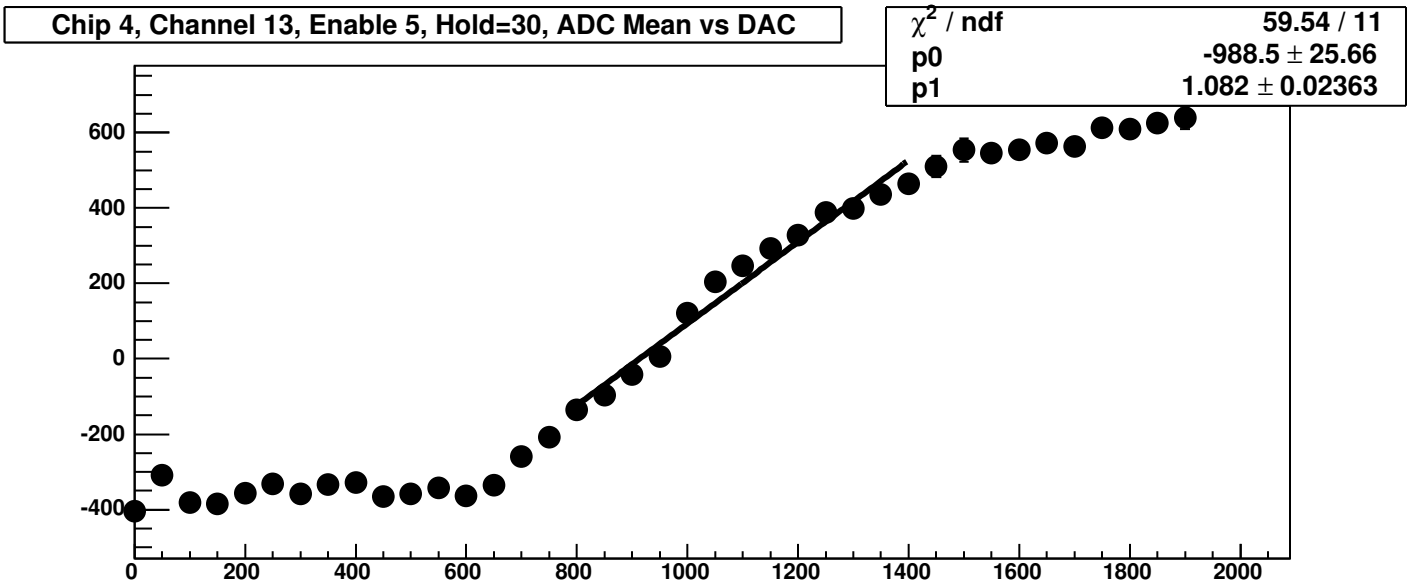
Chip 4, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC

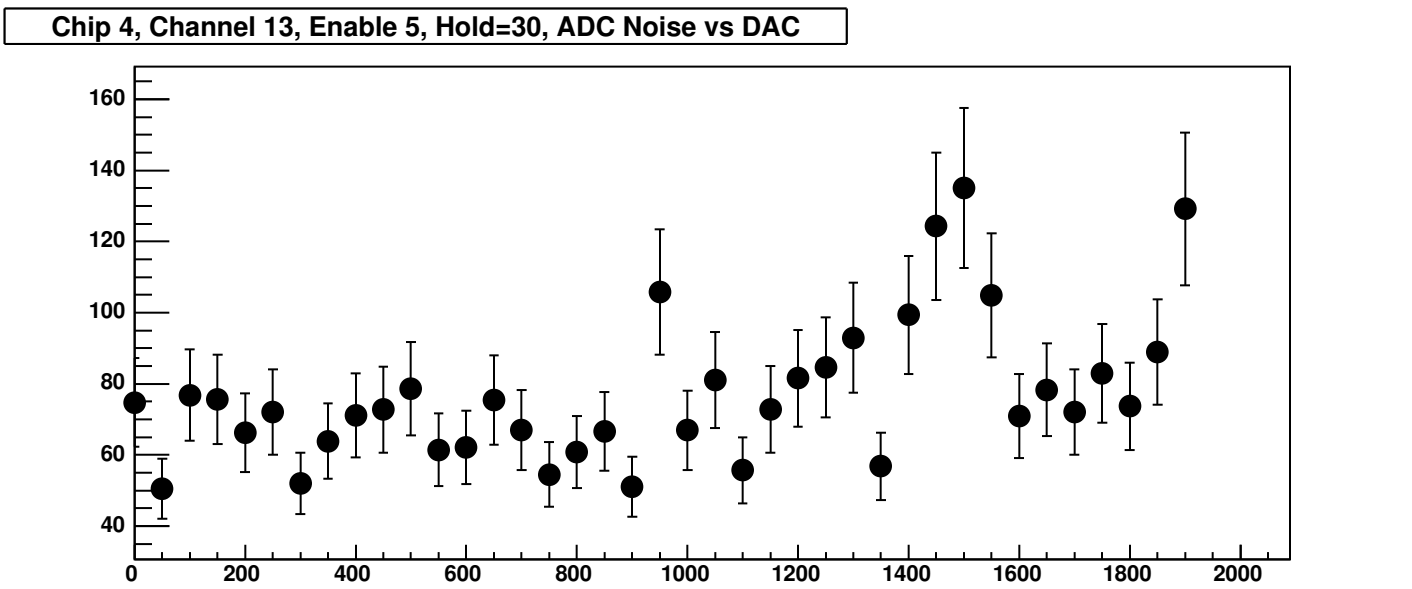


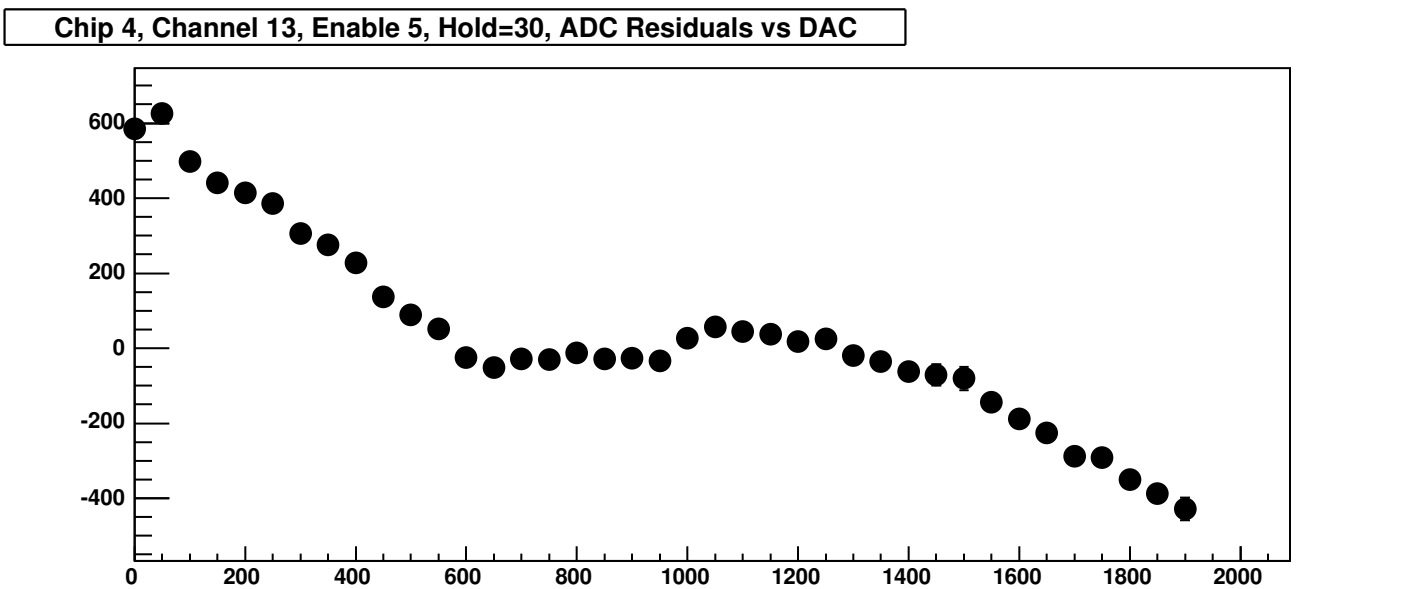

Chip 4, Channel 13, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 774.6 / 11
p0 -558.1 ± 19.09
p1 19.13 ± 0.01685
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

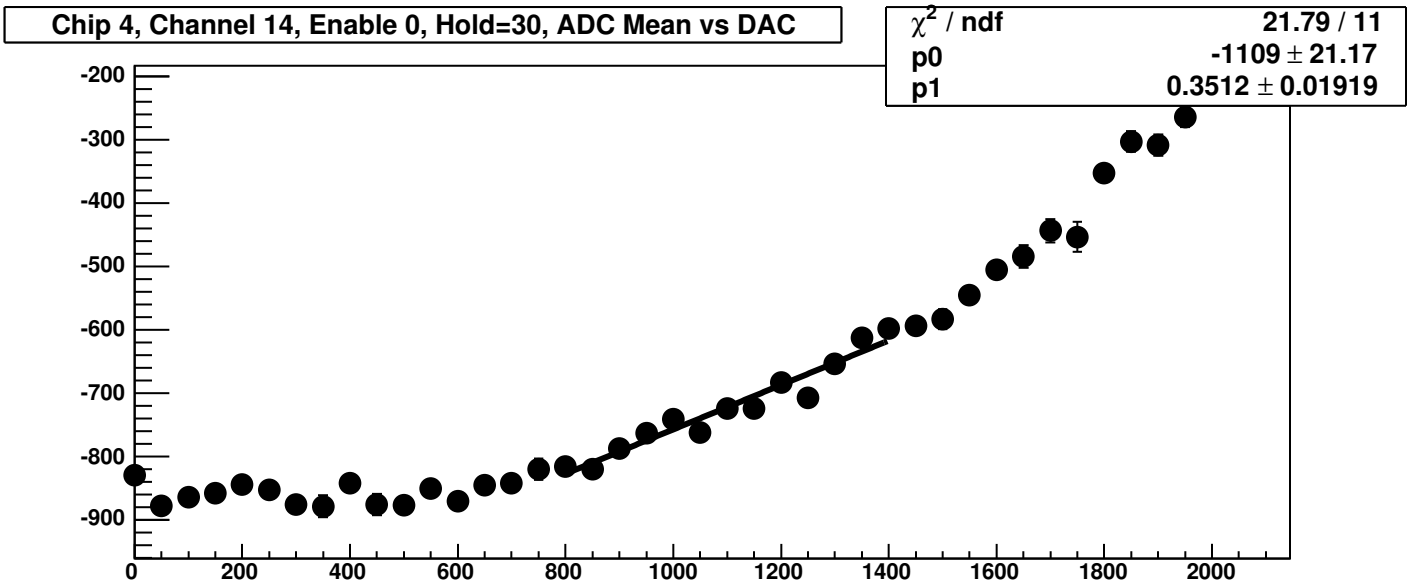

Chip 4, Channel 13, Enable 4!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

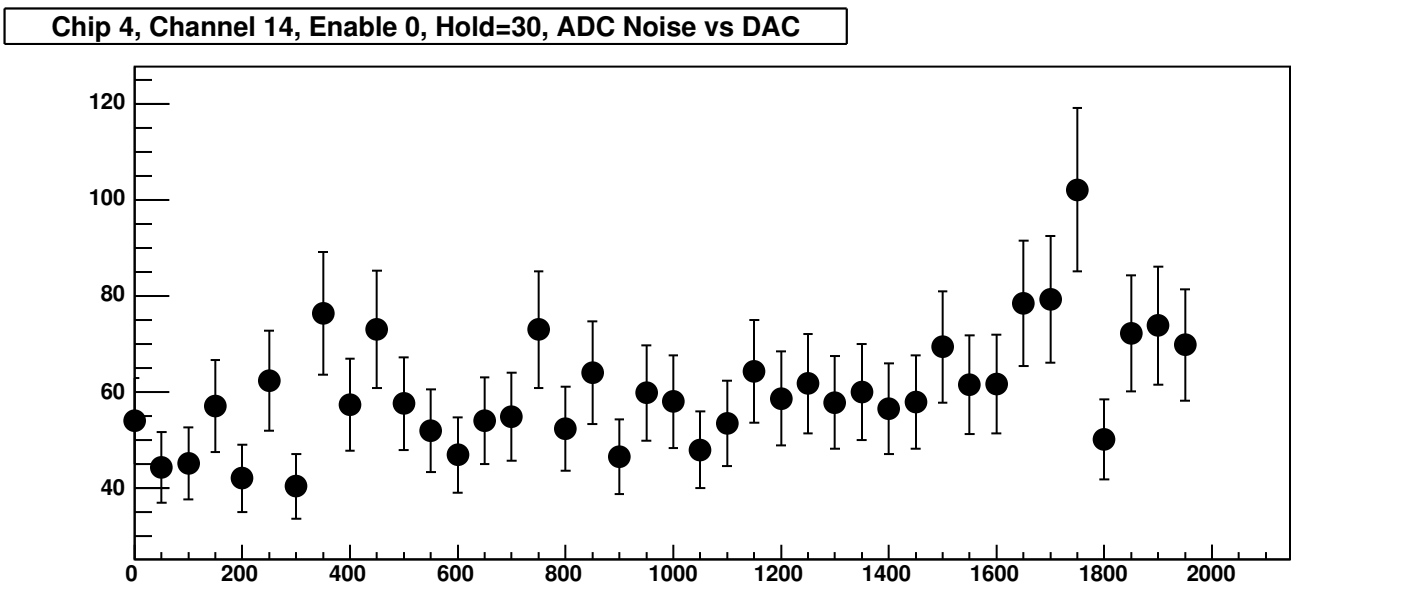

Chip 4, Channel 13, Enable 4!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

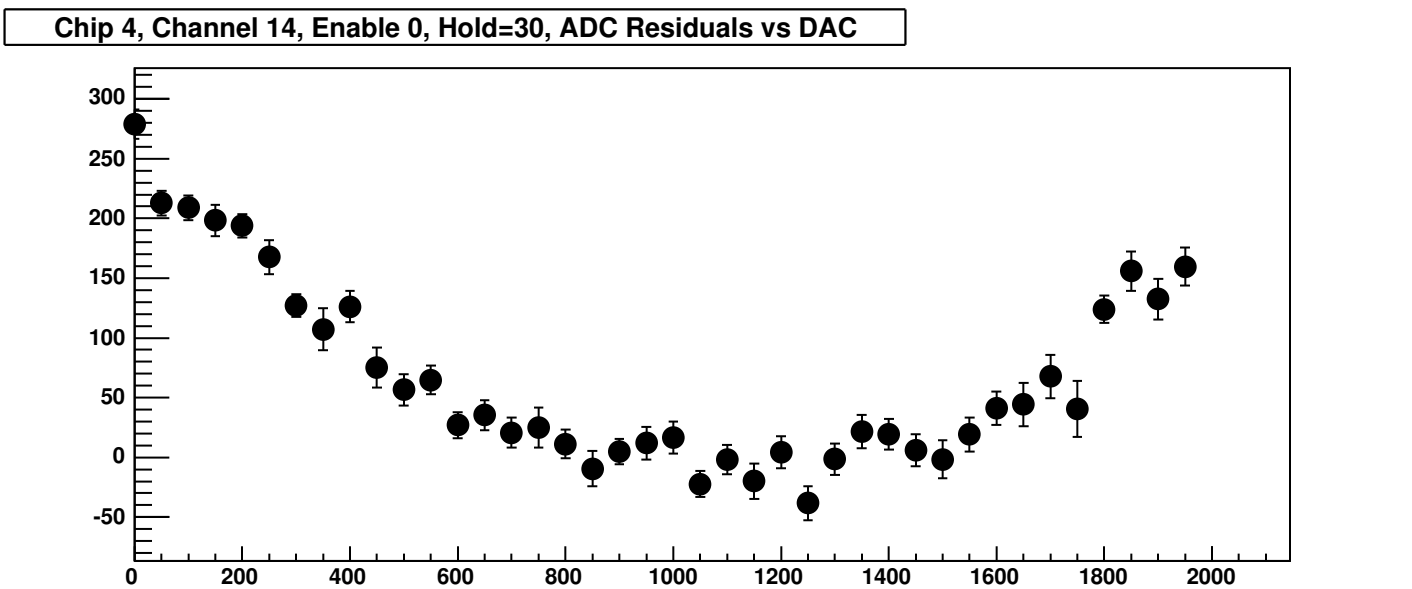

Chip 4, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 59.54 / 11
p0 -988.5 ± 25.66
p1 1.082 ± 0.02363
600
400
200
0
-200
-400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

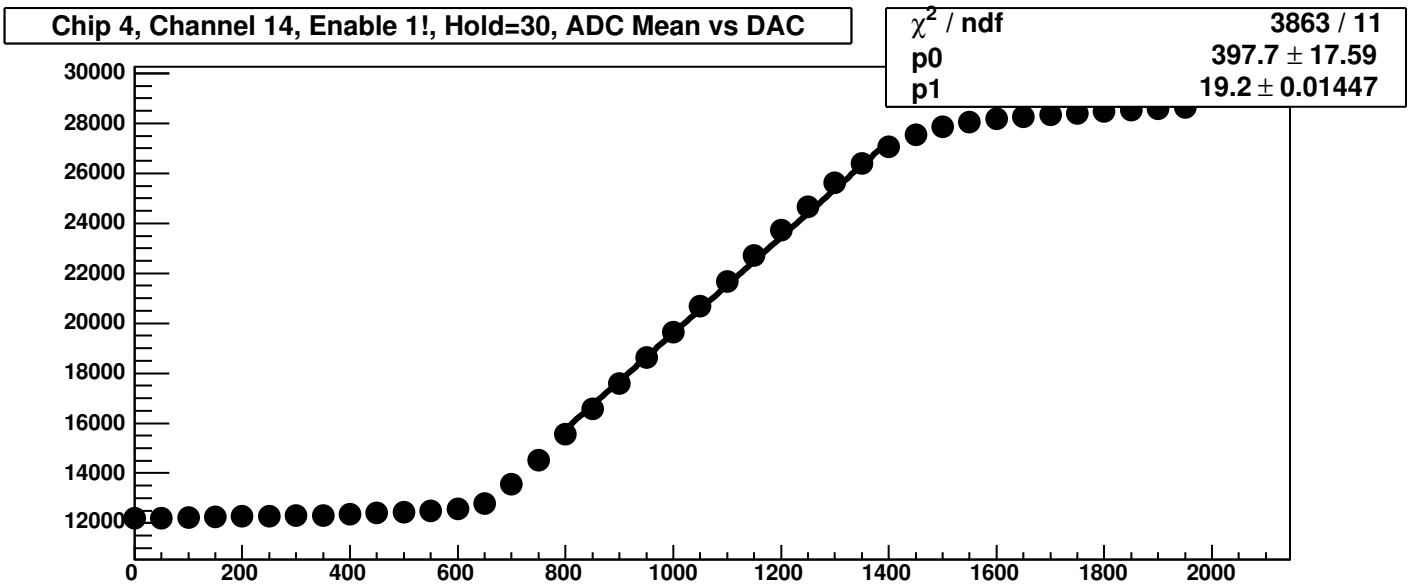

Chip 4, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

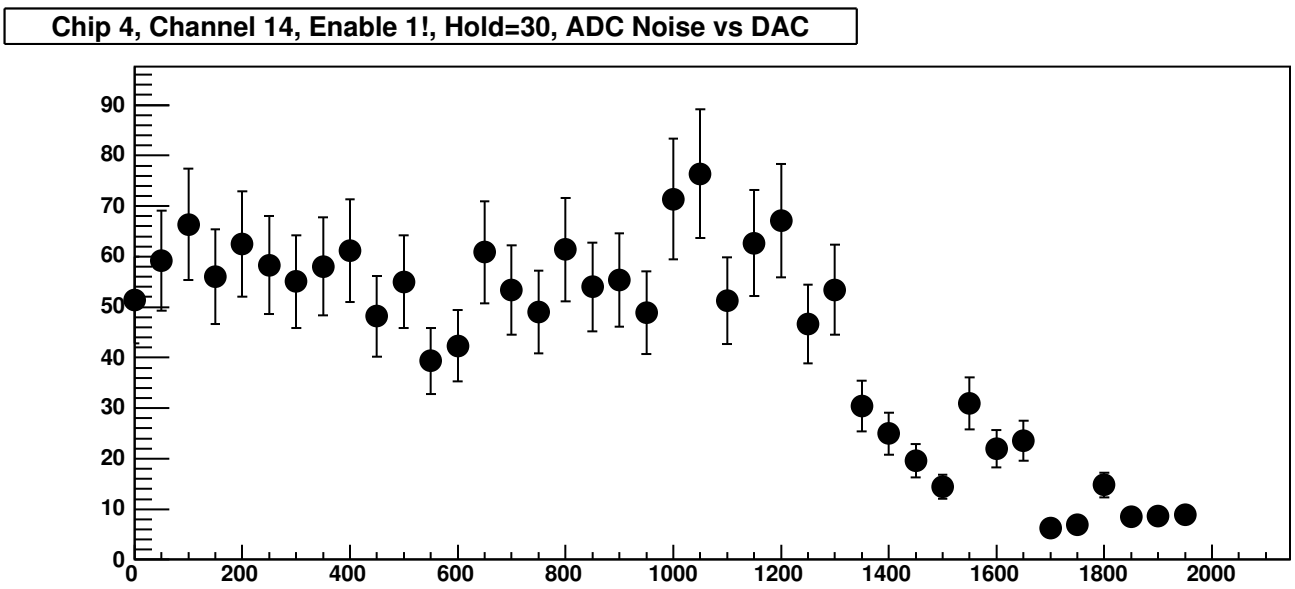

Chip 4, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
600
400
200
0
-200
-400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

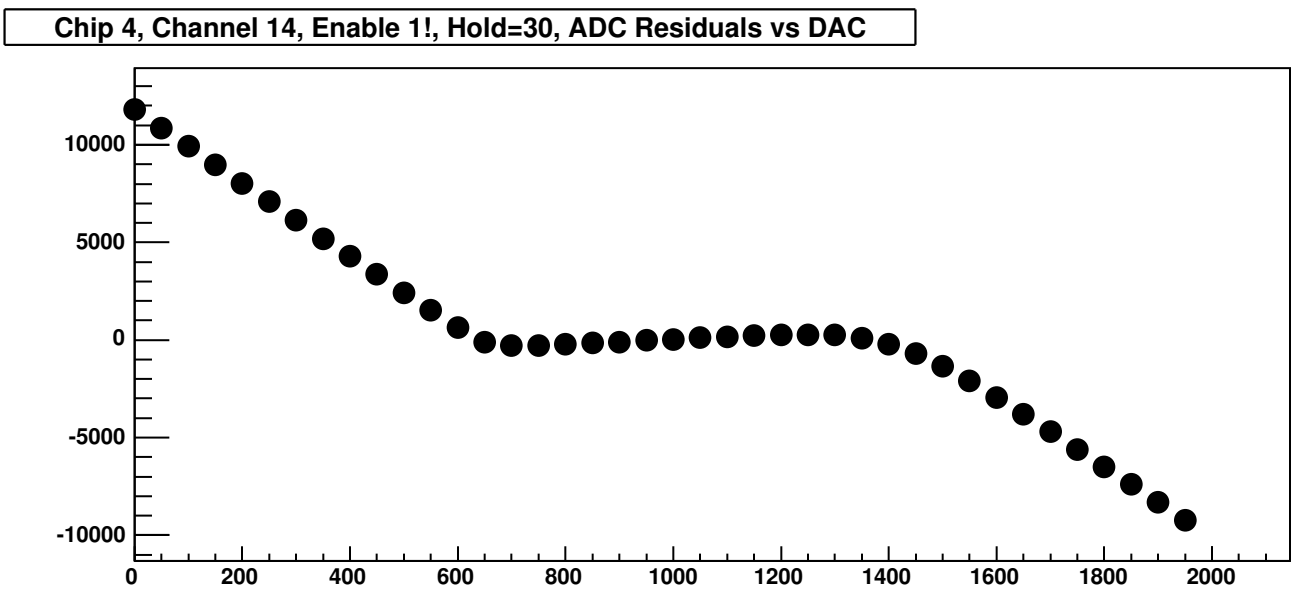

Chip 4, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 21.79 / 11
p0 -1109 ± 21.17
p1 0.3512 ± 0.01919
-200
-300
-400
-500
-600
-700
-800
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

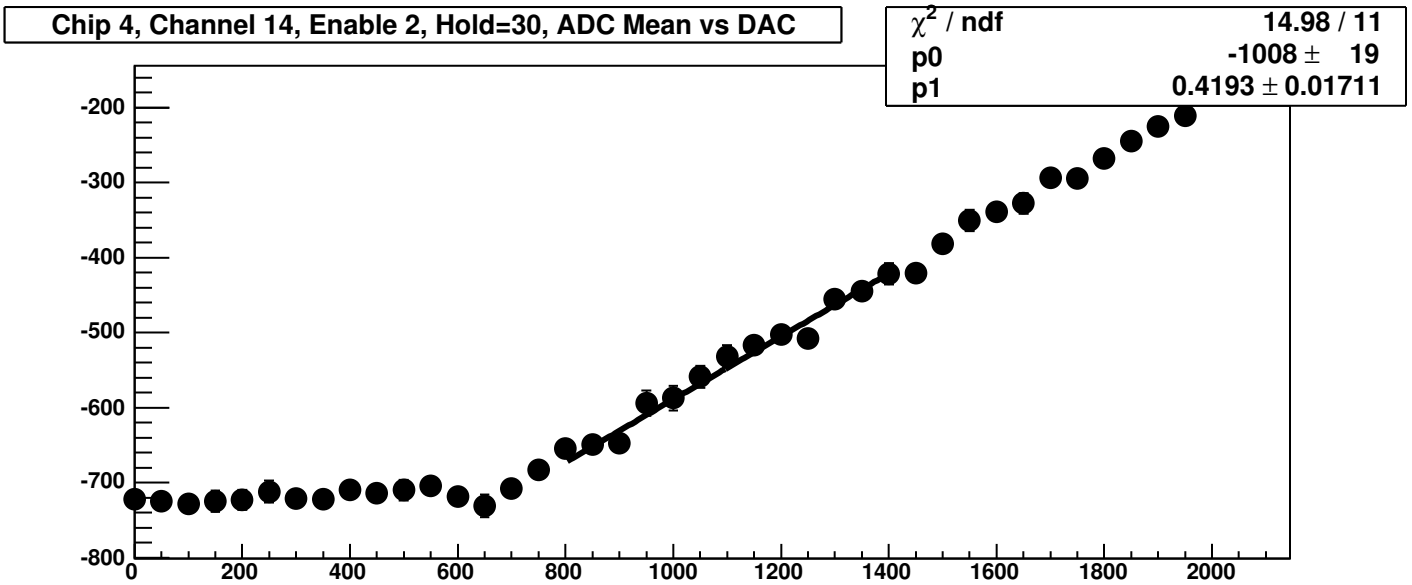

Chip 4, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

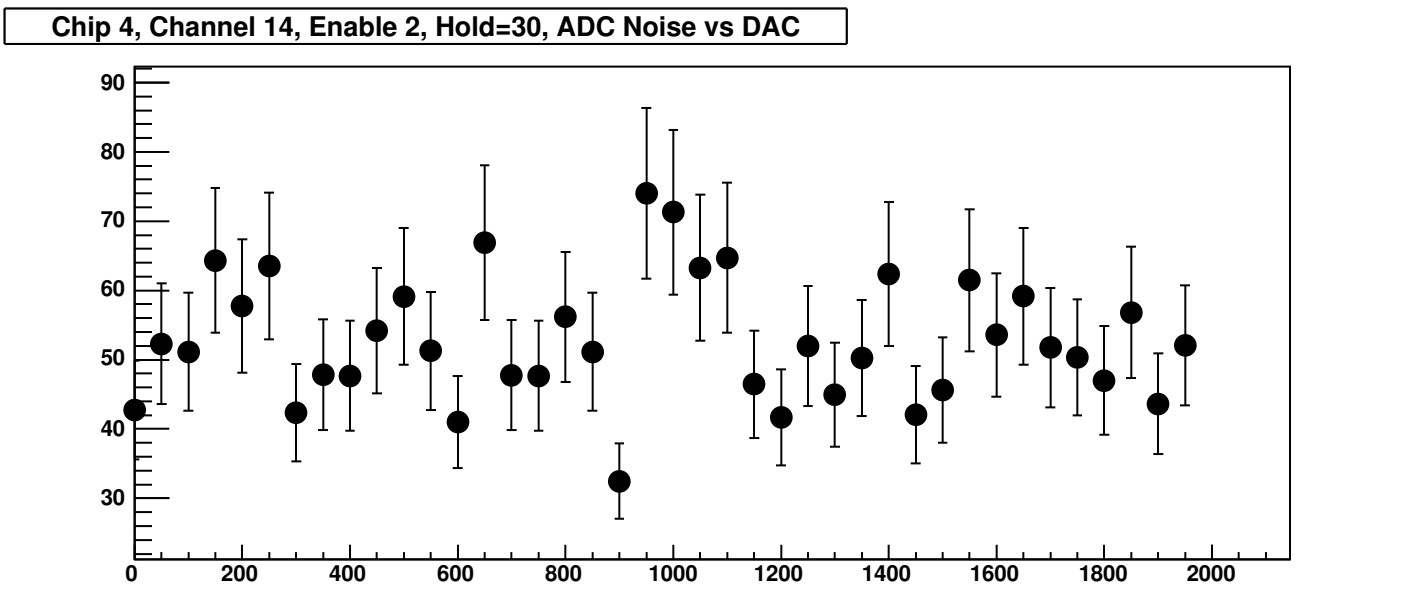

Chip 4, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

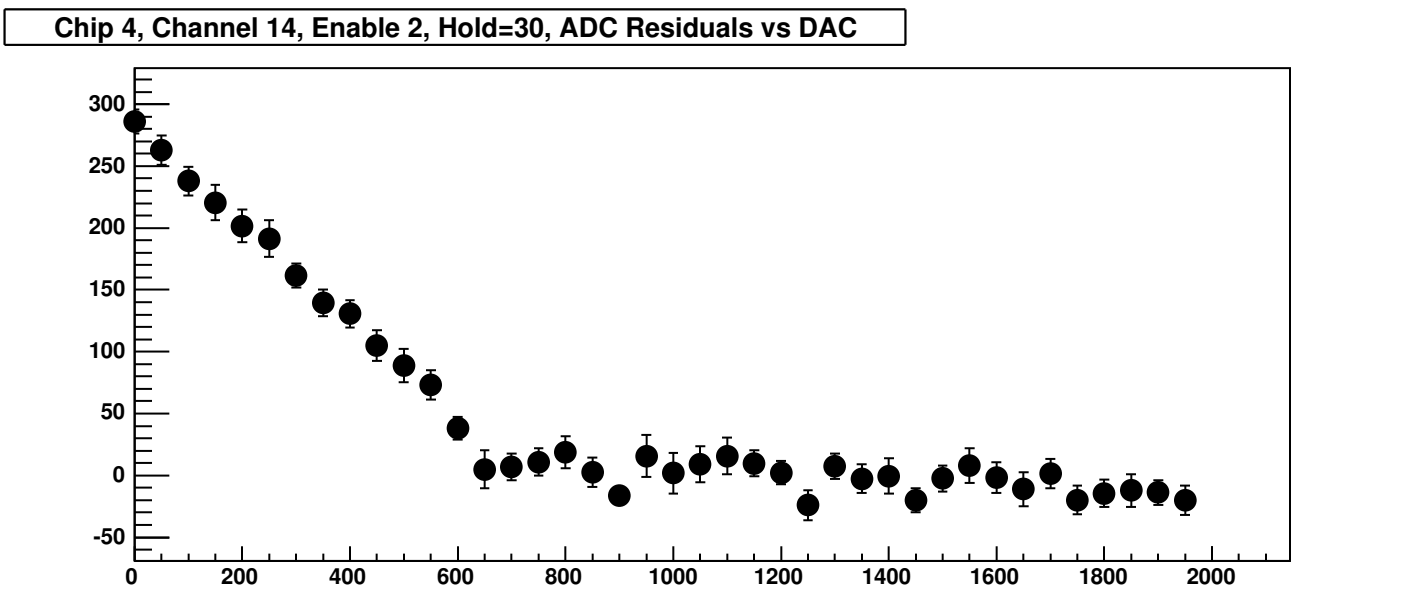

Chip 4, Channel 14, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 3863 / 11
p0 397.7 ± 17.59
p1 19.2 ± 0.01447
30000
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

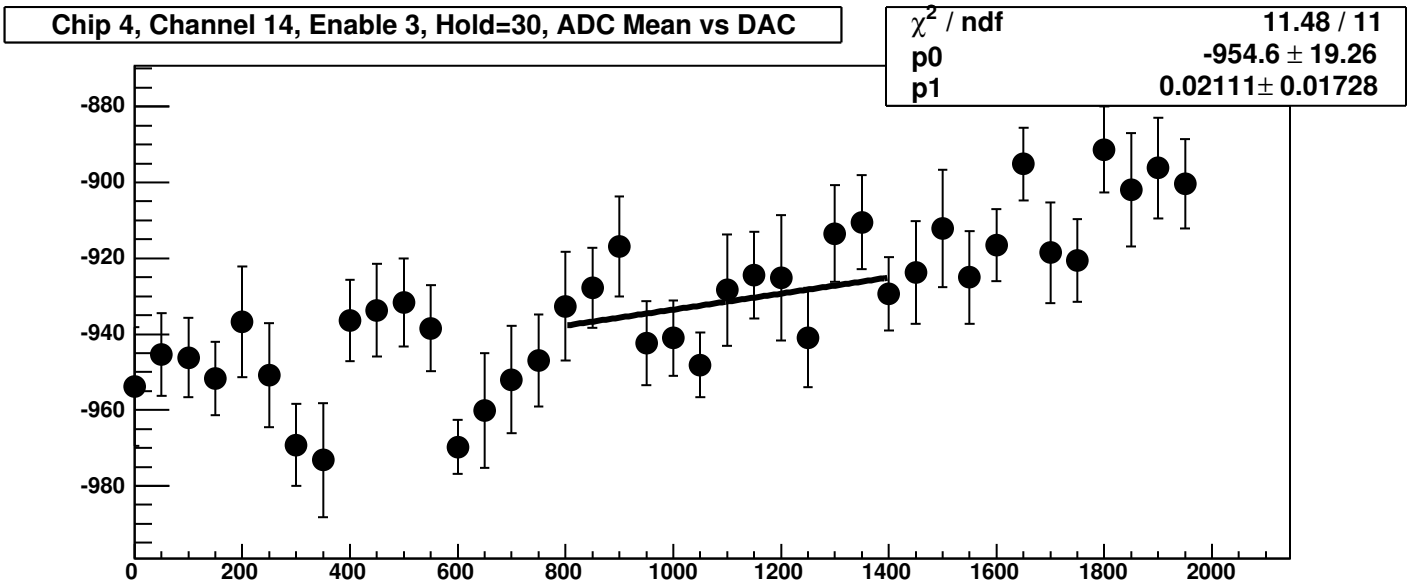

Chip 4, Channel 14, Enable 1!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

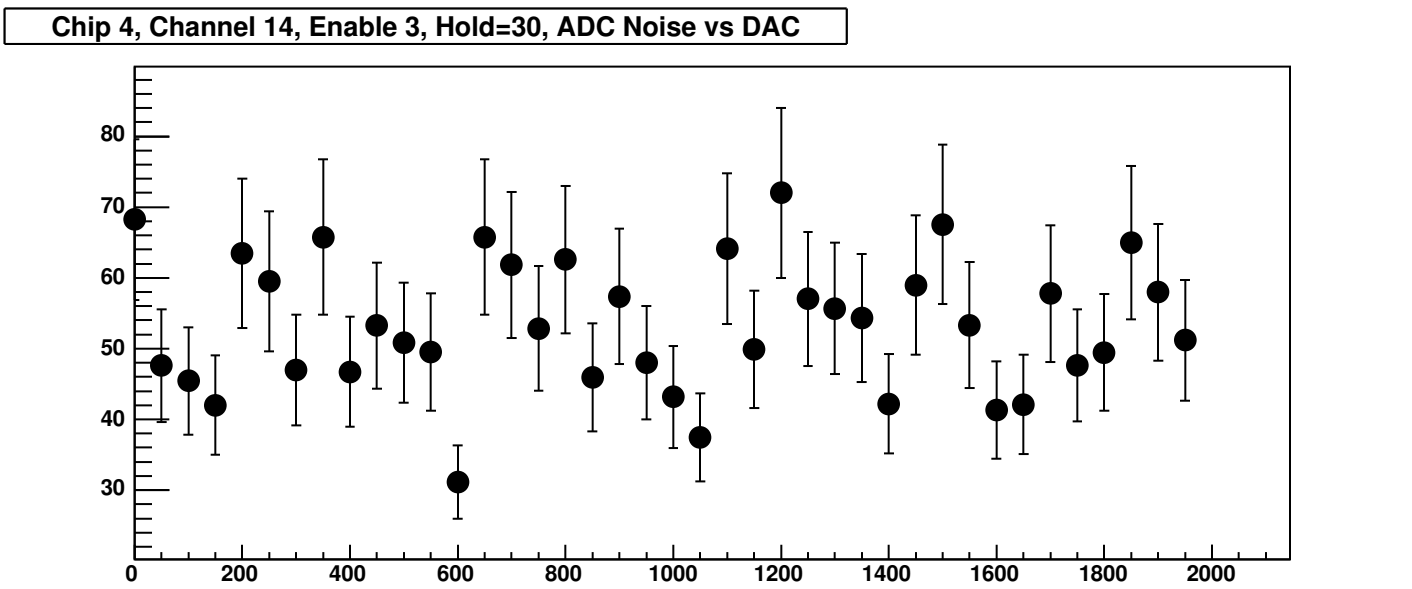

Chip 4, Channel 14, Enable 1!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

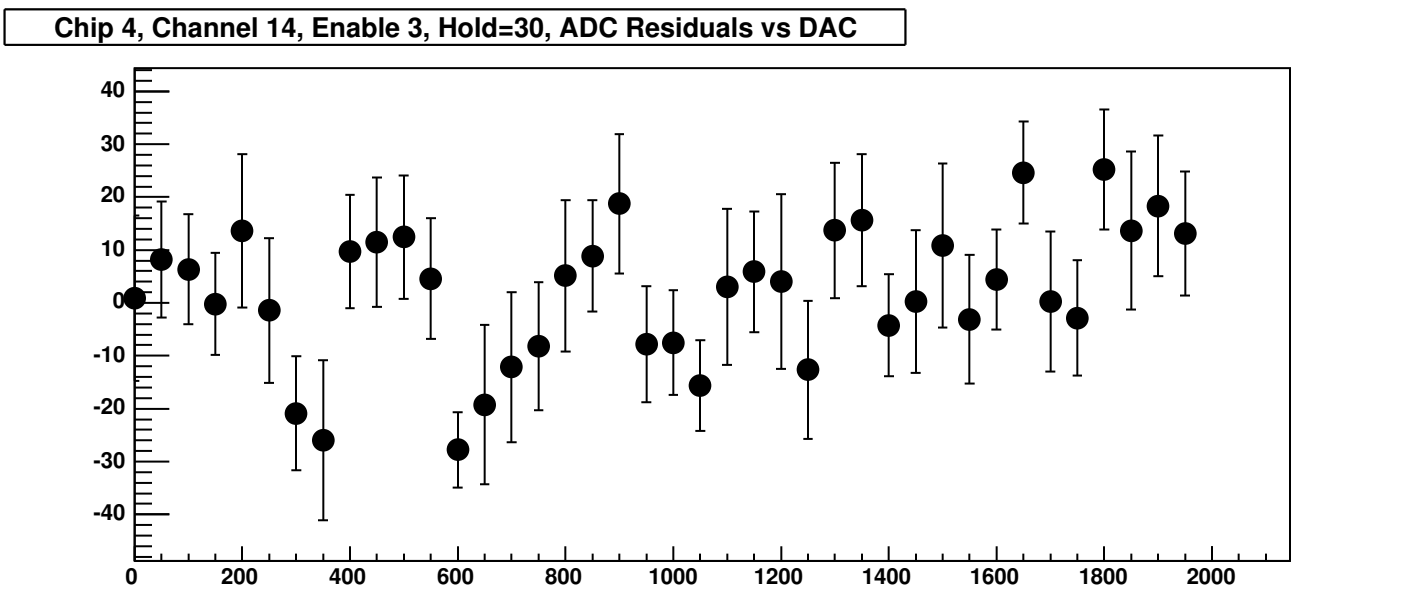

Chip 4, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 14.98 / 11
p0 -1008 ± 19
p1 0.4193 ± 0.01711
-200
-300
-400
-500
-600
-700
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

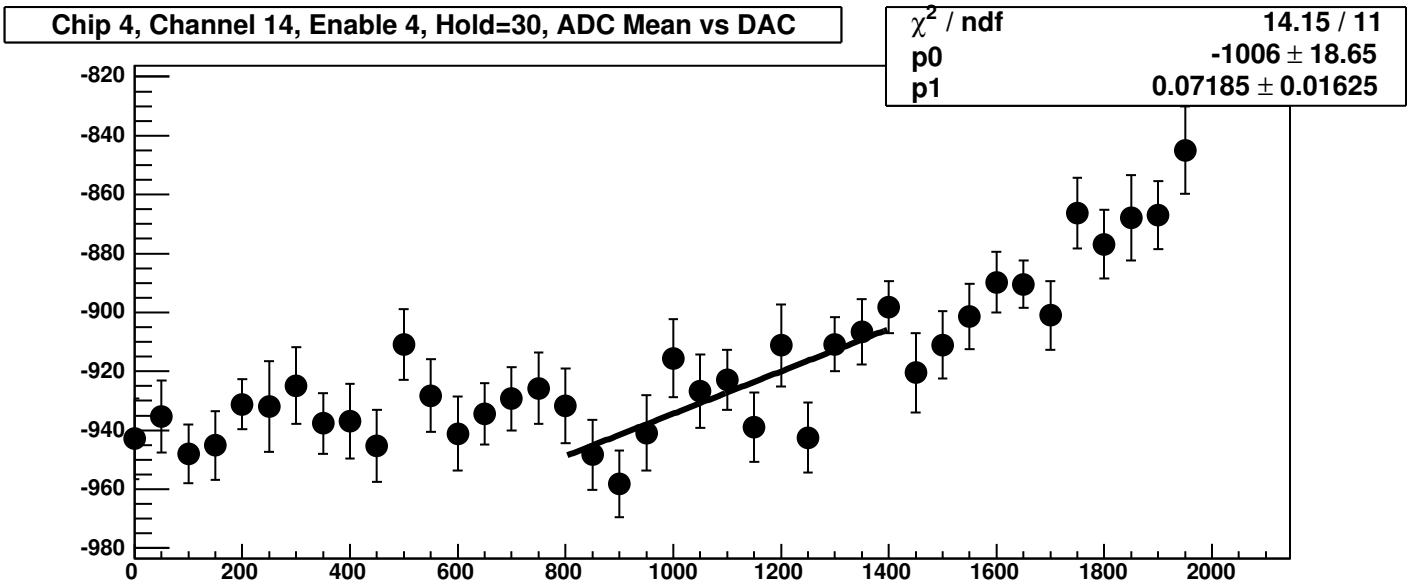

Chip 4, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

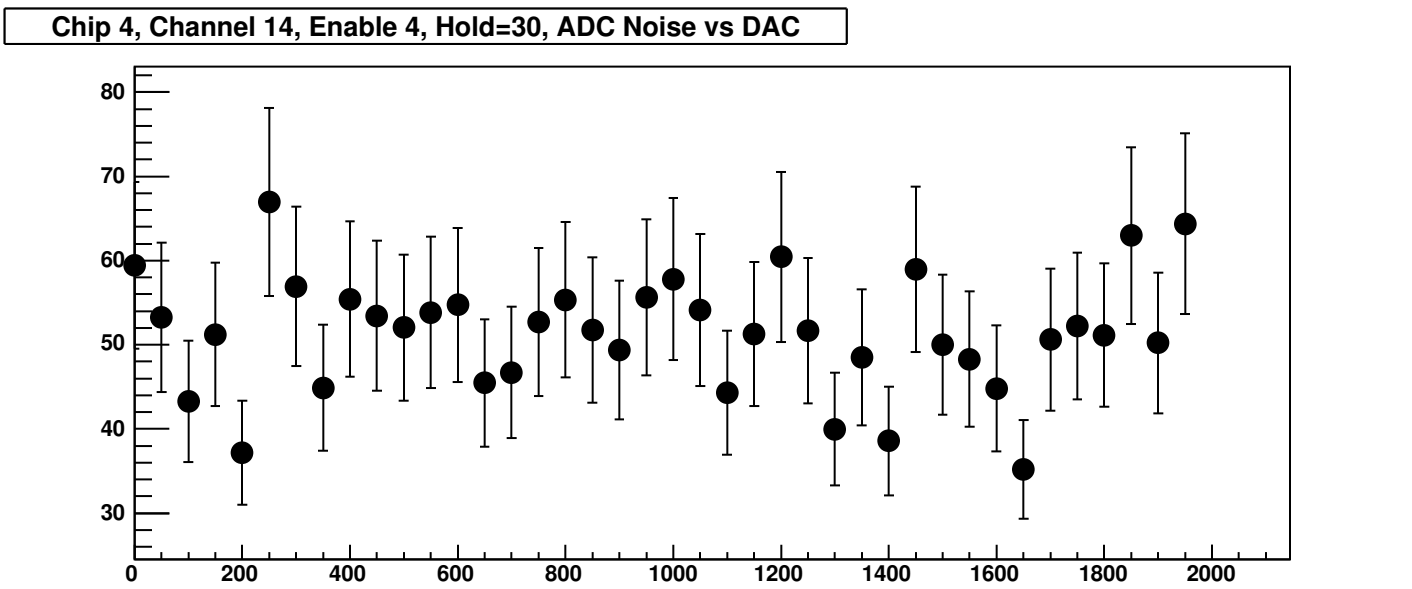

Chip 4, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

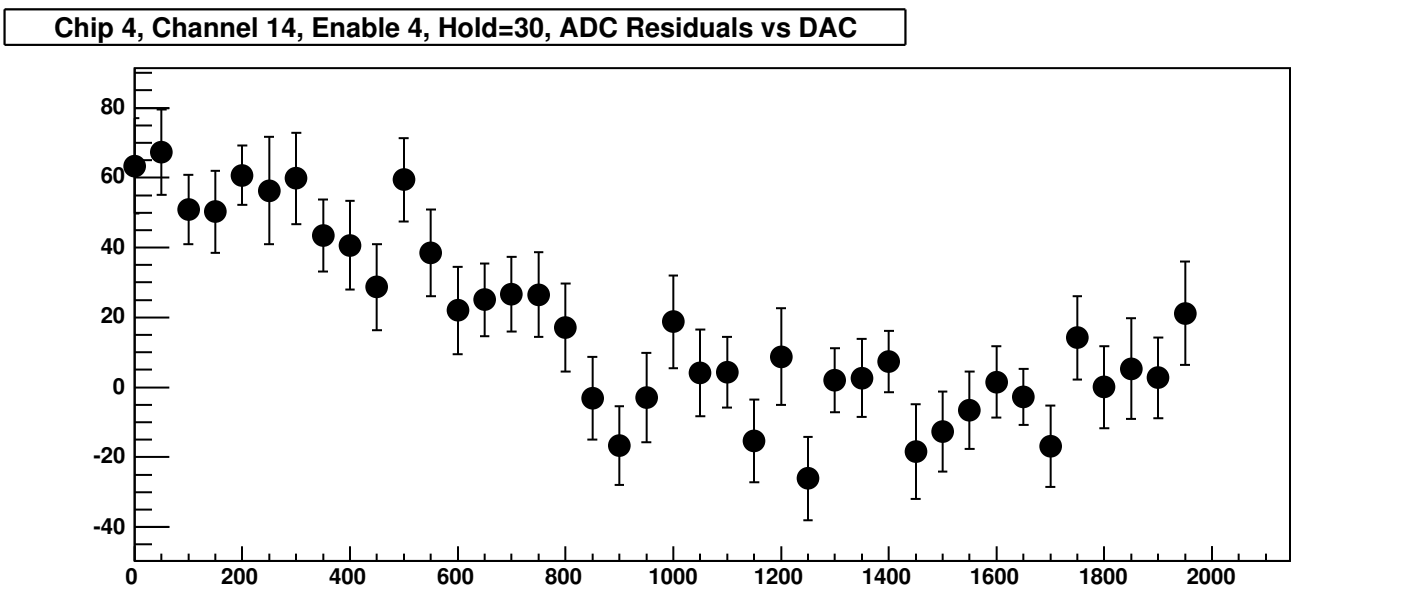

Chip 4, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 11.48 / 11
p0 -954.6 ± 19.26
p1 0.02111 ± 0.01728
-880
-900
-920
-940
-960
-980
0
200
400
600
800
1000
1200
1400
1600
1800
2000

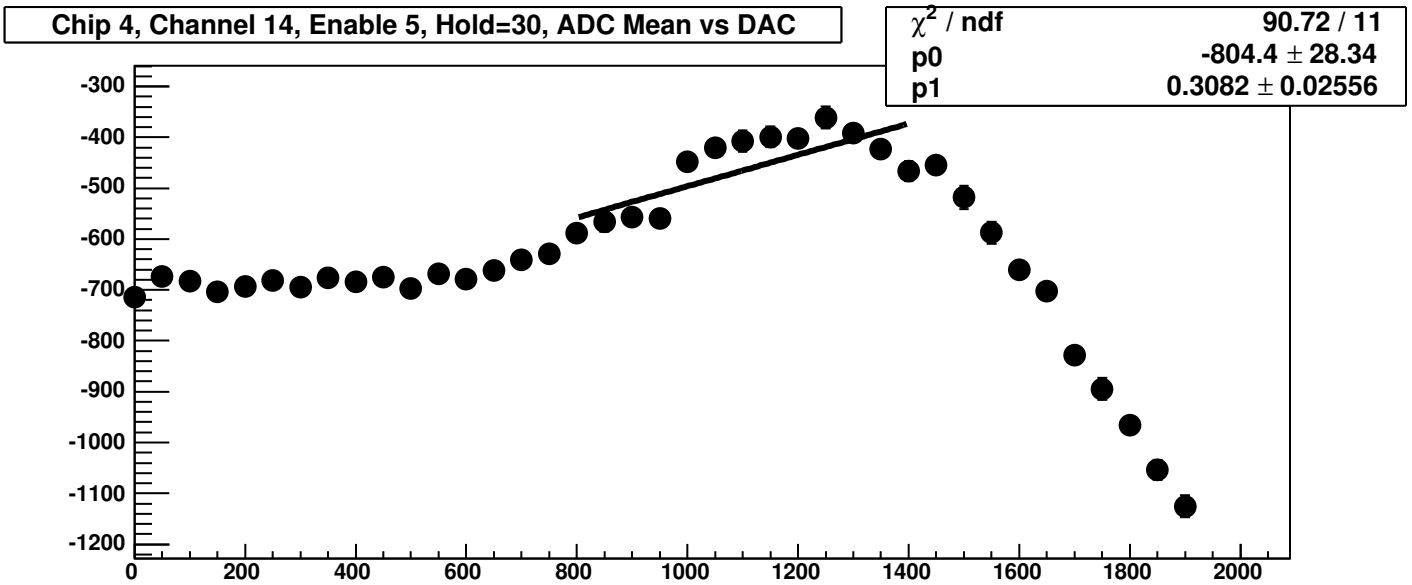

Chip 4, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

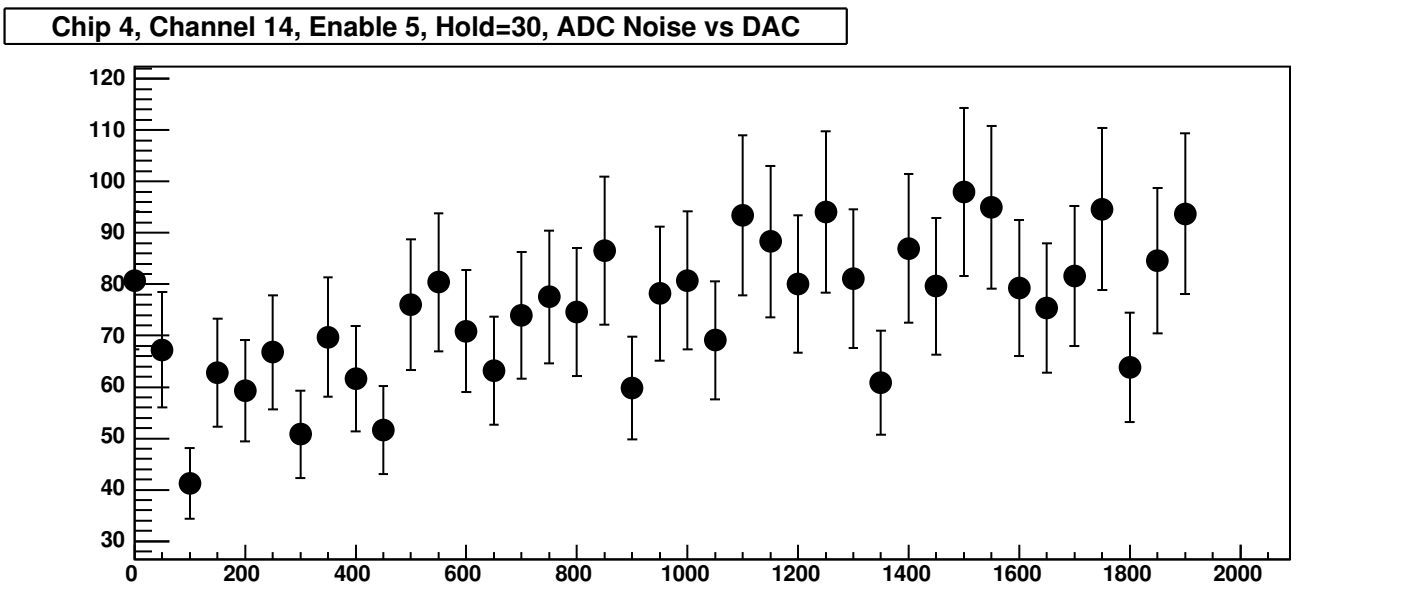

Chip 4, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
40
30
20
10
0
-10
-20
-30
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

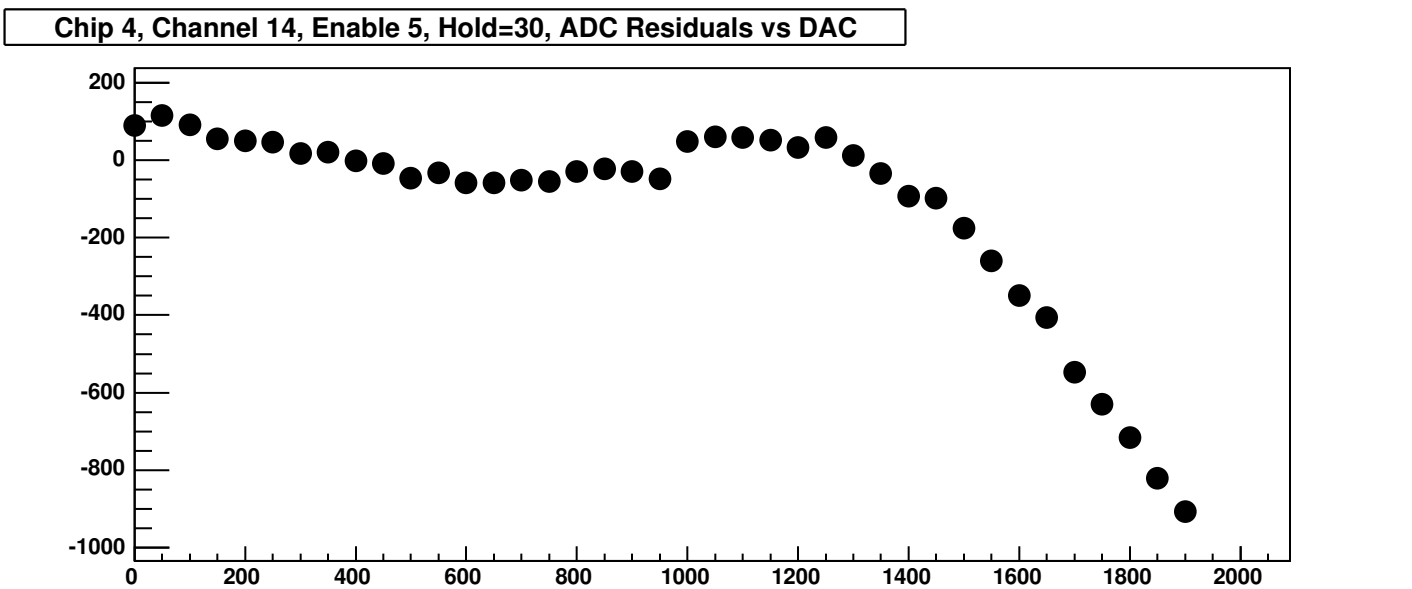

Chip 4, Channel 14, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 14.15 / 11
p0 -1006 ± 18.65
p1 0.07185 ± 0.01625
-820
-840
-860
-880
-900
-920
-940
-960
-980
0
200
400
600
800
1000
1200
1400
1600
1800
2000

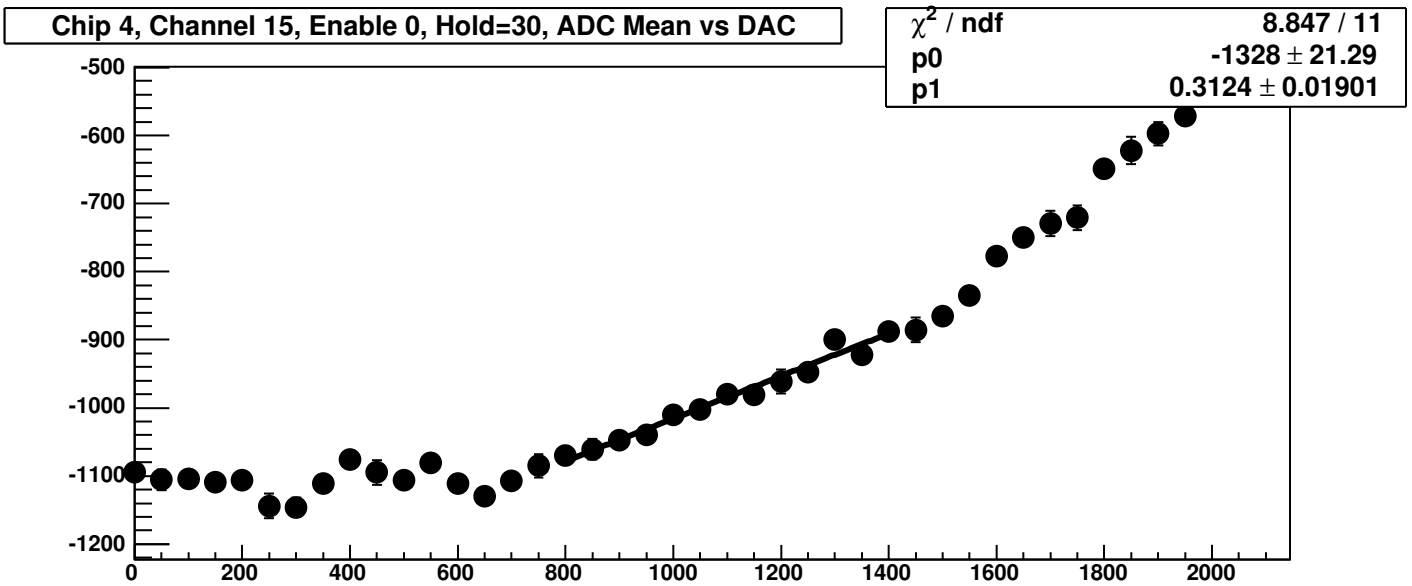

Chip 4, Channel 14, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

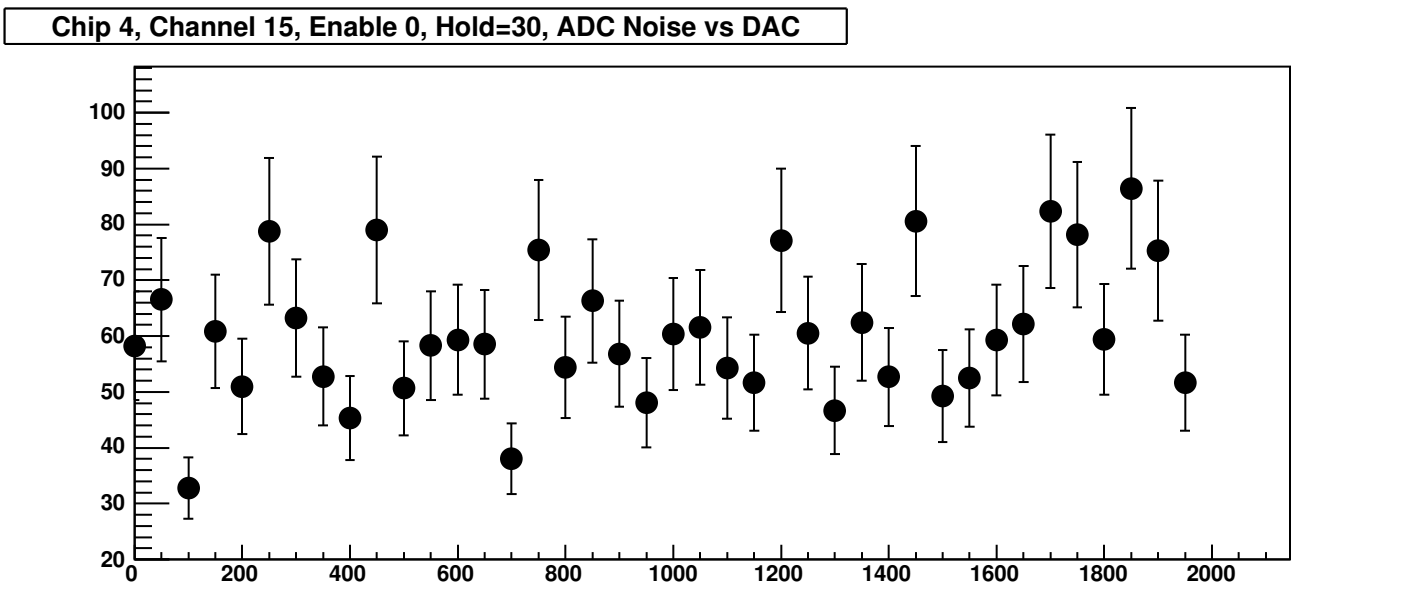

Chip 4, Channel 14, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

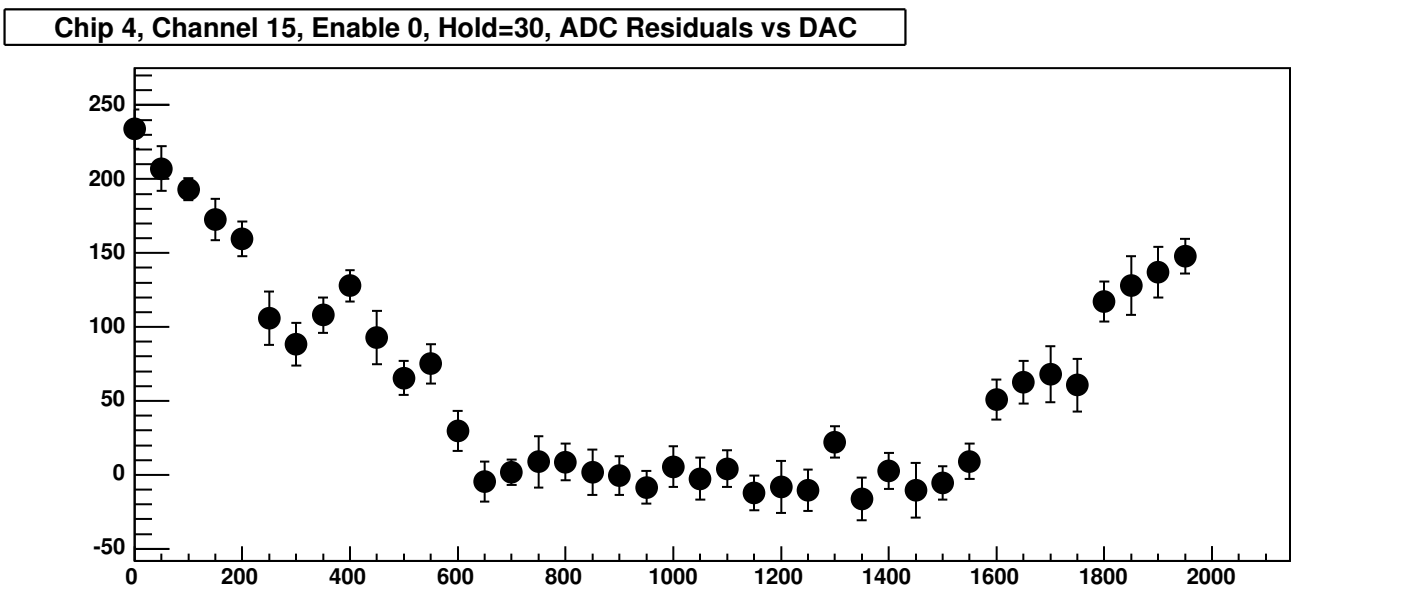

Chip 4, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 90.72 / 11
p0 -804.4 ± 28.34
p1 0.3082 ± 0.02556
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

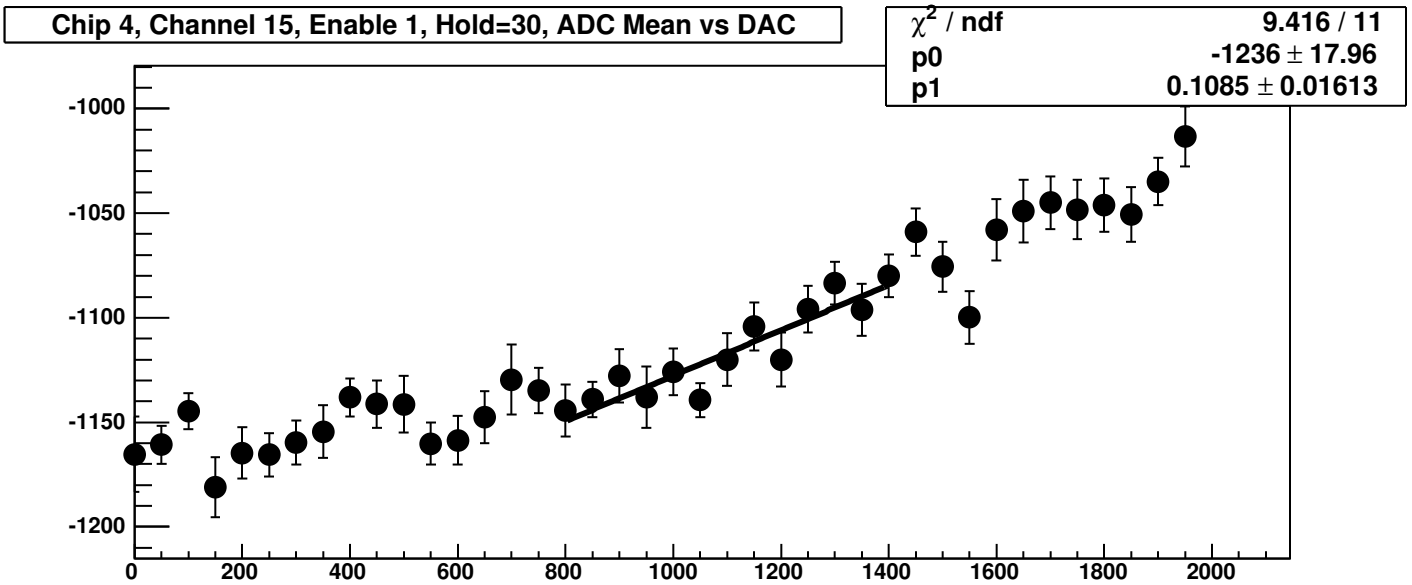

Chip 4, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
120
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

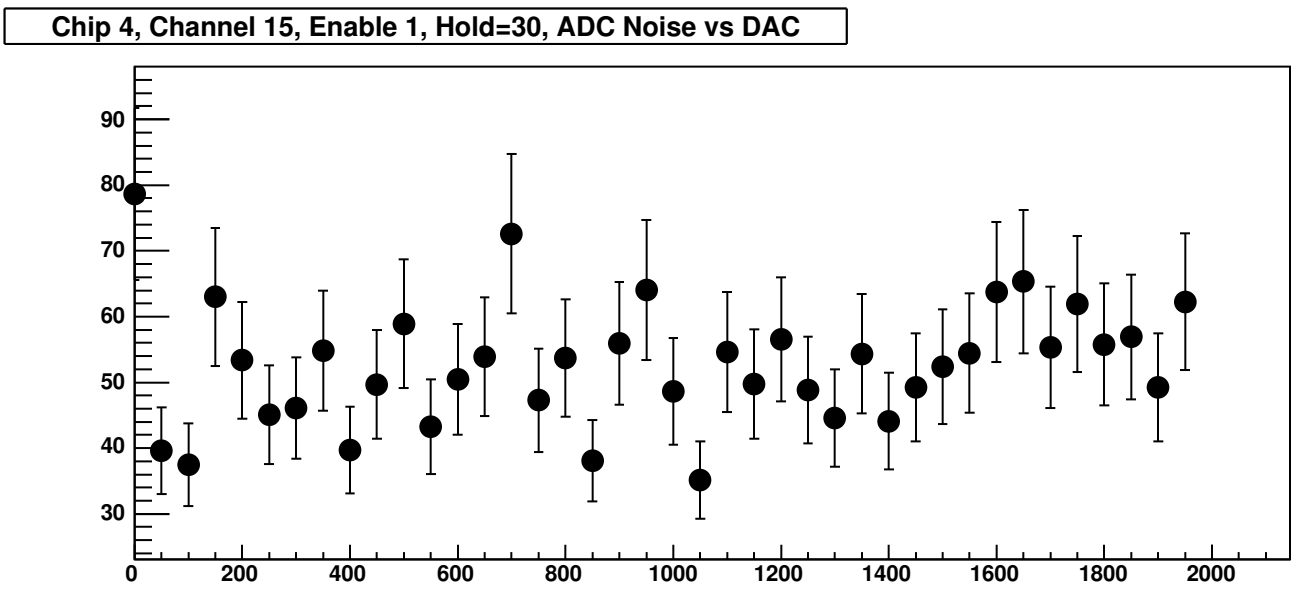

Chip 4, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

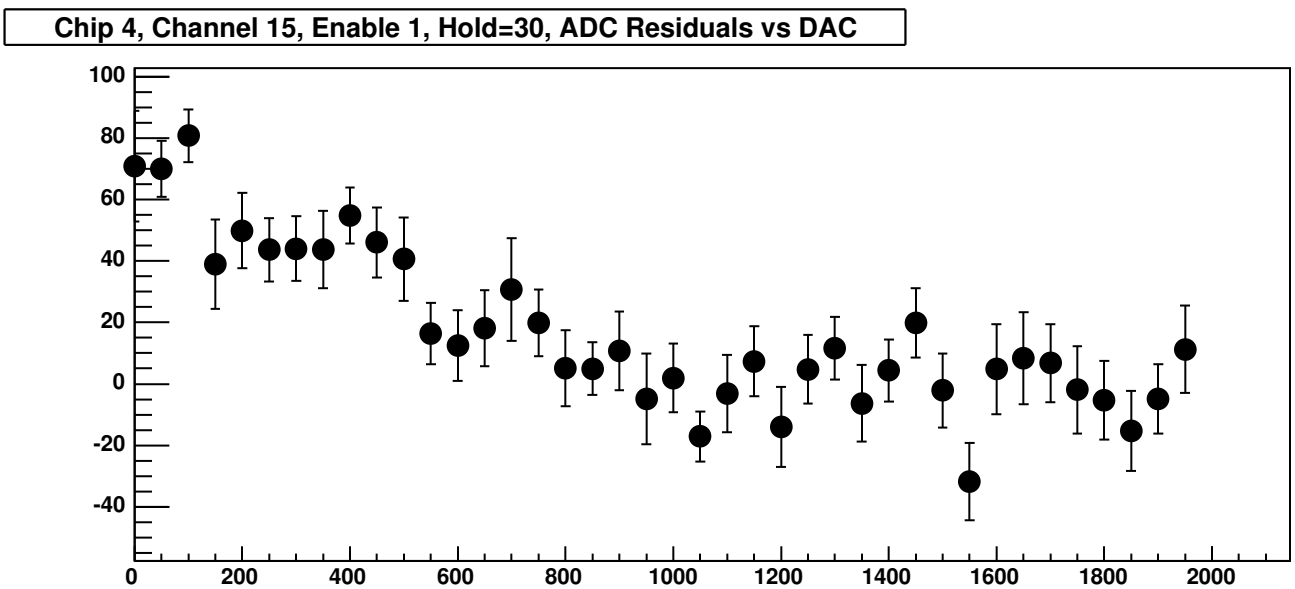

Chip 4, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 8.847 / 11
p0 -1328 ± 21.29
p1 0.3124 ± 0.01901
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

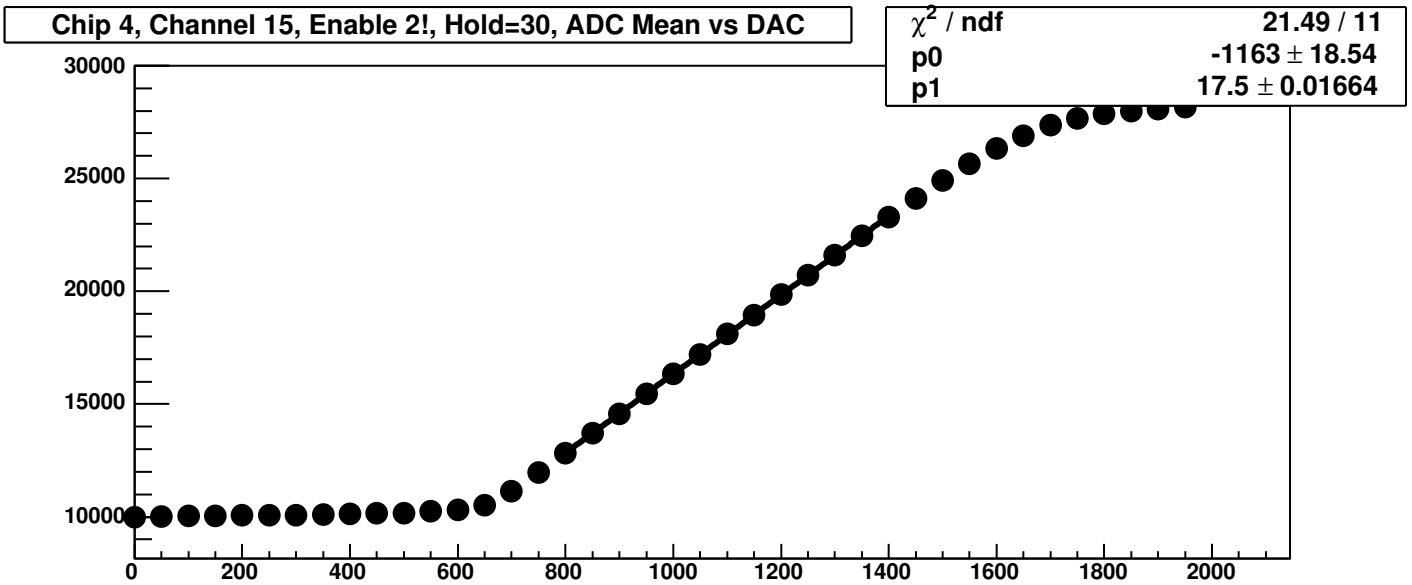

Chip 4, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

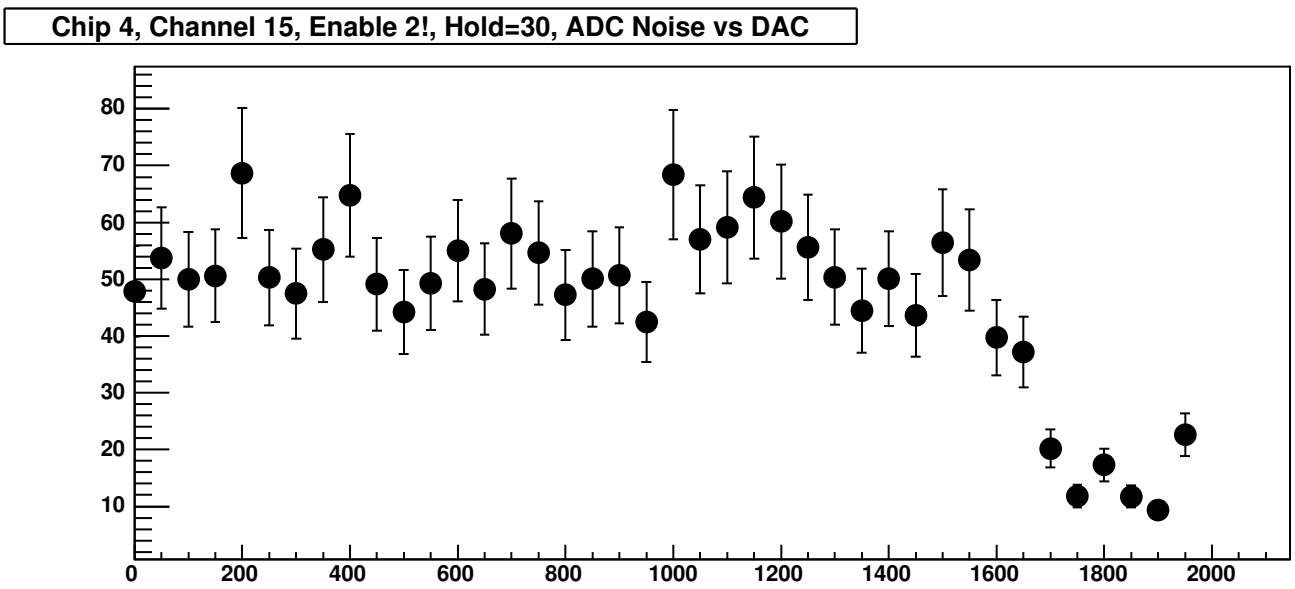

Chip 4, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

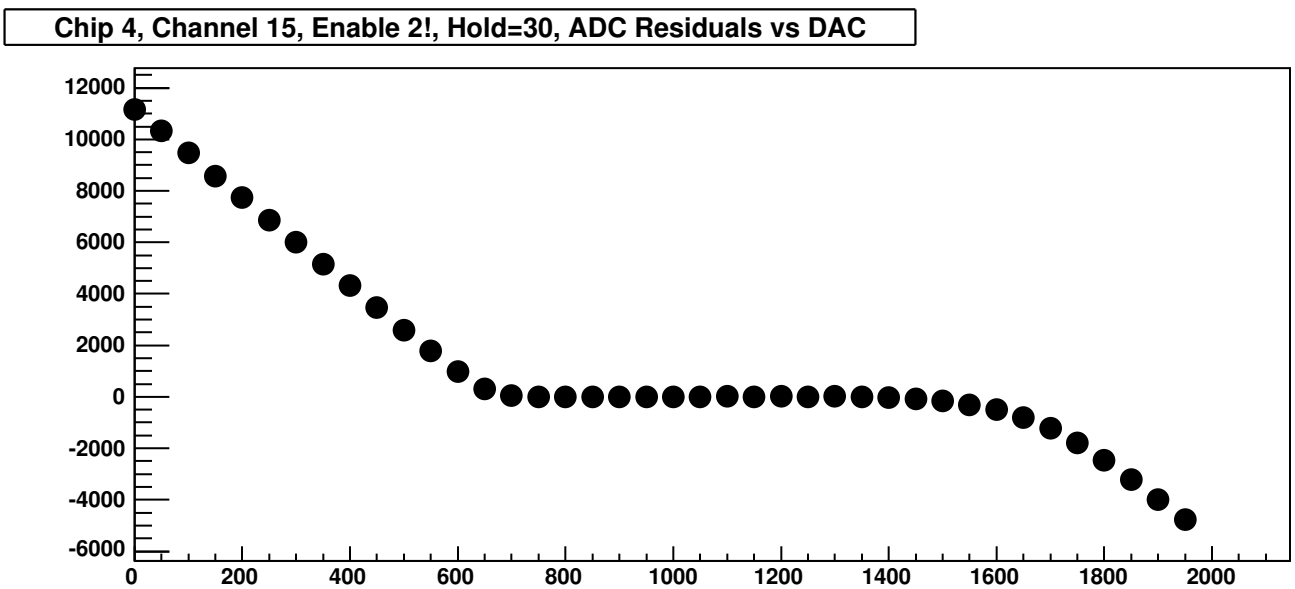

Chip 4, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 9.416 / 11
p0 -1236 ± 17.96
p1 0.1085 ± 0.01613
-1000
-1050
-1100
-1150
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

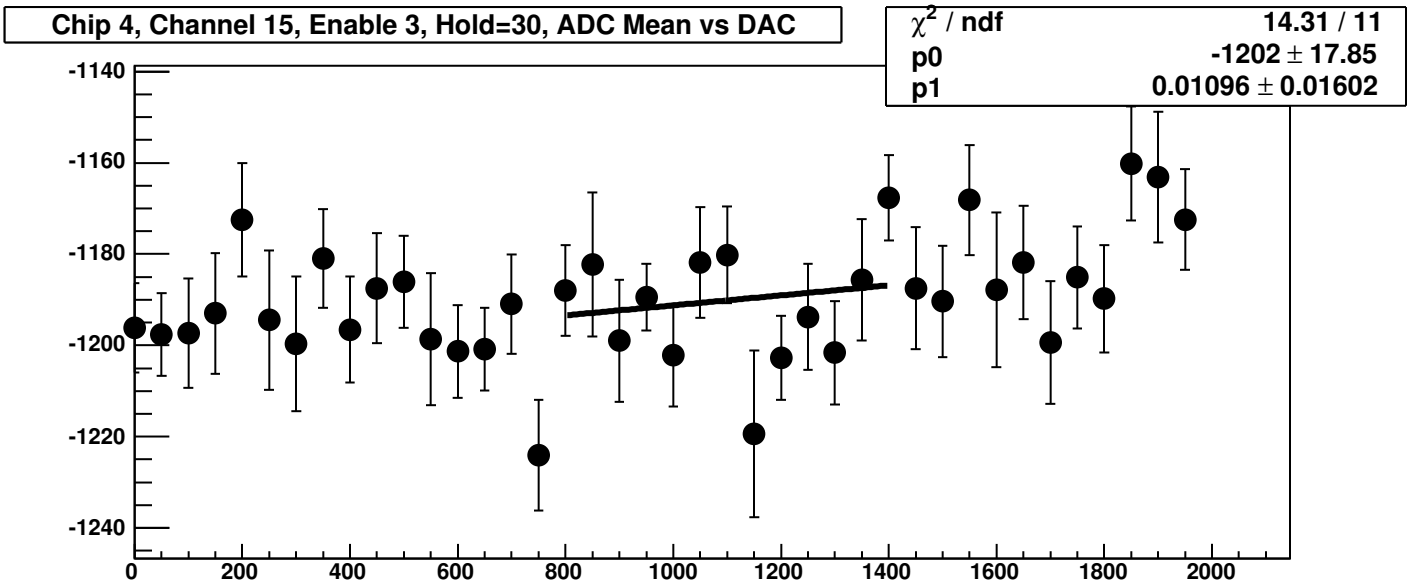

Chip 4, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

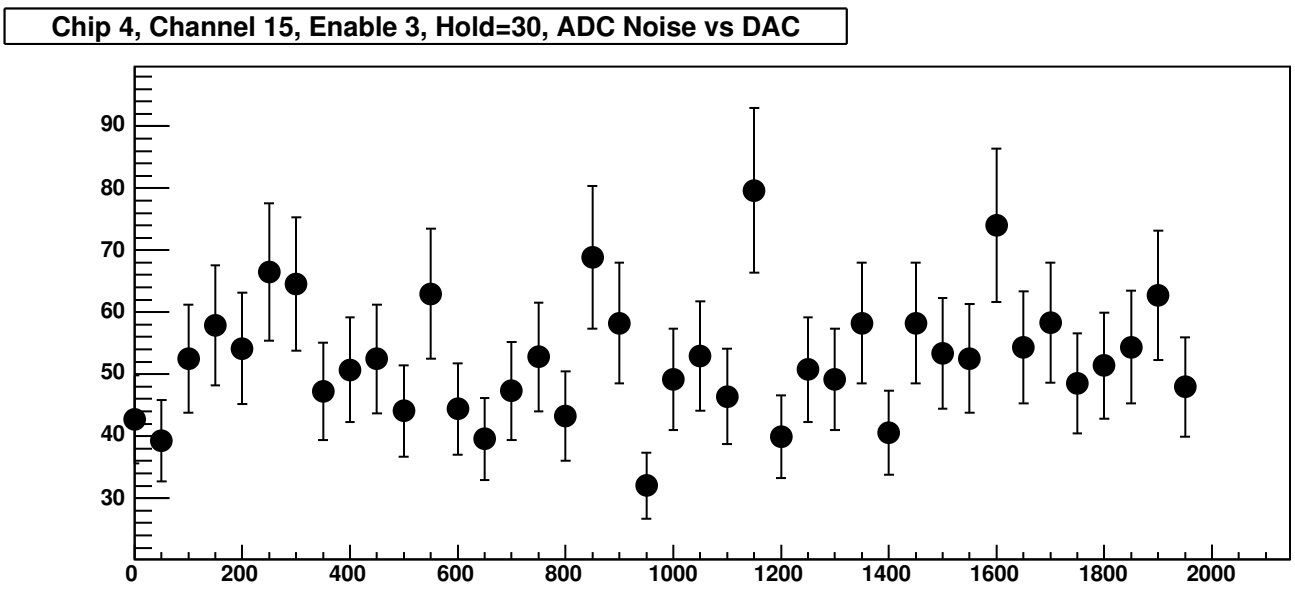

Chip 4, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

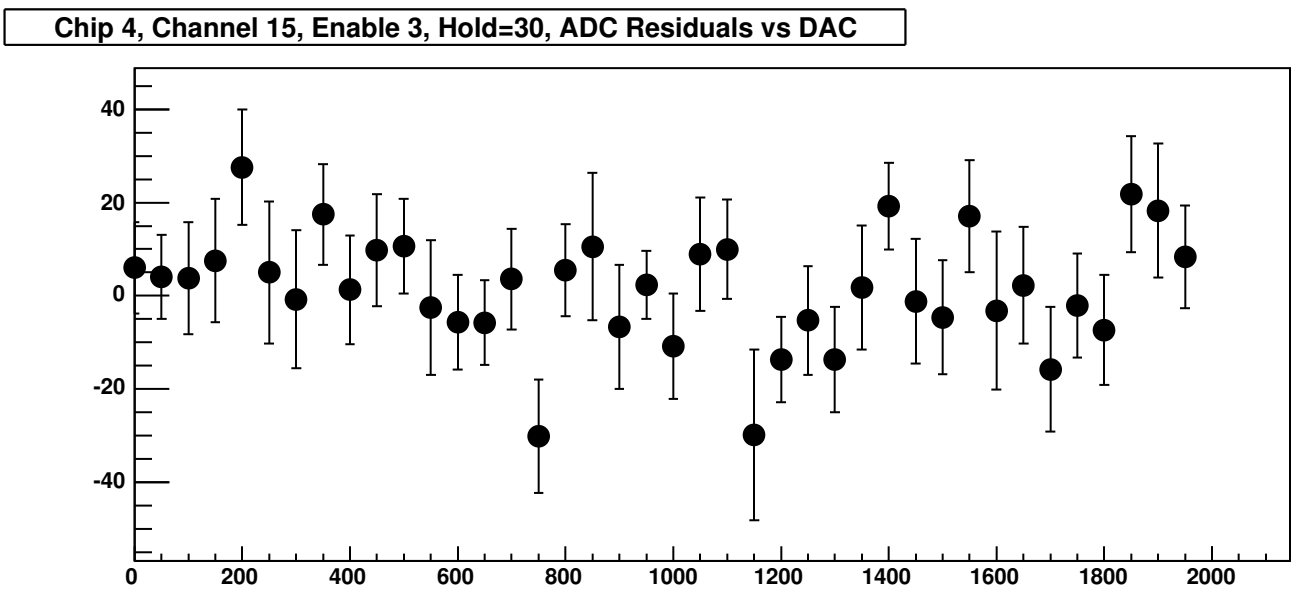

Chip 4, Channel 15, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 21.49 / 11
p0 -1163 ± 18.54
p1 17.5 ± 0.01664
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 15, Enable 2!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

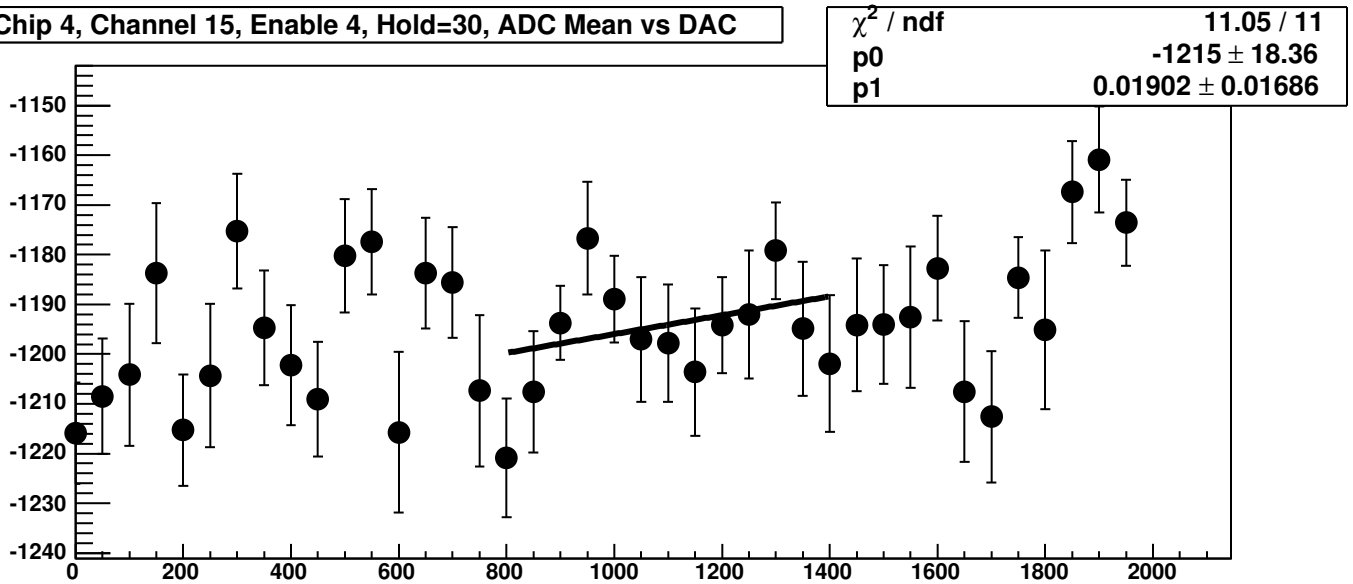

Chip 4, Channel 15, Enable 2!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 14.31 / 11
p0 -1202 ± 17.85
p1 0.01096 ± 0.01602
-1140
-1160
-1180
-1200
-1220
-1240
0
200
400
600
800
1000
1200
1400
1600
1800
2000

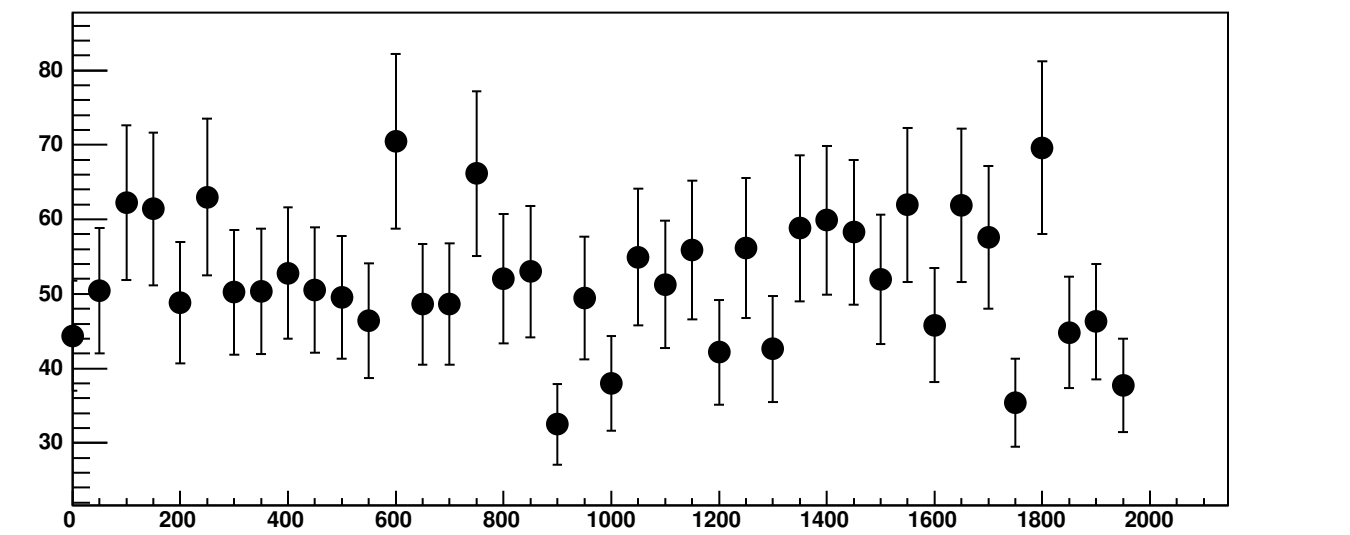

Chip 4, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

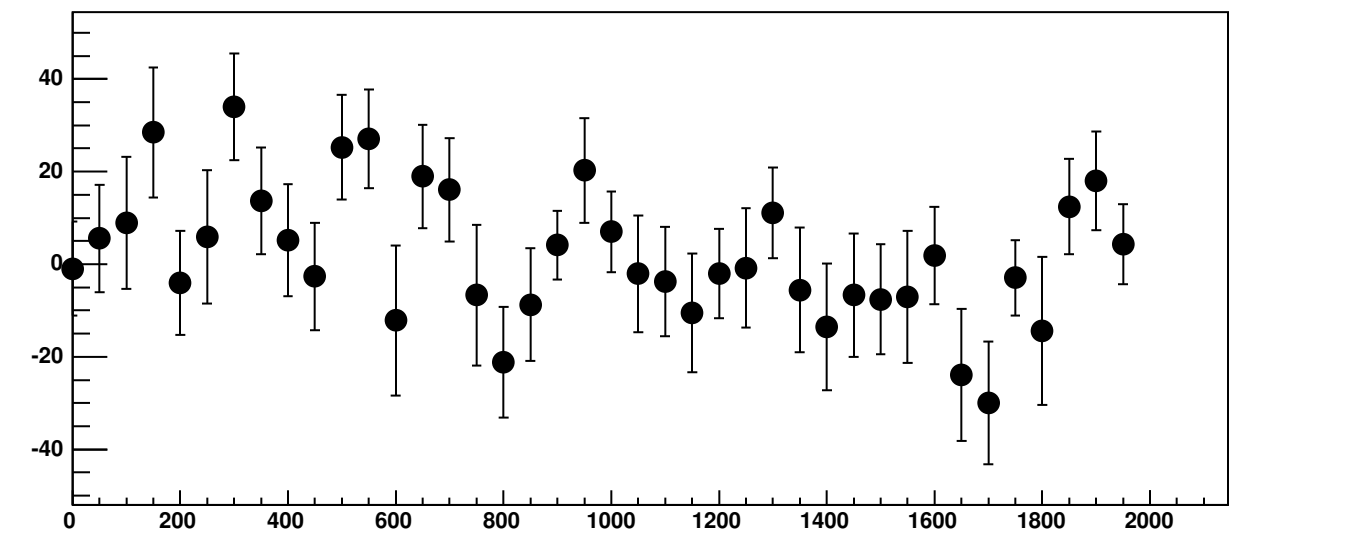
Chip 4, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC

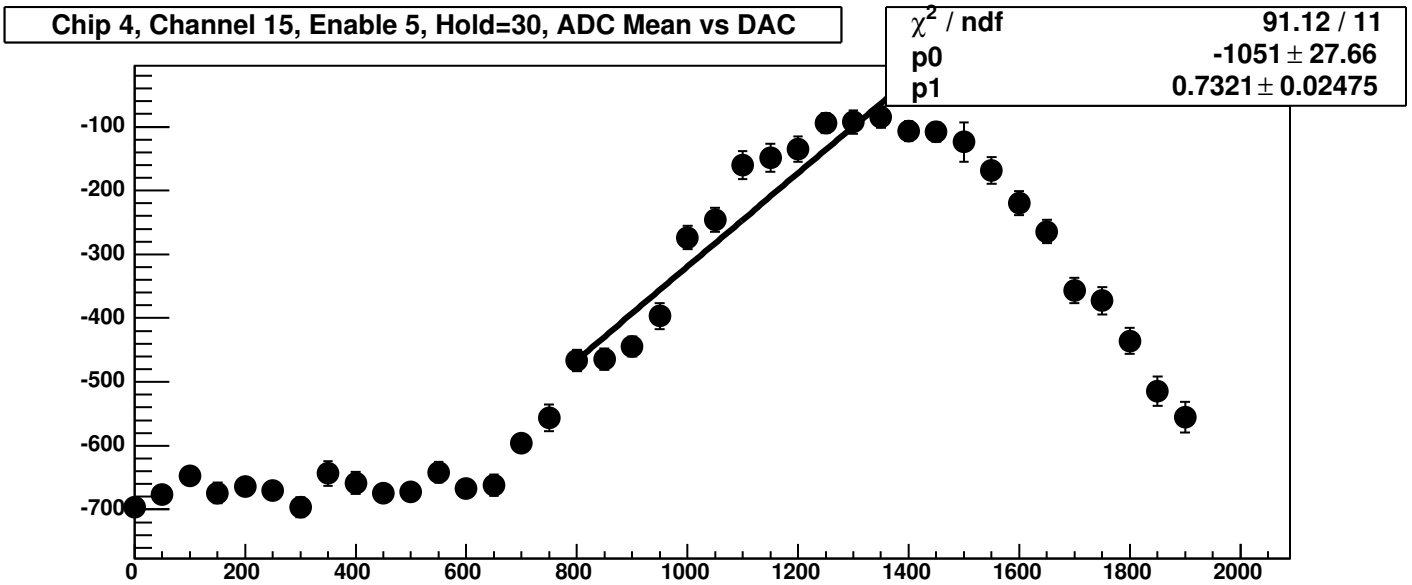


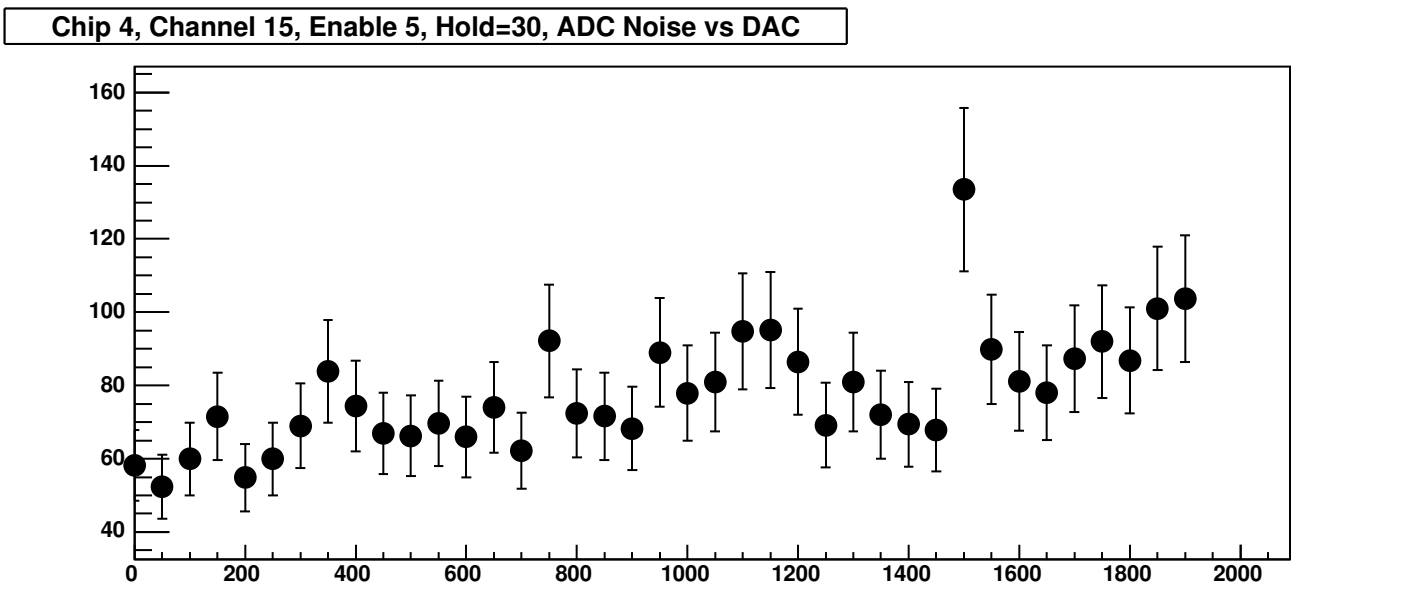
Chip 4, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC

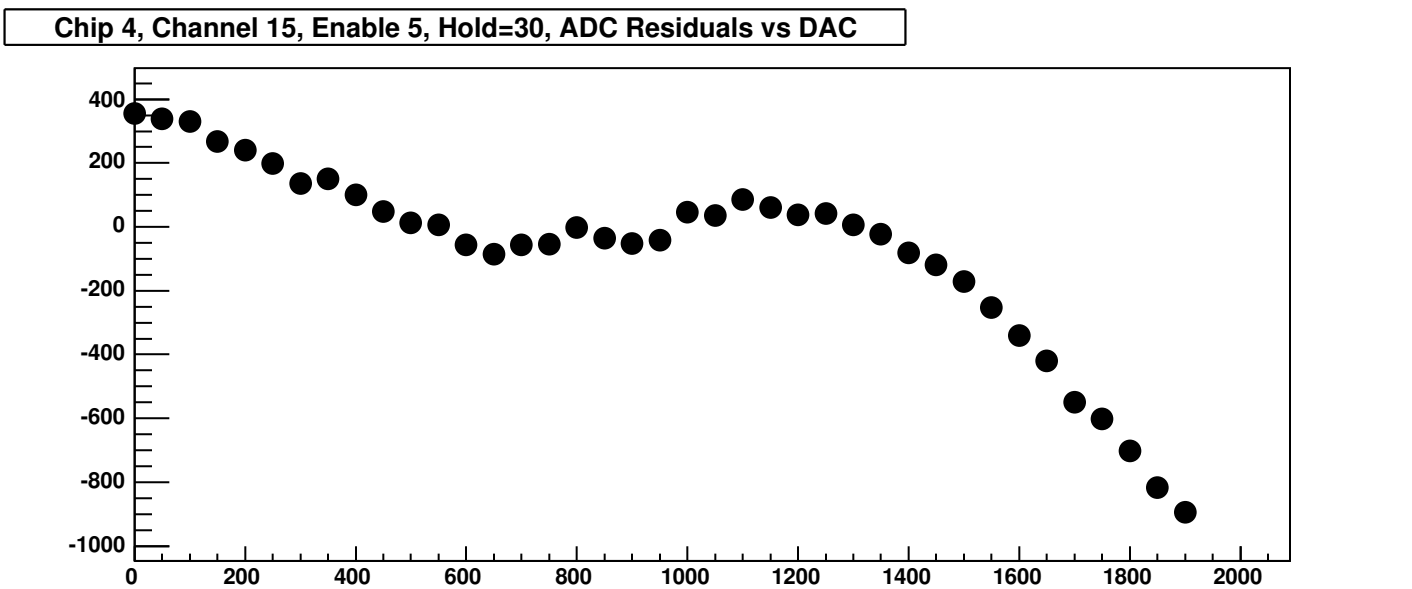


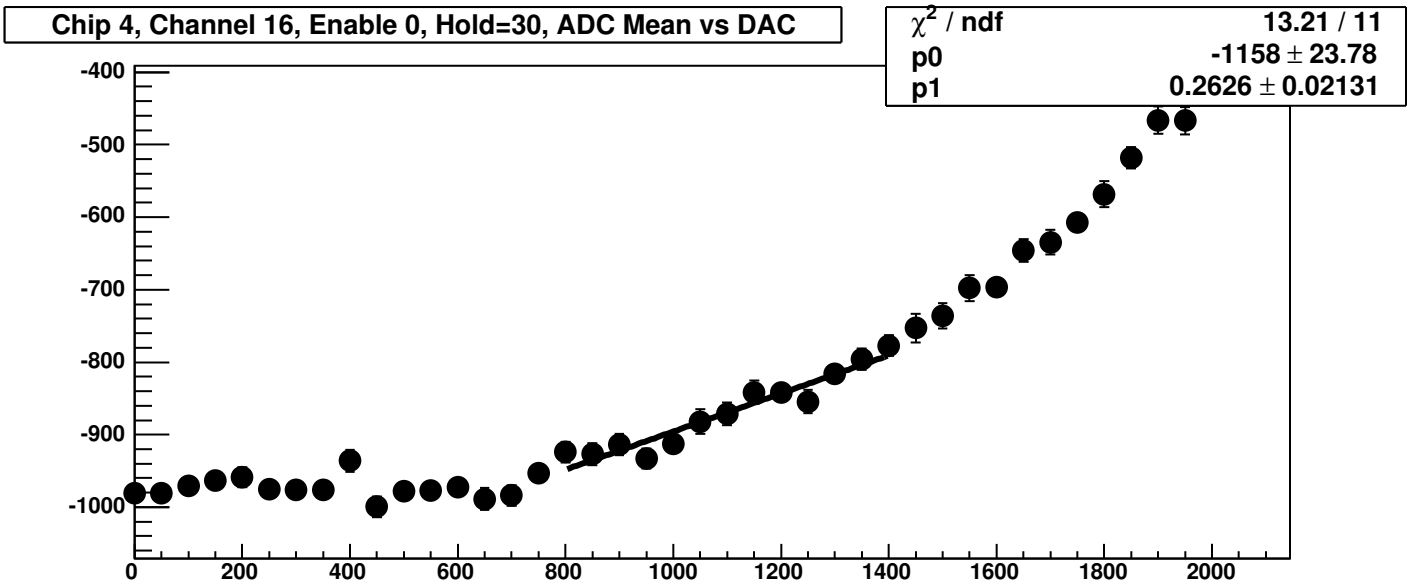
Chip 4, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC

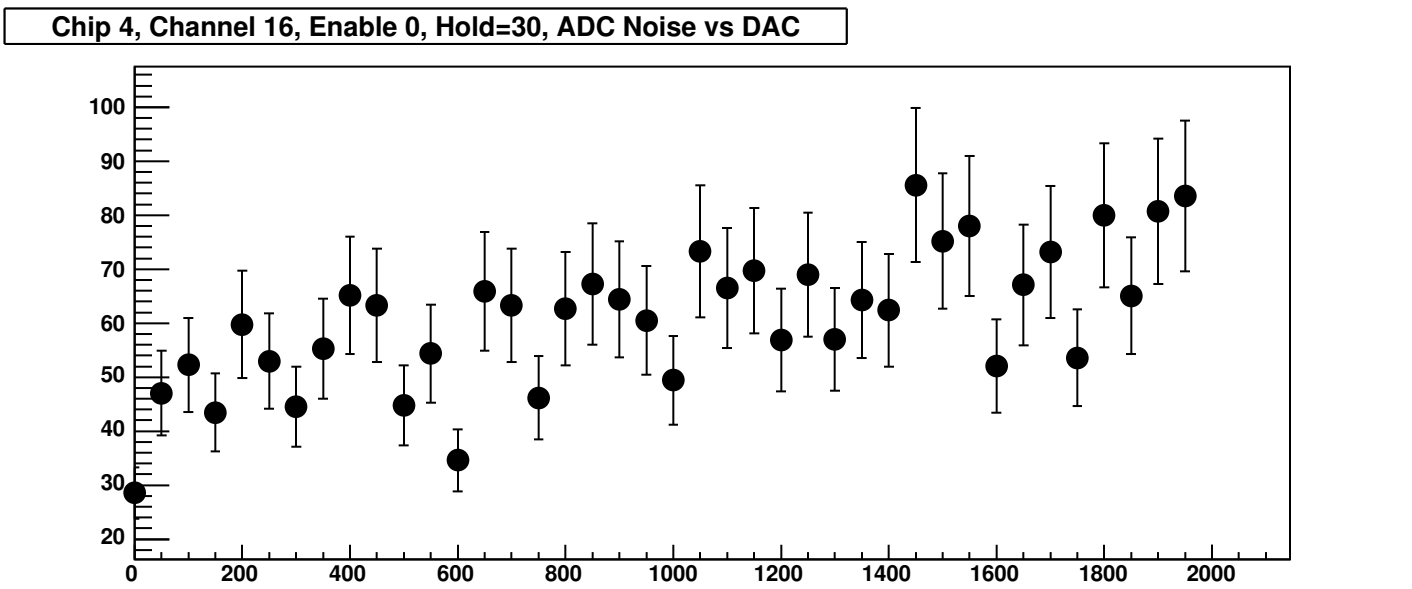


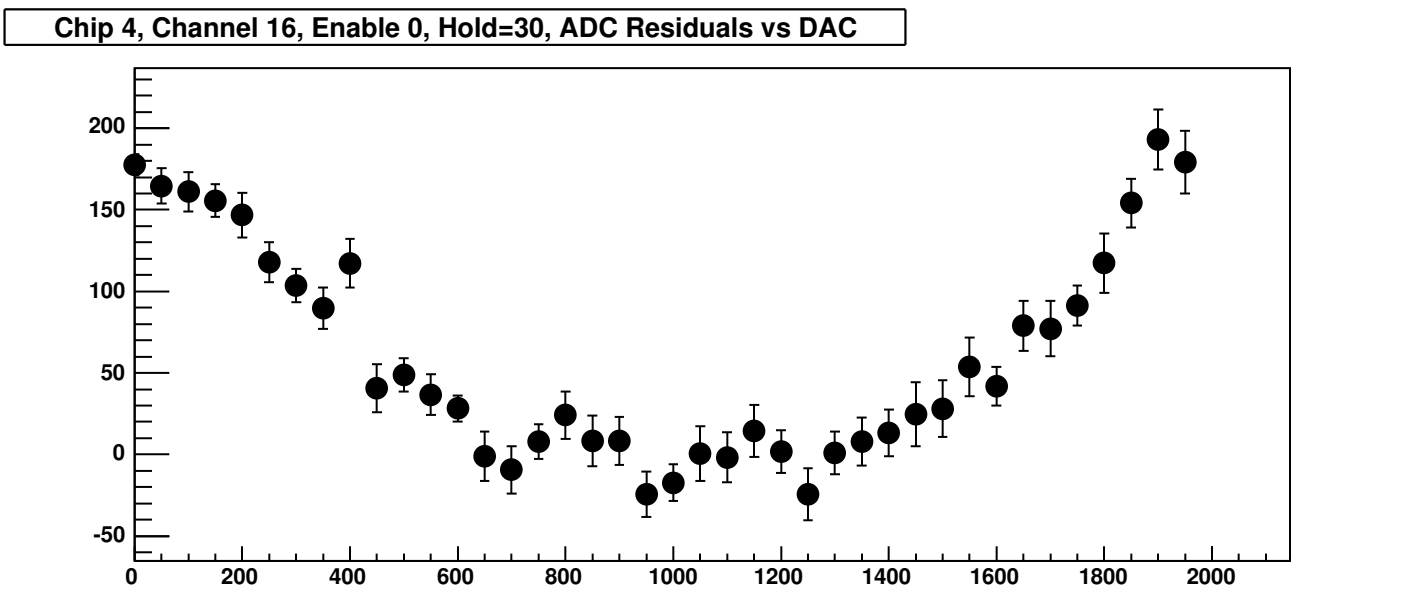

Chip 4, Channel 15, Enable 5, Hold=30, ADC Mean vs DAC
χ^2 / ndf 91.12 / 11
p0 -1051 ± 27.66
p1 0.7321 ± 0.02475
-100
-200
-300
-400
-500
-600
-700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

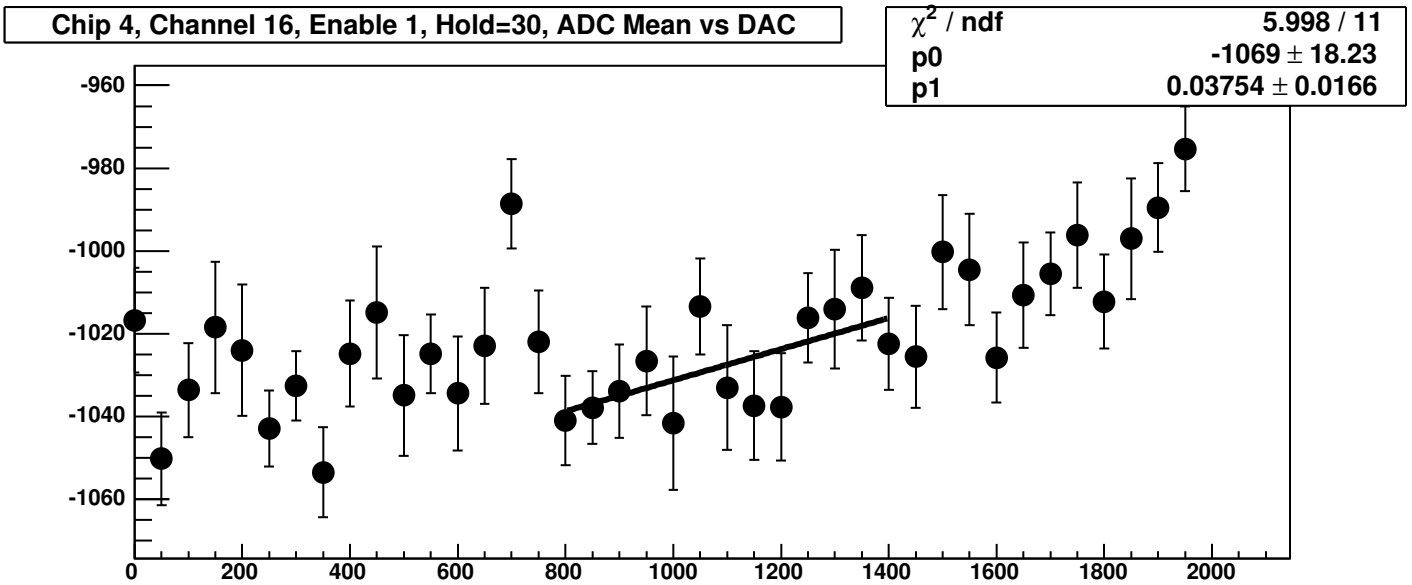

Chip 4, Channel 15, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

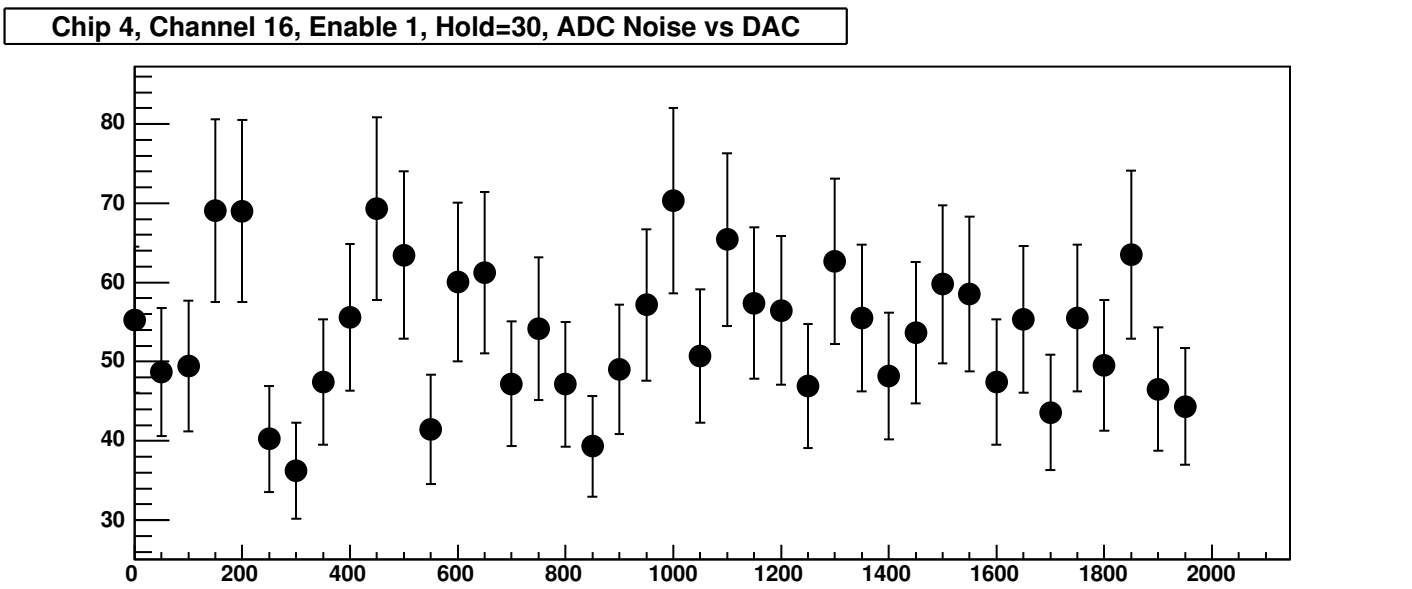

Chip 4, Channel 15, Enable 5, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

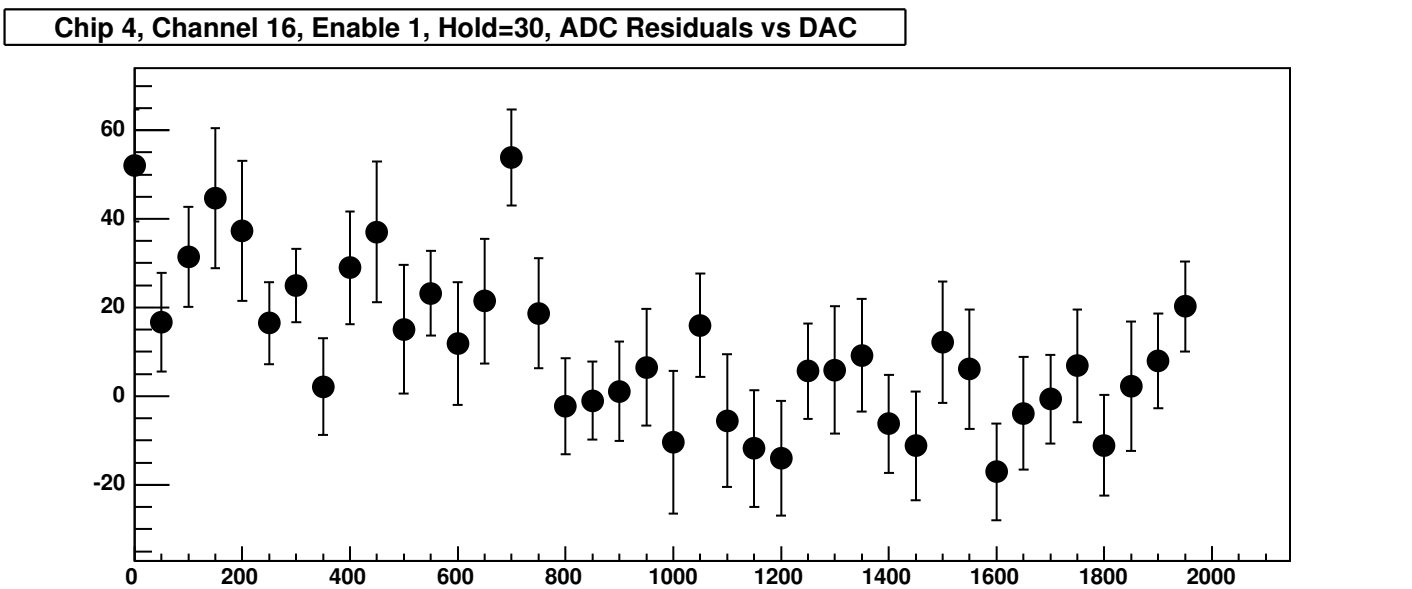

Chip 4, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 13.21 / 11
p0 -1158 ± 23.78
p1 0.2626 ± 0.02131
-400
-500
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

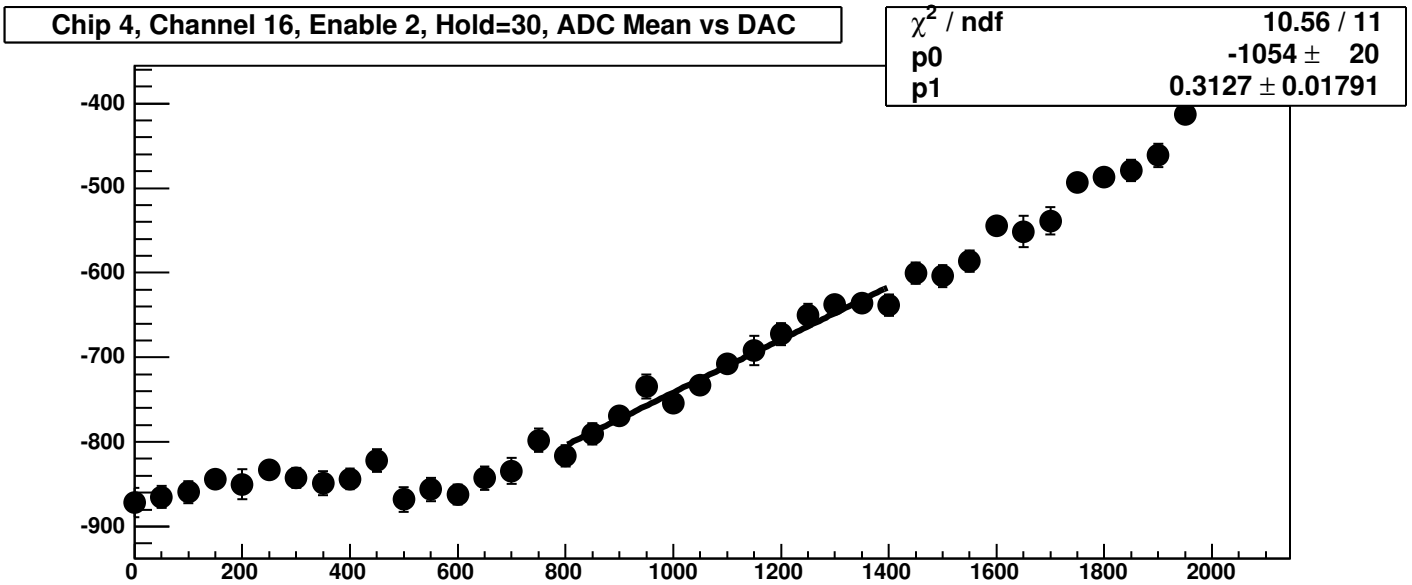

Chip 4, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

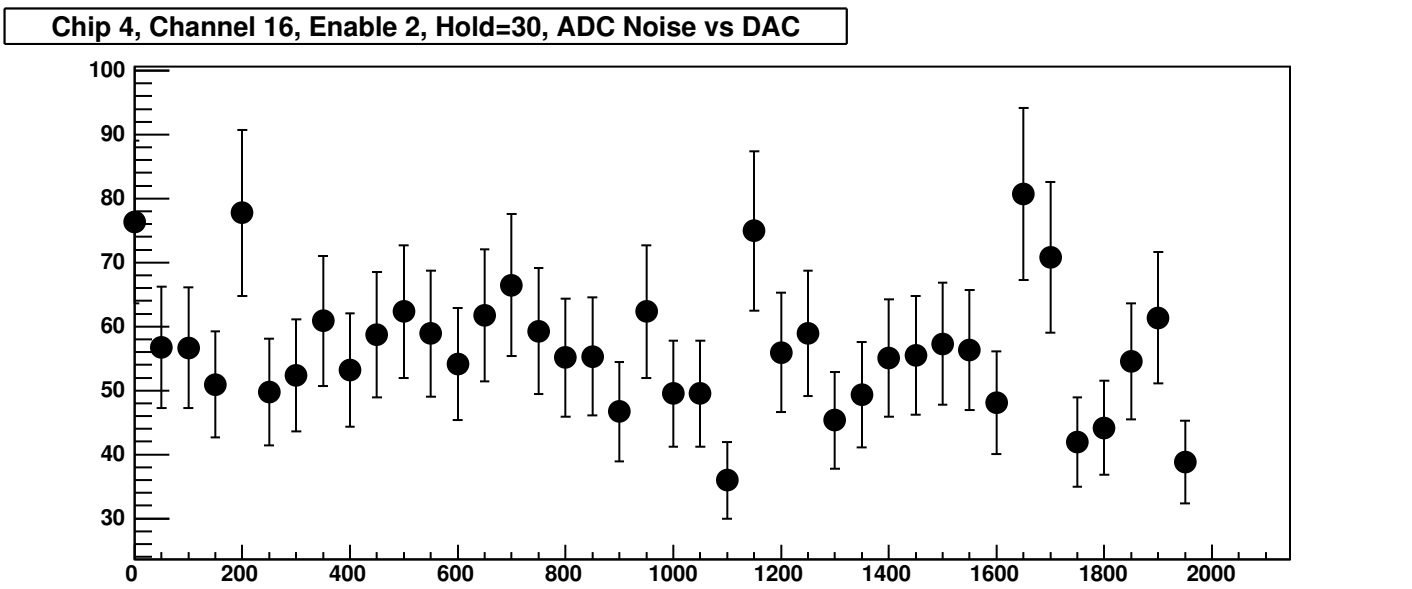

Chip 4, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

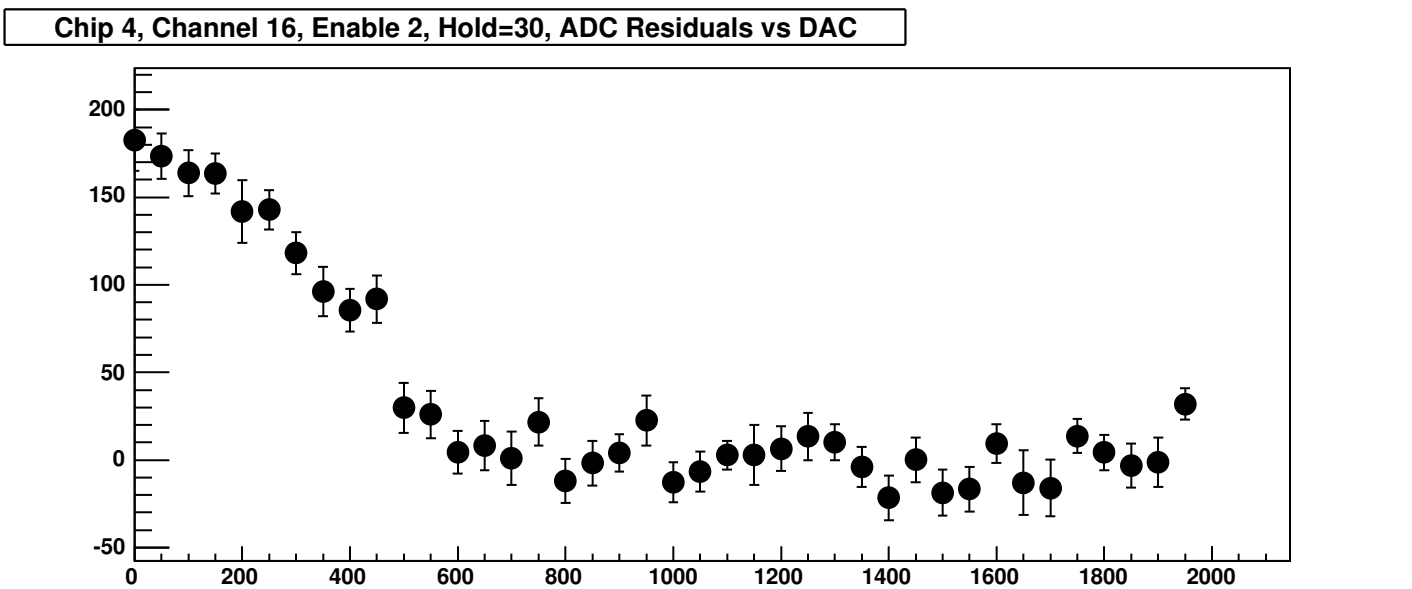

Chip 4, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 5.998 / 11
p0 -1069 ± 18.23
p1 0.03754 ± 0.0166
-960
-980
-1000
-1020
-1040
-1060
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

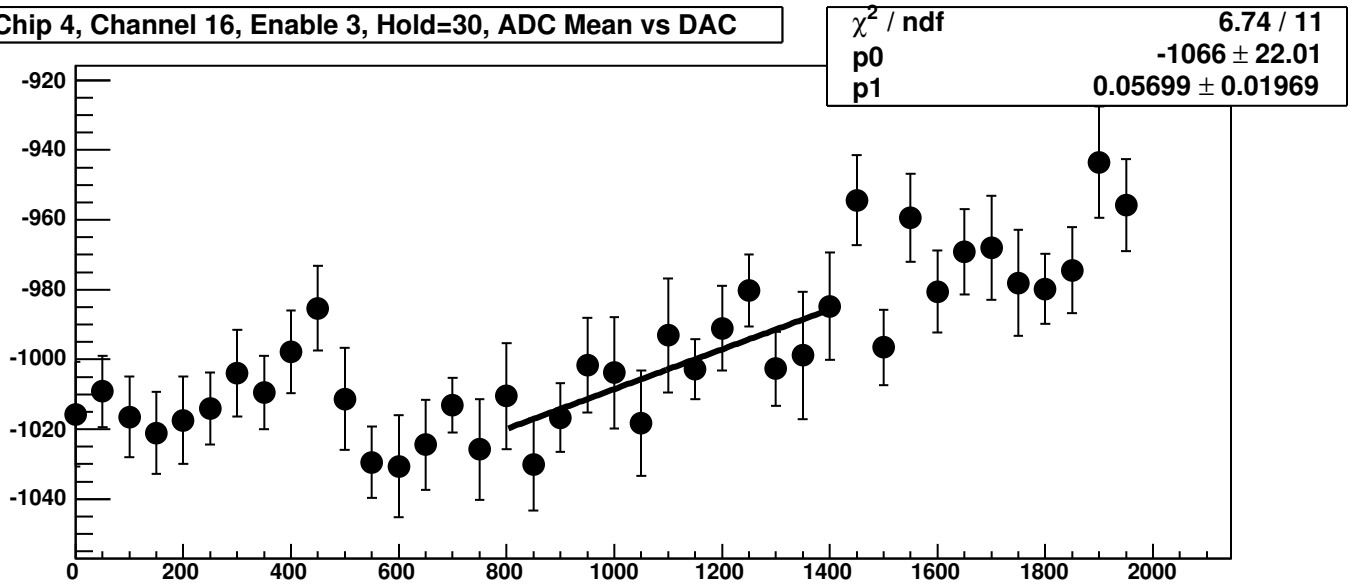

Chip 4, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 16, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 10.56 / 11
p0 -1054 ± 20
p1 0.3127 ± 0.01791
-400
-500
-600
-700
-800
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

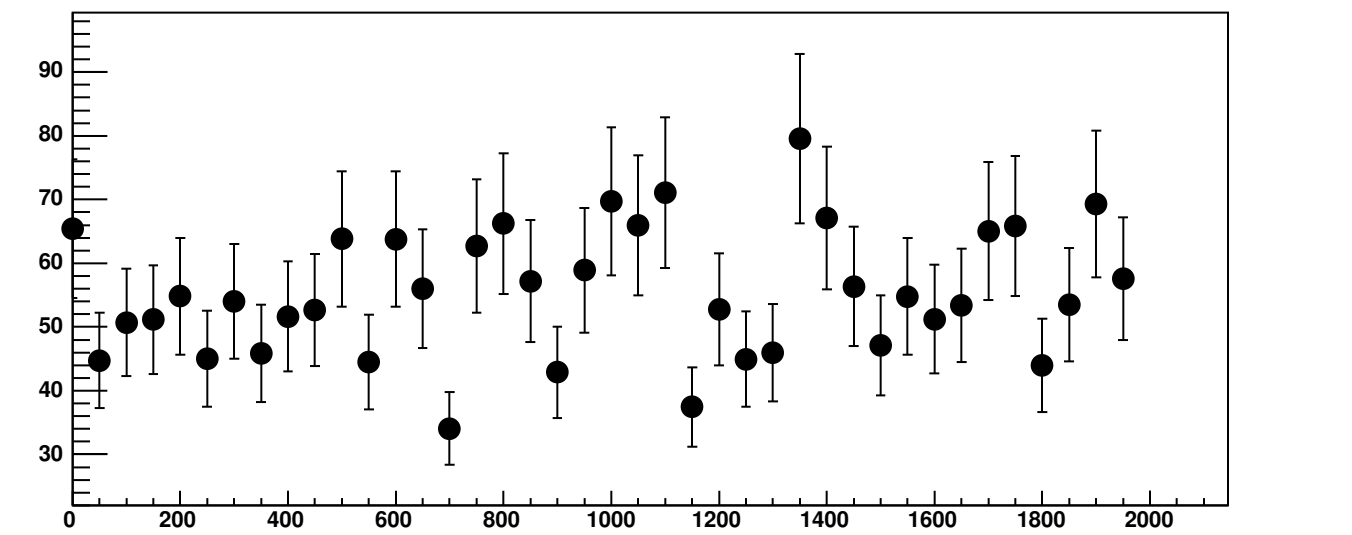

Chip 4, Channel 16, Enable 2, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 4, Channel 16, Enable 2, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

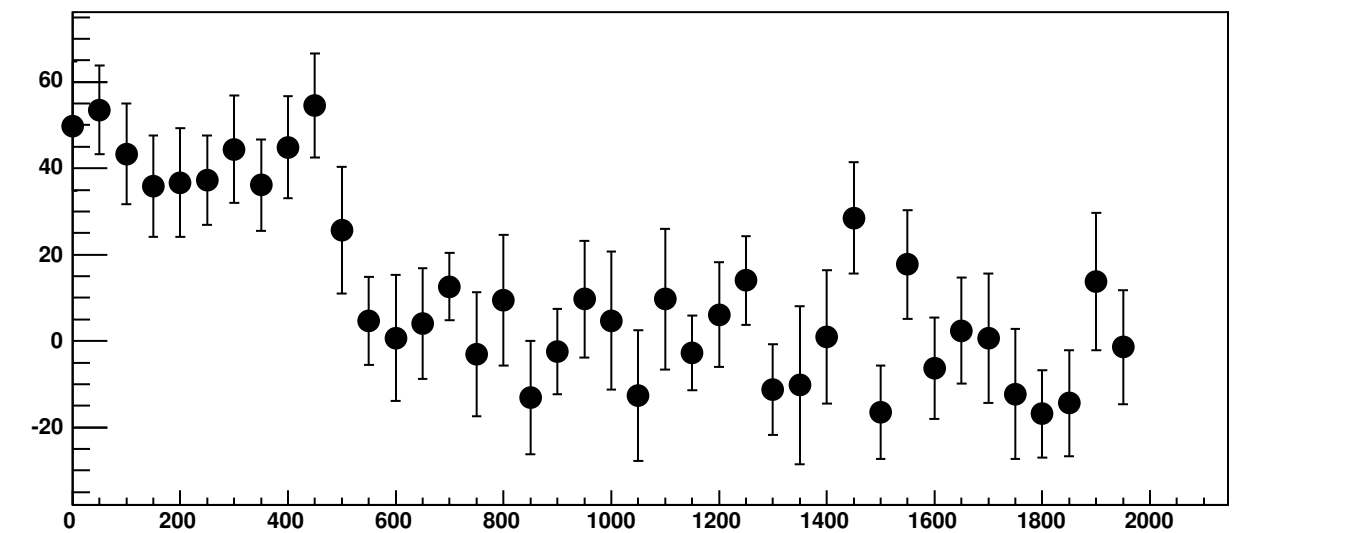
Chip 4, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC

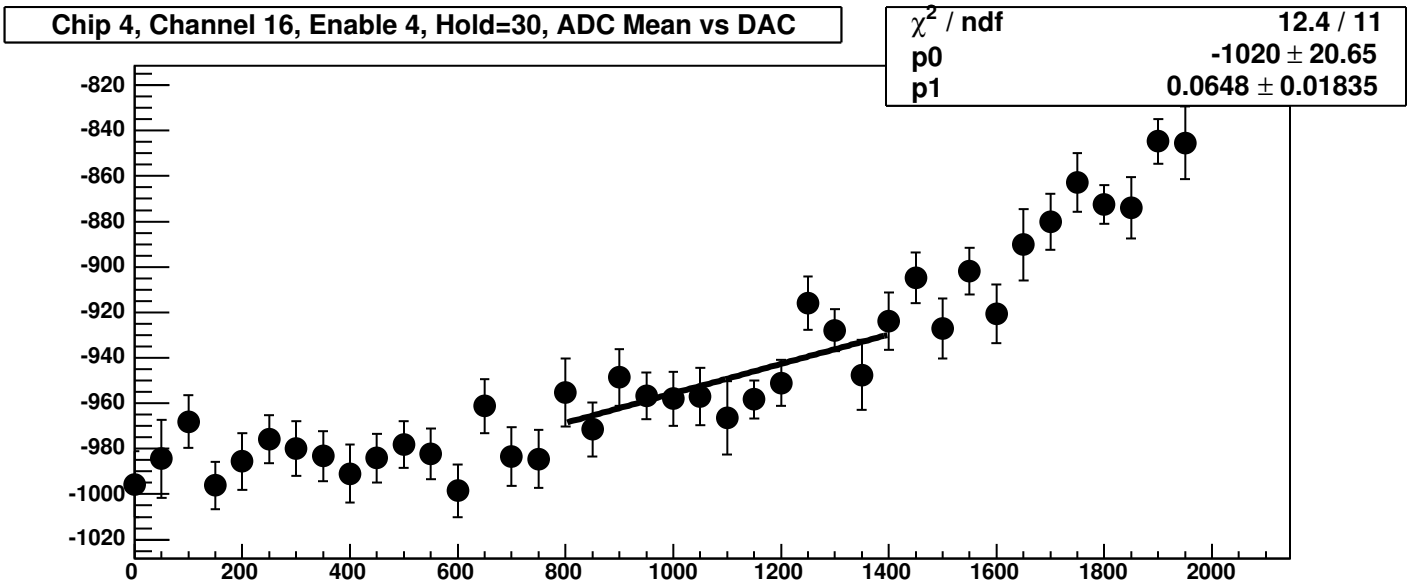


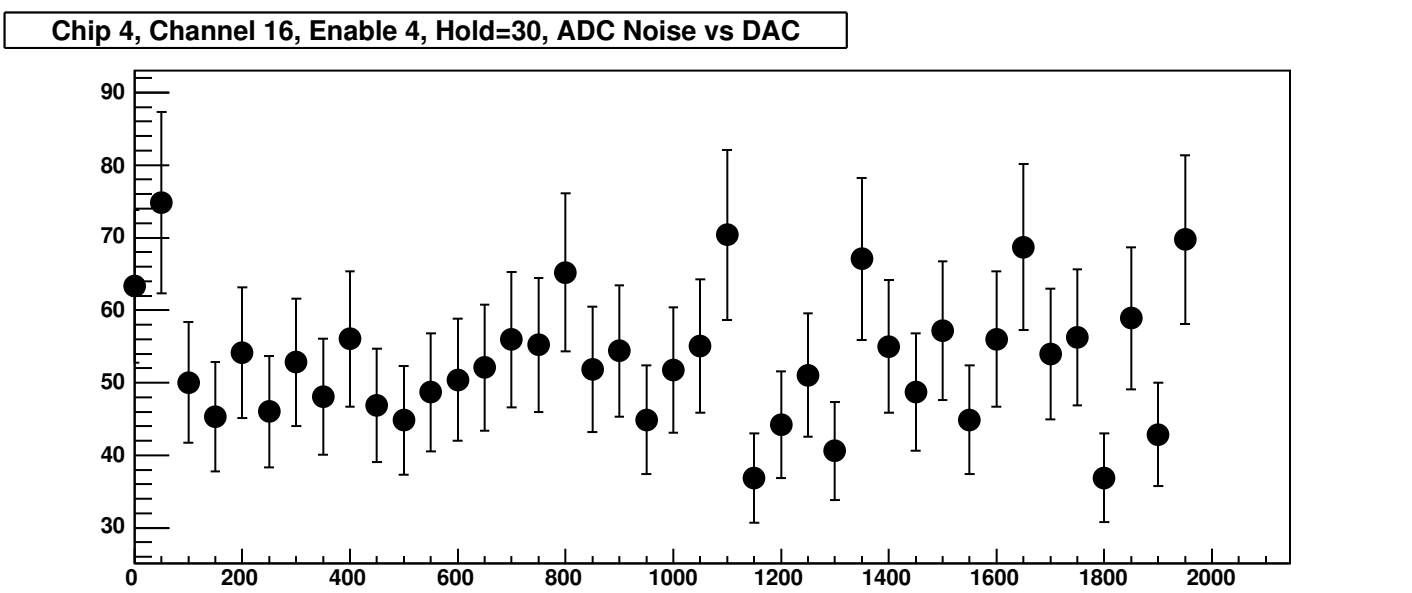
Chip 4, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC

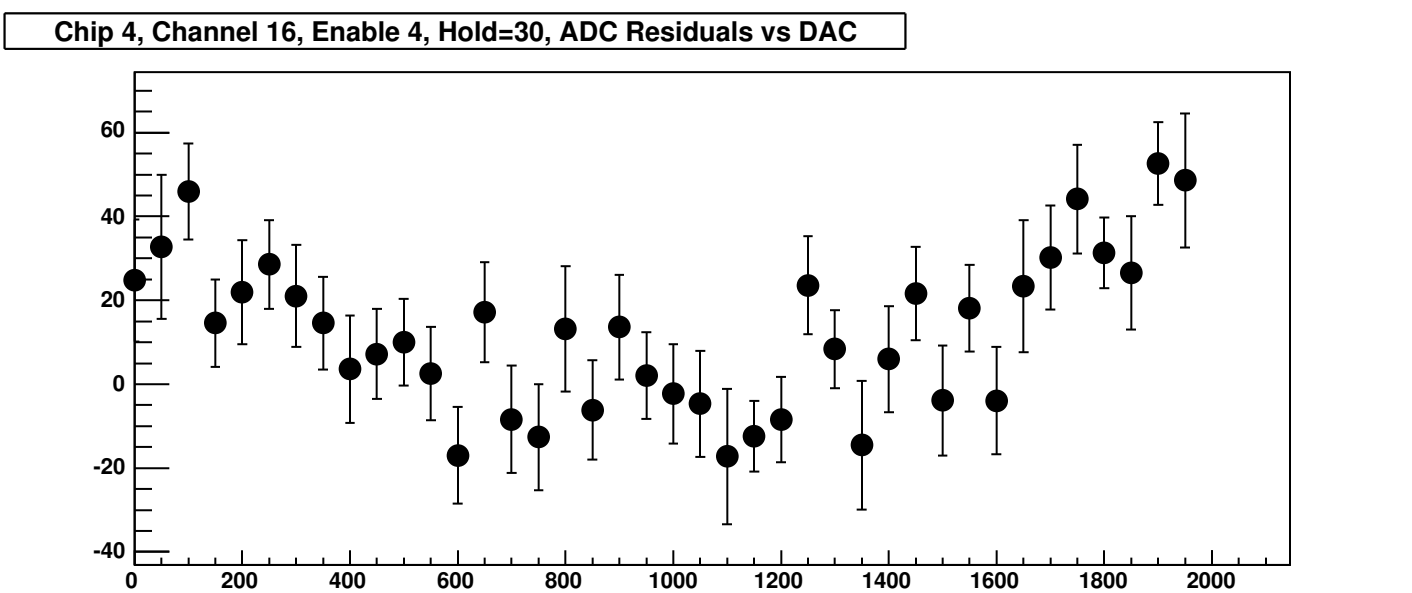


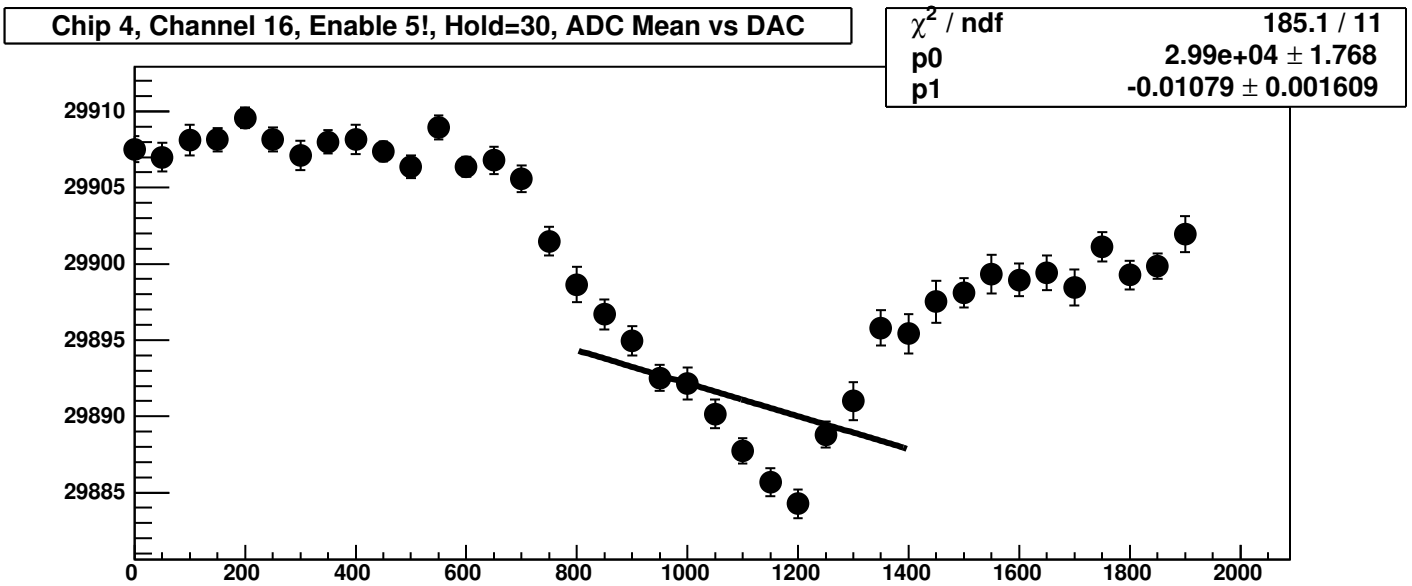
Chip 4, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC

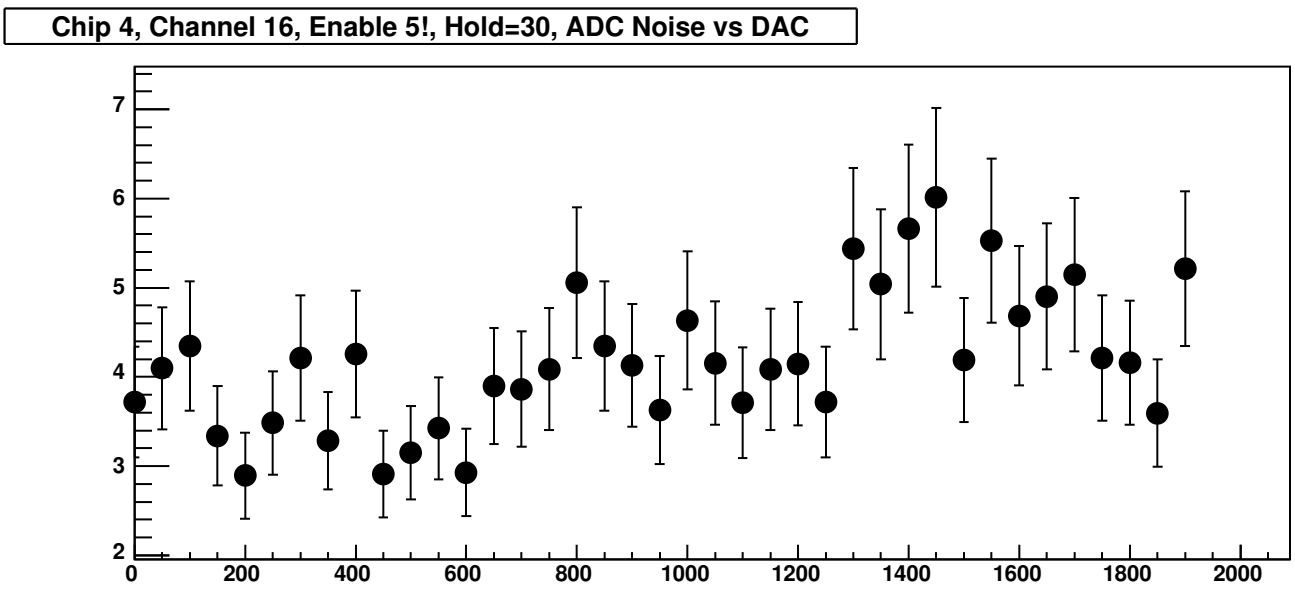


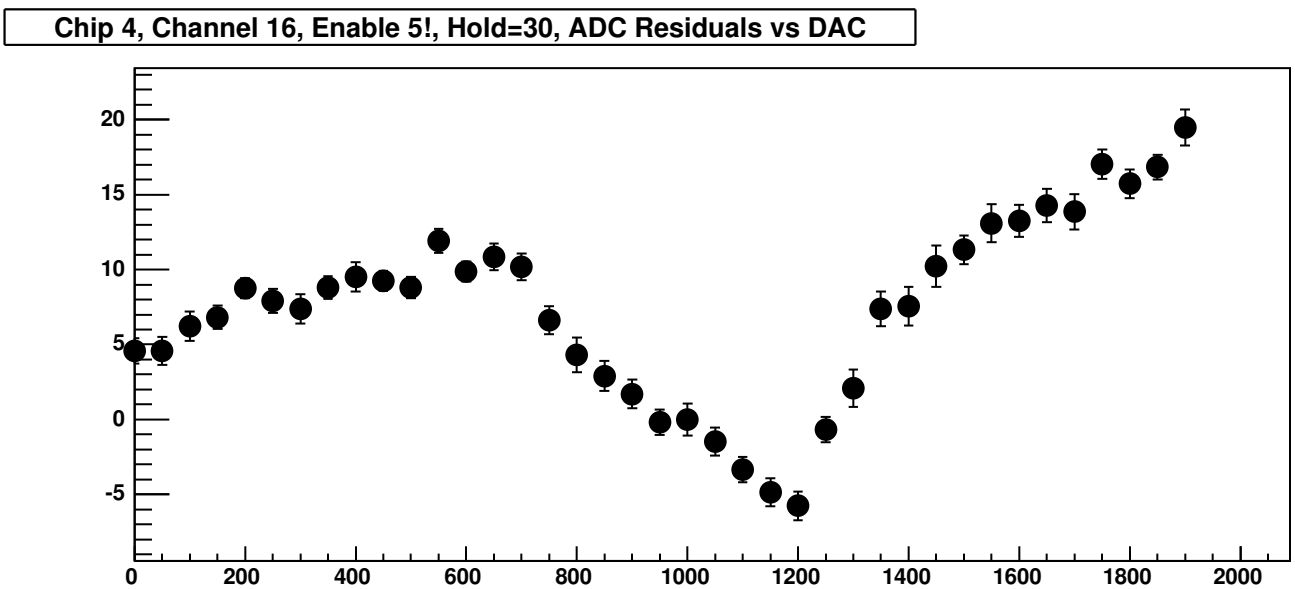

Chip 4, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 12.4 / 11
p0 -1020 ± 20.65
p1 0.0648 ± 0.01835
-820
-840
-860
-880
-900
-920
-940
-960
-980
-1000
-1020
0
200
400
600
800
1000
1200
1400
1600
1800
2000

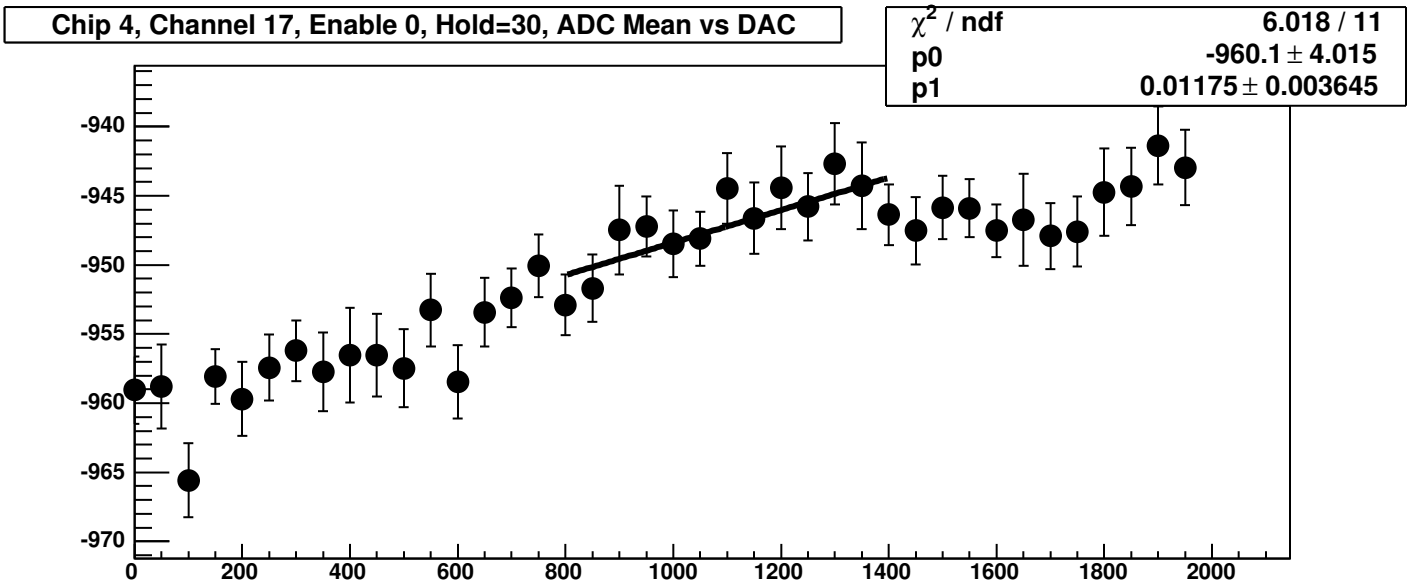

Chip 4, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

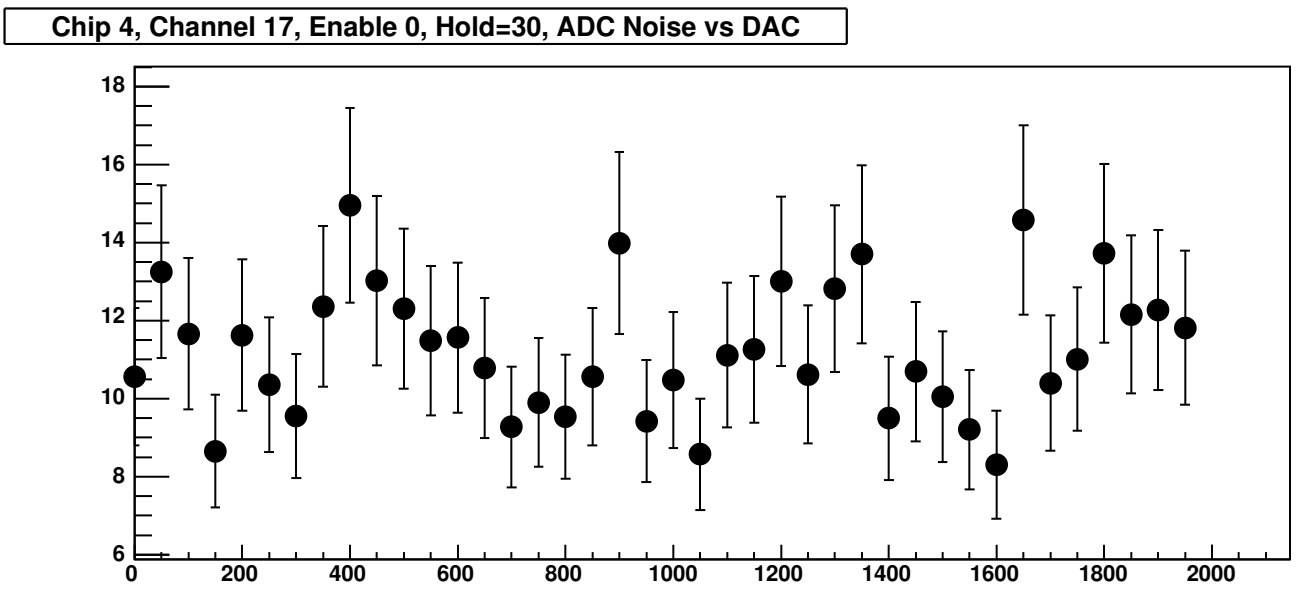

Chip 4, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

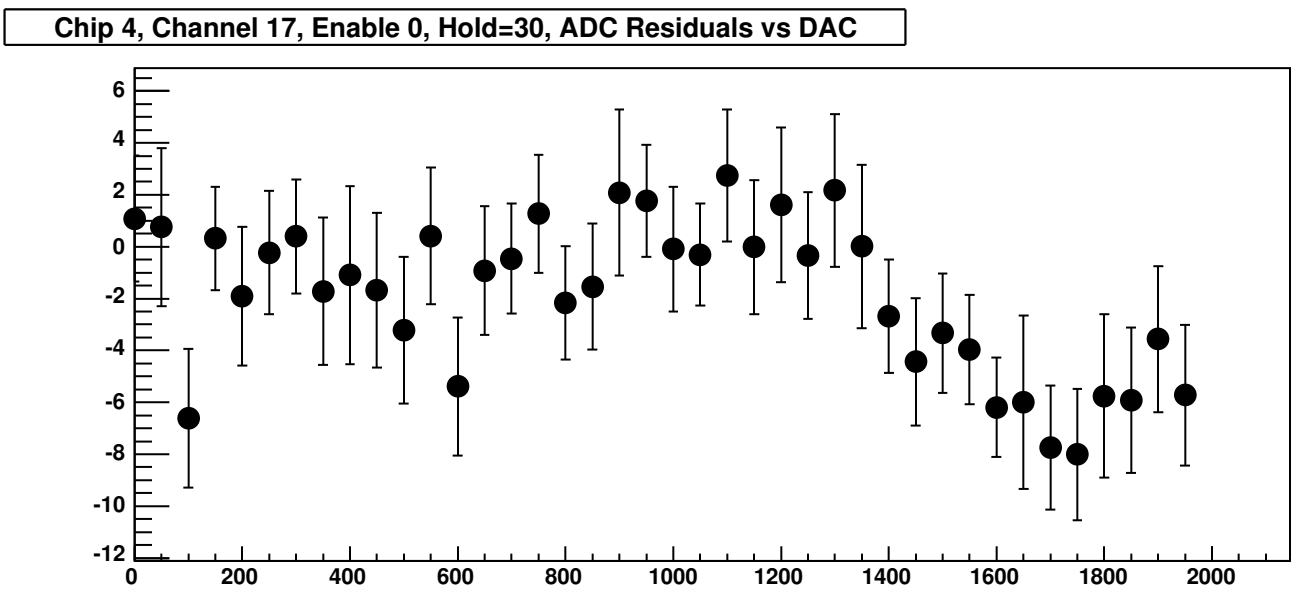

Chip 4, Channel 16, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 185.1 / 11
p0 2.99e+04 ± 1.768
p1 -0.01079 ± 0.001609
29910
29905
29900
29895
29890
29885
0
200
400
600
800
1000
1200
1400
1600
1800
2000

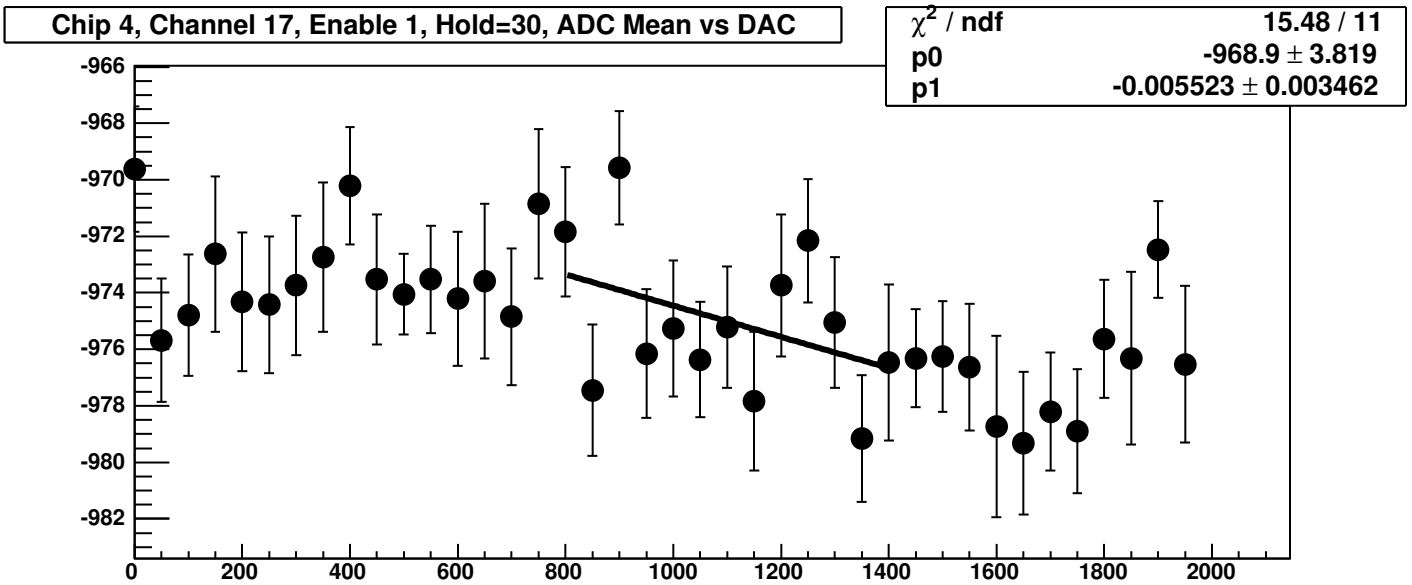

Chip 4, Channel 16, Enable 5!, Hold=30, ADC Noise vs DAC
7
6
5
4
3
2
0
200
400
600
800
1000
1200
1400
1600
1800
2000

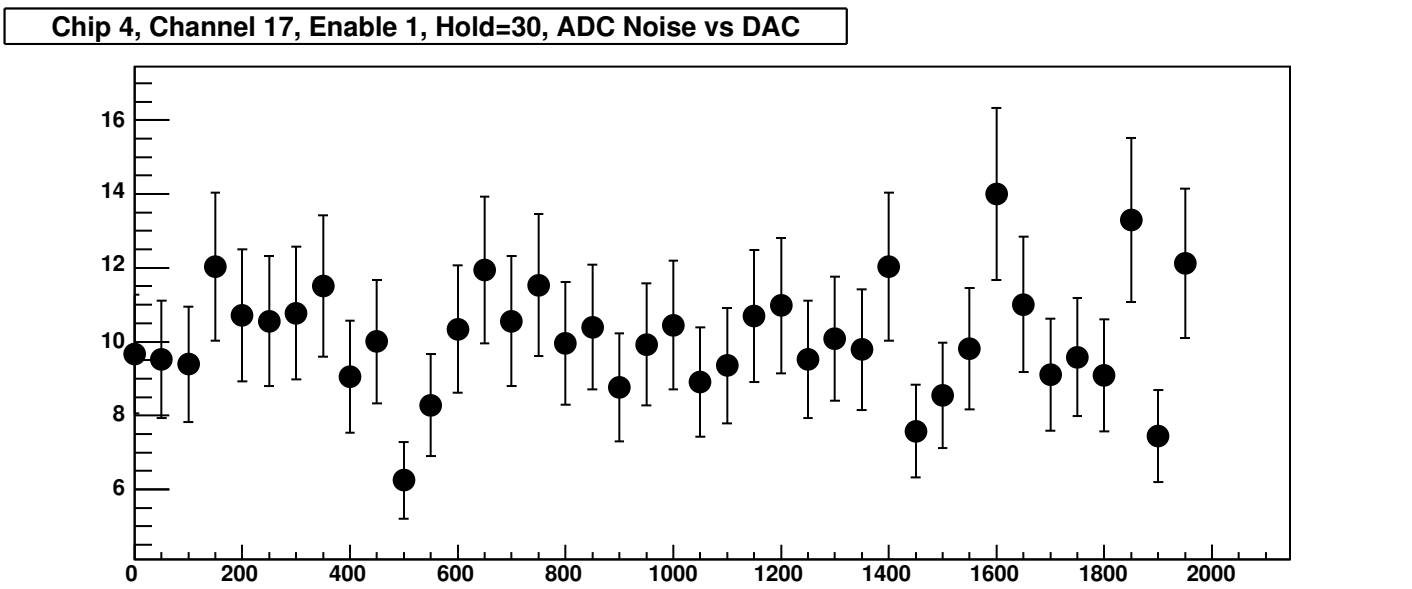

Chip 4, Channel 16, Enable 5!, Hold=30, ADC Residuals vs DAC
20
15
10
5
0
-5
0
200
400
600
800
1000
1200
1400
1600
1800
2000

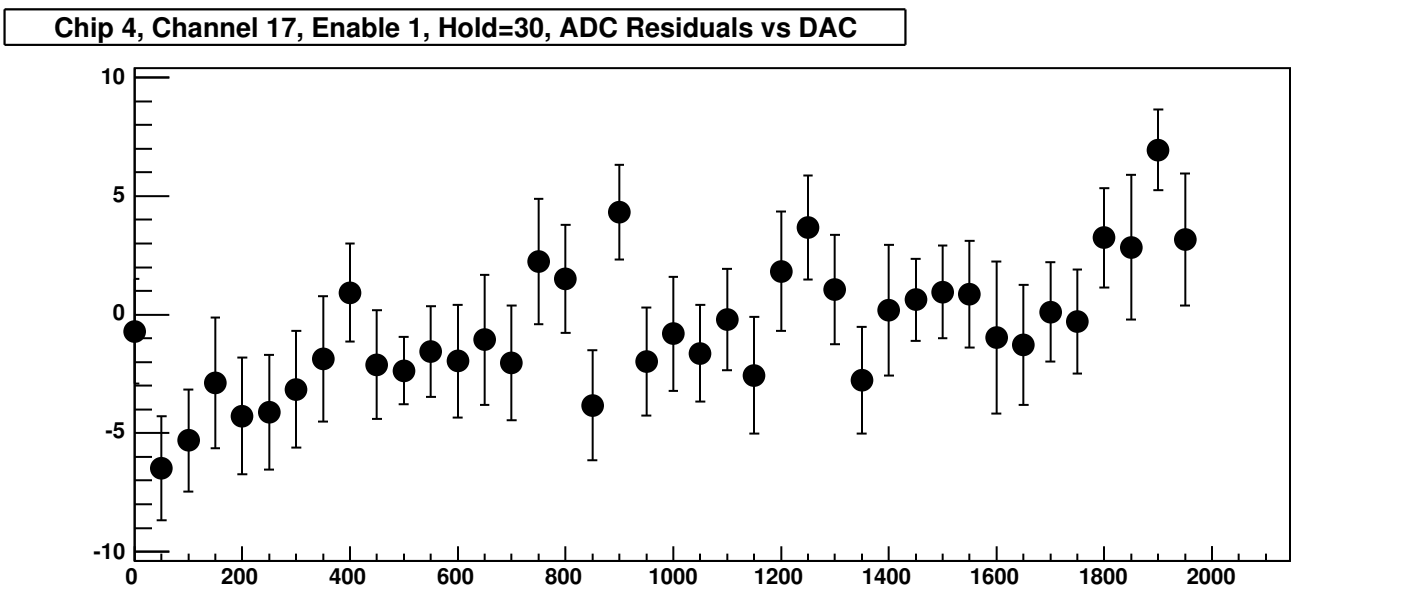

Chip 4, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 6.018 / 11
p0 -960.1 ± 4.015
p1 0.01175 ± 0.003645
-940
-945
-950
-955
-960
-965
-970
0
200
400
600
800
1000
1200
1400
1600
1800
2000

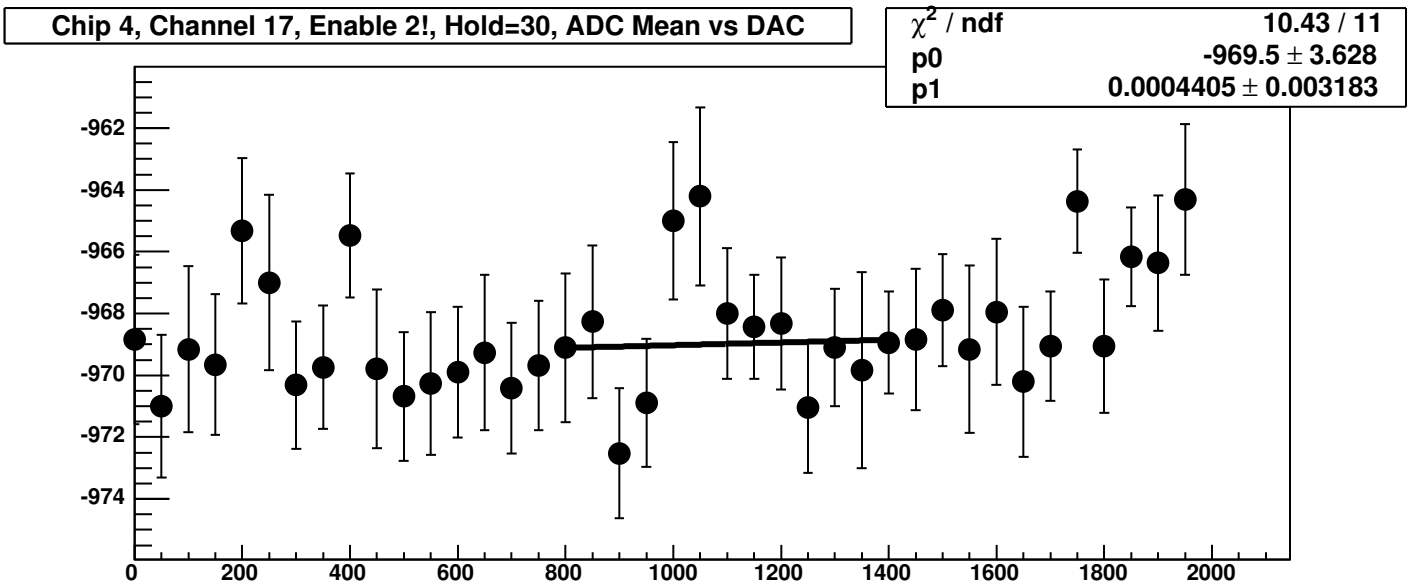

Chip 4, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
18
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

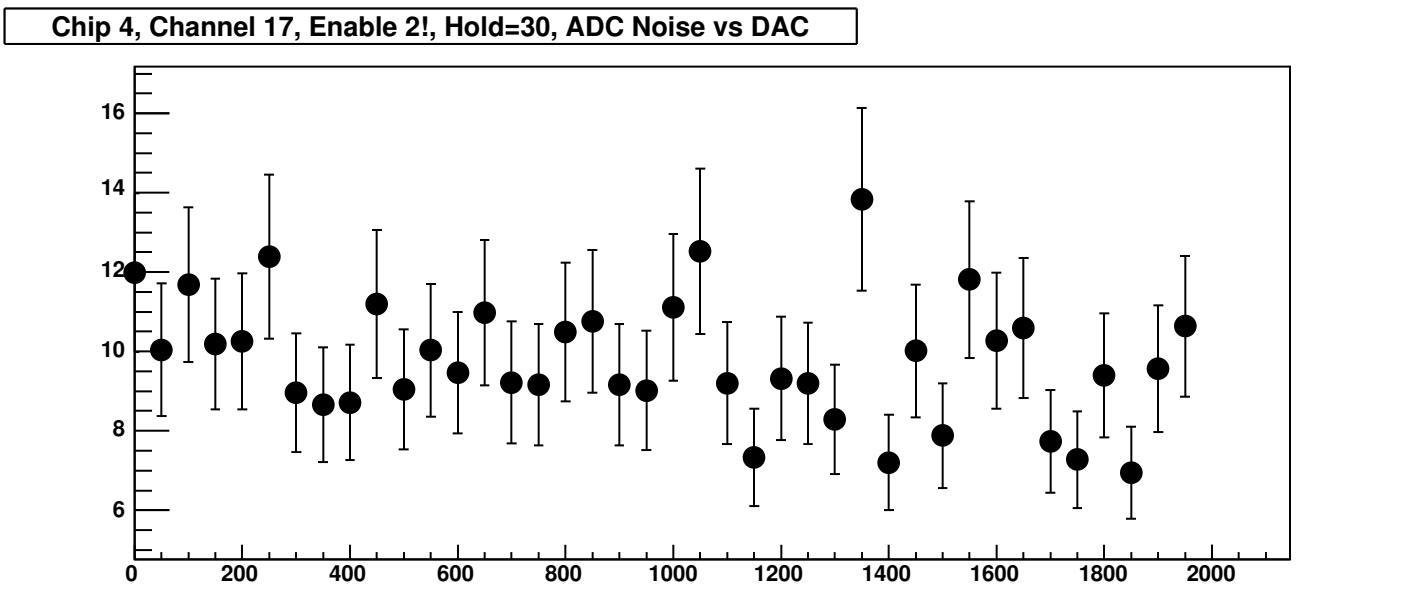

Chip 4, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
0
200
400
600
800
1000
1200
1400
1600
1800
2000

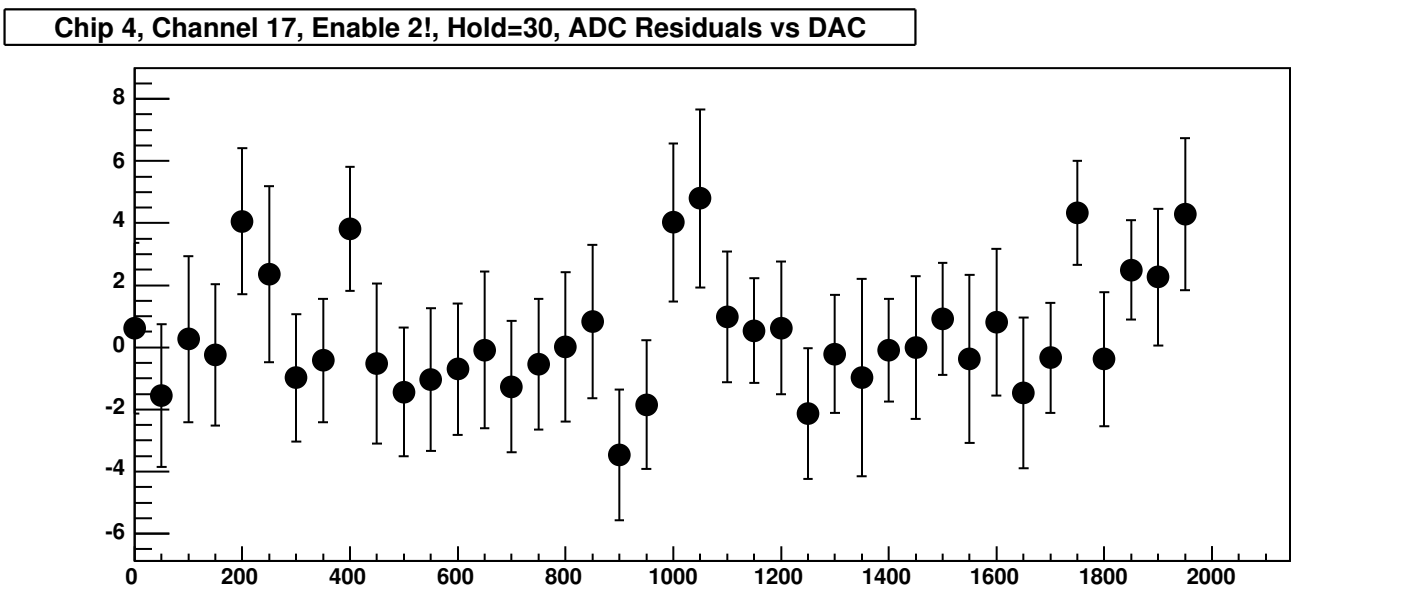

Chip 4, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 15.48 / 11
p0 -968.9 ± 3.819
p1 -0.005523 ± 0.003462
-966
-968
-970
-972
-974
-976
-978
-980
-982
0
200
400
600
800
1000
1200
1400
1600
1800
2000

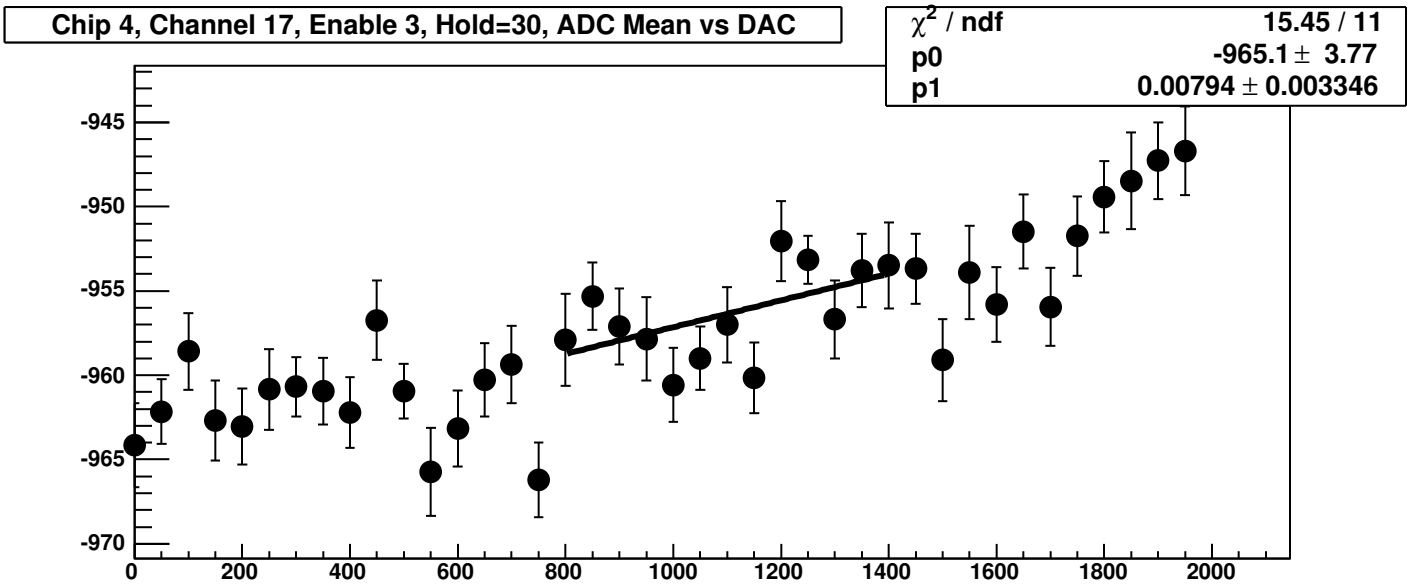

Chip 4, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

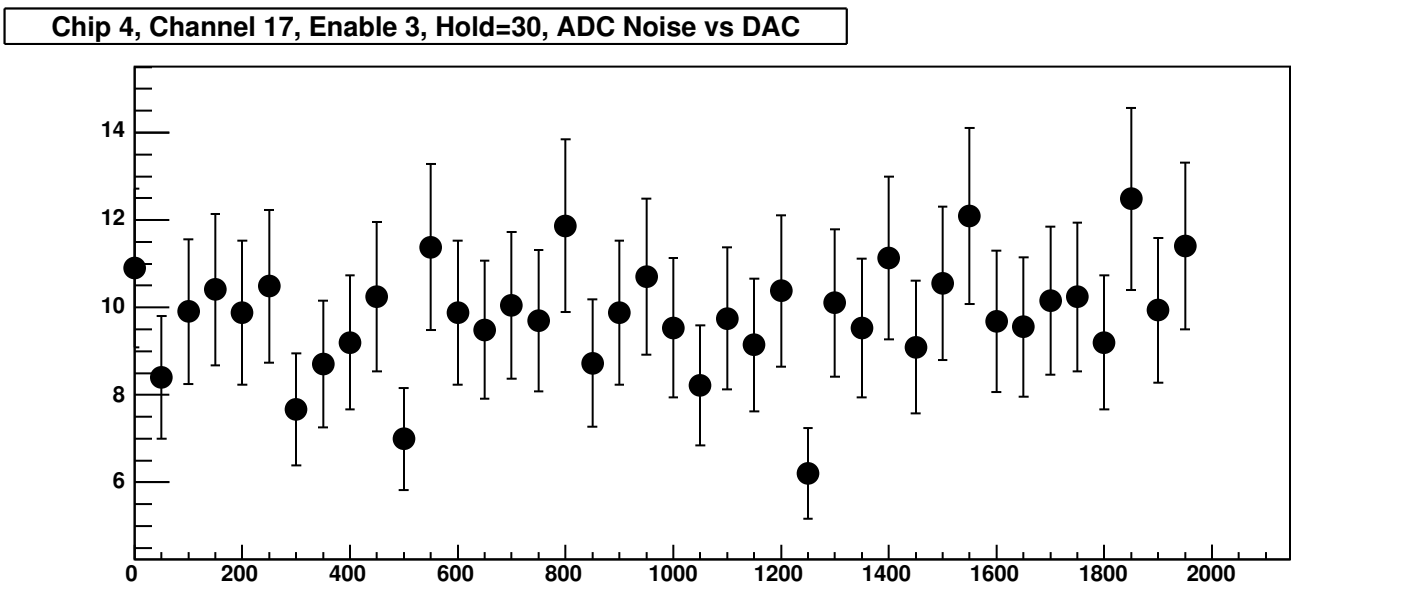

Chip 4, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

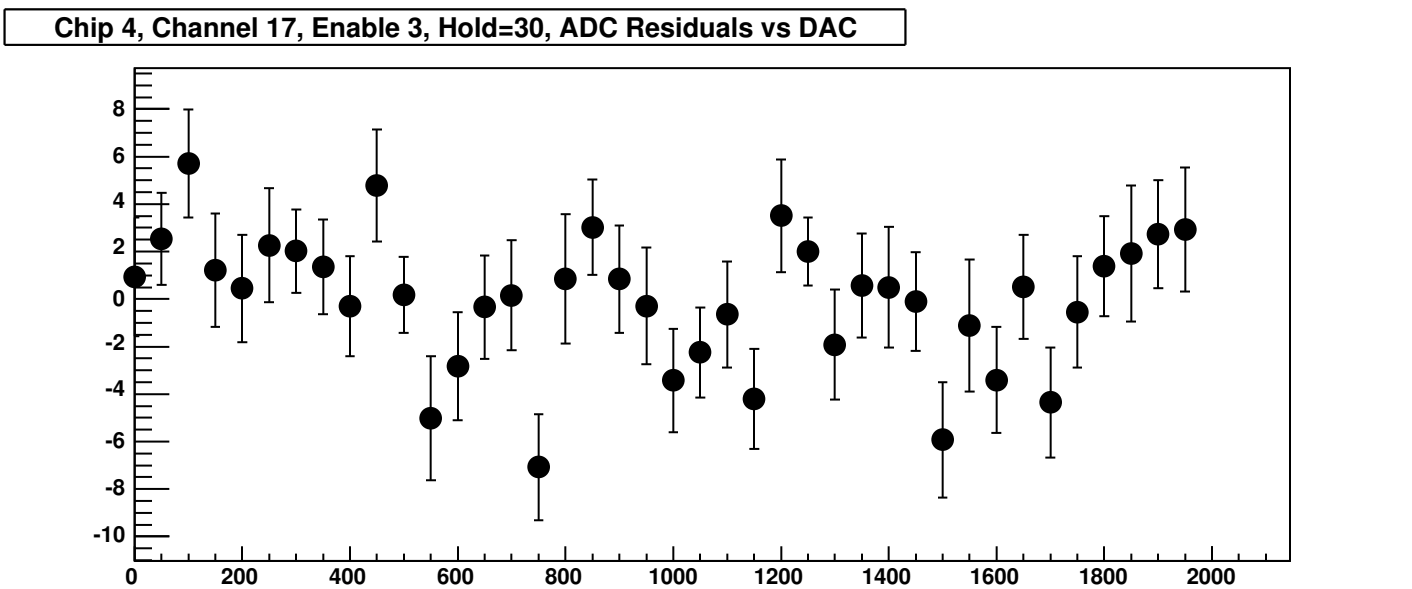

Chip 4, Channel 17, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 10.43 / 11
p0 -969.5 ± 3.628
p1 0.0004405 ± 0.003183
-962
-964
-966
-968
-970
-972
-974
0
200
400
600
800
1000
1200
1400
1600
1800
2000

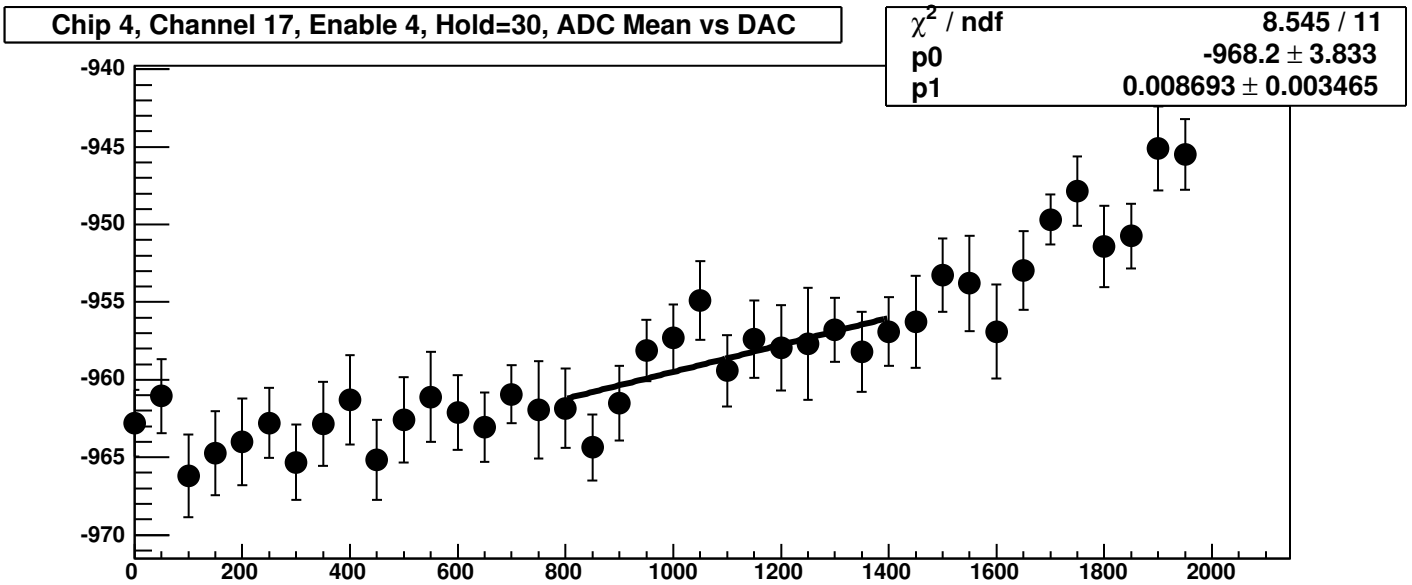

Chip 4, Channel 17, Enable 2!, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

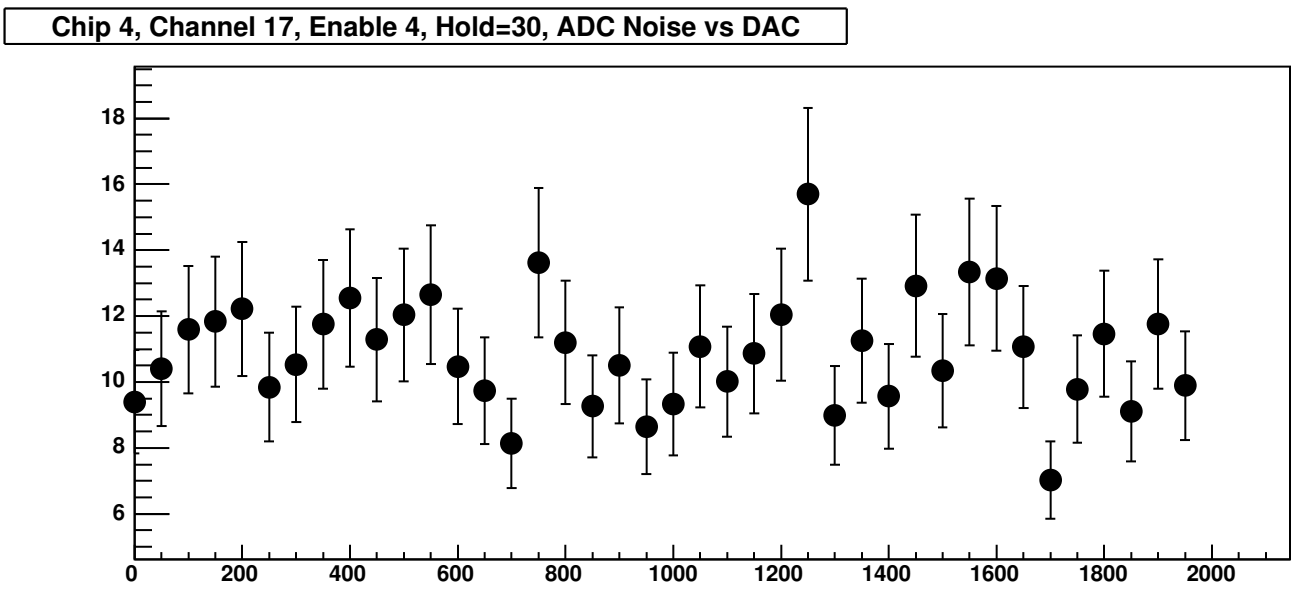

Chip 4, Channel 17, Enable 2!, Hold=30, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

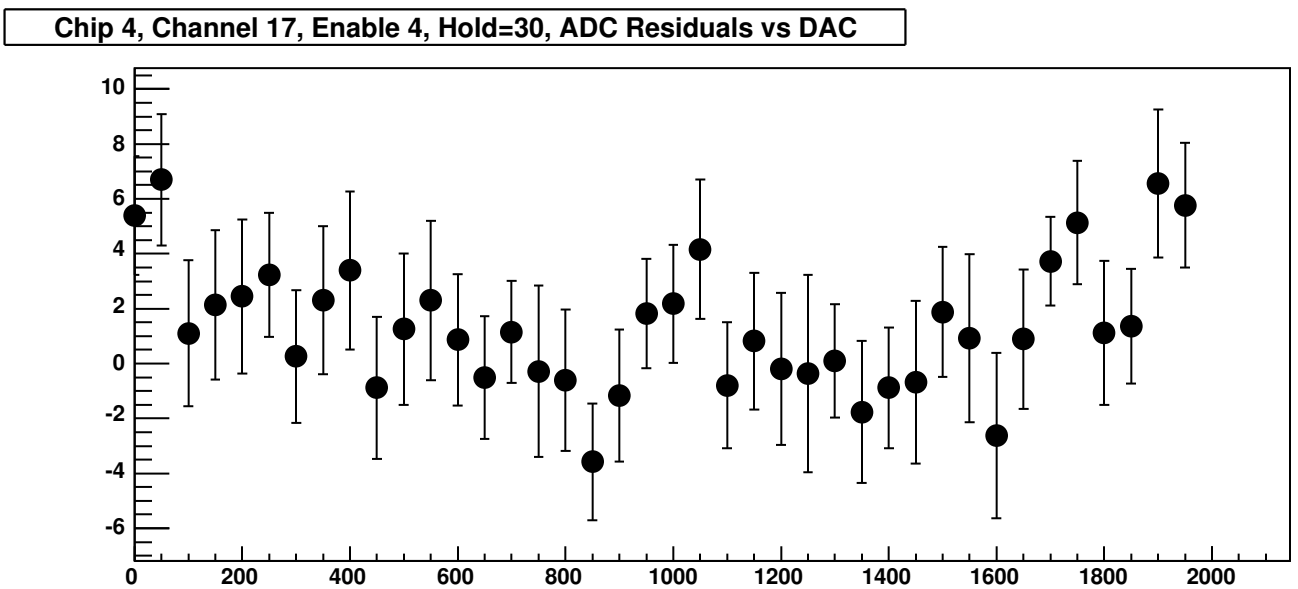

Chip 4, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 15.45 / 11
p0 -965.1 ± 3.77
p1 0.00794 ± 0.003346
-945
-950
-955
-960
-965
-970
0
200
400
600
800
1000
1200
1400
1600
1800
2000

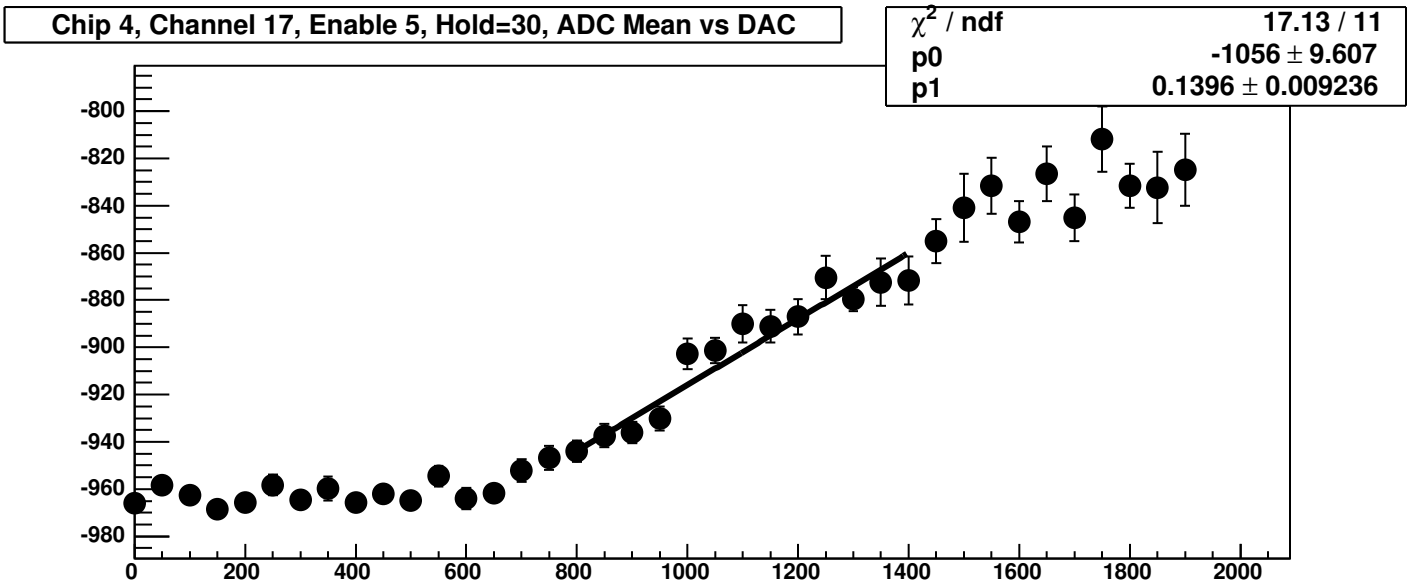

Chip 4, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

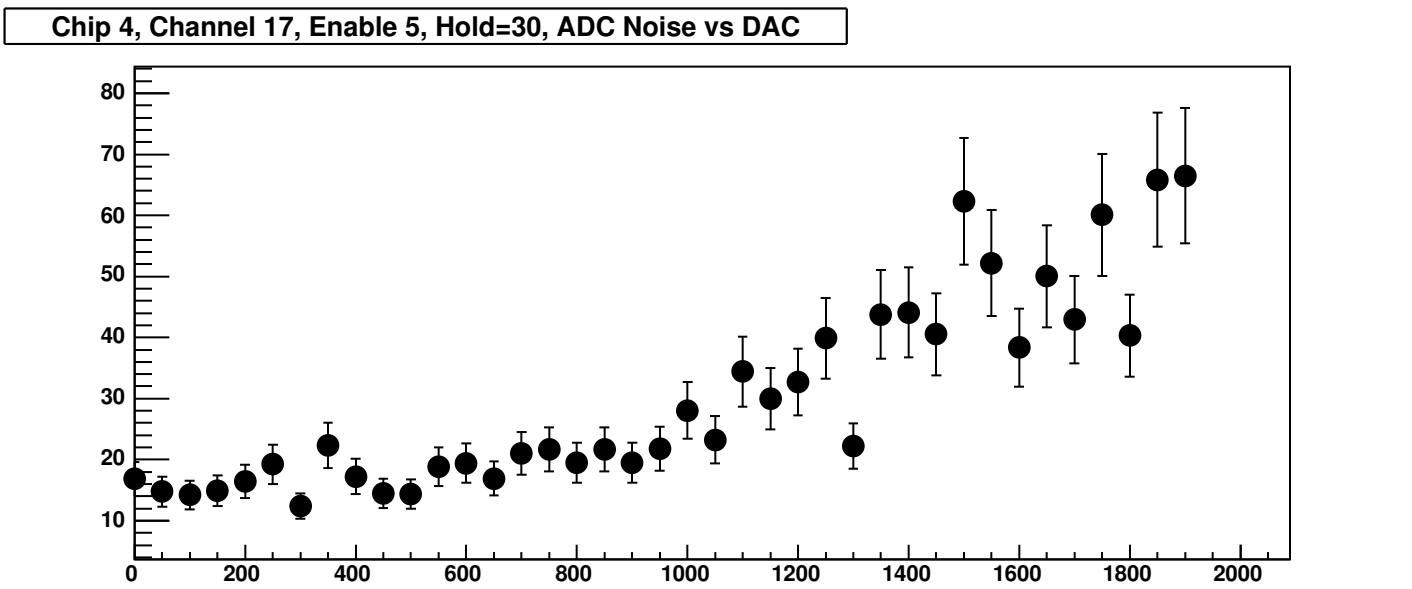

Chip 4, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

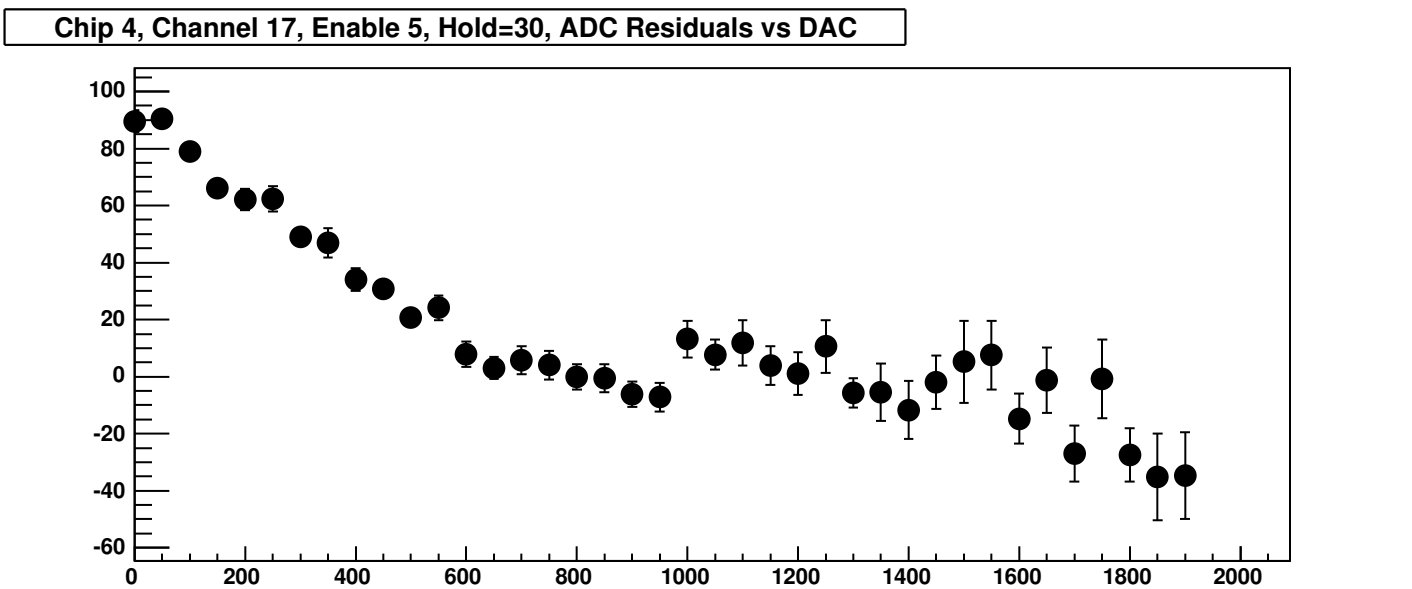

Chip 4, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 8.545 / 11
p0 -968.2 ± 3.833
p1 0.008693 ± 0.003465
-940
-945
-950
-955
-960
-965
-970
0
200
400
600
800
1000
1200
1400
1600
1800
2000

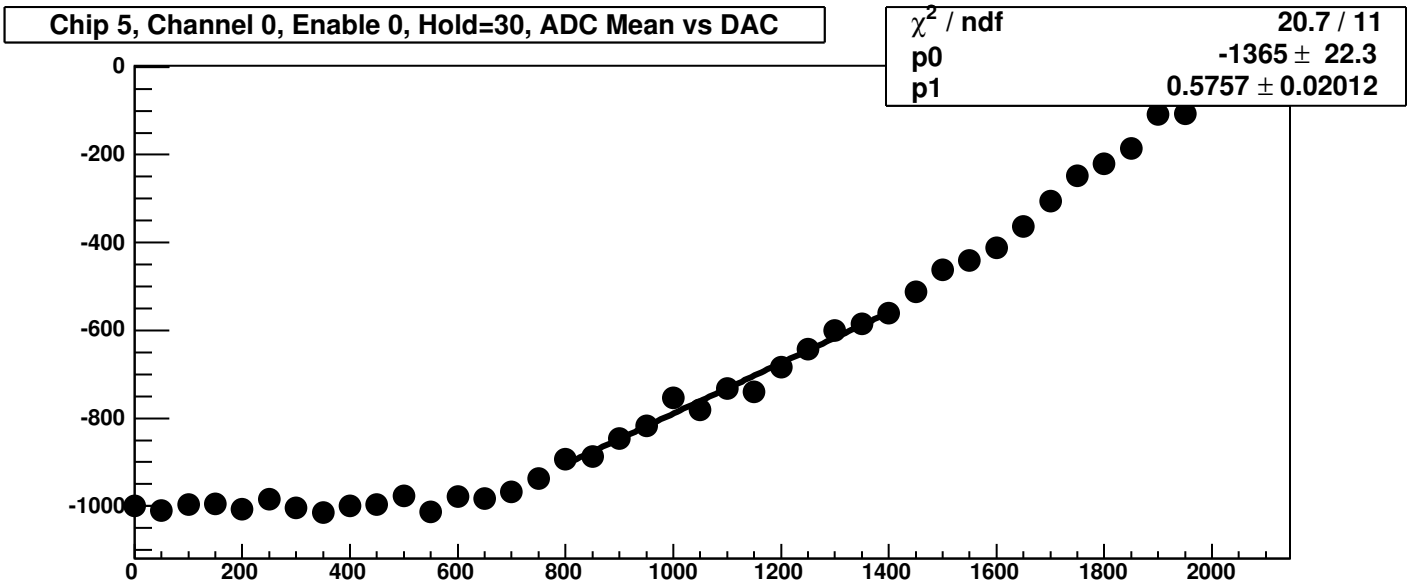

Chip 4, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC
18
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

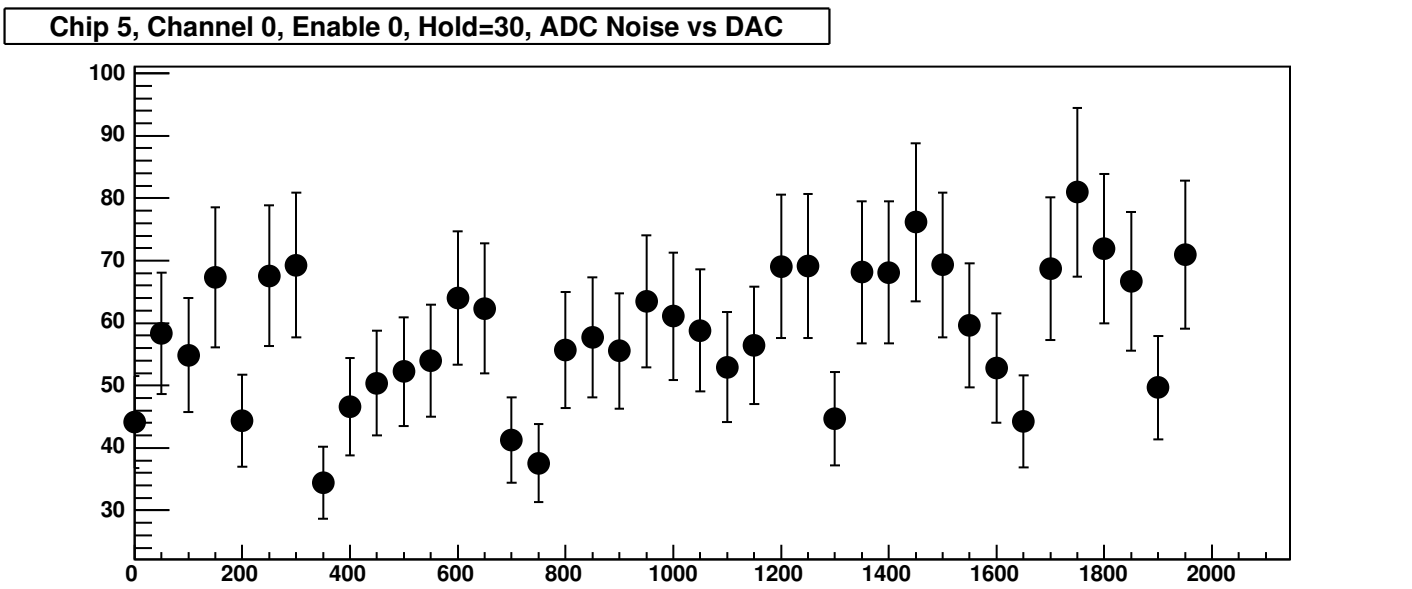

Chip 4, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

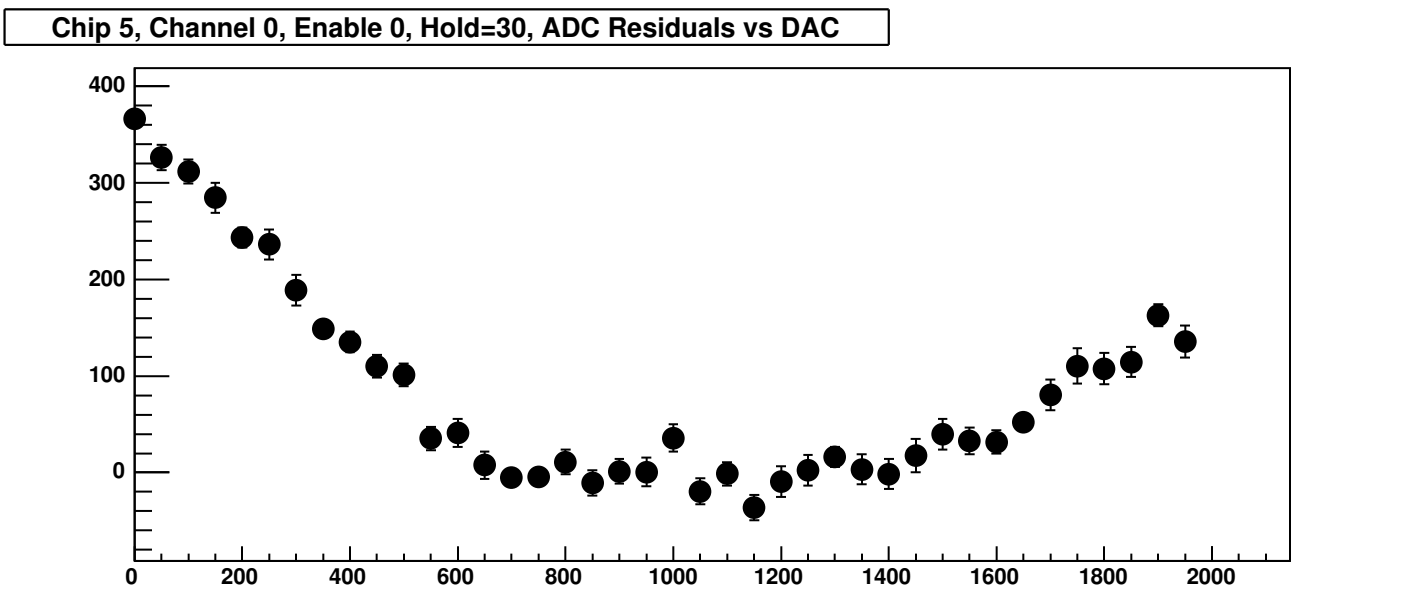

Chip 4, Channel 17, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 17.13 / 11
p0 -1056 ± 9.607
p1 0.1396 ± 0.009236
-800
-820
-840
-860
-880
-900
-920
-940
-960
-980
0
200
400
600
800
1000
1200
1400
1600
1800
2000

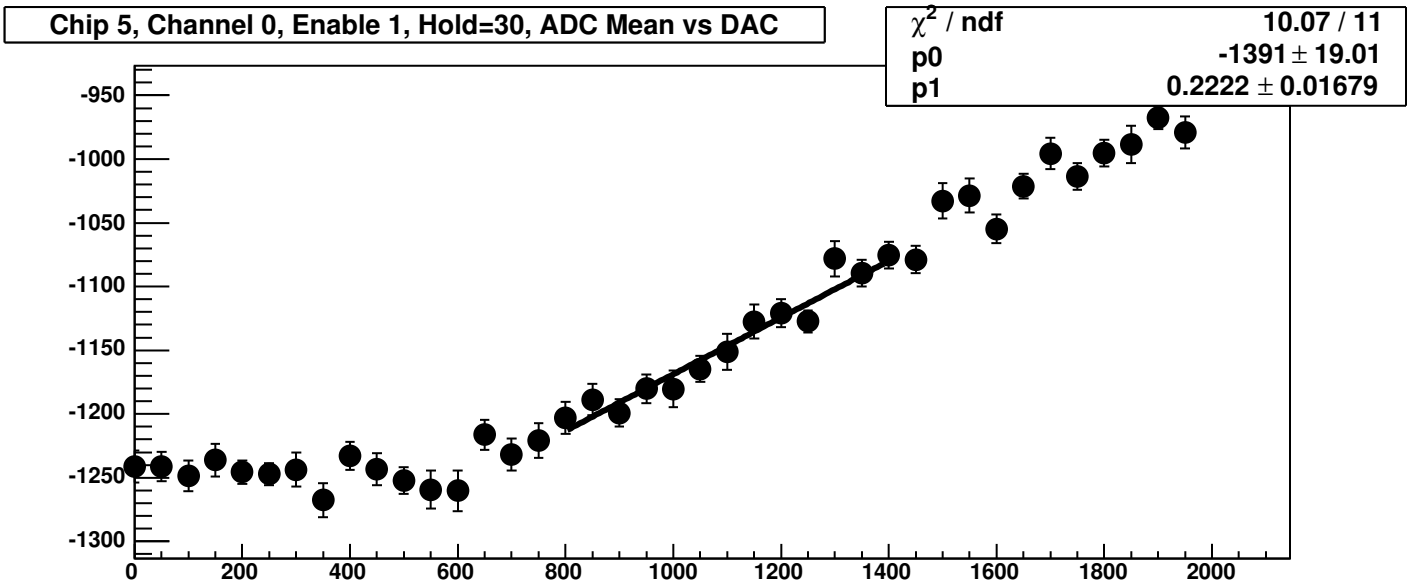

Chip 4, Channel 17, Enable 5, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

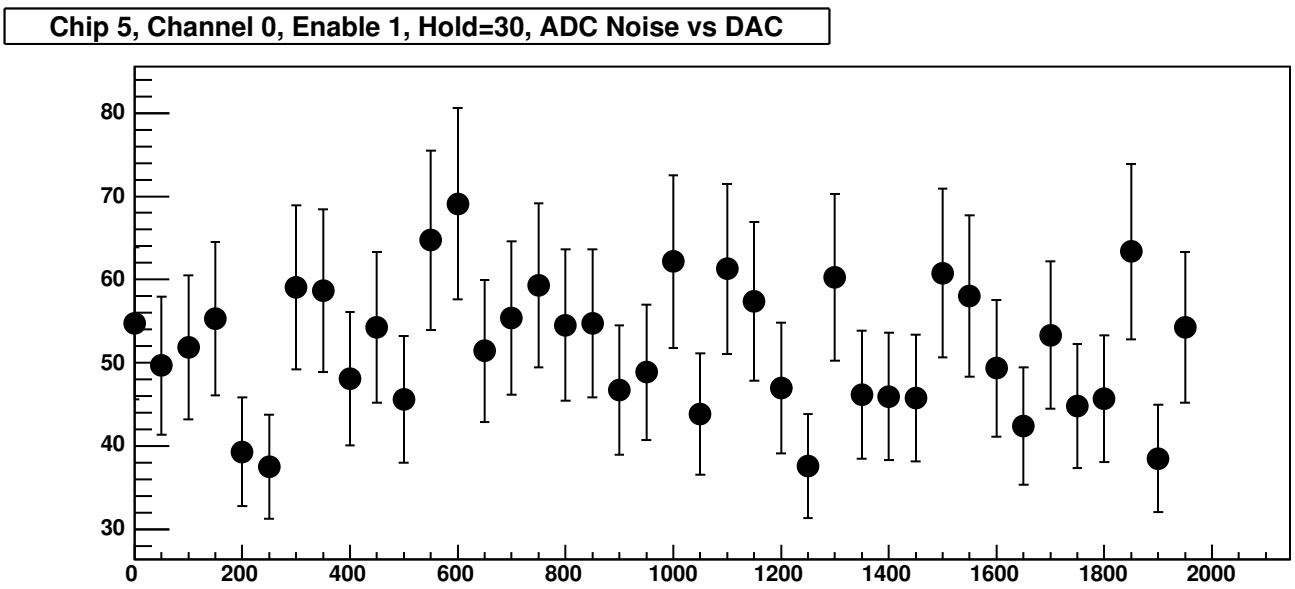

Chip 4, Channel 17, Enable 5, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

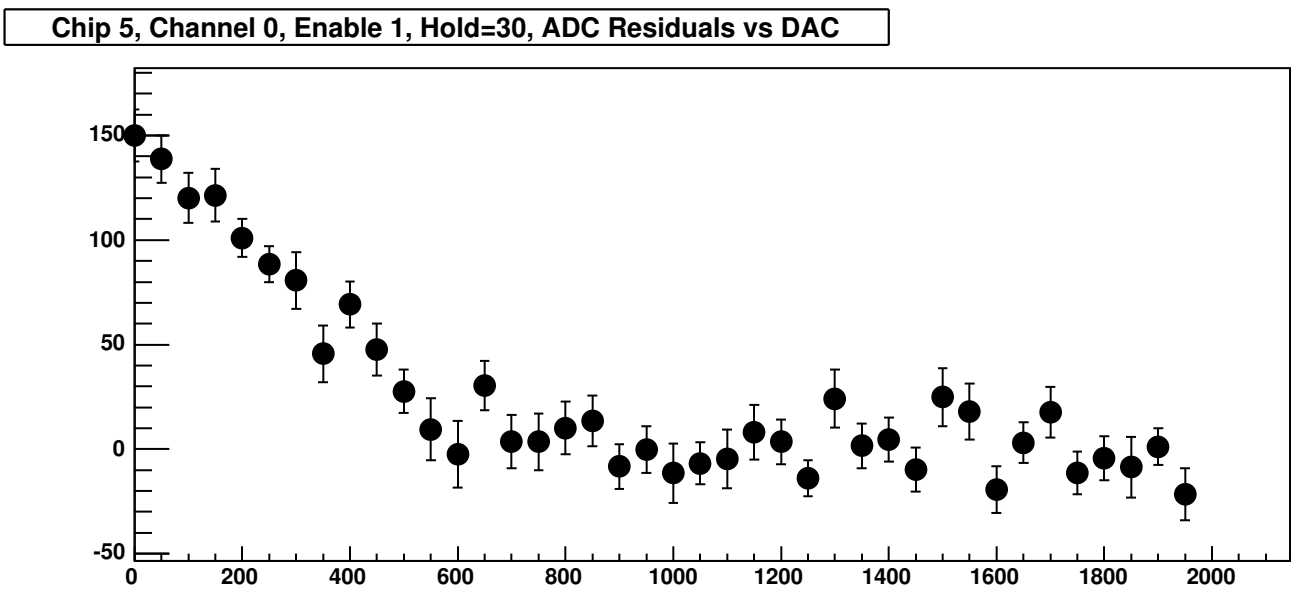

Chip 5, Channel 0, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 20.7 / 11
p0 -1365 ± 22.3
p1 0.5757 ± 0.02012
0
-200
-400
-600
-800
-1000
0 200 400 600 800 1000 1200 1400 1600 1800 2000

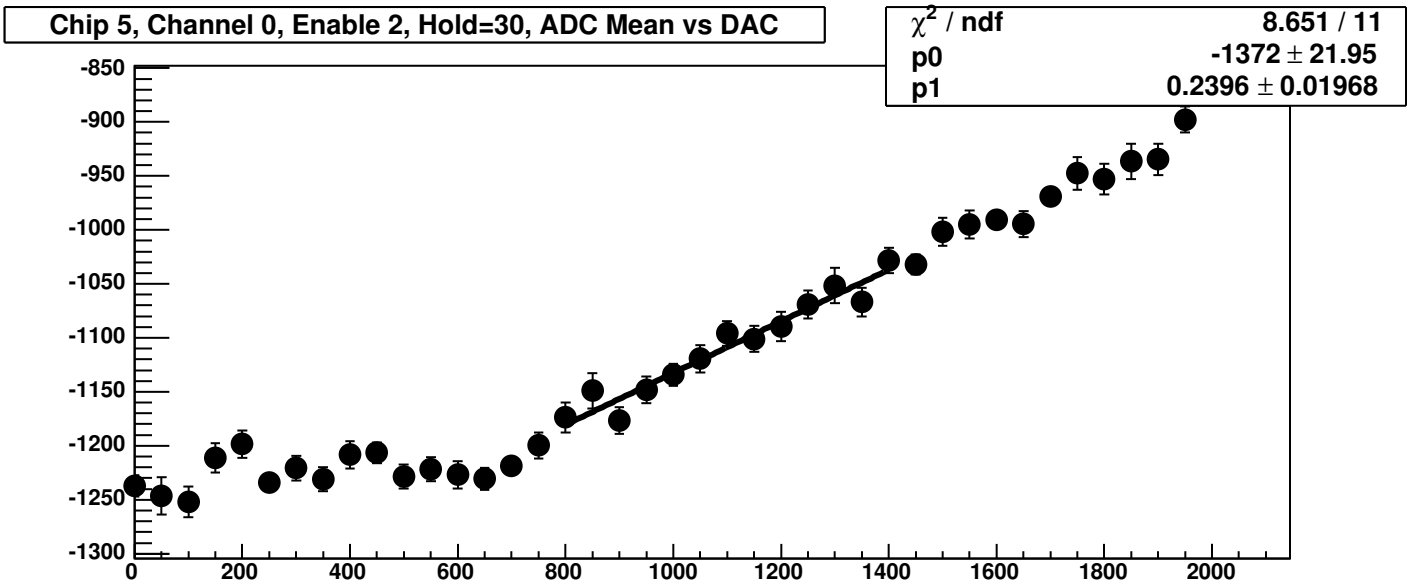

Chip 5, Channel 0, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0 200 400 600 800 1000 1200 1400 1600 1800 2000

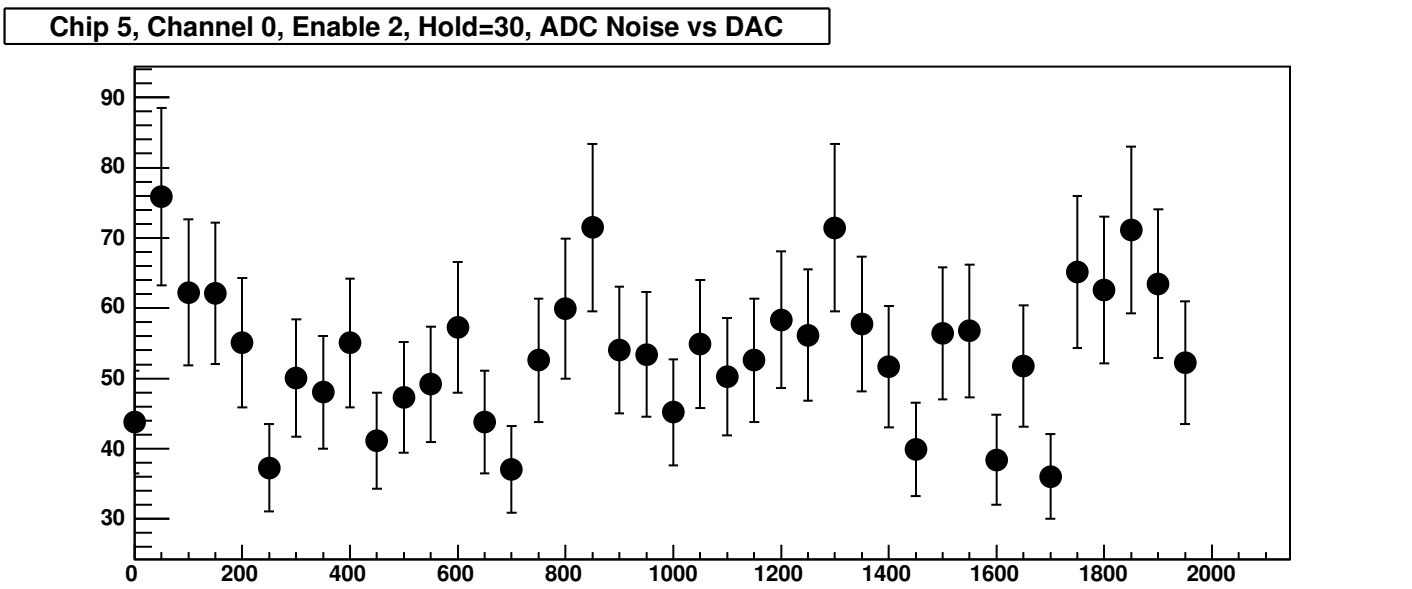

Chip 5, Channel 0, Enable 0, Hold=30, ADC Residuals vs DAC
400
300
200
100
0
0 200 400 600 800 1000 1200 1400 1600 1800 2000

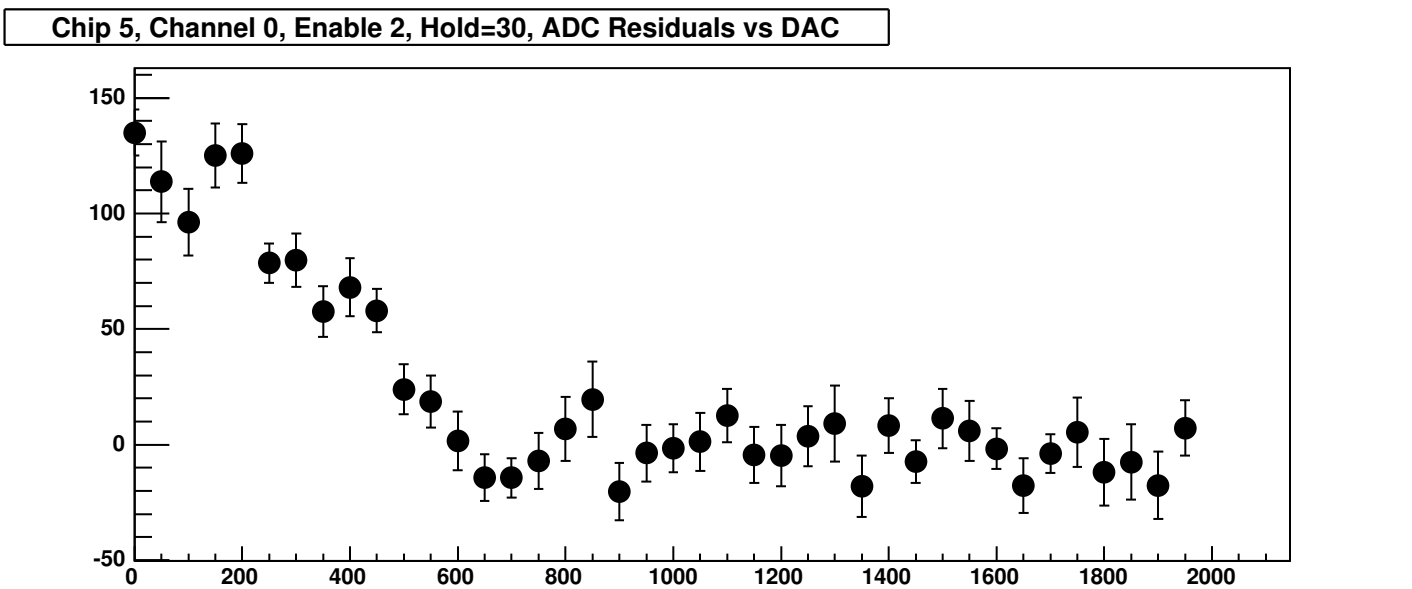

Chip 5, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 10.07 / 11
p0 -1391 ± 19.01
p1 0.2222 ± 0.01679
-950
-1000
-1050
-1100
-1150
-1200
-1250
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

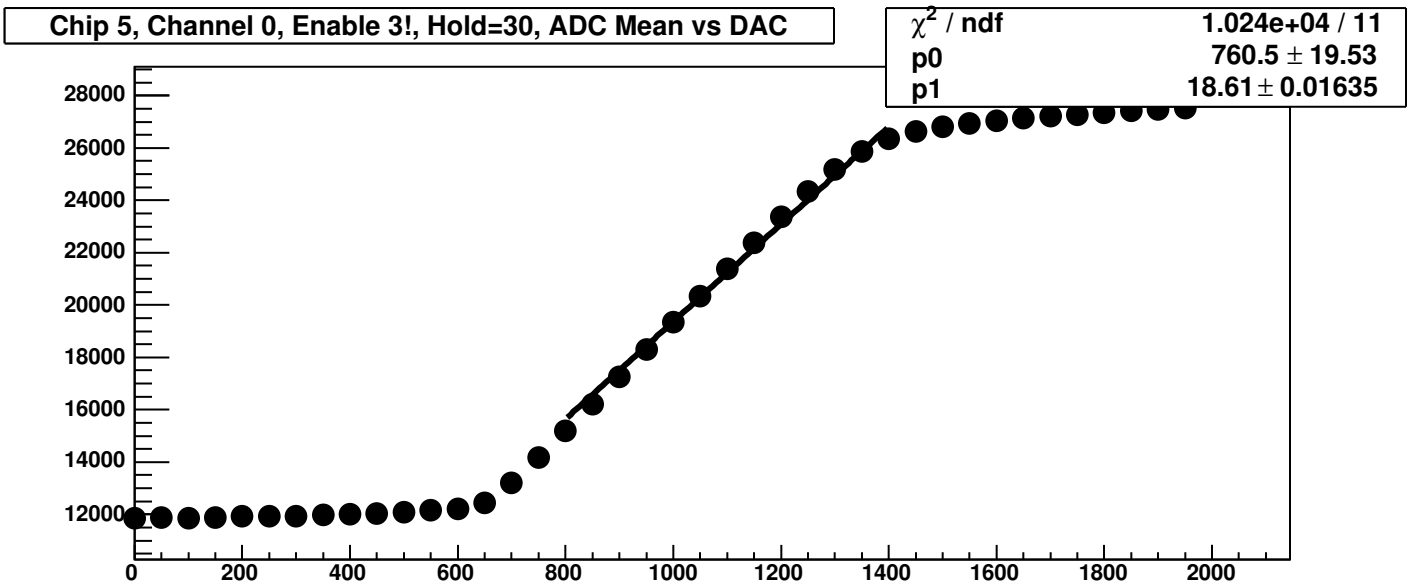

Chip 5, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

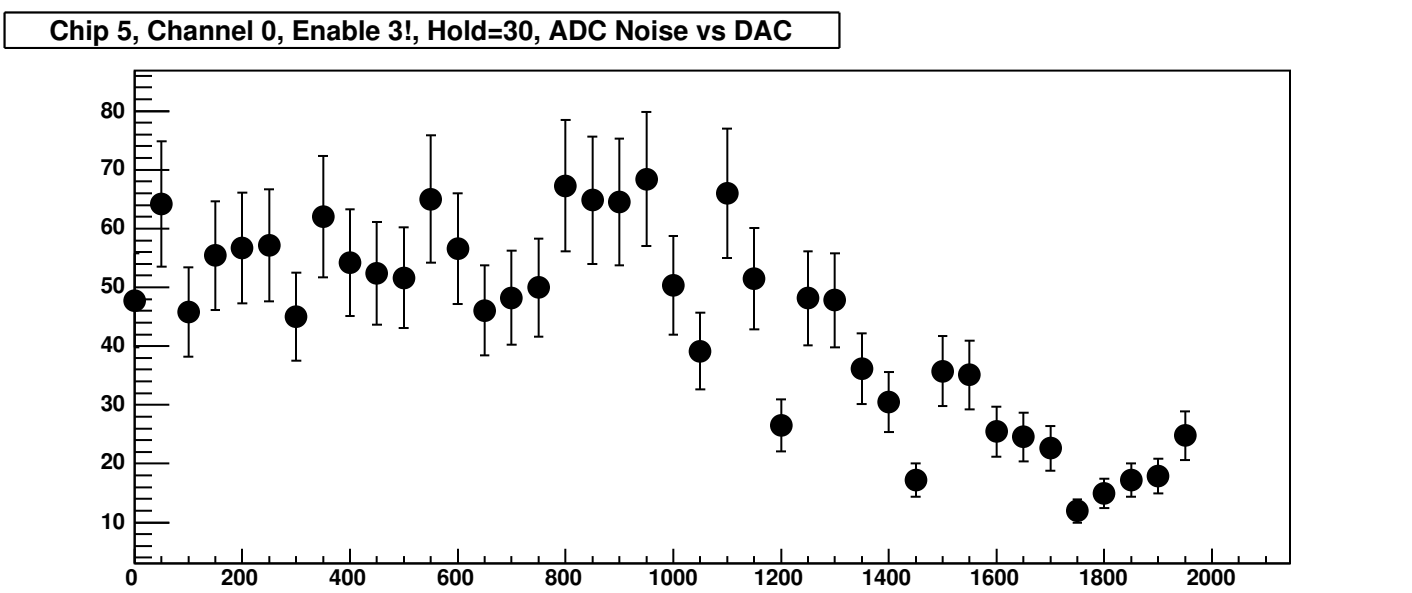

Chip 5, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

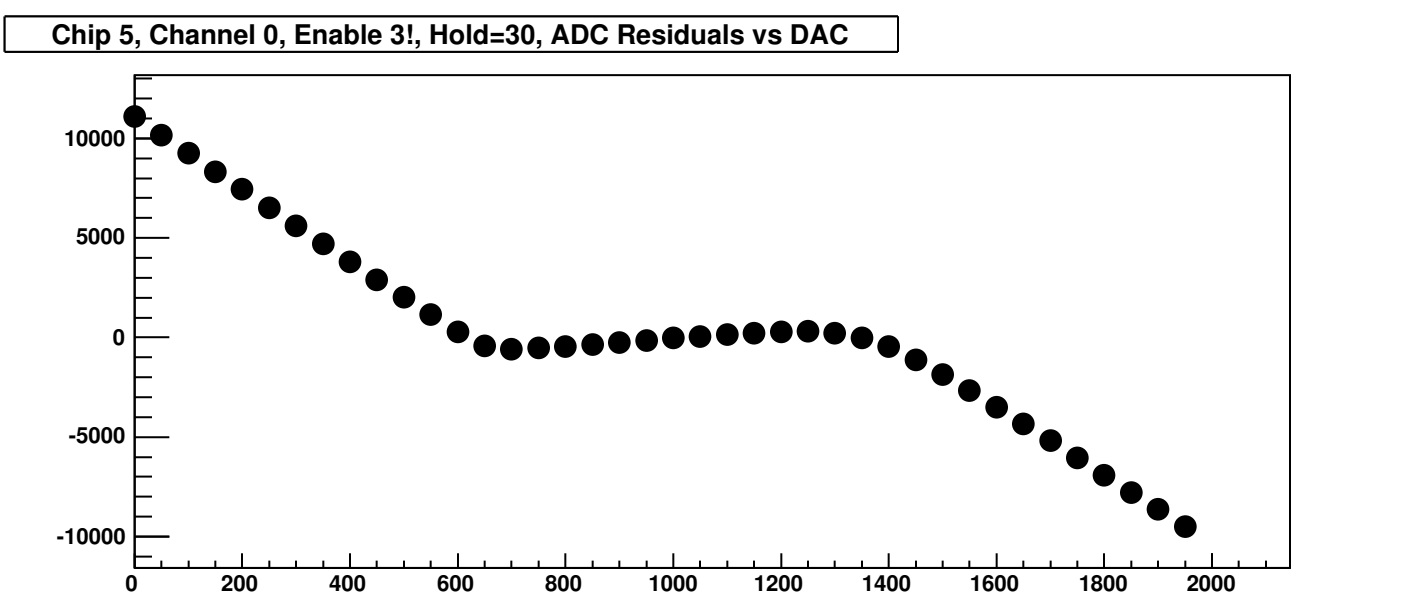

Chip 5, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 8.651 / 11
p0 -1372 ± 21.95
p1 0.2396 ± 0.01968
-850
-900
-950
-1000
-1050
-1100
-1150
-1200
-1250
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

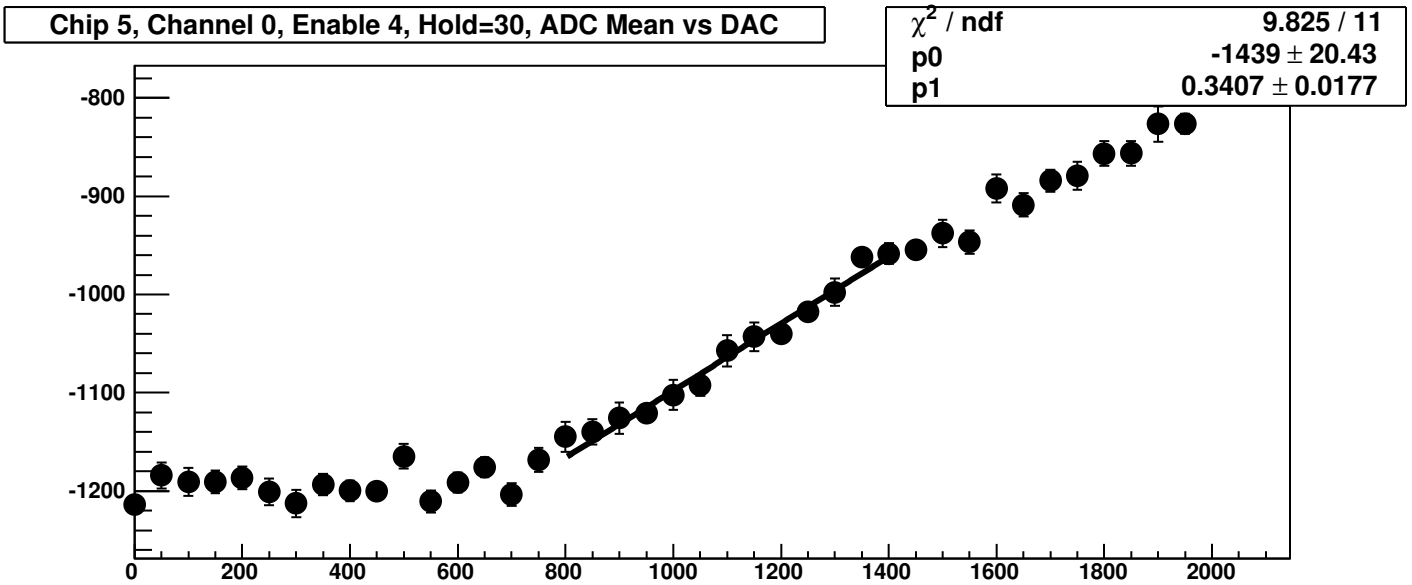

Chip 5, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

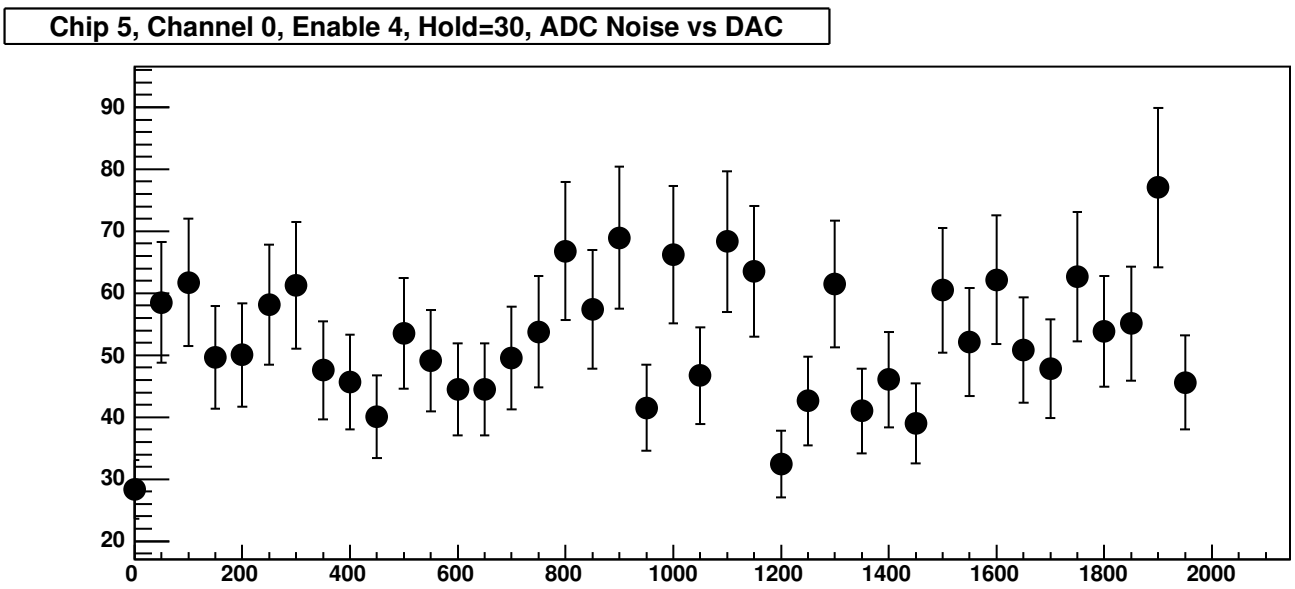

Chip 5, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

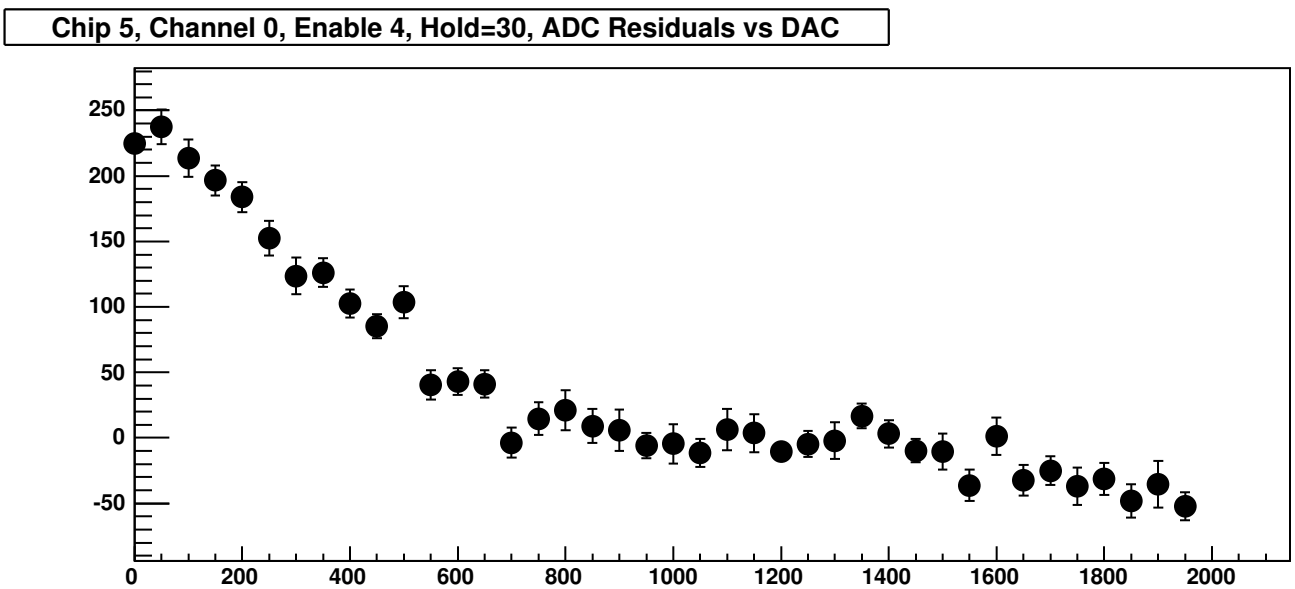

Chip 5, Channel 0, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.024e+04 / 11
p0 760.5 ± 19.53
p1 18.61 ± 0.01635
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 0, Enable 3!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

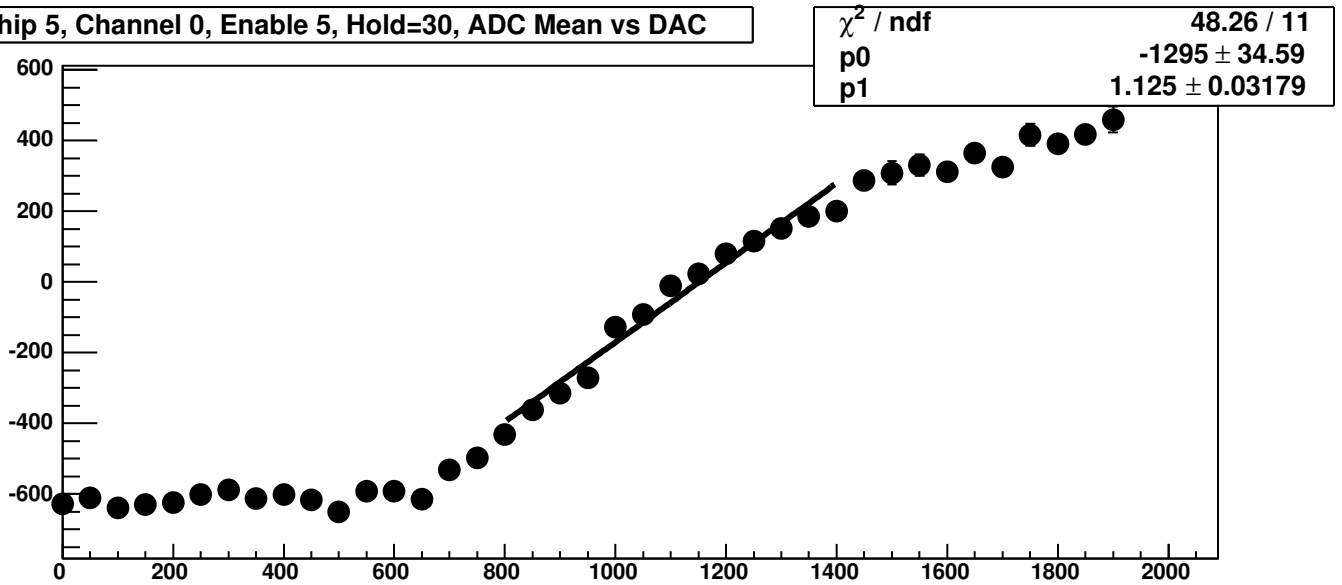

Chip 5, Channel 0, Enable 3!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 9.825 / 11
p0 -1439 ± 20.43
p1 0.3407 ± 0.0177
-800
-900
-1000
-1100
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

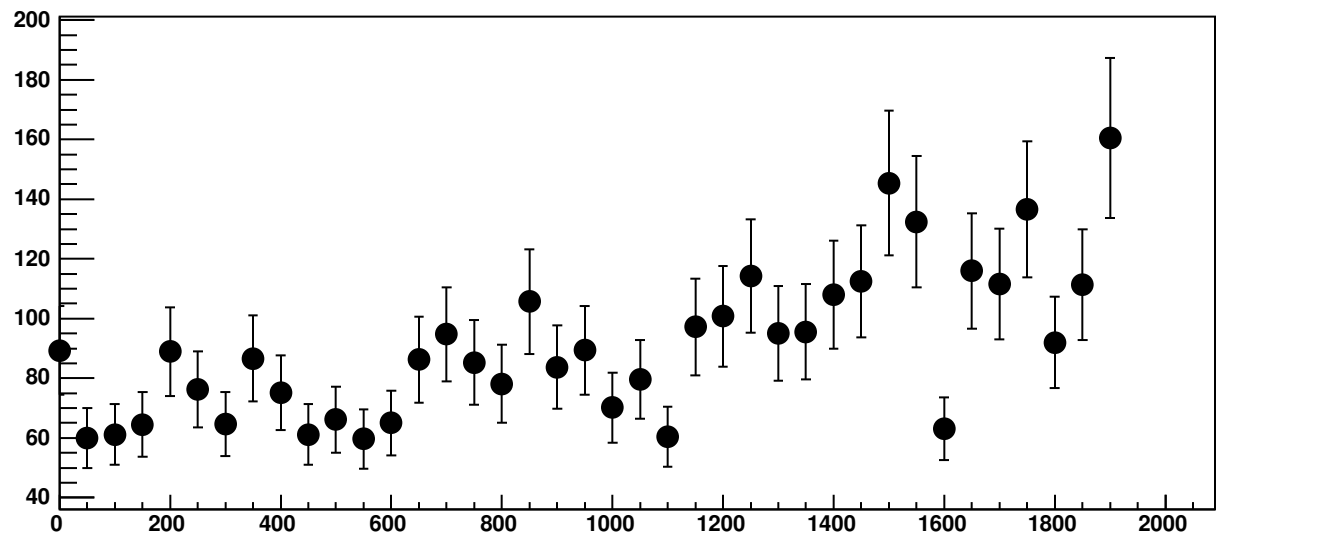

Chip 5, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

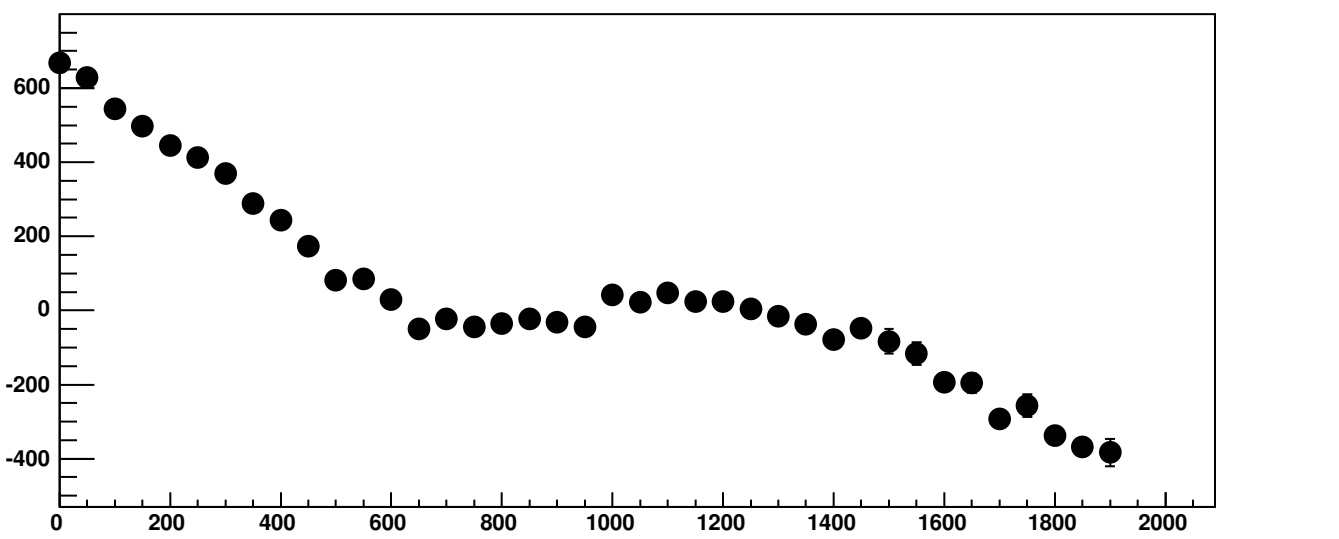
Chip 5, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC

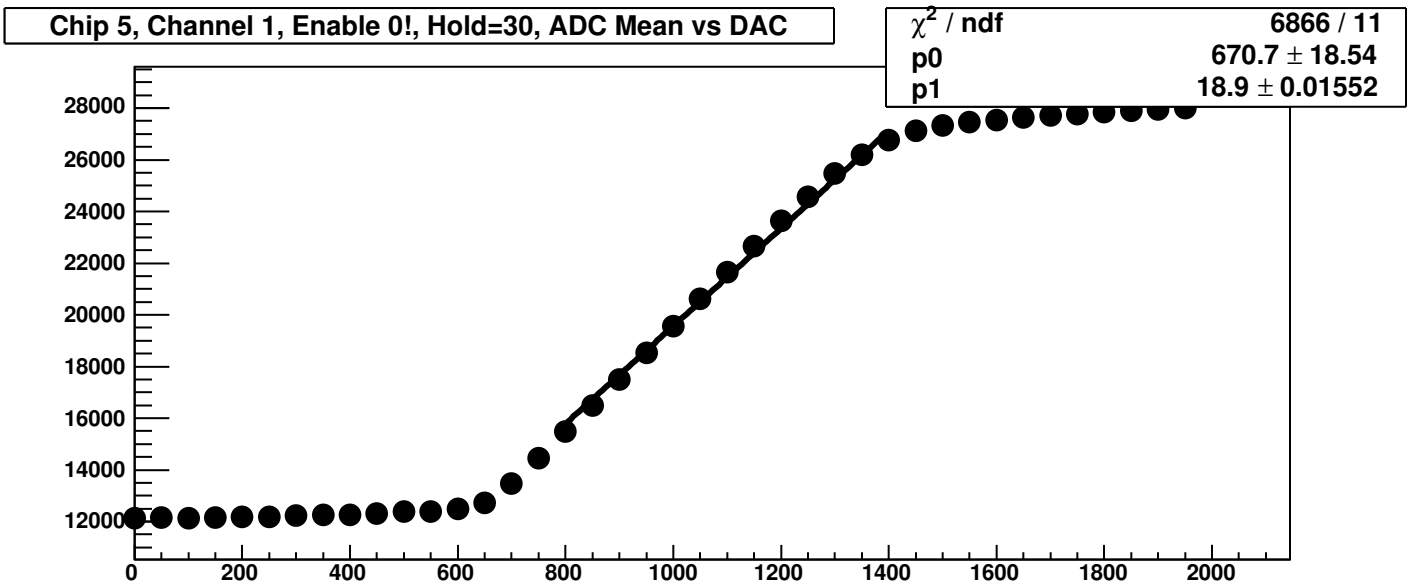


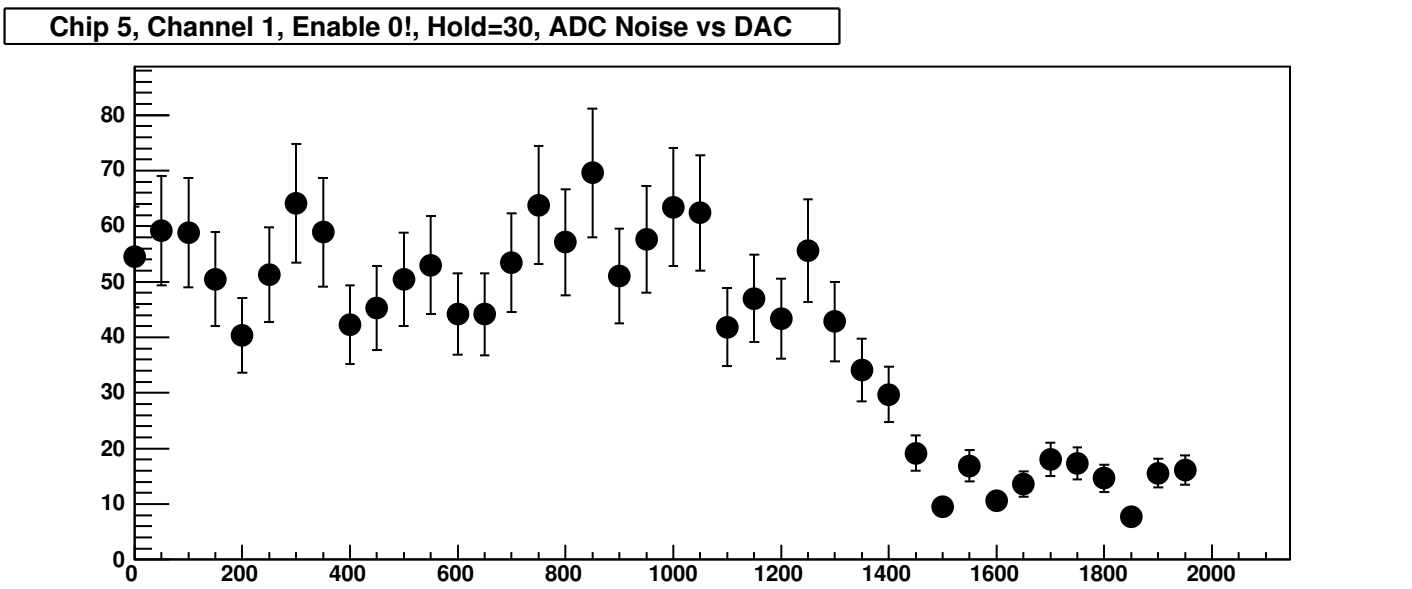
Chip 5, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC

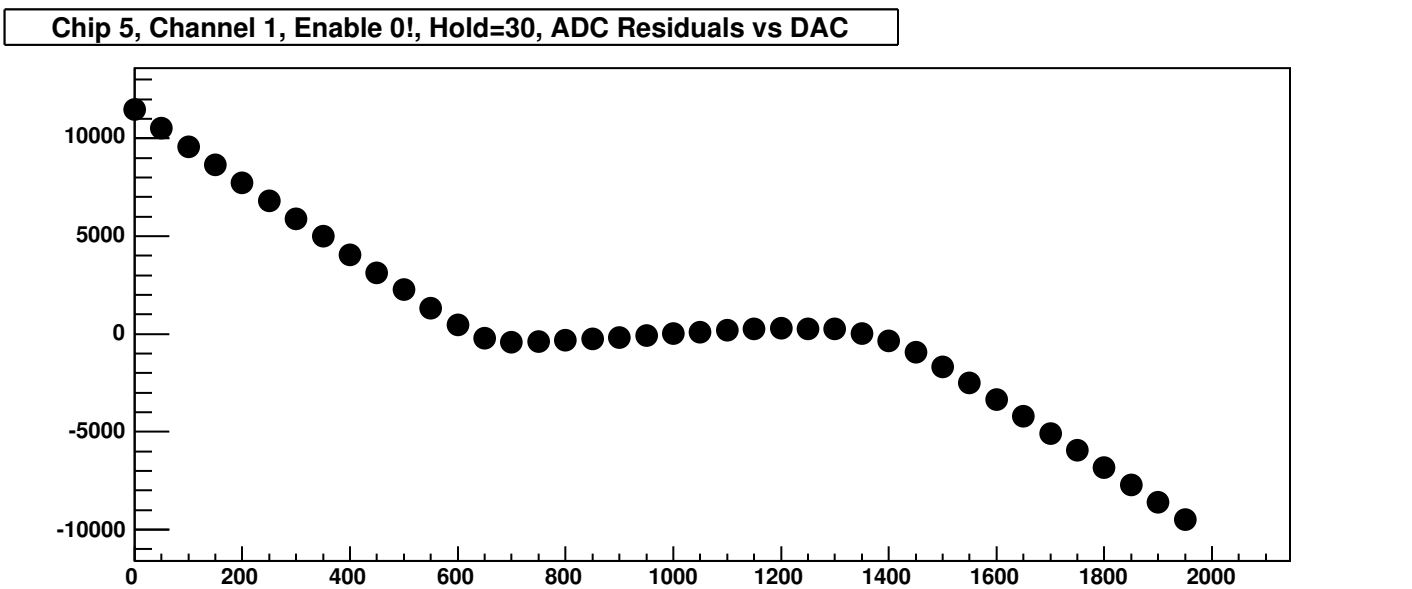


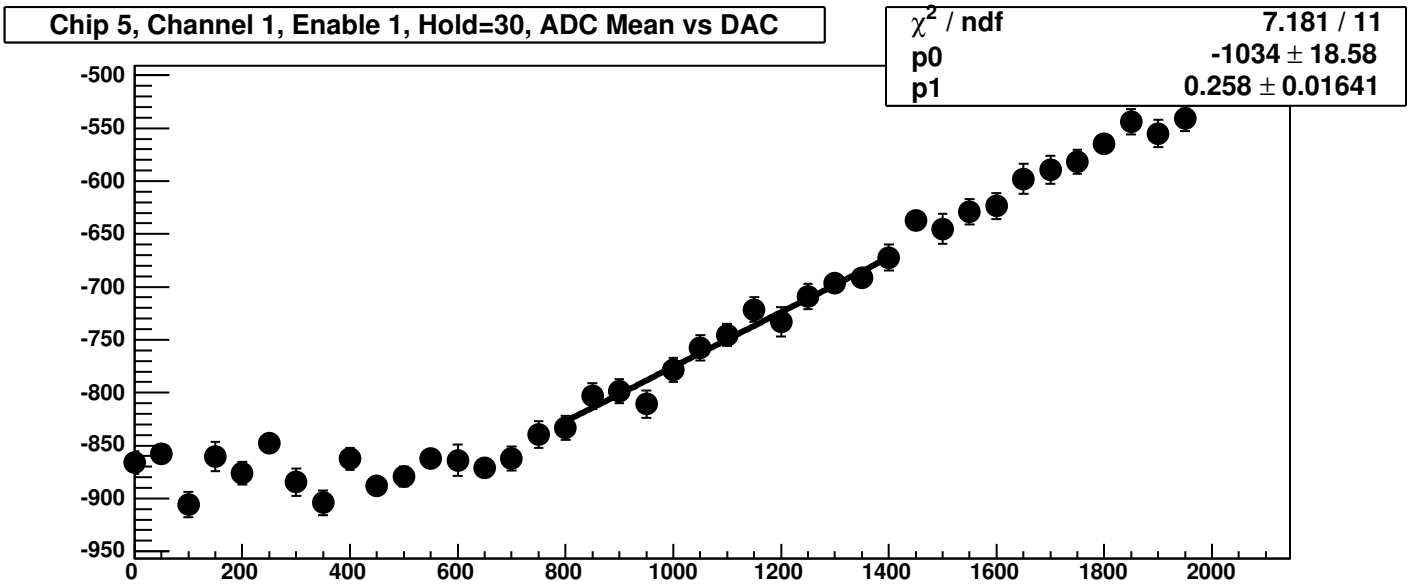
Chip 5, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC

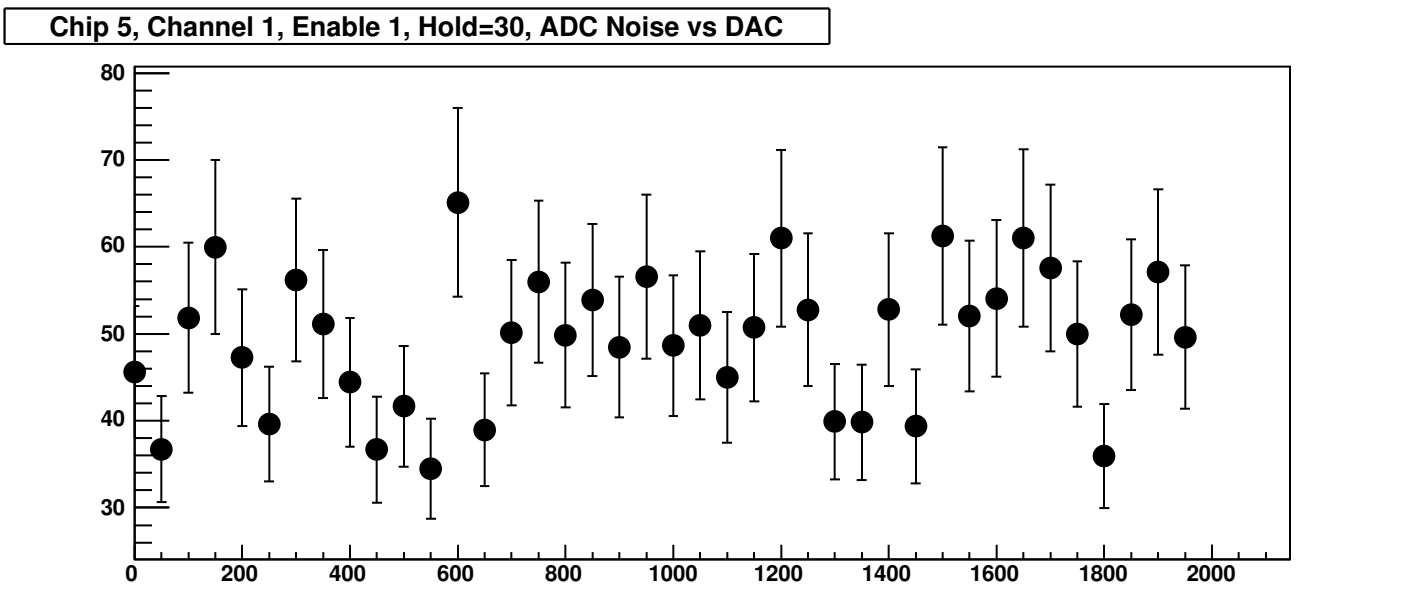


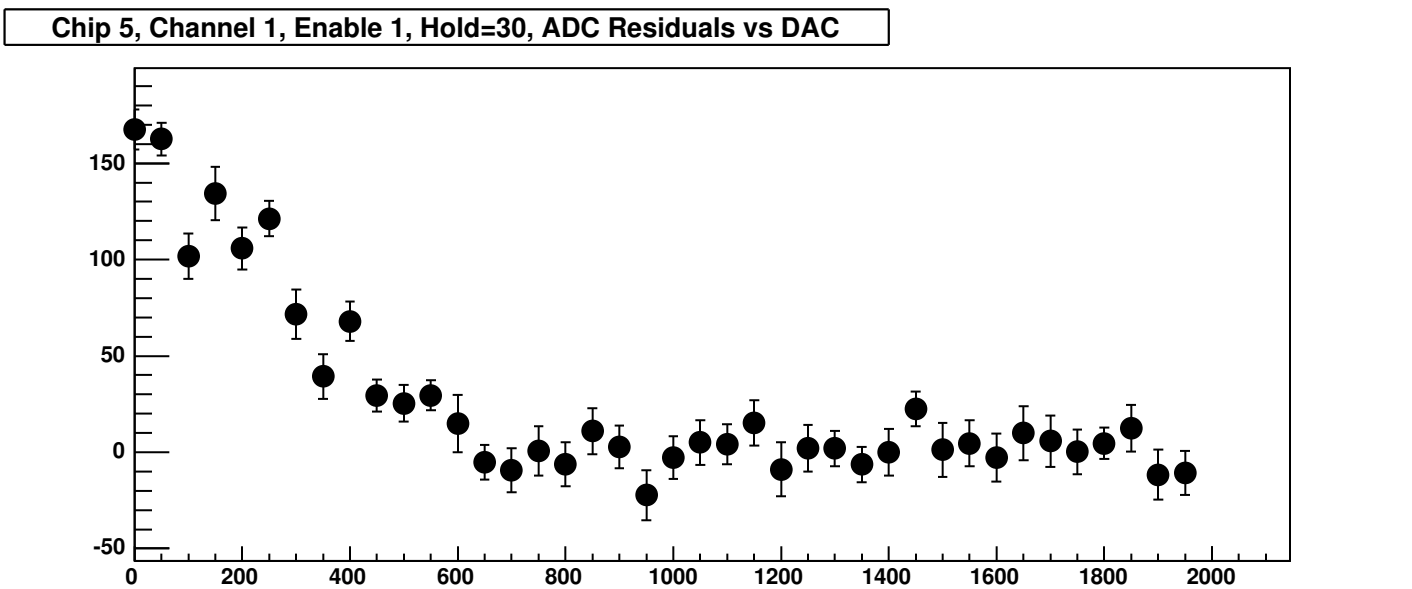

Chip 5, Channel 1, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 6866 / 11
p0 670.7 ± 18.54
p1 18.9 ± 0.01552
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

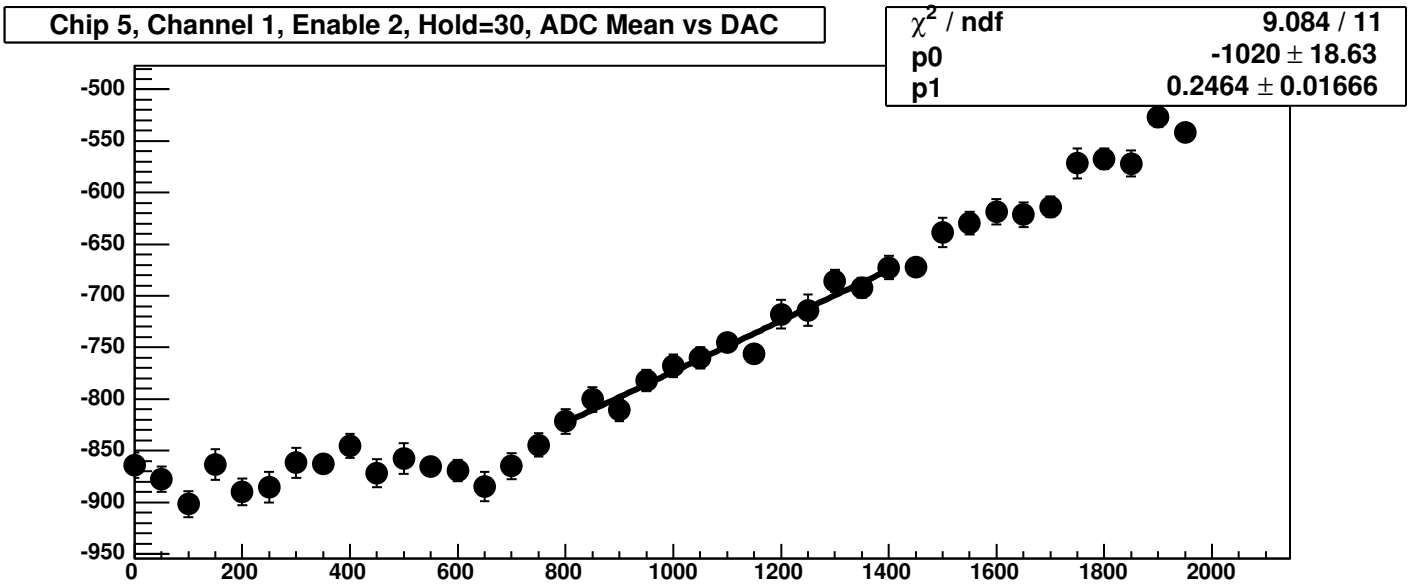

Chip 5, Channel 1, Enable 0!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

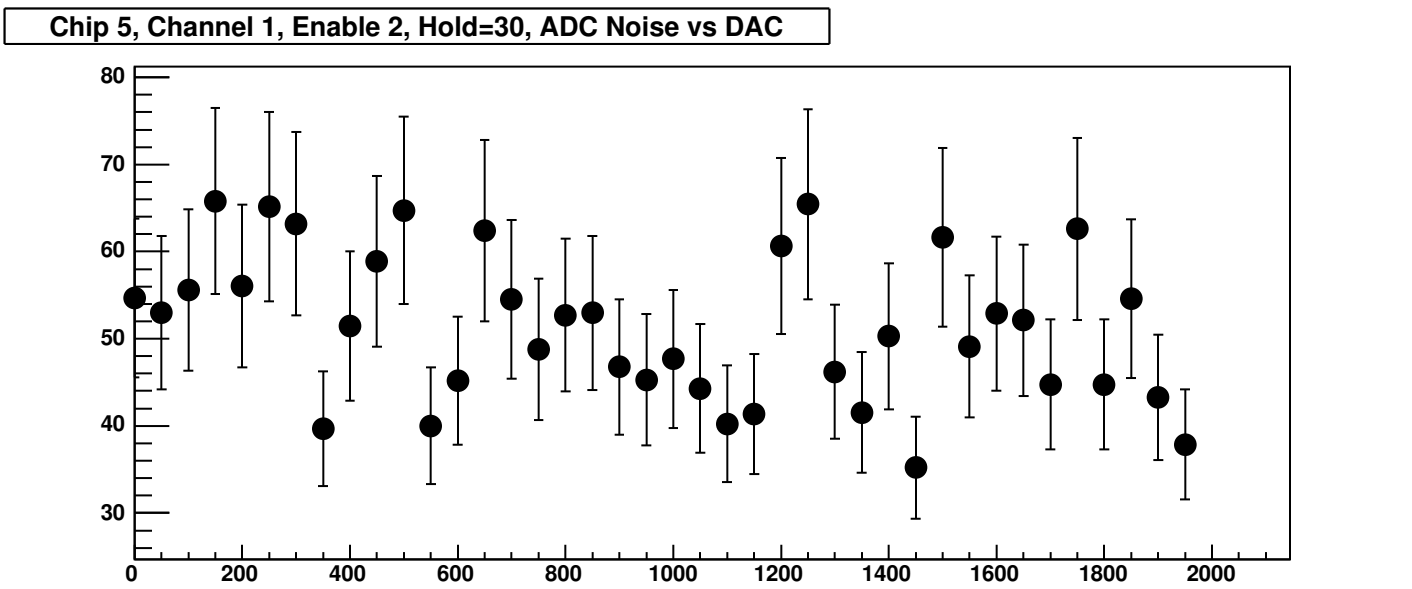

Chip 5, Channel 1, Enable 0!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

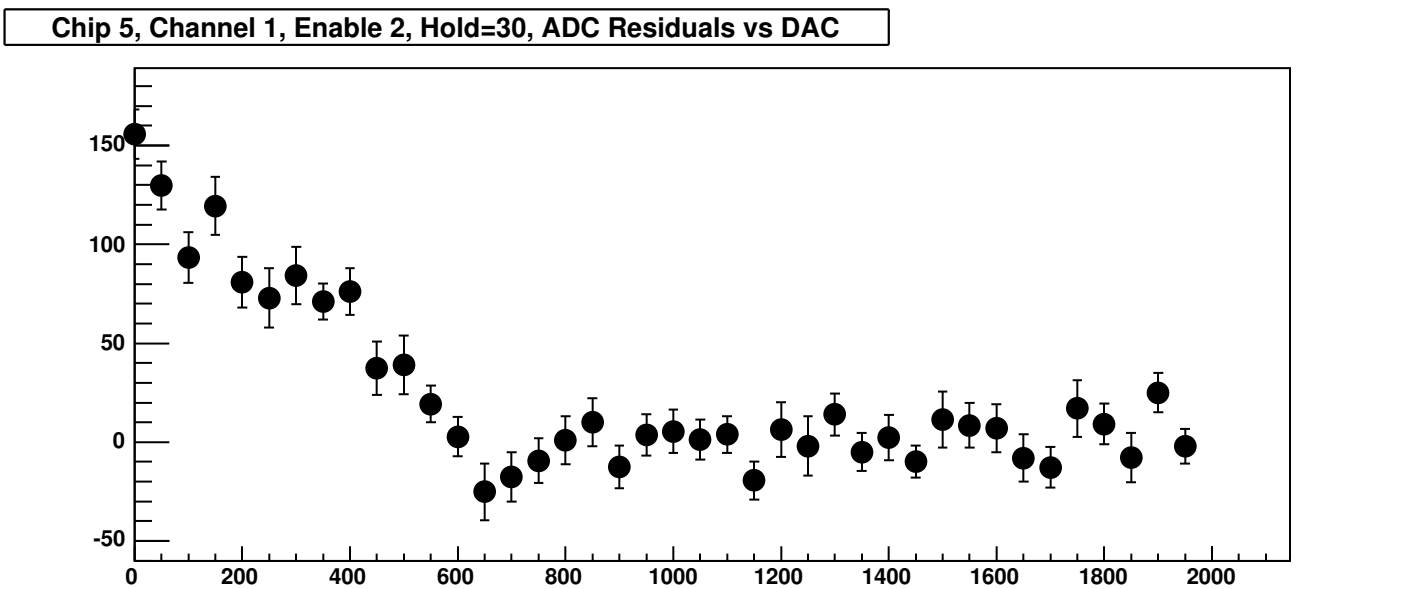

Chip 5, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7.181 / 11
p0 -1034 ± 18.58
p1 0.258 ± 0.01641
-500
-550
-600
-650
-700
-750
-800
-850
-900
-950
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

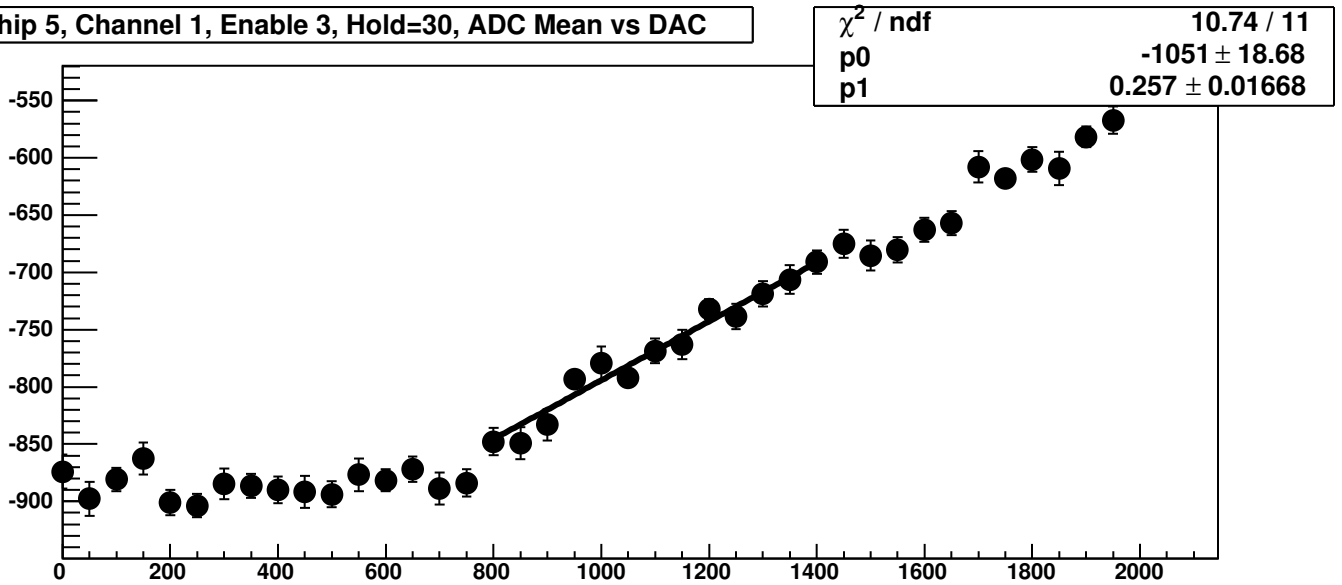

Chip 5, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 9.084 / 11
p0 -1020 ± 18.63
p1 0.2464 ± 0.01666
-500
-550
-600
-650
-700
-750
-800
-850
-900
-950
0
200
400
600
800
1000
1200
1400
1600
1800
2000

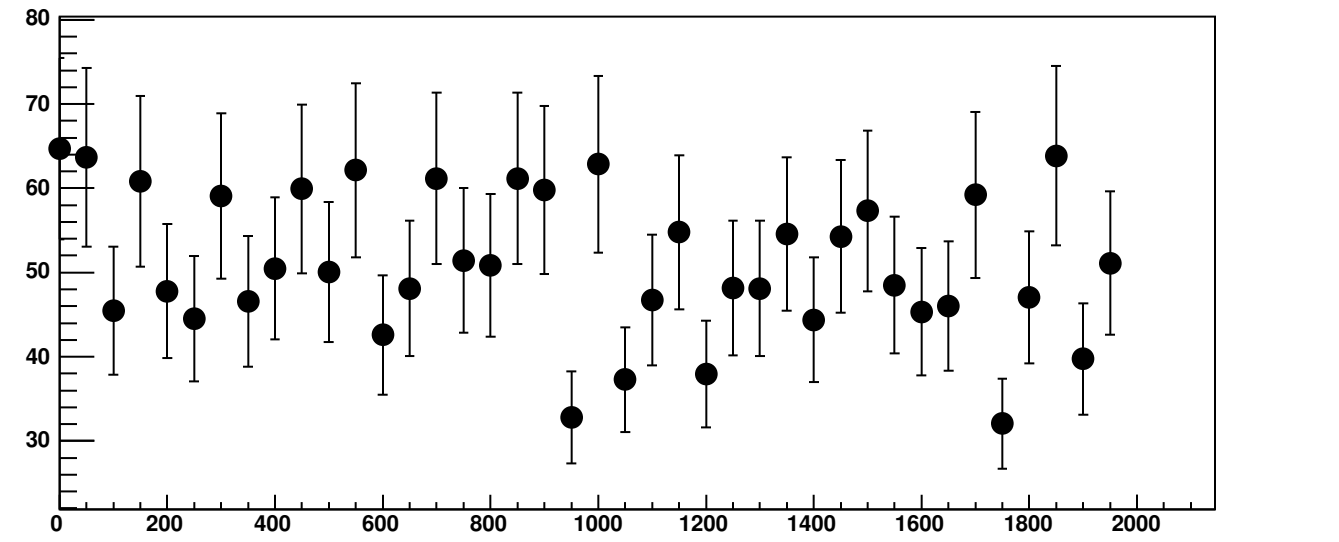

Chip 5, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

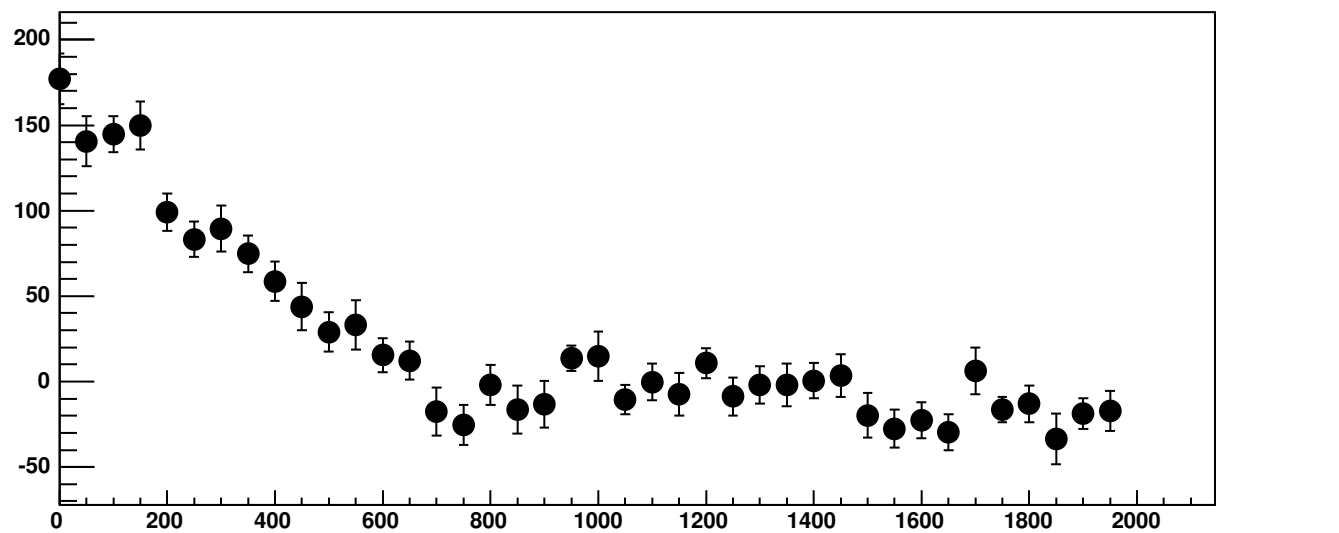
Chip 5, Channel 1, Enable 3, Hold=30, ADC Mean vs DAC

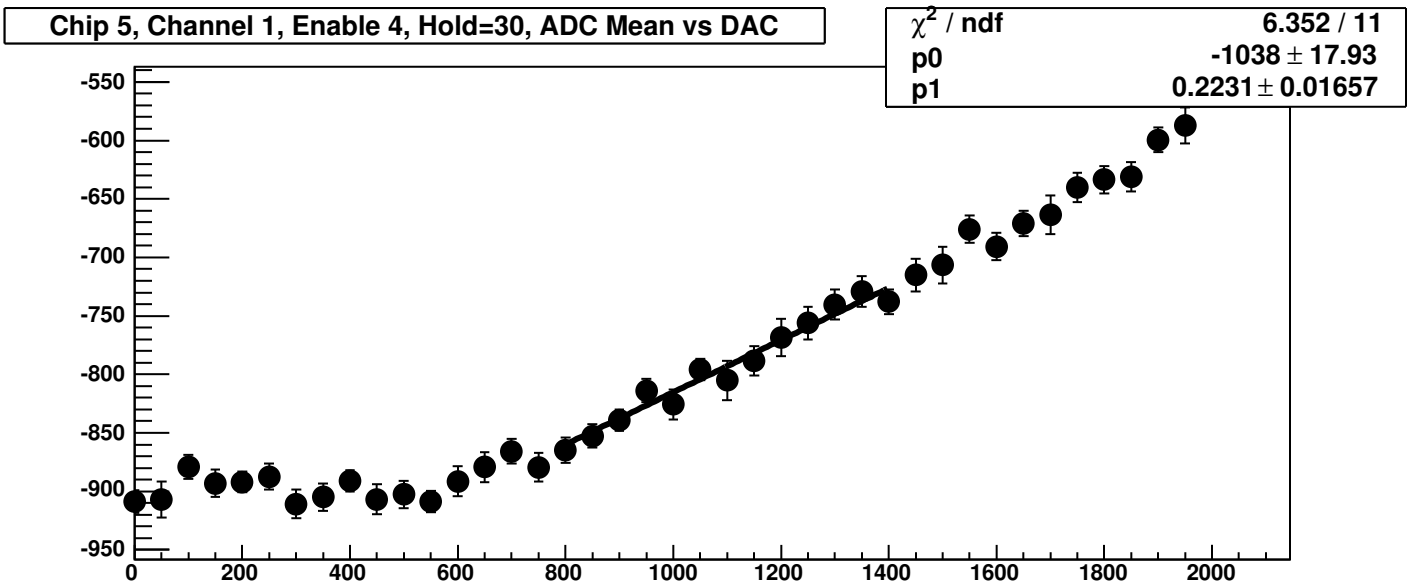


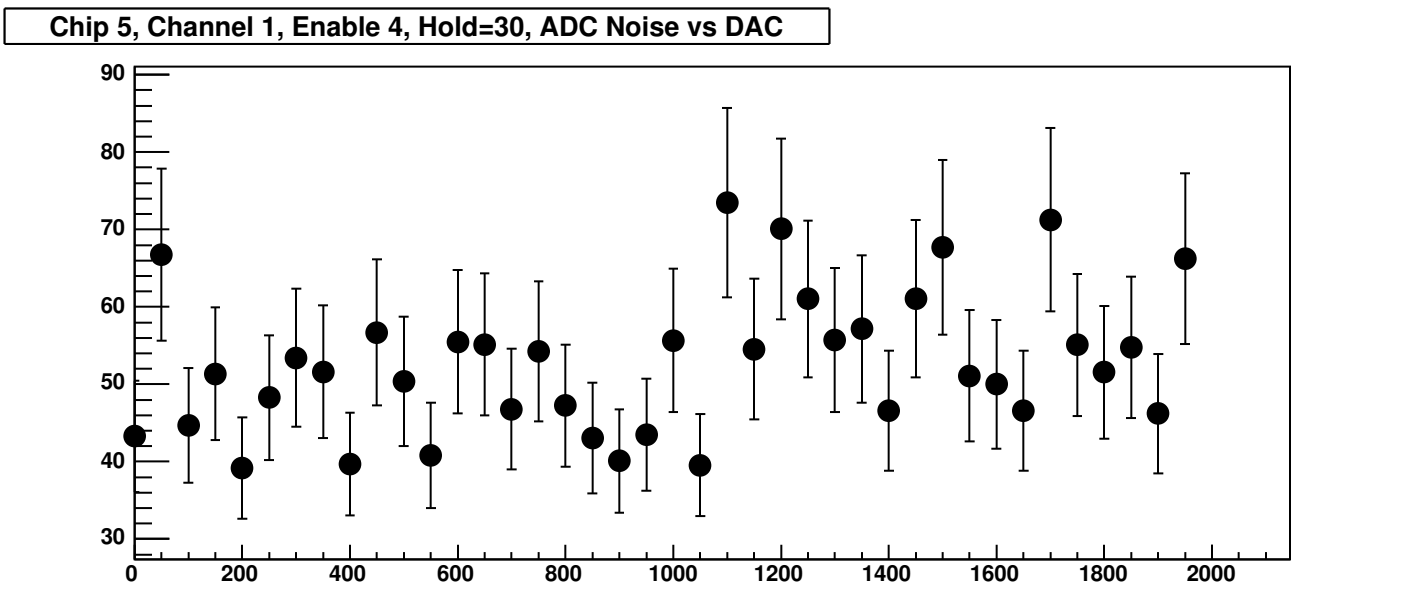
Chip 5, Channel 1, Enable 3, Hold=30, ADC Noise vs DAC

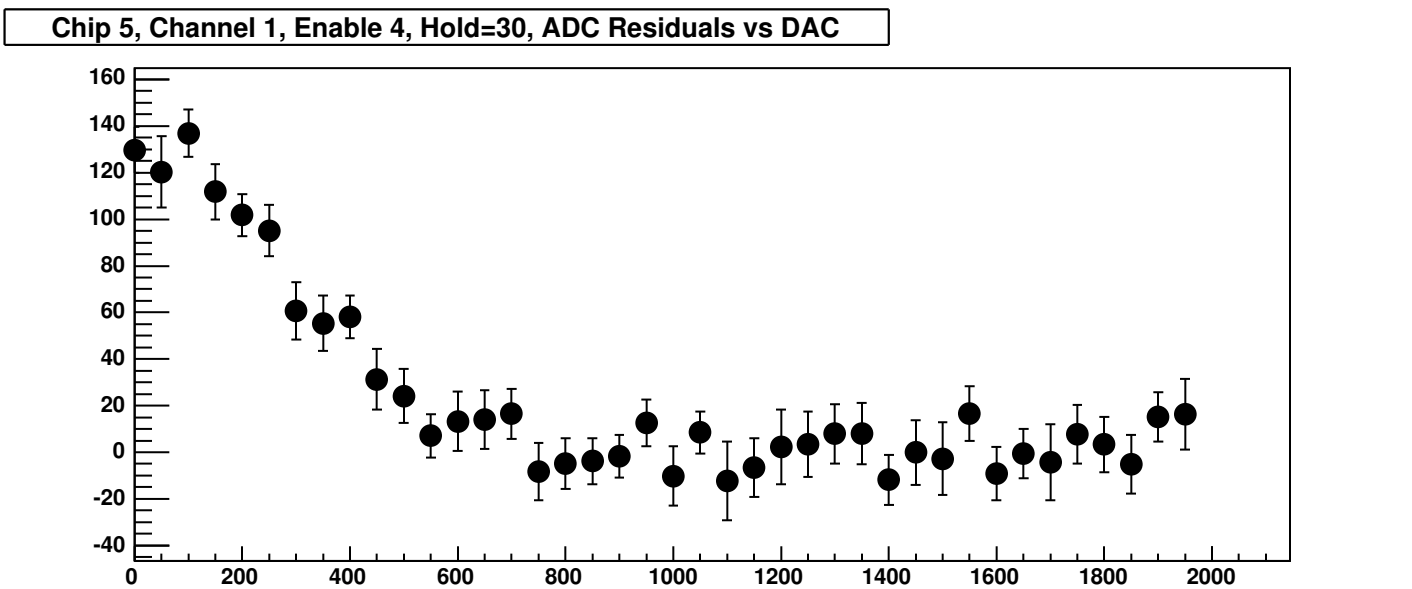


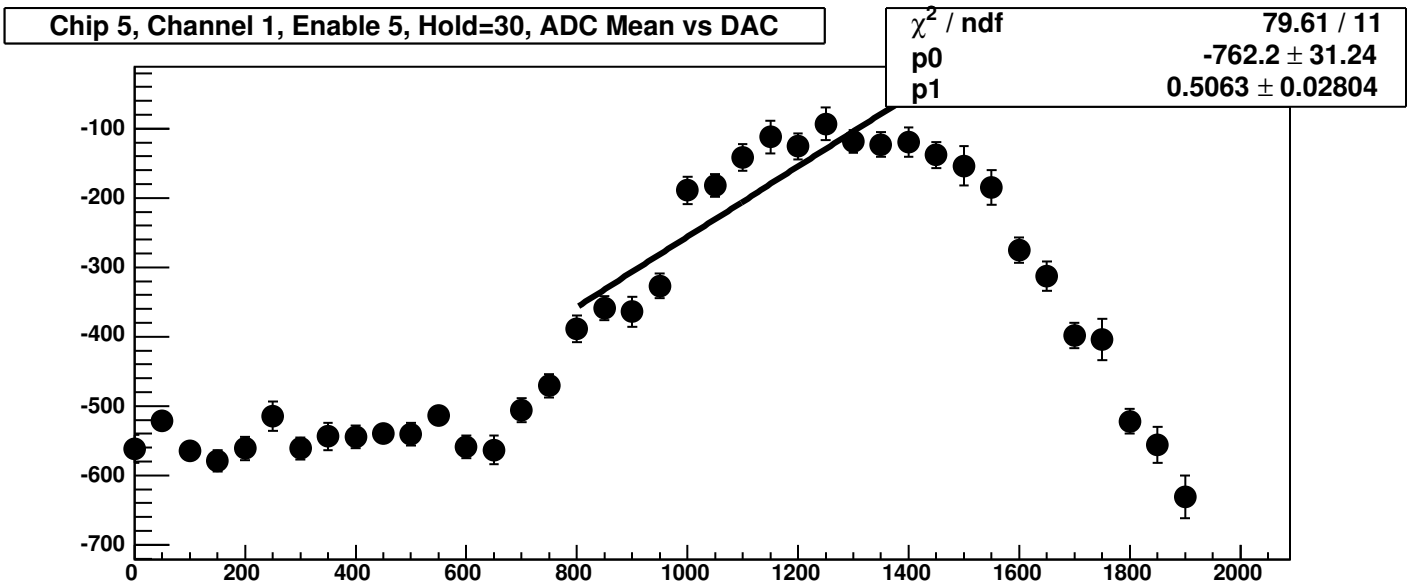
Chip 5, Channel 1, Enable 3, Hold=30, ADC Residuals vs DAC

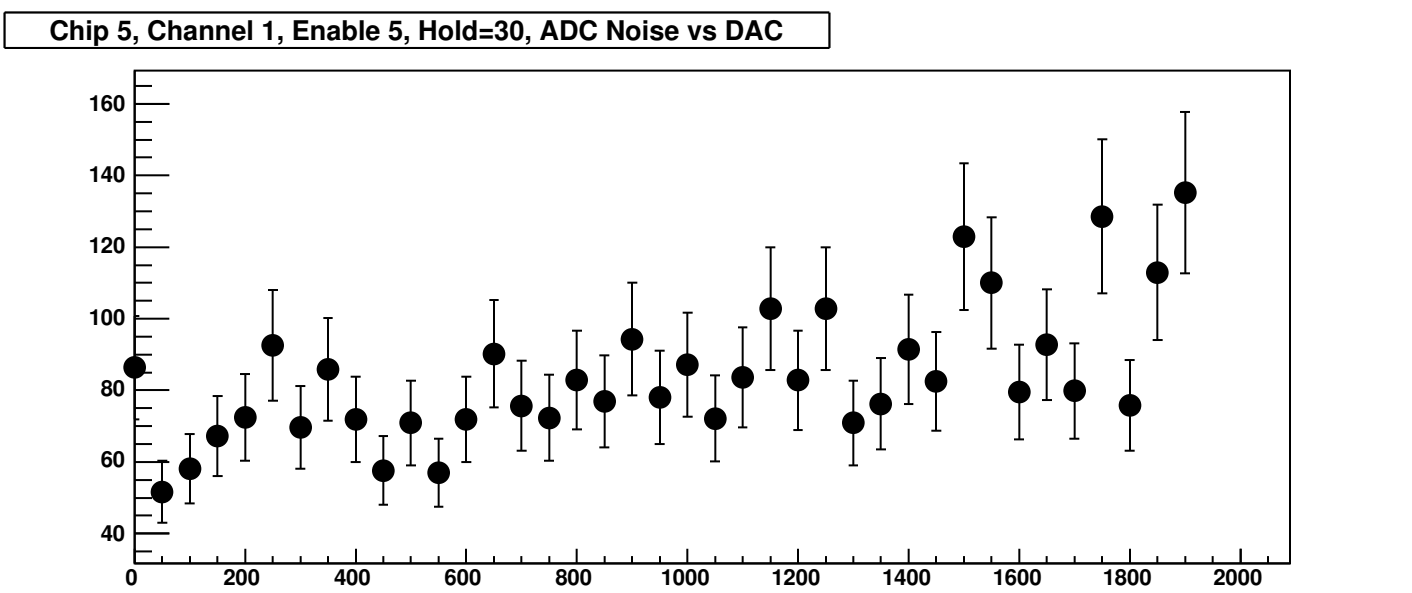


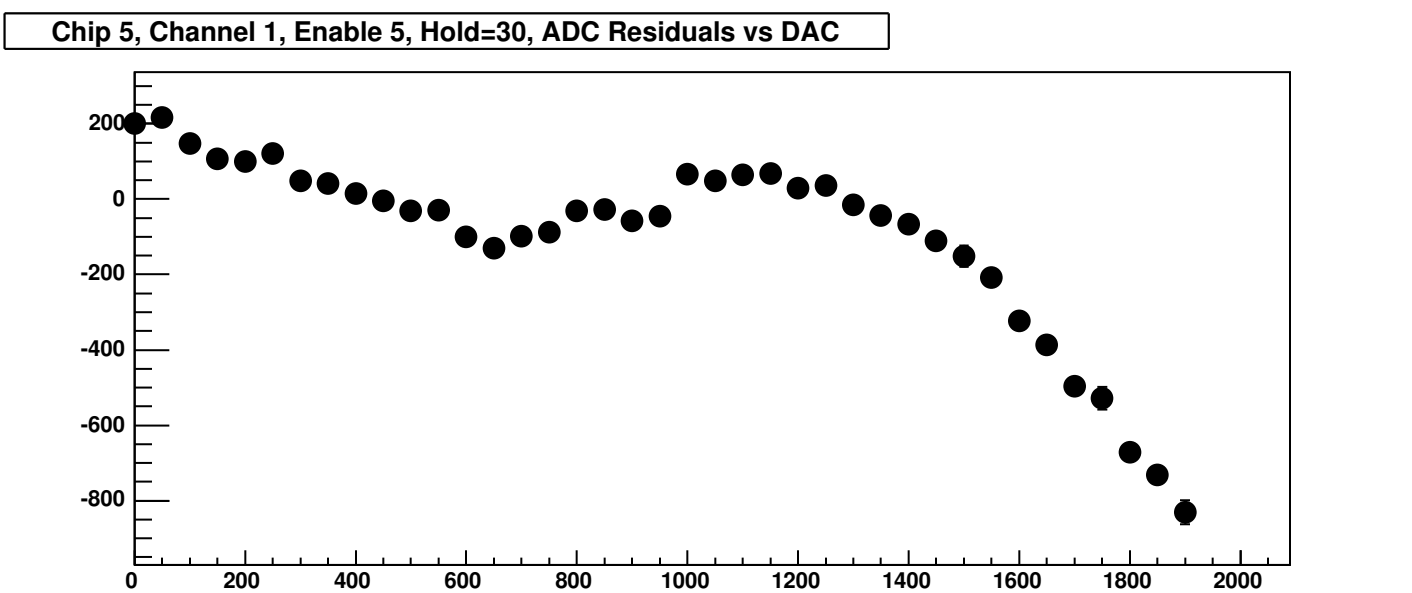

Chip 5, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6.352 / 11
p0 -1038 ± 17.93
p1 0.2231 ± 0.01657
-550
-600
-650
-700
-750
-800
-850
-900
-950
0
200
400
600
800
1000
1200
1400
1600
1800
2000

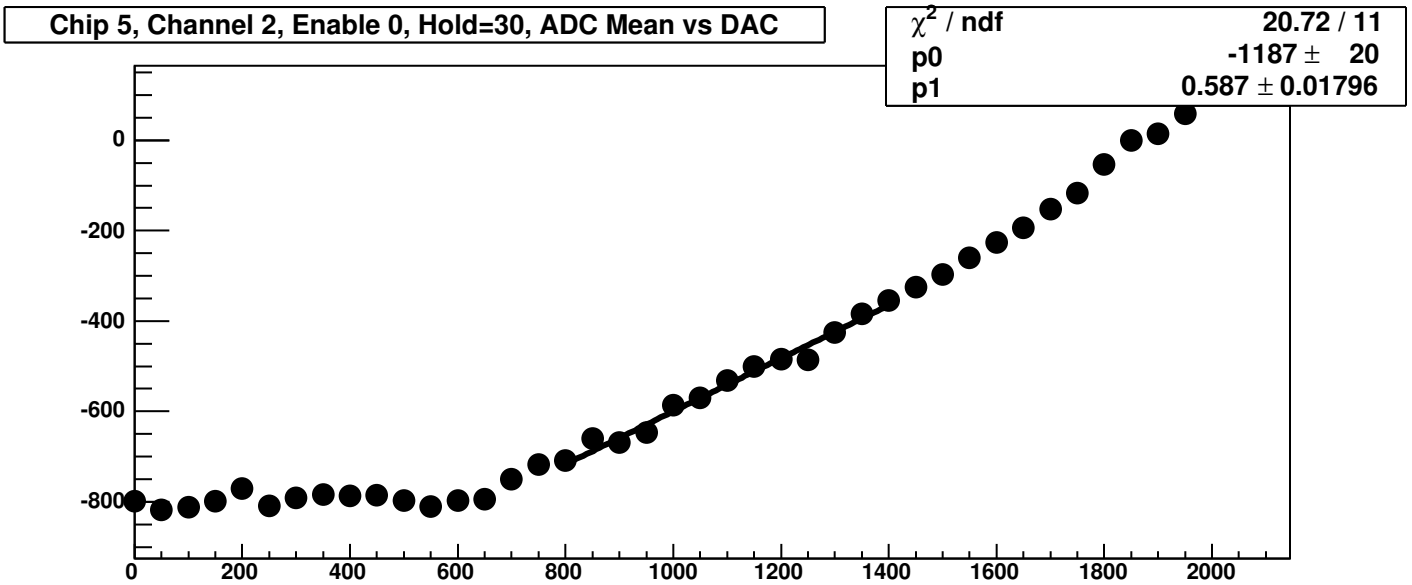

Chip 5, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

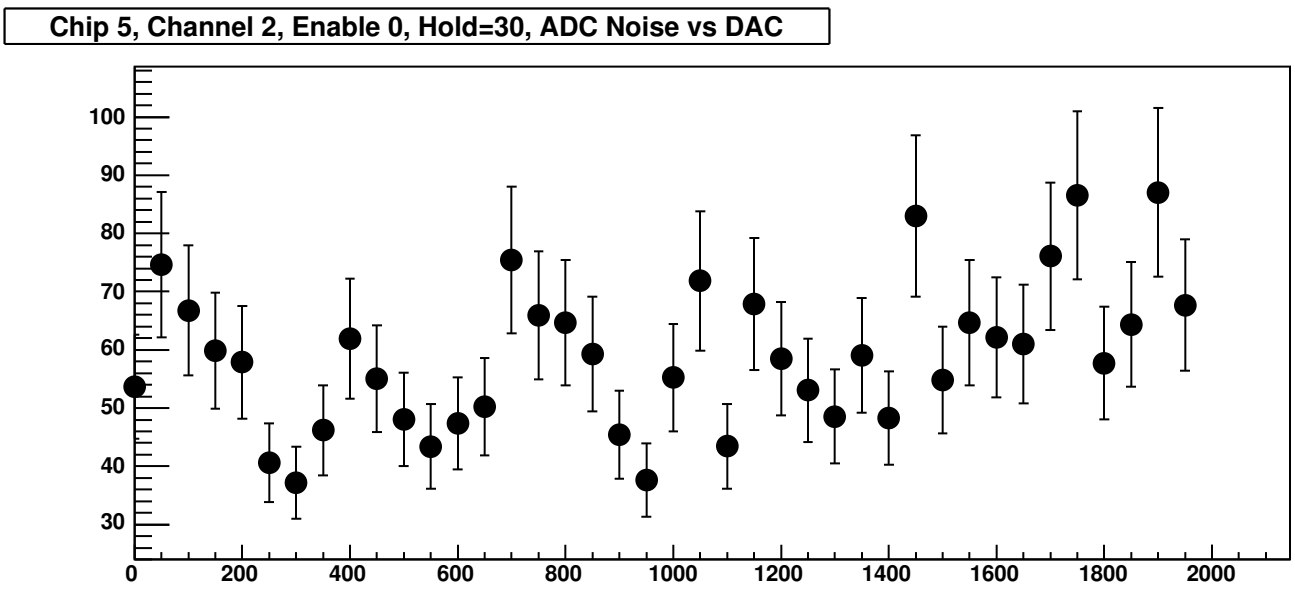

Chip 5, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
160
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

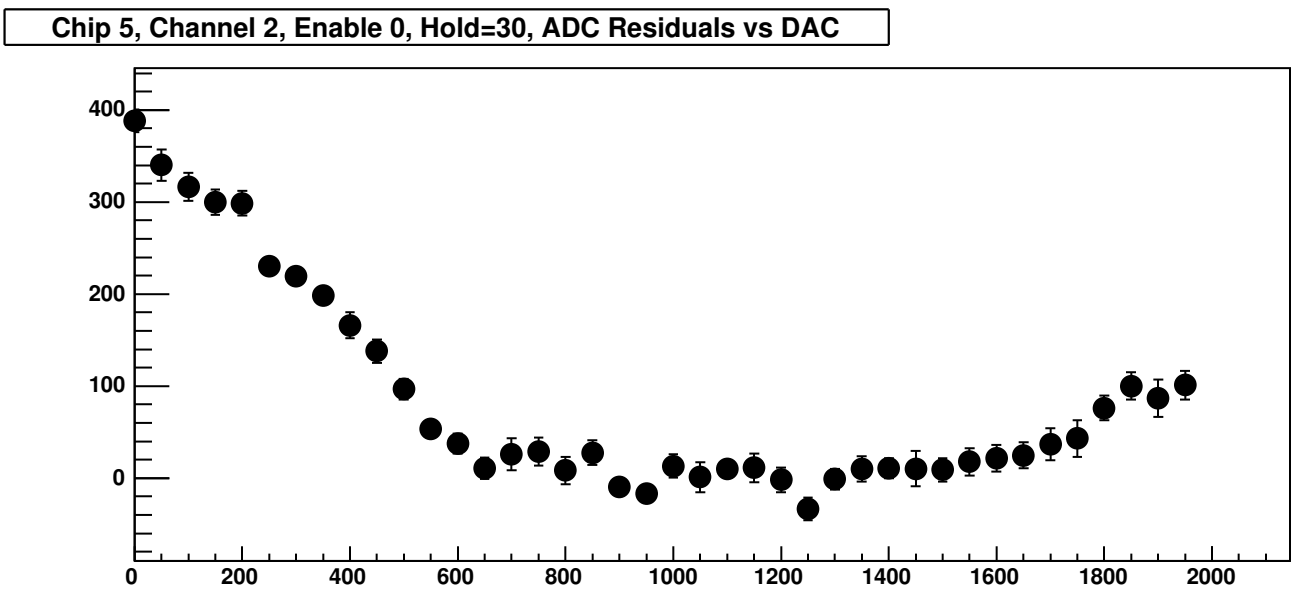

Chip 5, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 79.61 / 11
p0 -762.2 ± 31.24
p1 0.5063 ± 0.02804
-100
-200
-300
-400
-500
-600
-700
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

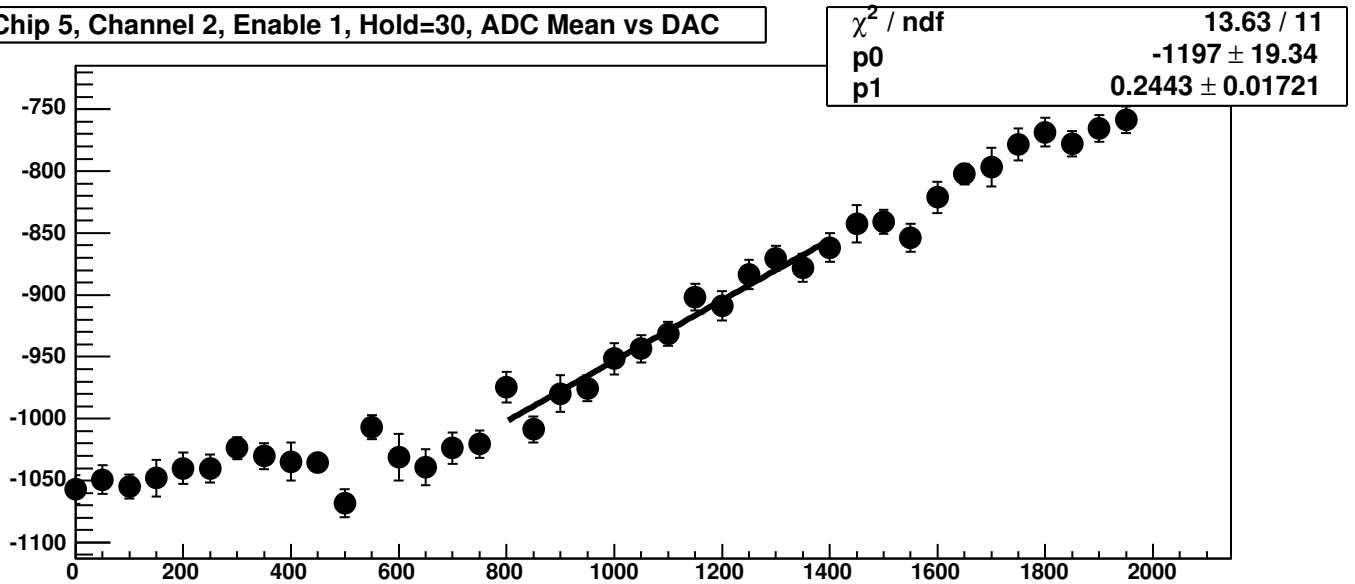

Chip 5, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 2, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 20.72 / 11
p0 -1187 ± 20
p1 0.587 ± 0.01796
0
-200
-400
-600
-800
0 200 400 600 800 1000 1200 1400 1600 1800 2000

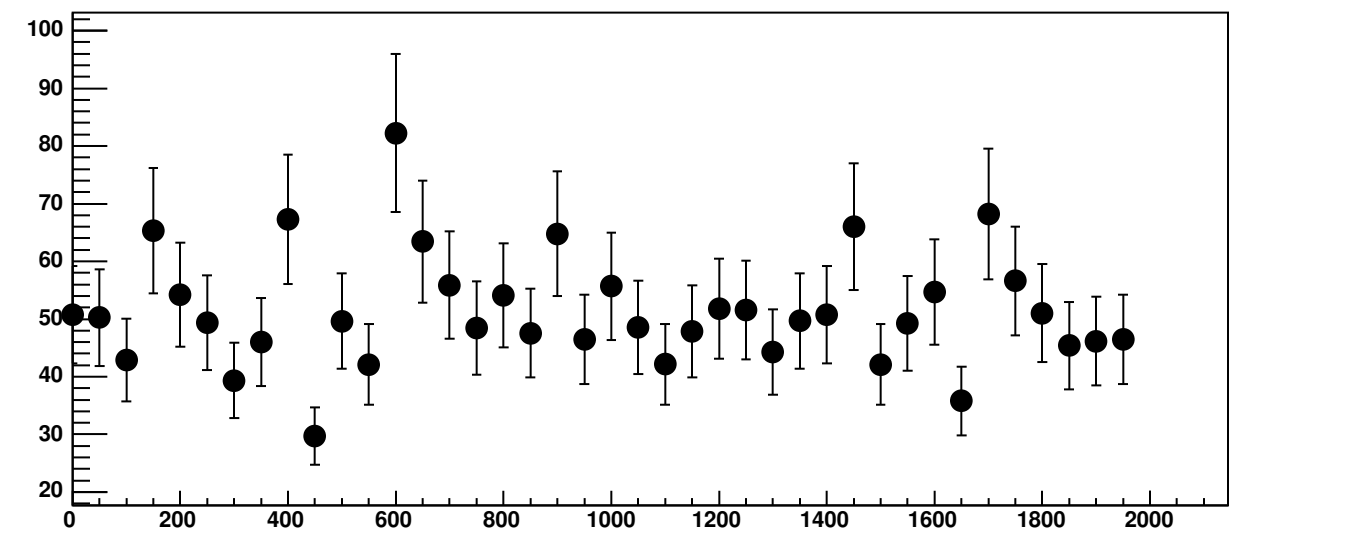

Chip 5, Channel 2, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0 200 400 600 800 1000 1200 1400 1600 1800 2000


Chip 5, Channel 2, Enable 0, Hold=30, ADC Residuals vs DAC
400
300
200
100
0
0 200 400 600 800 1000 1200 1400 1600 1800 2000

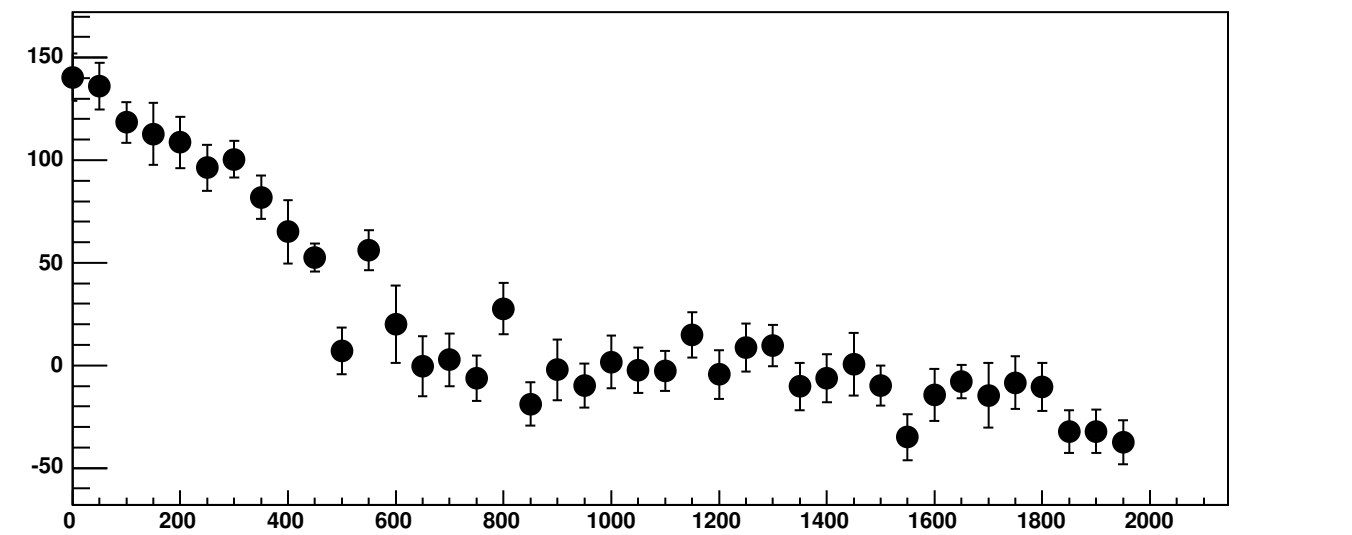
Chip 5, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC

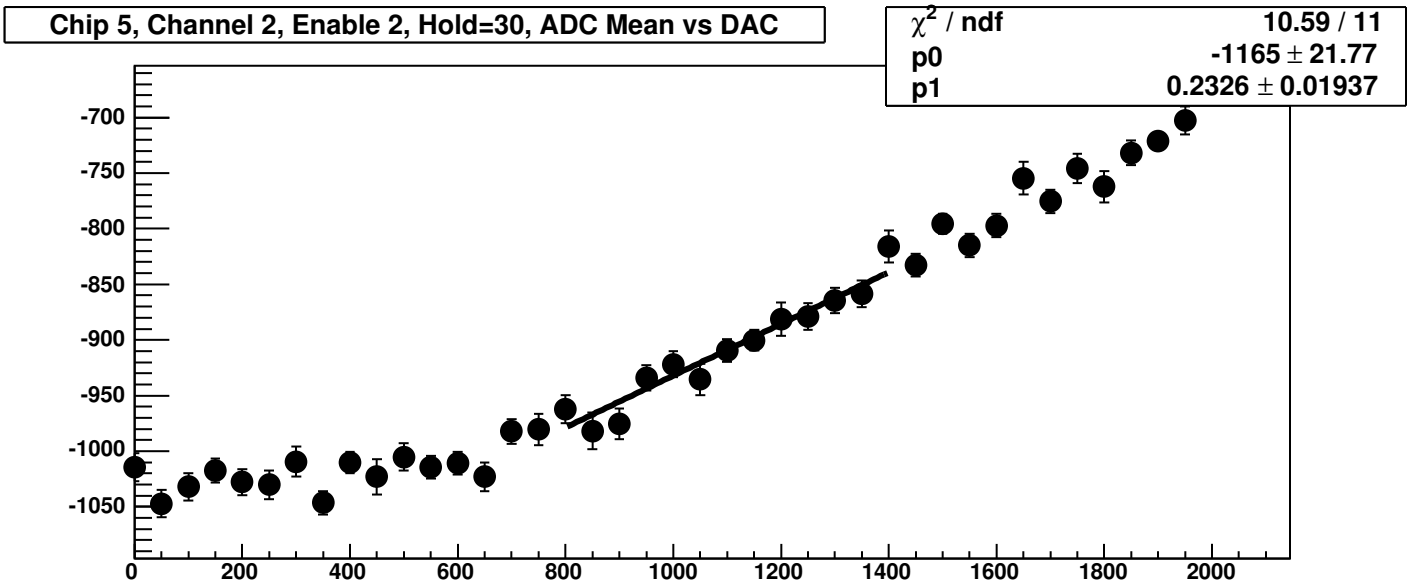


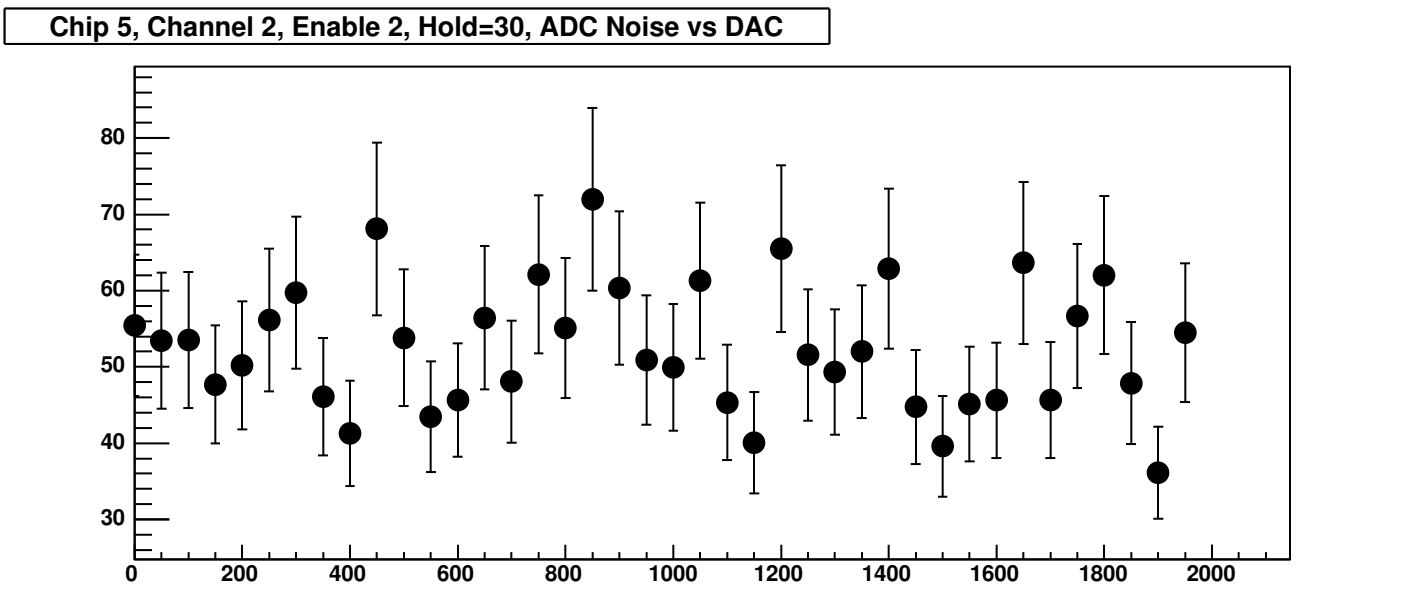
Chip 5, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC

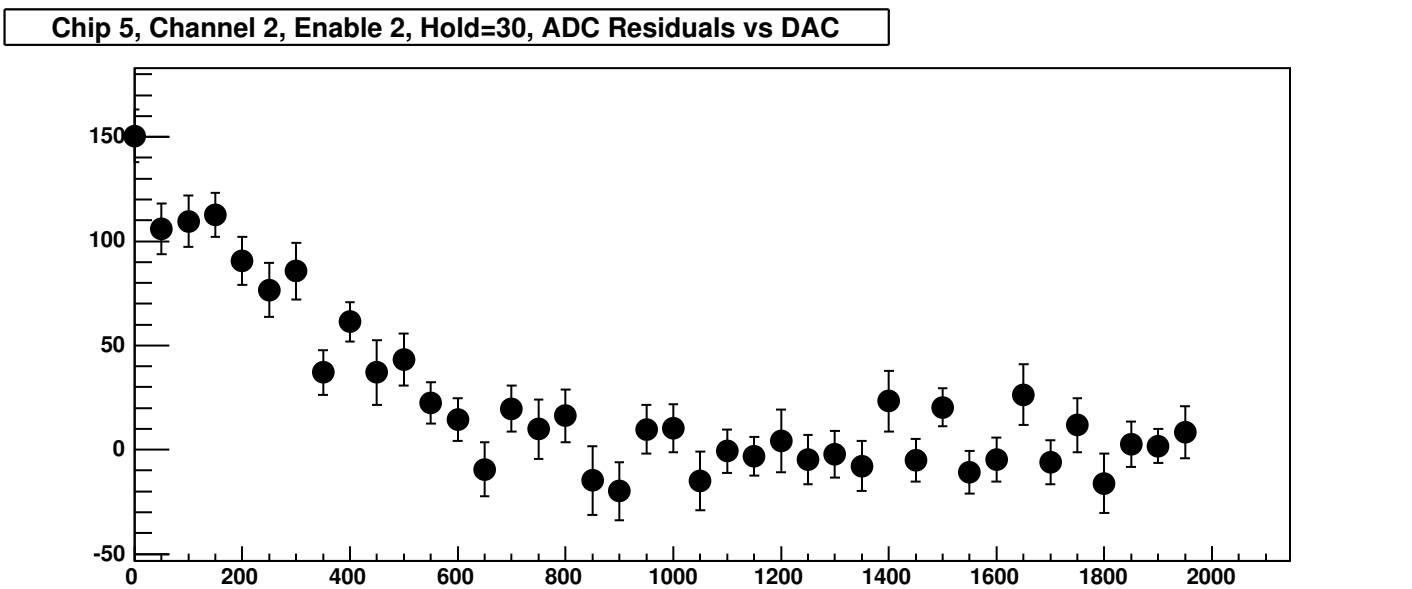


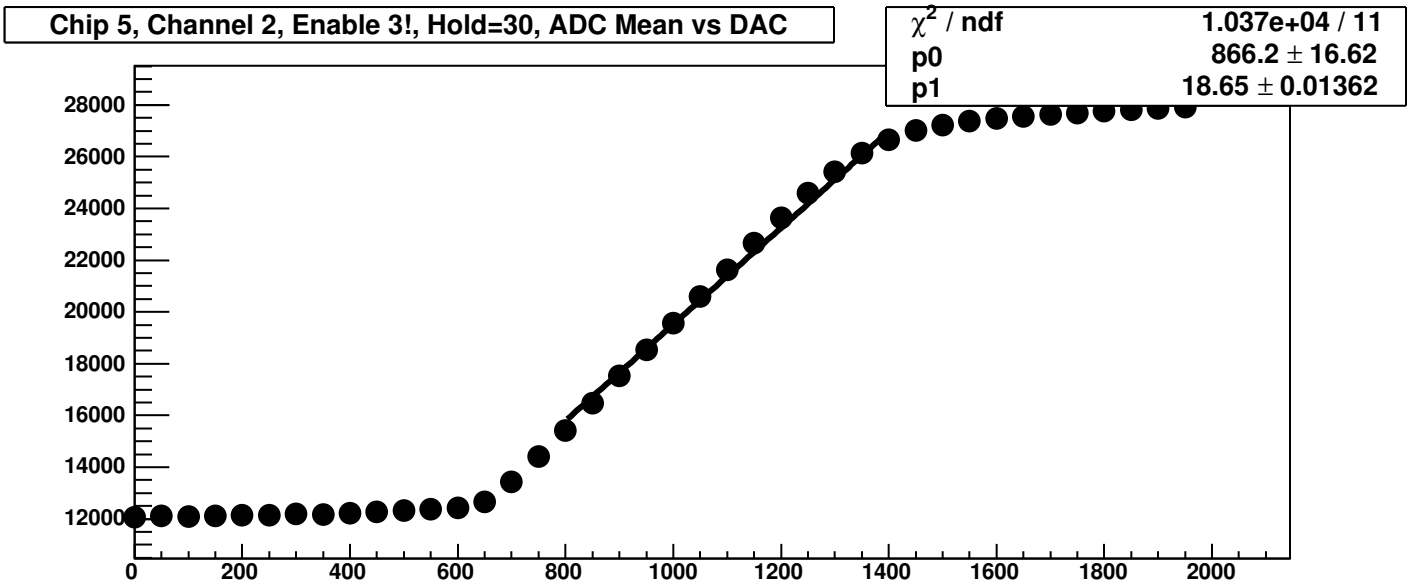
Chip 5, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC

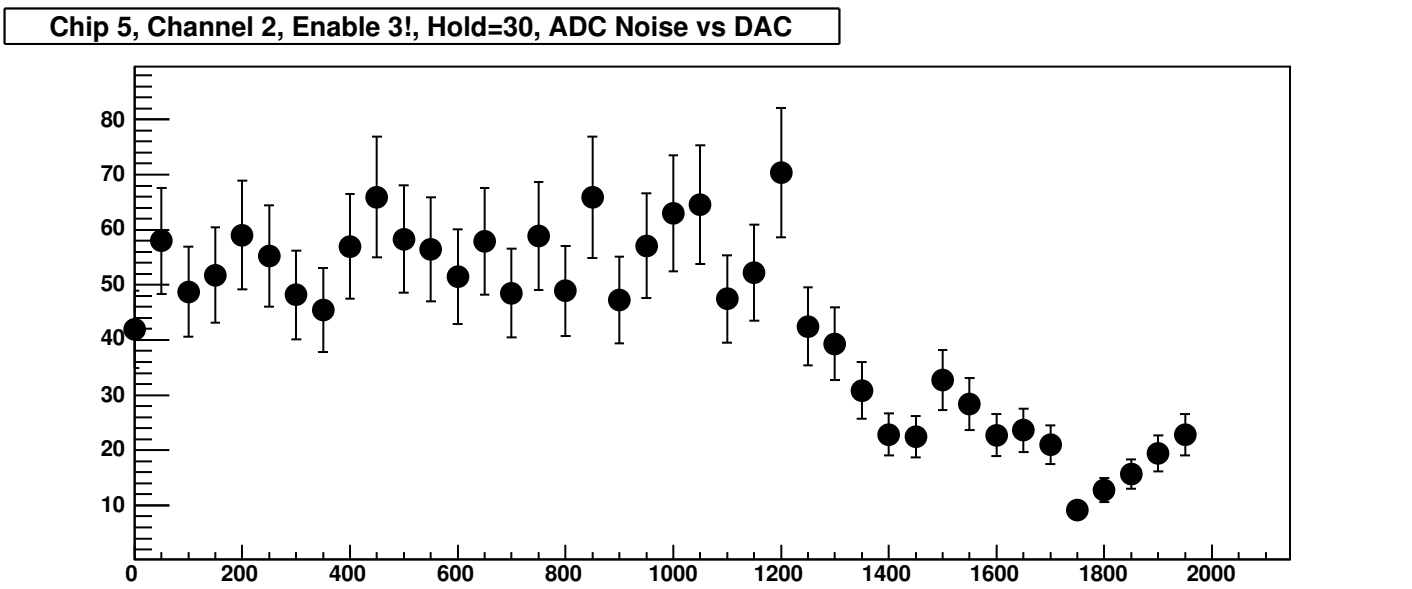


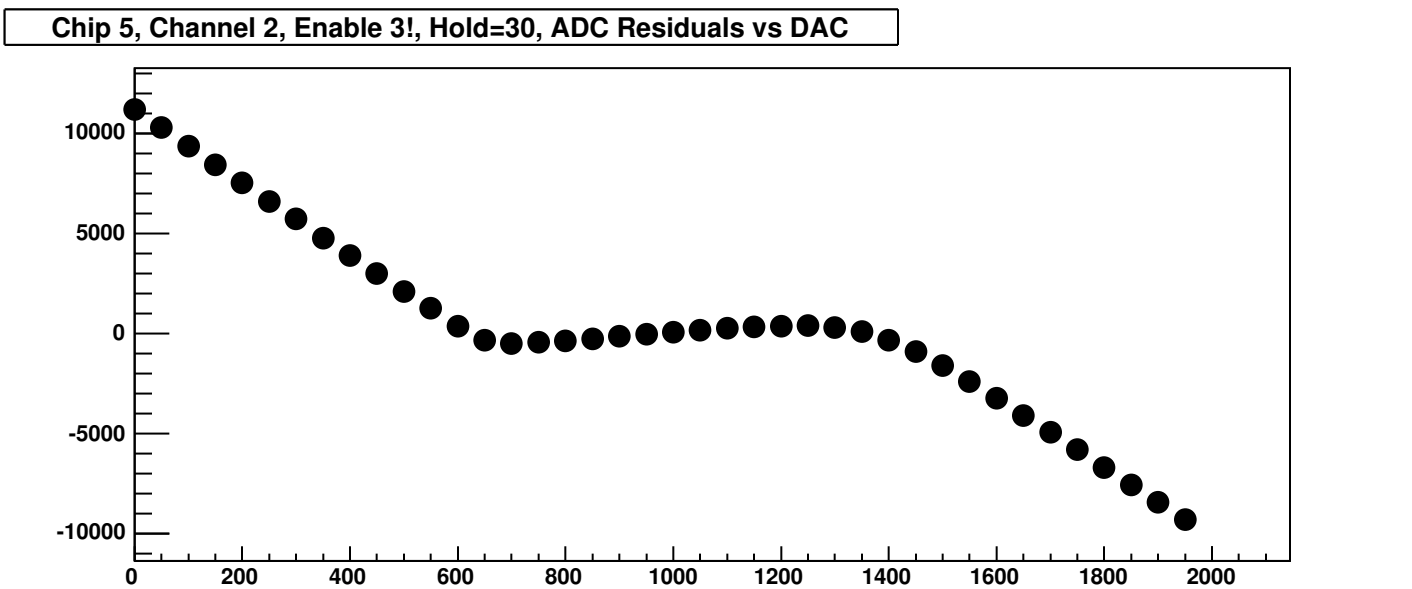

Chip 5, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 10.59 / 11
p0 -1165 ± 21.77
p1 0.2326 ± 0.01937
-700
-750
-800
-850
-900
-950
-1000
-1050
0
200
400
600
800
1000
1200
1400
1600
1800
2000

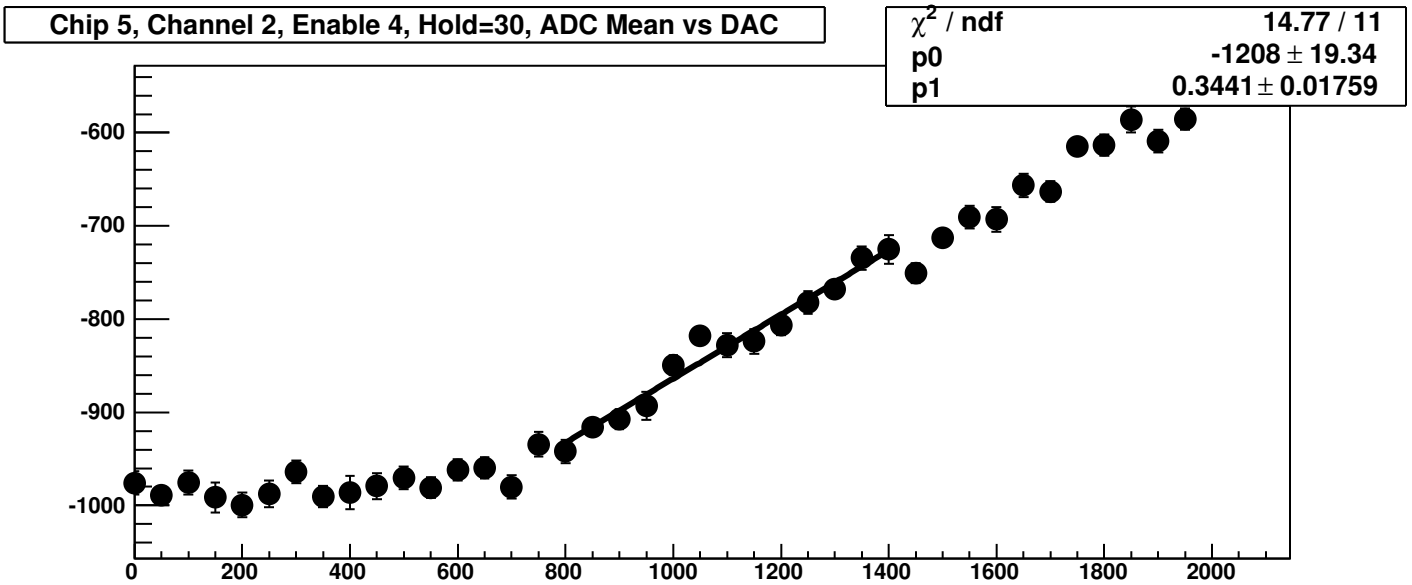

Chip 5, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

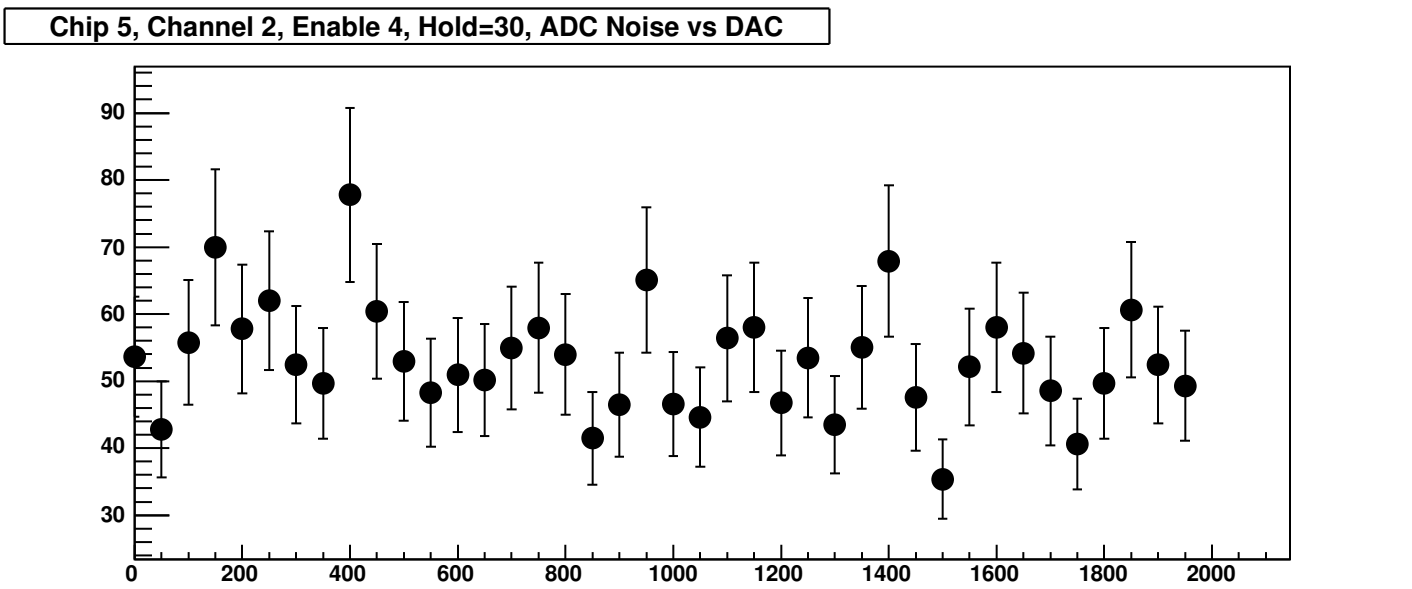

Chip 5, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

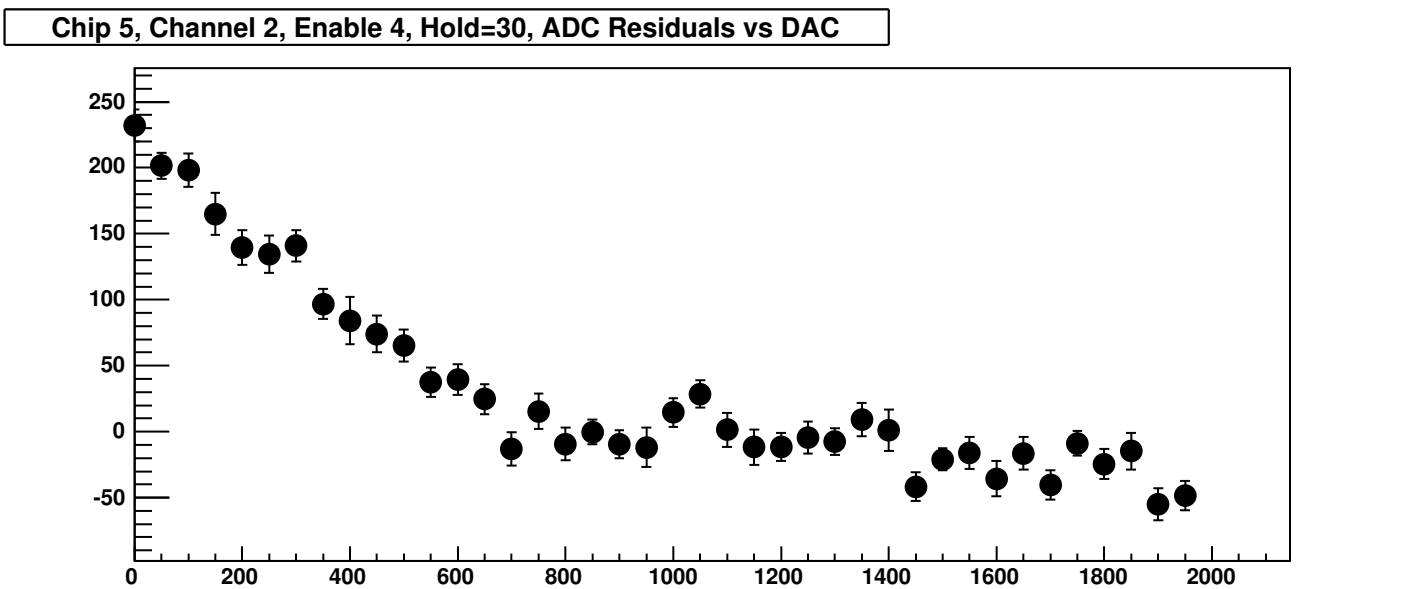

Chip 5, Channel 2, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.037e+04 / 11
p0 866.2 ± 16.62
p1 18.65 ± 0.01362
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

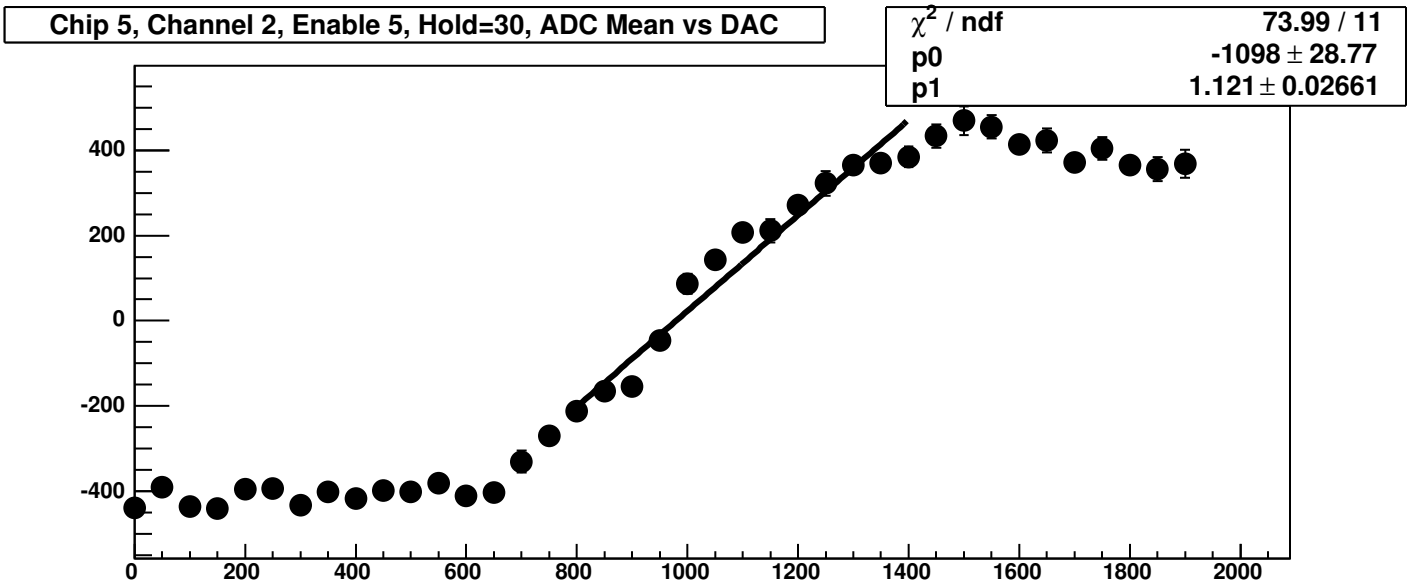

Chip 5, Channel 2, Enable 3!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

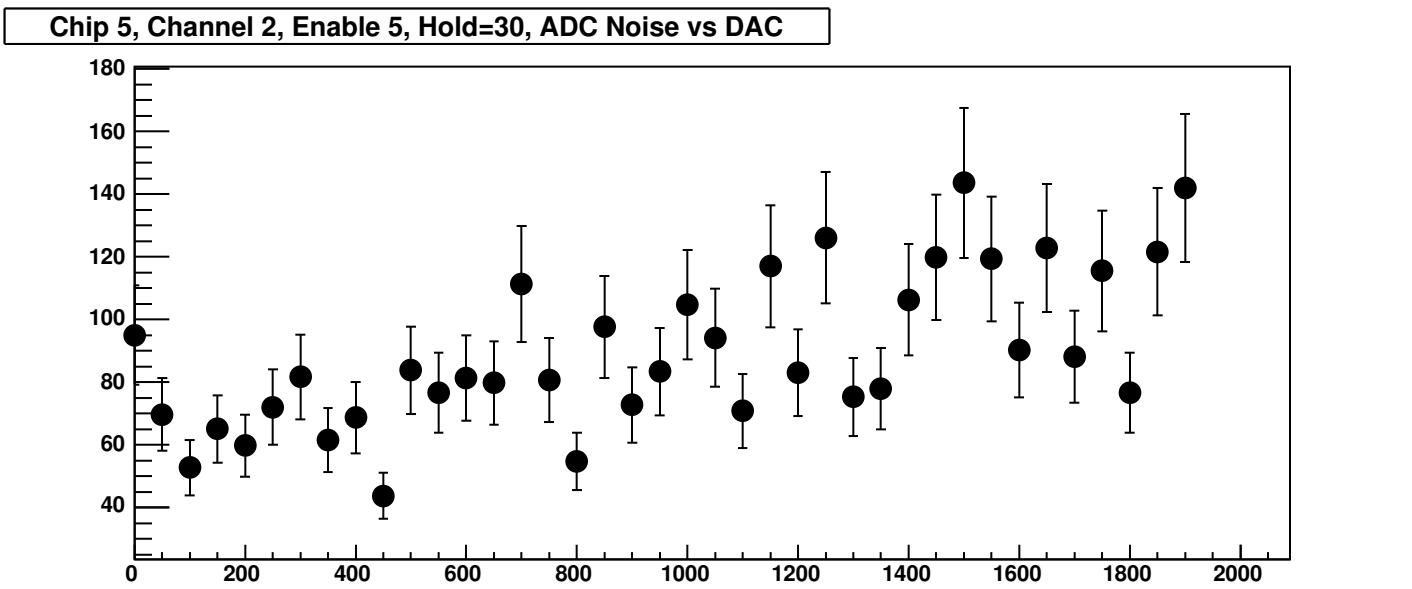

Chip 5, Channel 2, Enable 3!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

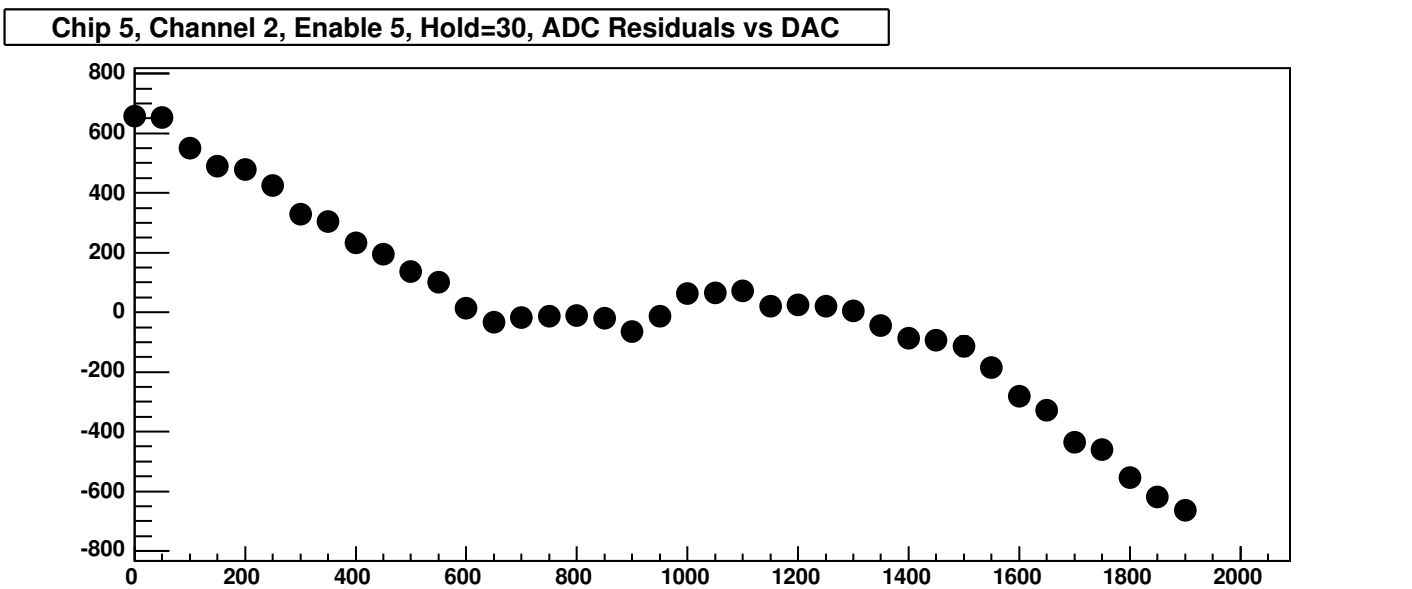

Chip 5, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 14.77 / 11
p0 -1208 ± 19.34
p1 0.3441 ± 0.01759
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

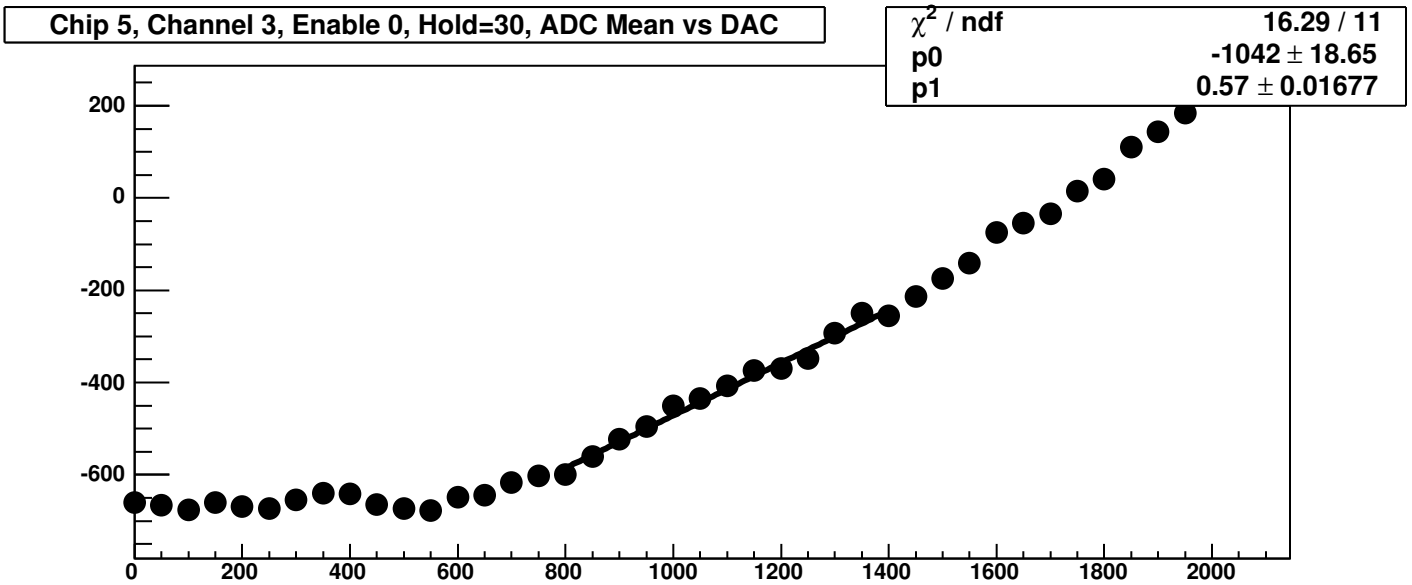

Chip 5, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

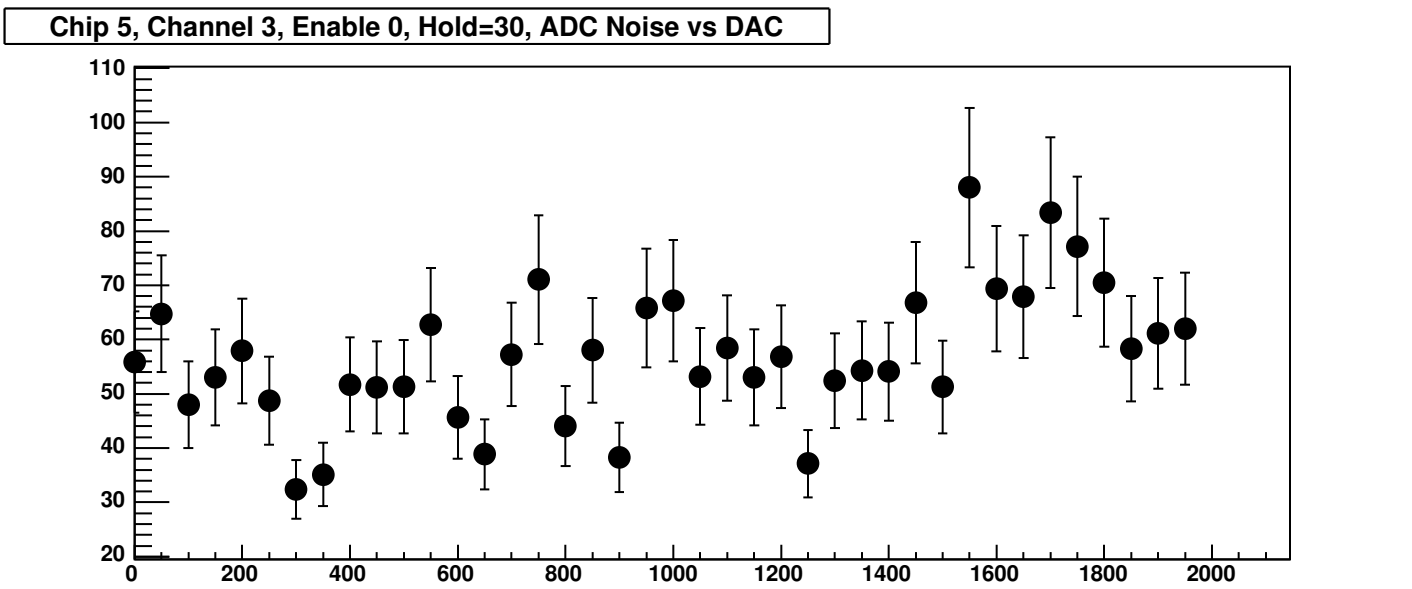

Chip 5, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

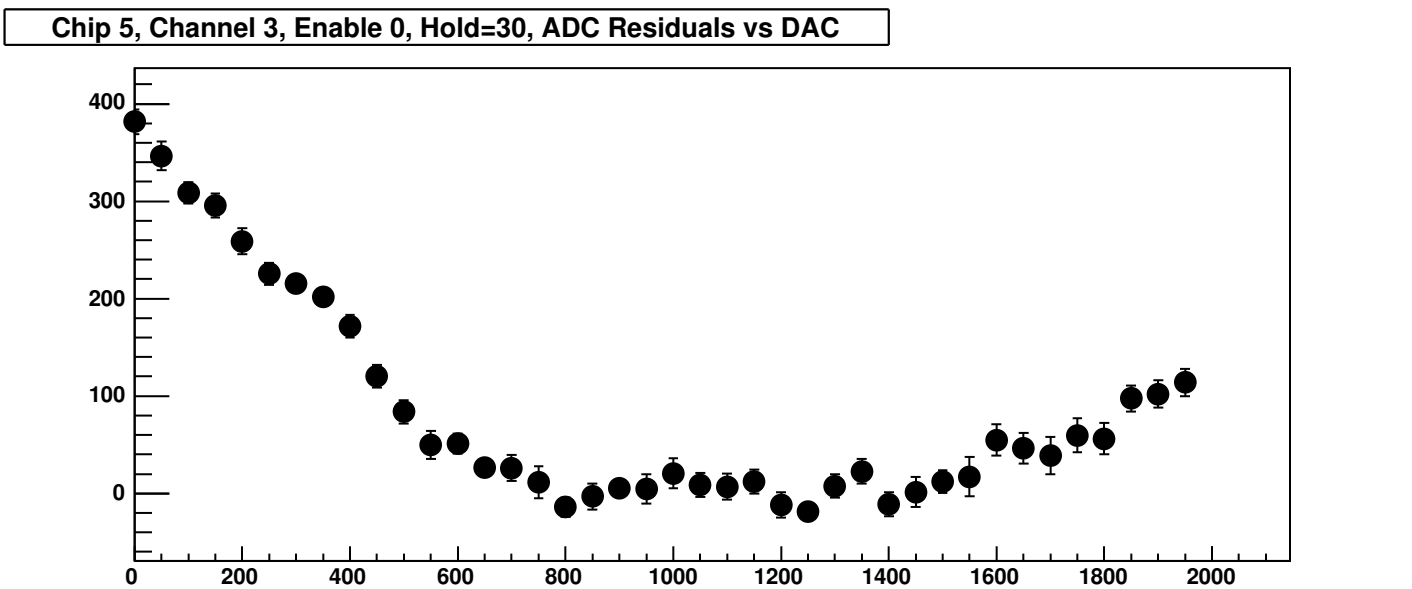

Chip 5, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 73.99 / 11
p0 -1098 ± 28.77
p1 1.121 ± 0.02661
400
200
0
-200
-400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

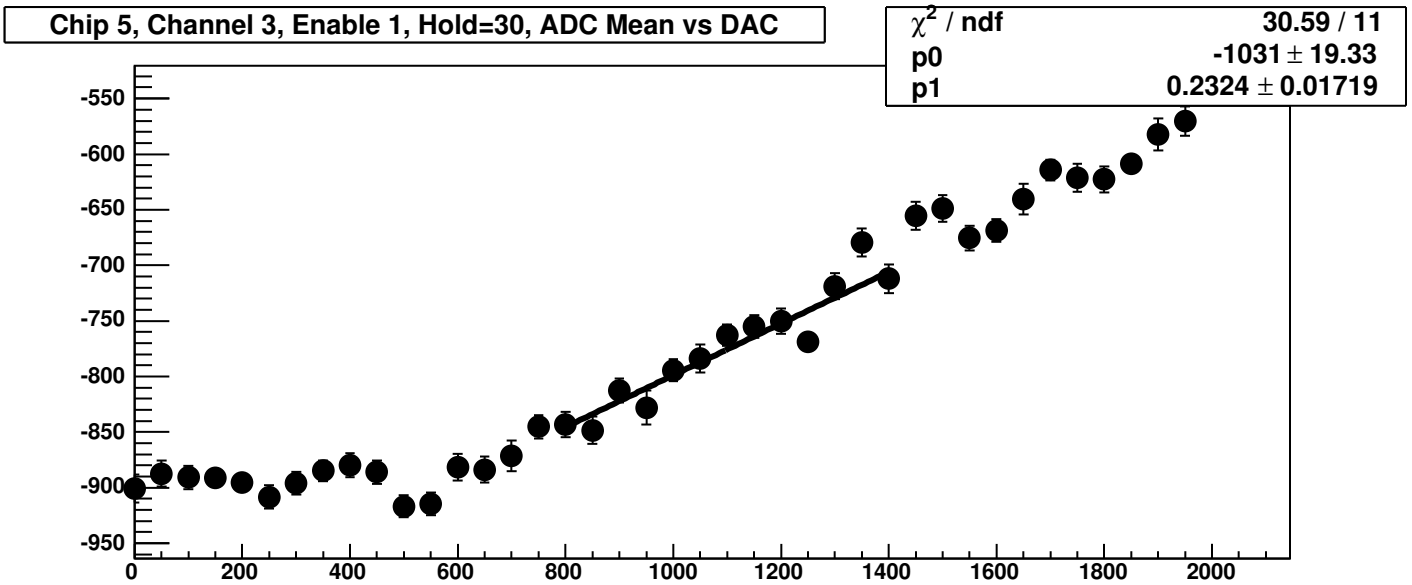

Chip 5, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

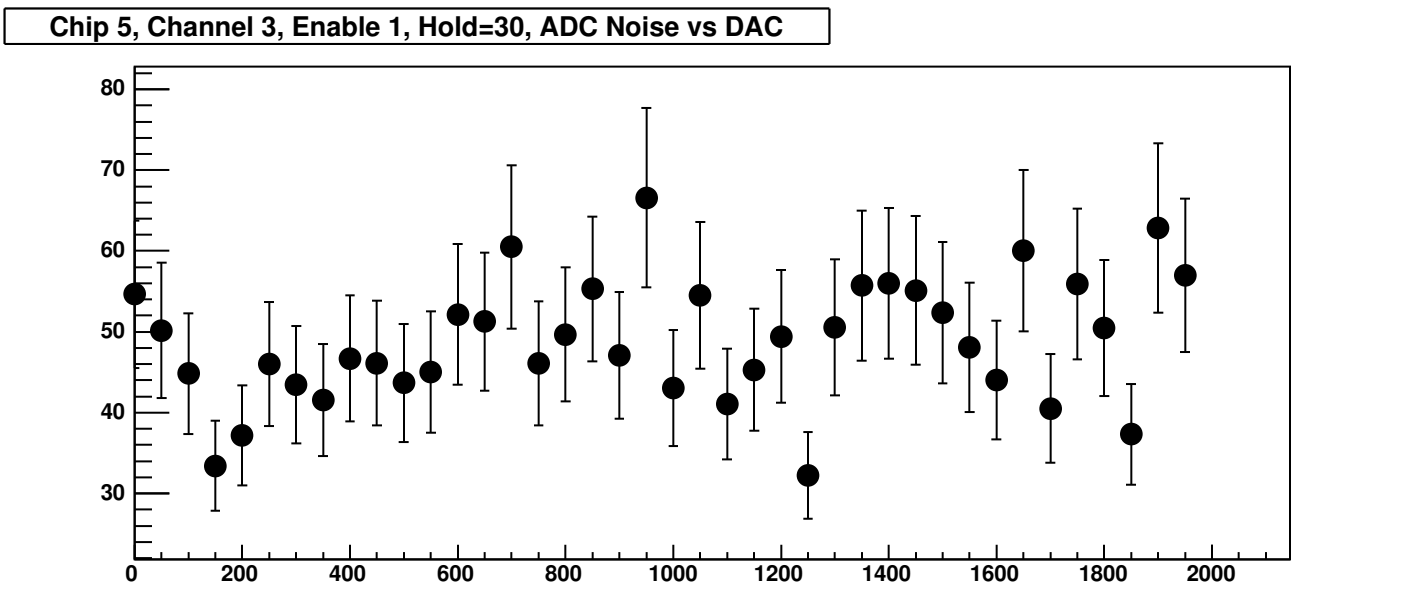

Chip 5, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

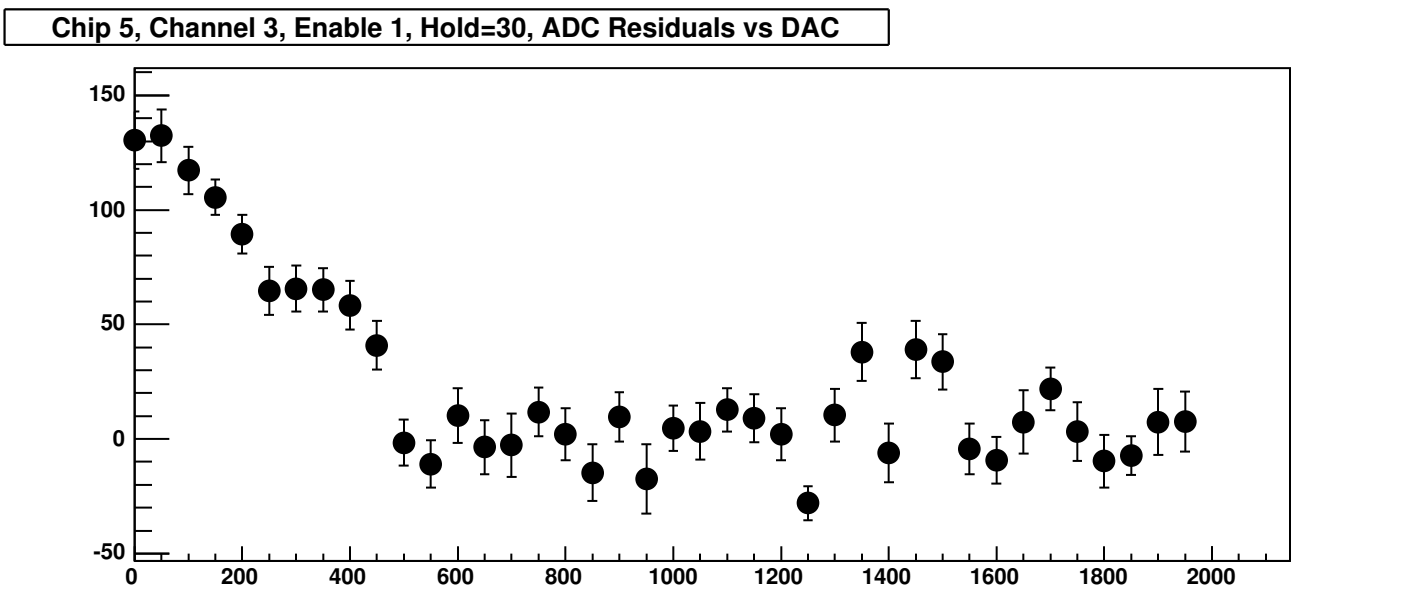

Chip 5, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 16.29 / 11
p0 -1042 ± 18.65
p1 0.57 ± 0.01677
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

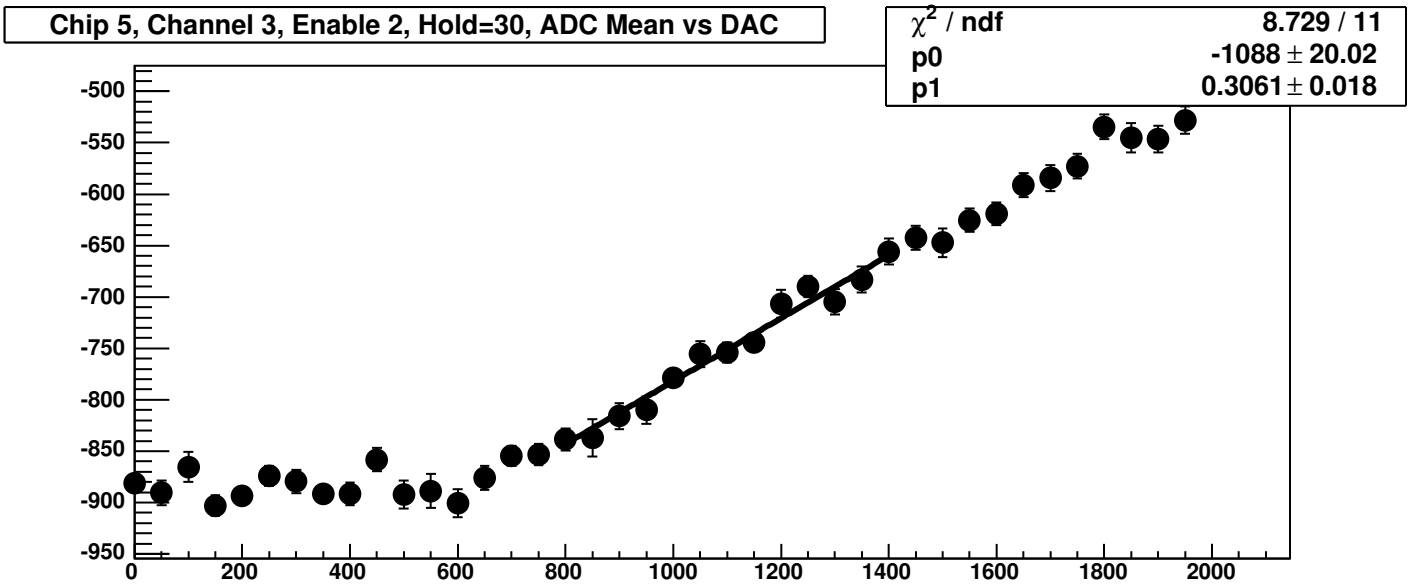

Chip 5, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

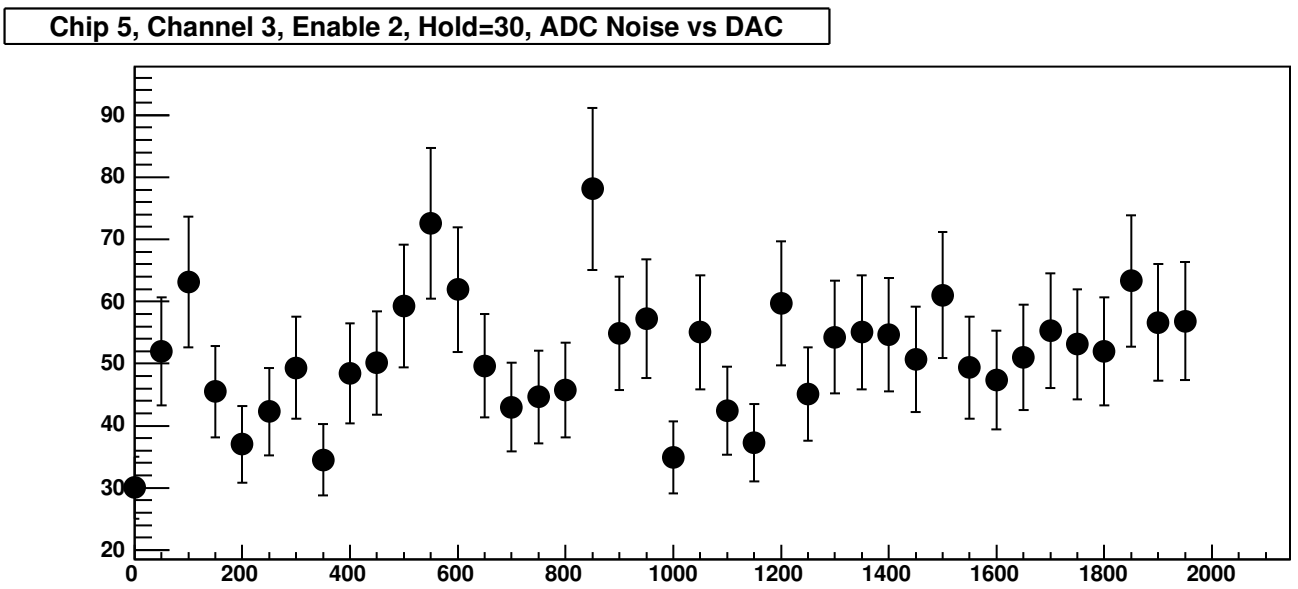

Chip 5, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC
400
300
200
100
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

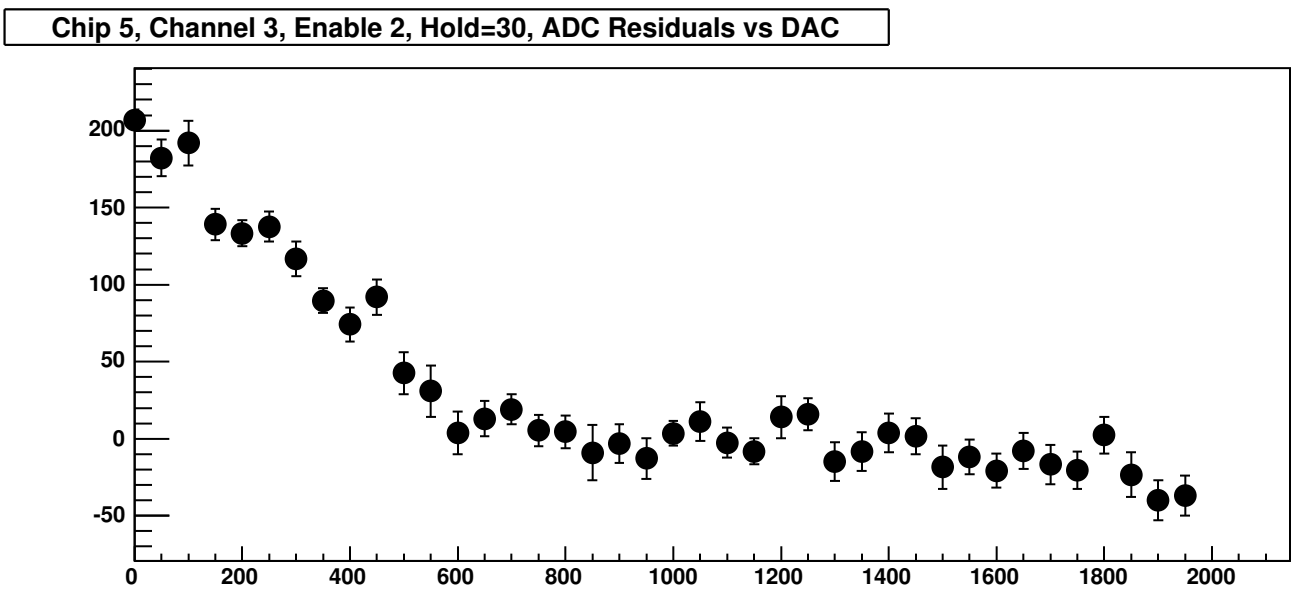

Chip 5, Channel 3, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 30.59 / 11
p0 -1031 ± 19.33
p1 0.2324 ± 0.01719
-550
-600
-650
-700
-750
-800
-850
-900
-950
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 3, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

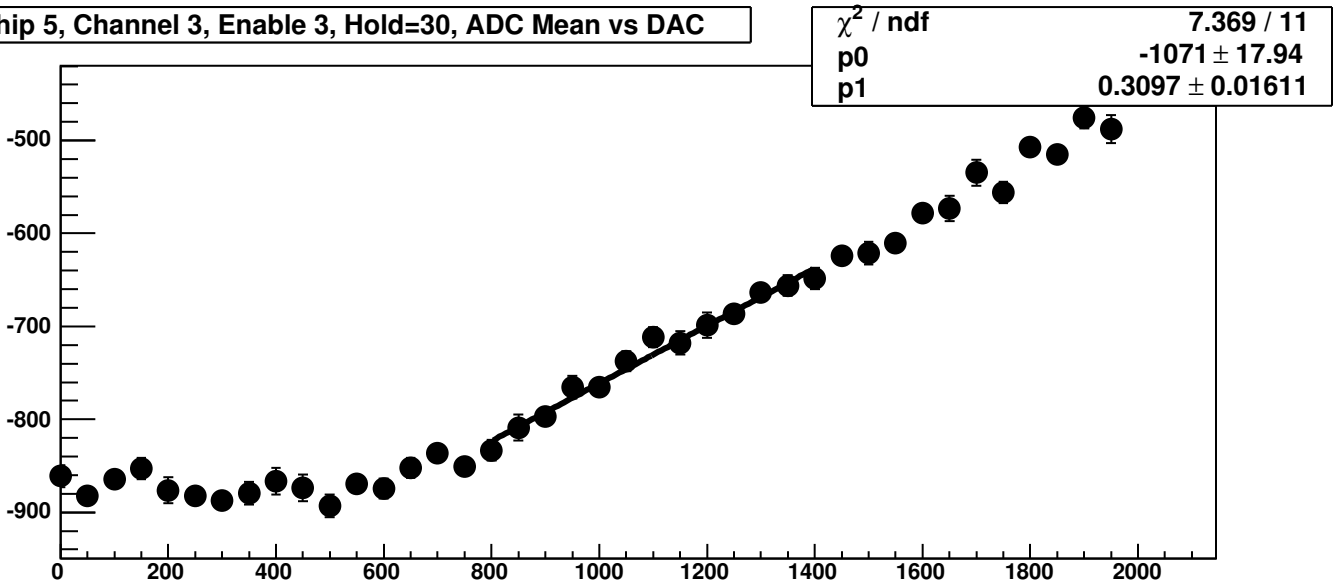

Chip 5, Channel 3, Enable 1, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 8.729 / 11
p0 -1088 ± 20.02
p1 0.3061 ± 0.018
-500
-550
-600
-650
-700
-750
-800
-850
-900
-950
0
200
400
600
800
1000
1200
1400
1600
1800
2000

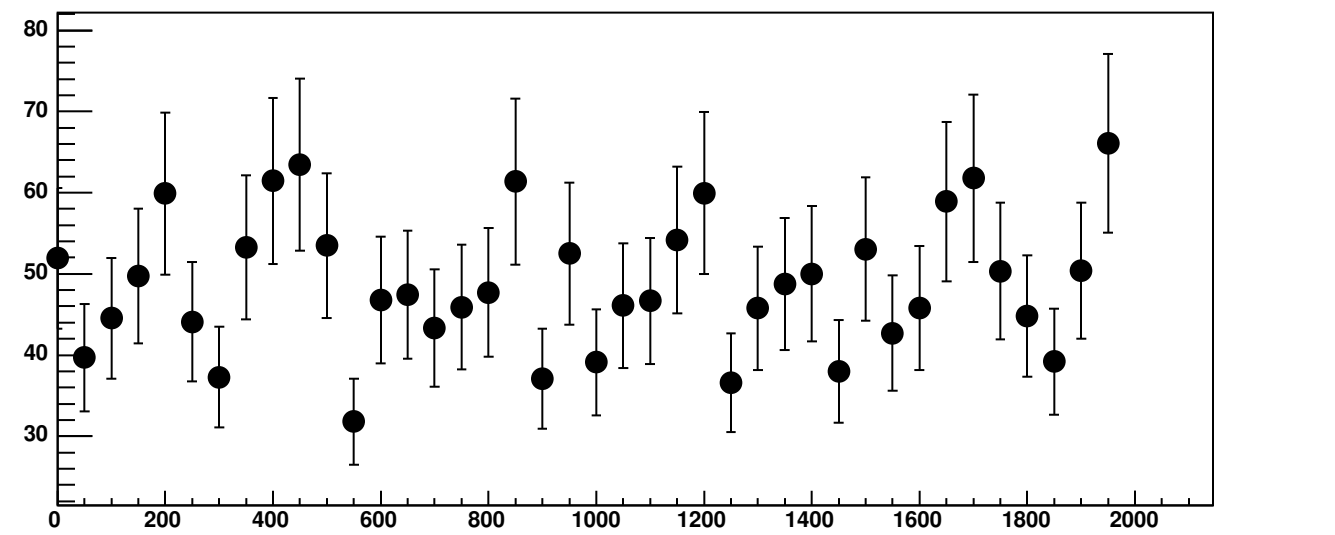

Chip 5, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

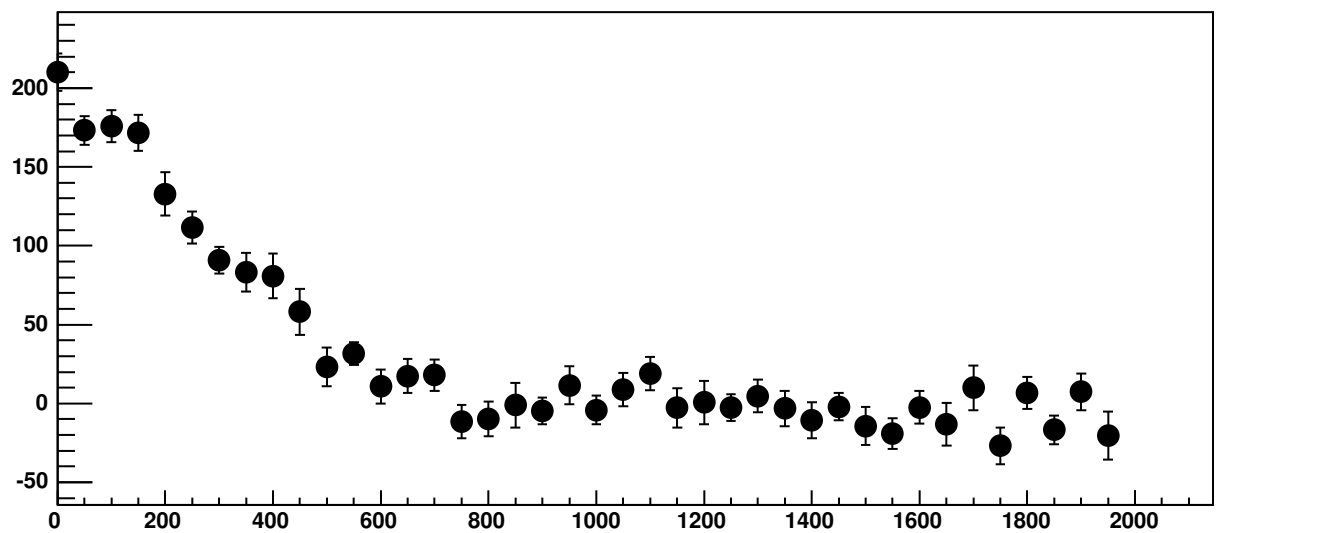
Chip 5, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC

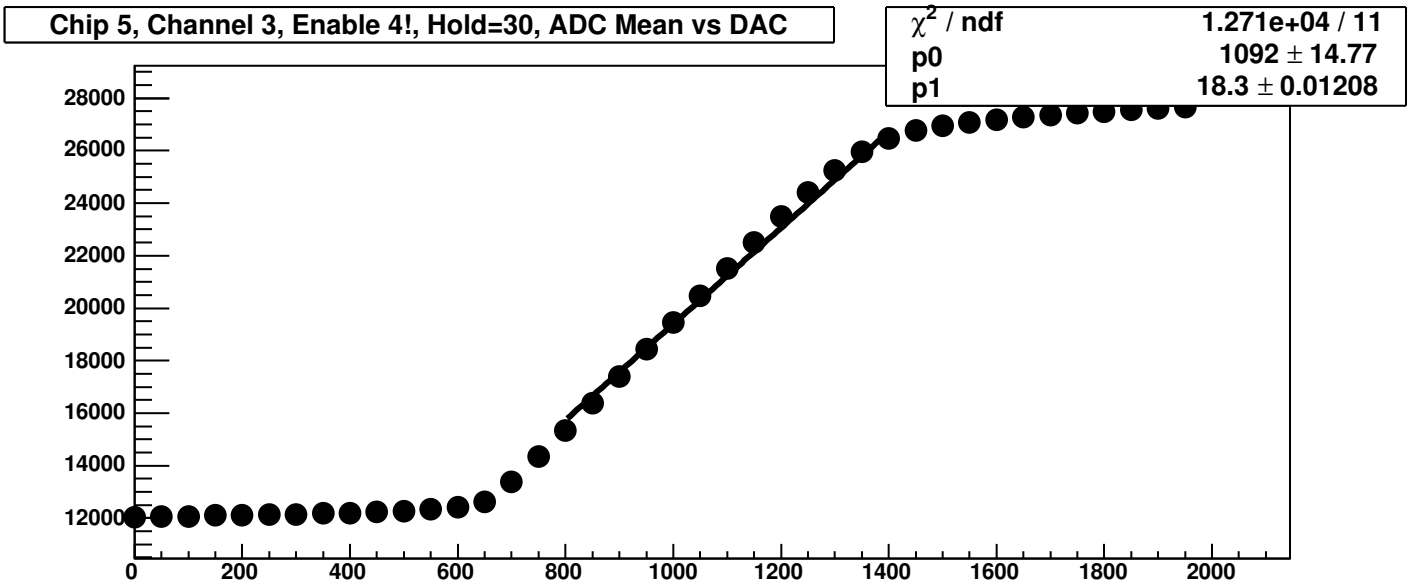


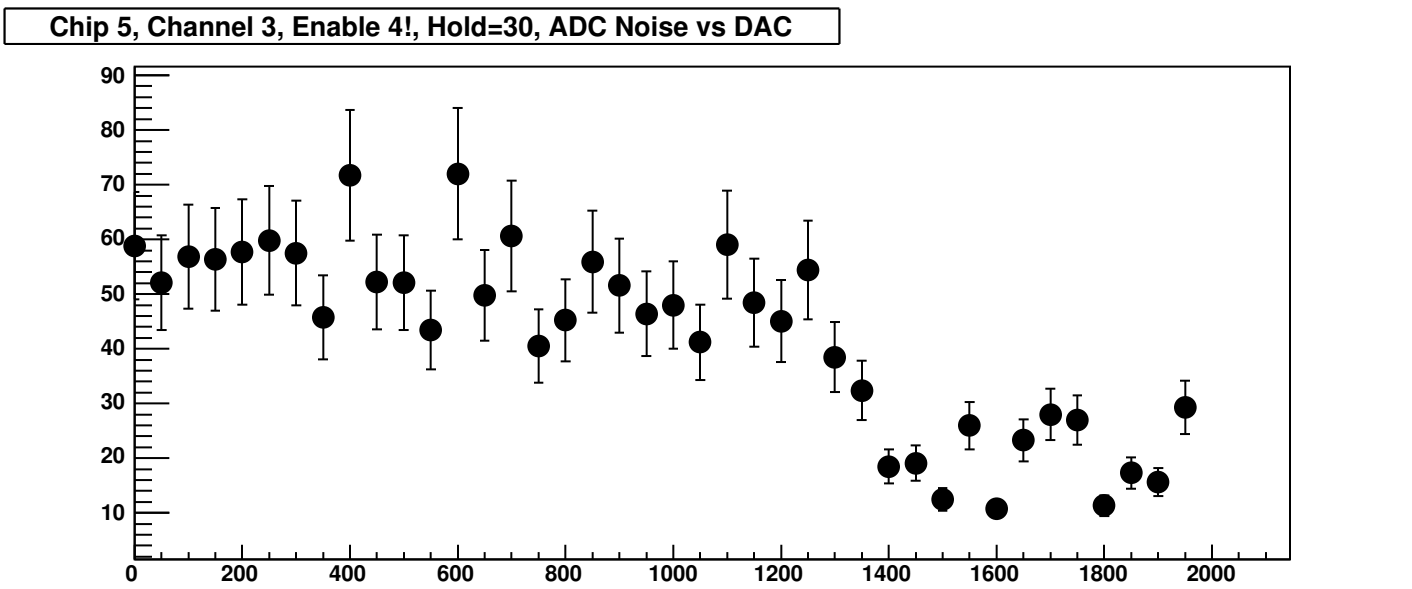
Chip 5, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC

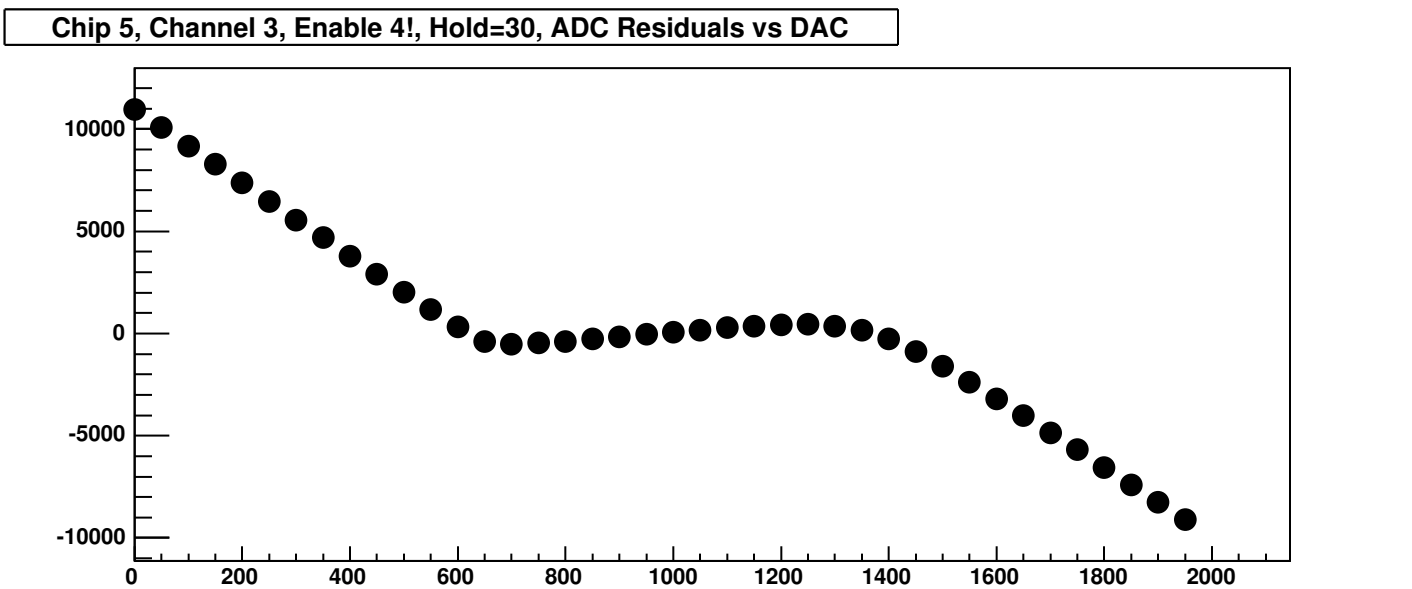


Chip 5, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC

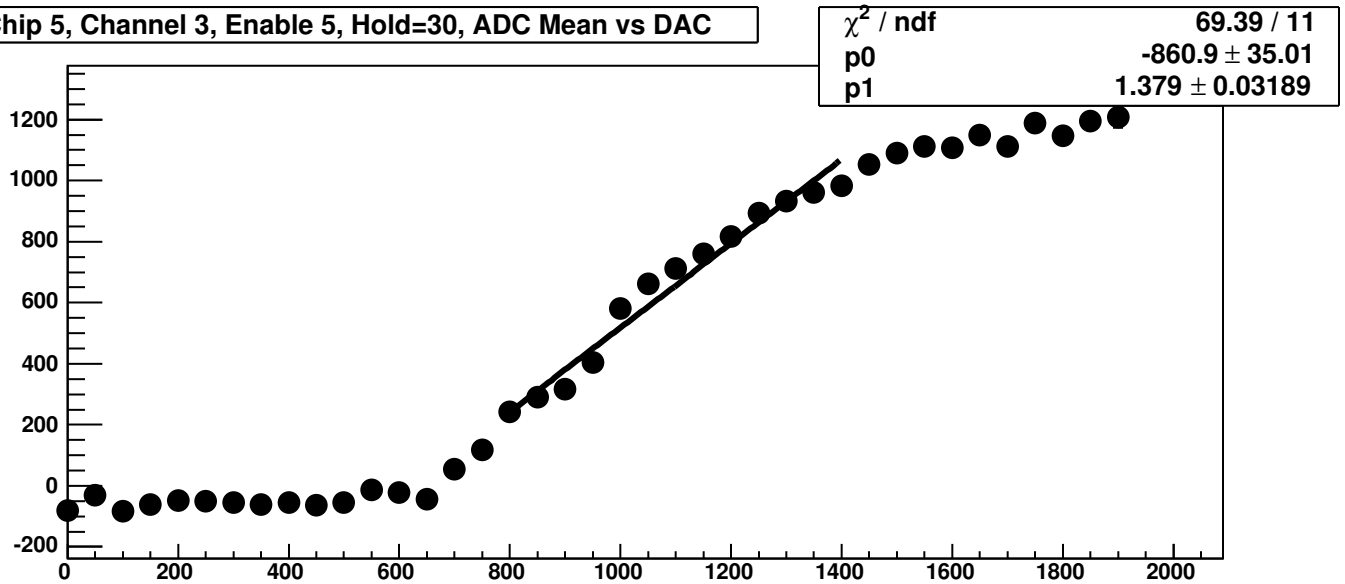



Chip 5, Channel 3, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.271e+04 / 11
p0 1092 ± 14.77
p1 18.3 ± 0.01208
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

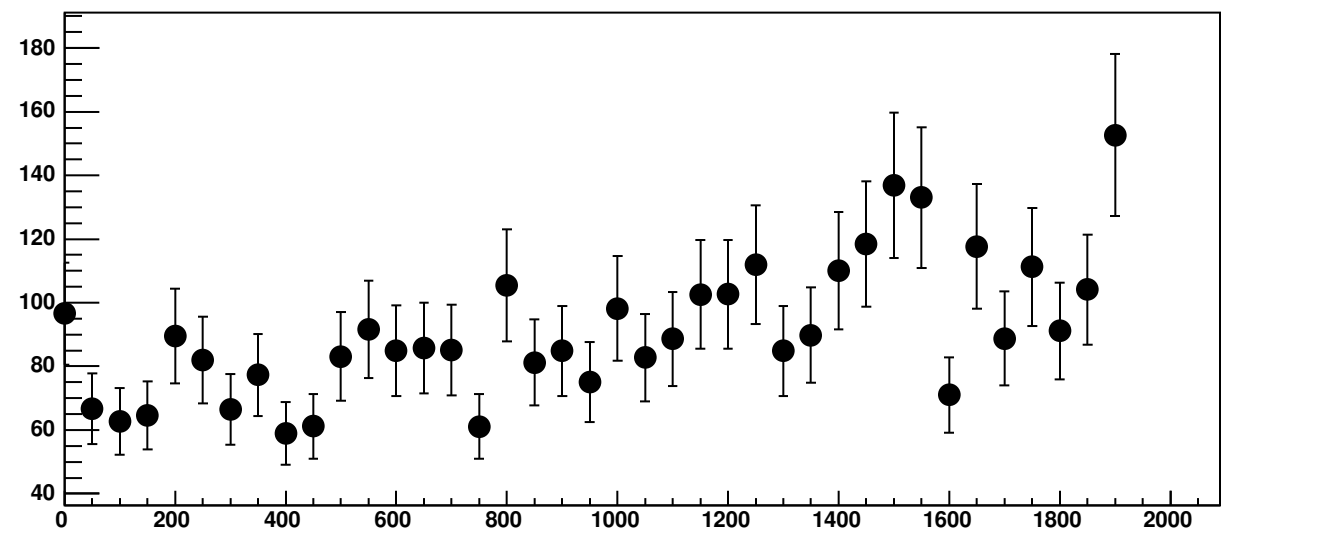

Chip 5, Channel 3, Enable 4!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 3, Enable 4!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

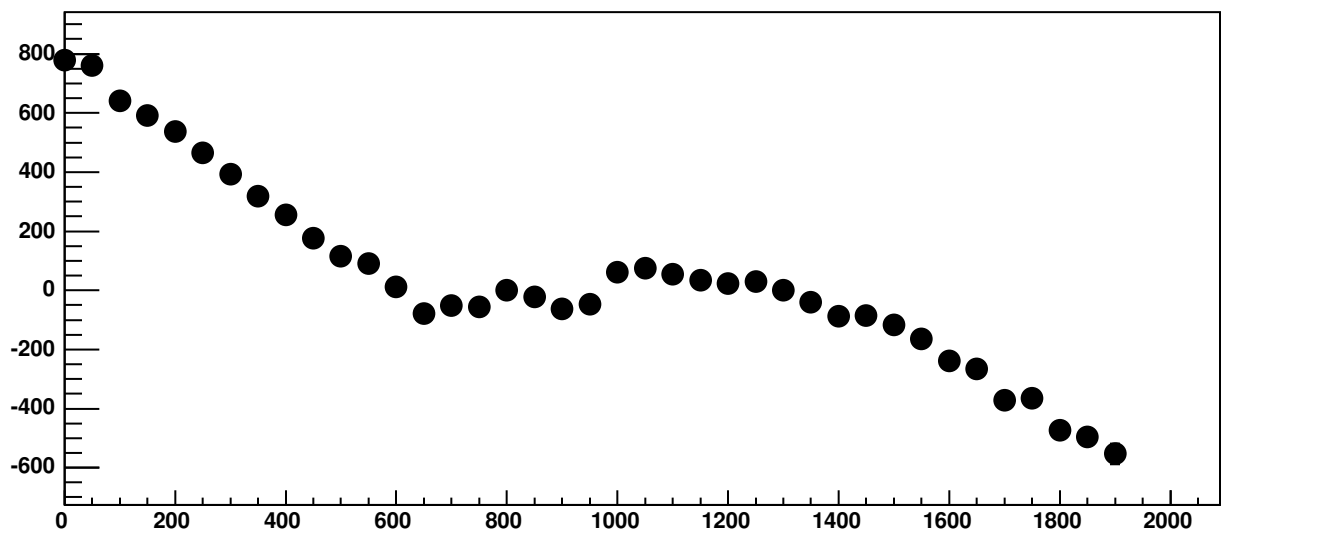
Chip 5, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC



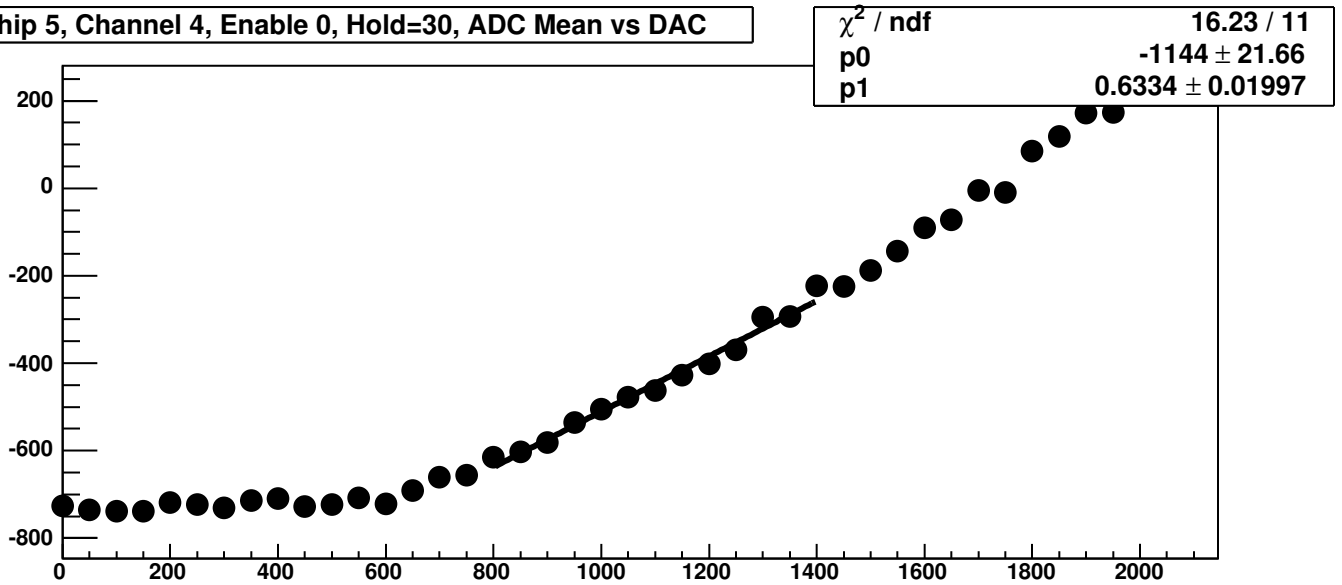
Chip 5, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC



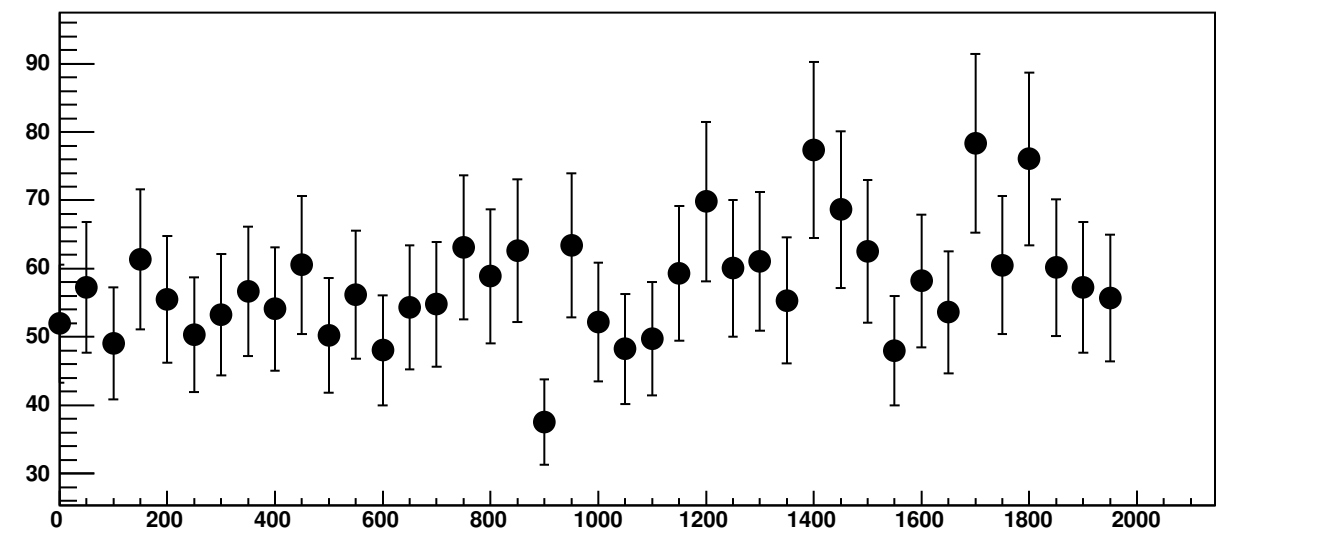
Chip 5, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC



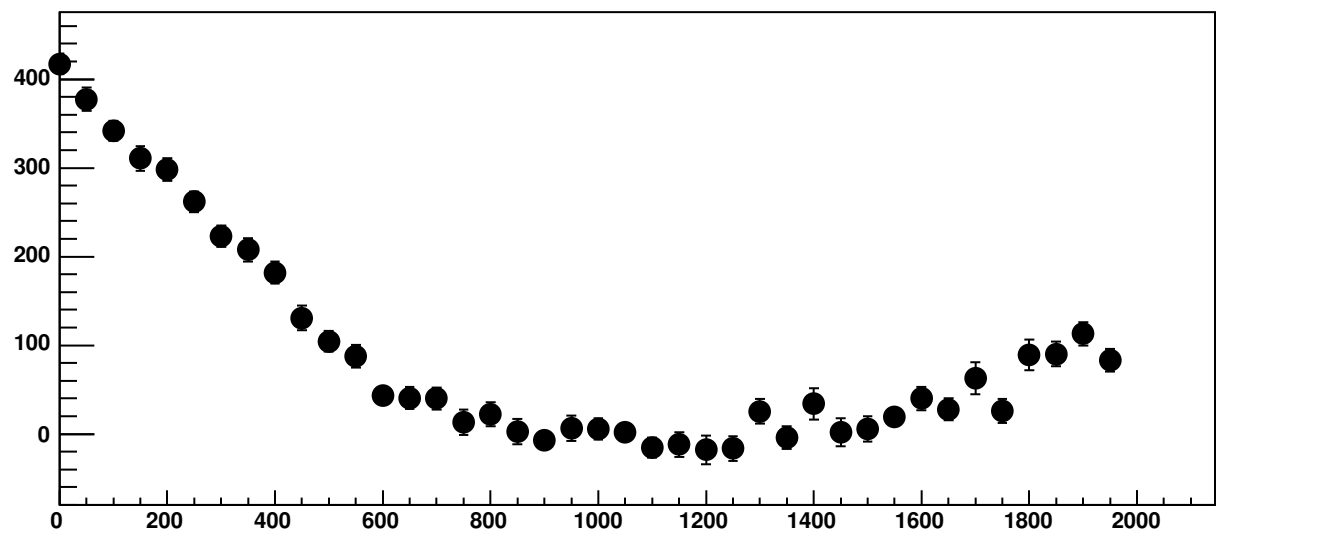
Chip 5, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC

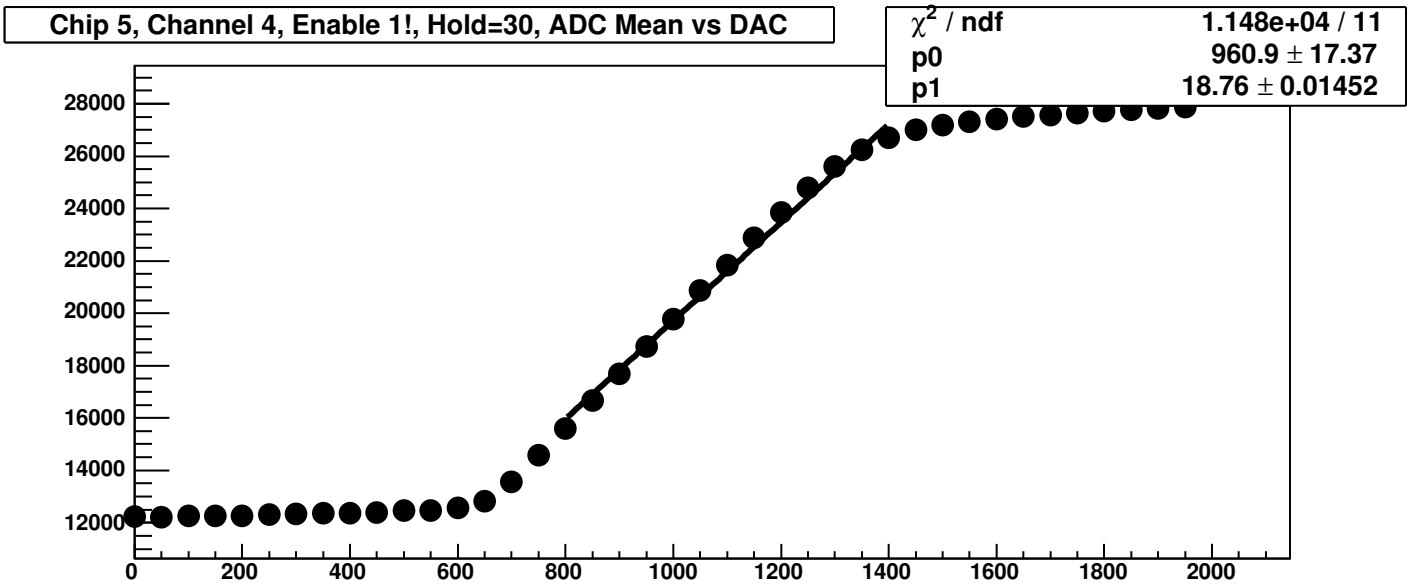


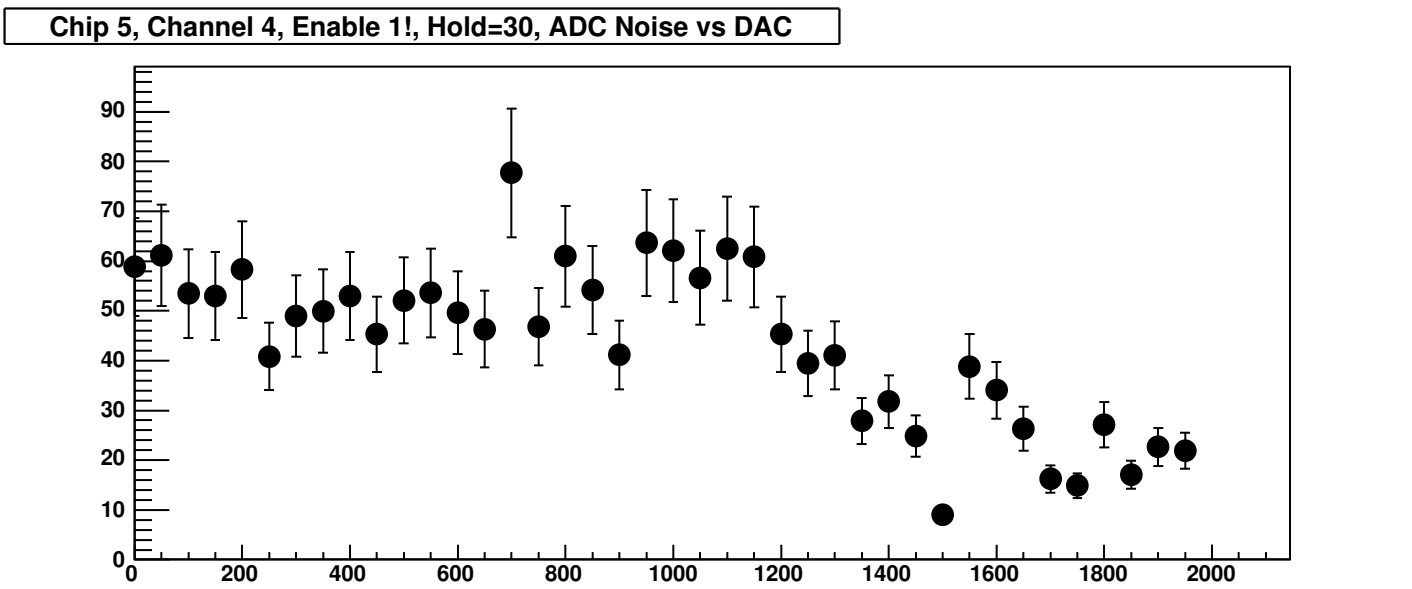
Chip 5, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC

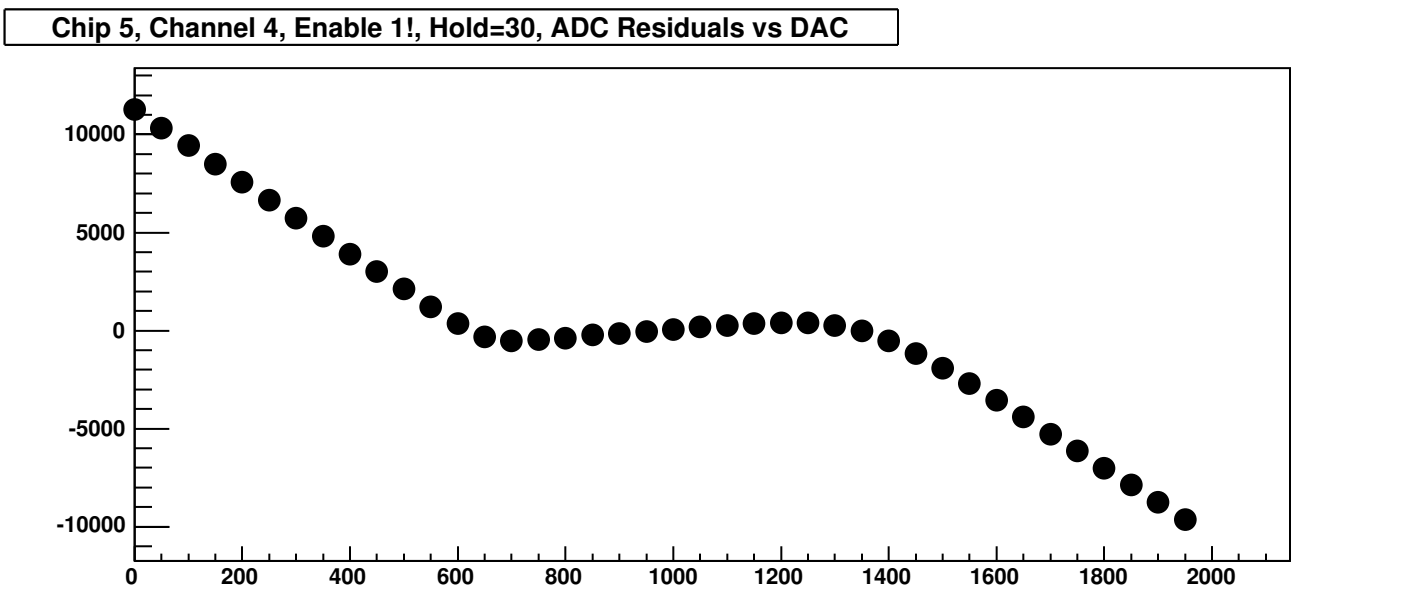


Chip 5, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC

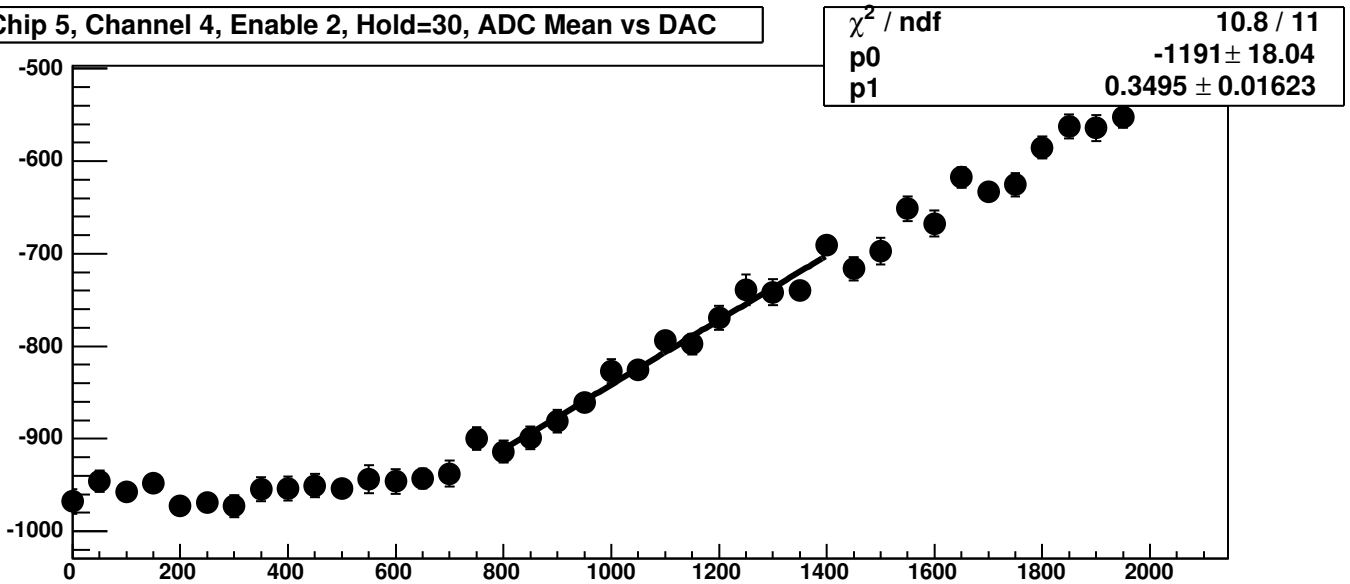



Chip 5, Channel 4, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.148e+04 / 11
p0 960.9 ± 17.37
p1 18.76 ± 0.01452
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

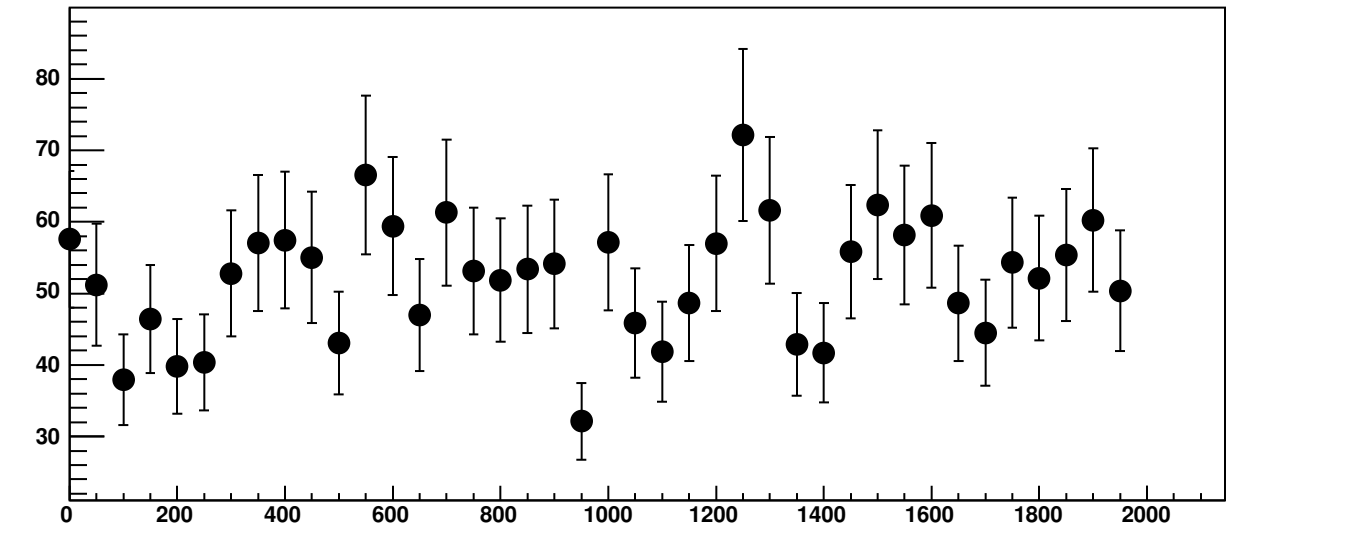

Chip 5, Channel 4, Enable 1!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 4, Enable 1!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

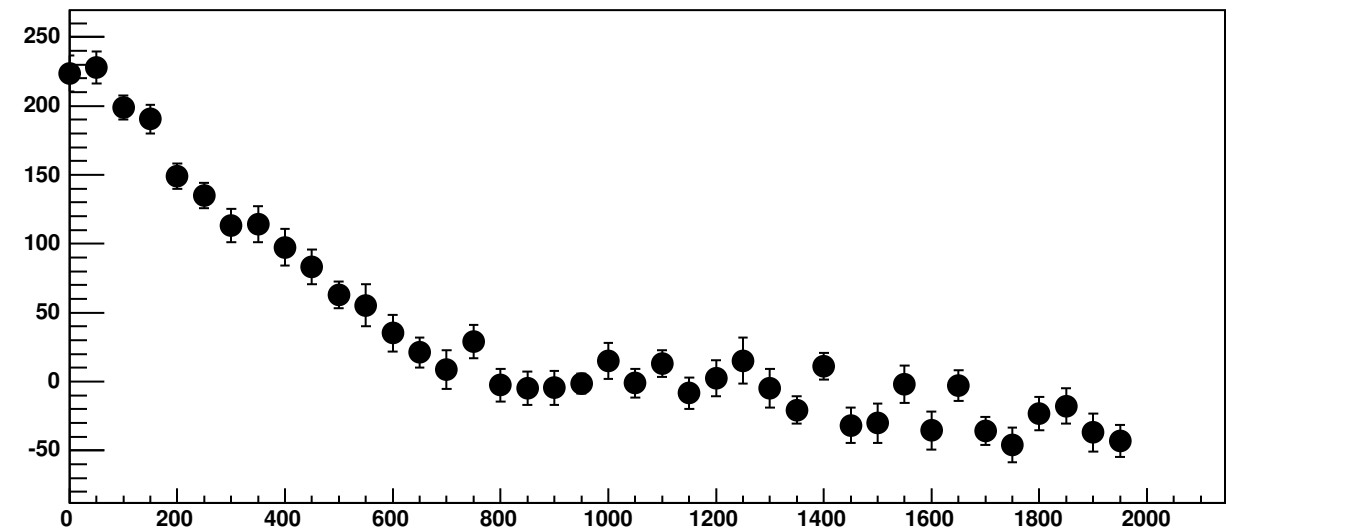
Chip 5, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC



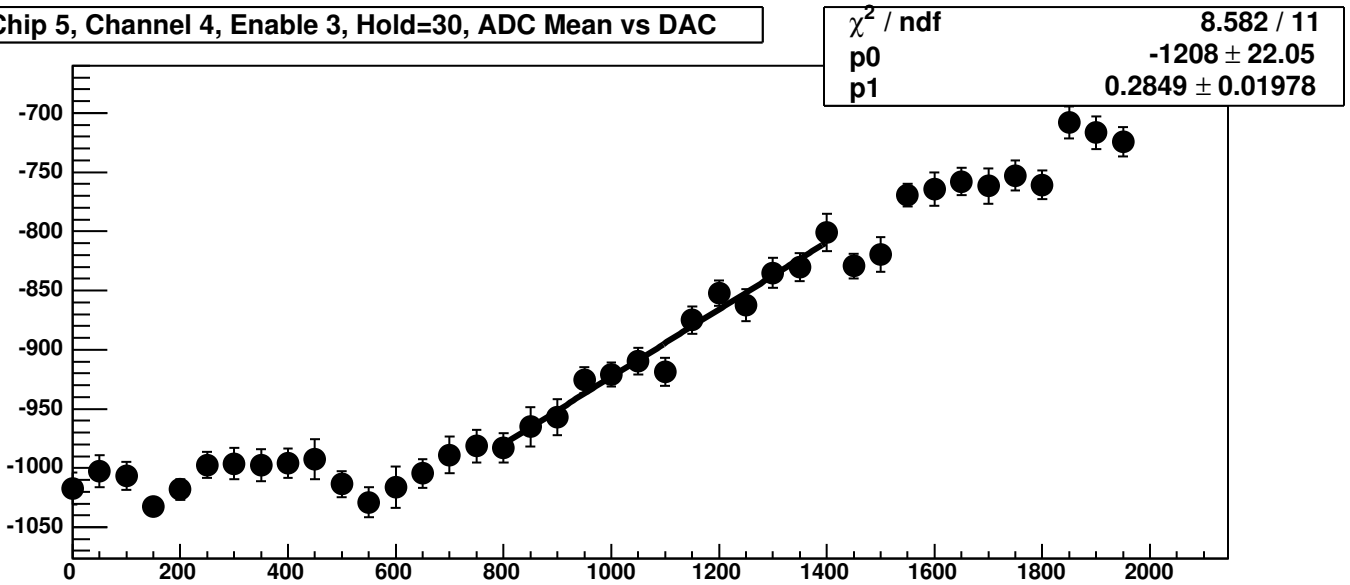
Chip 5, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC



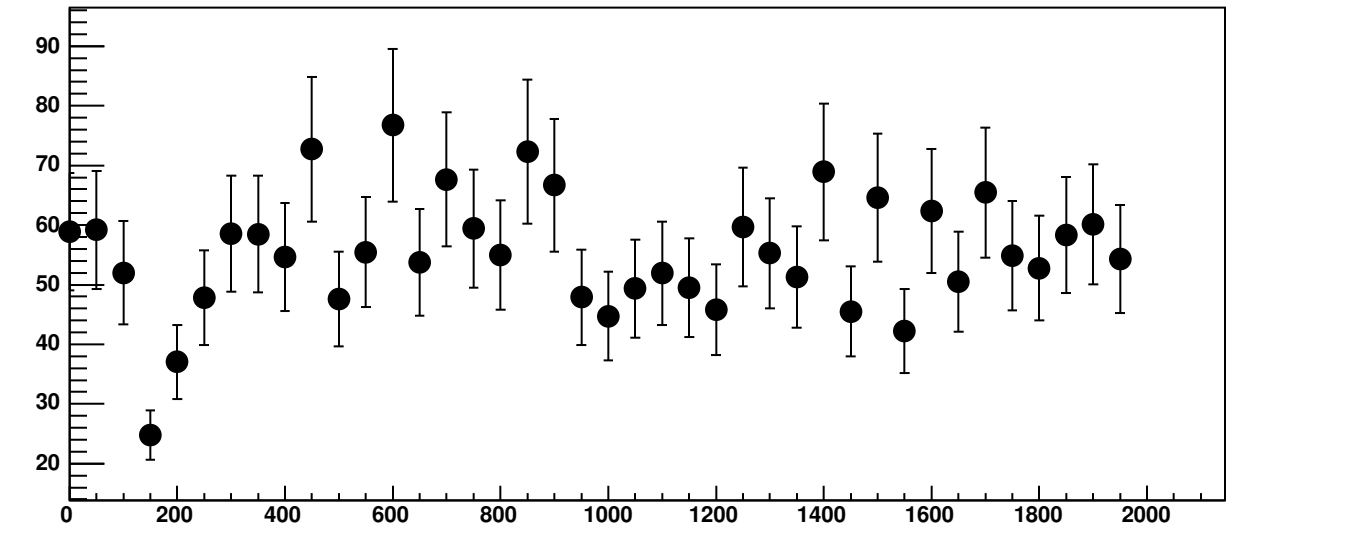
Chip 5, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC



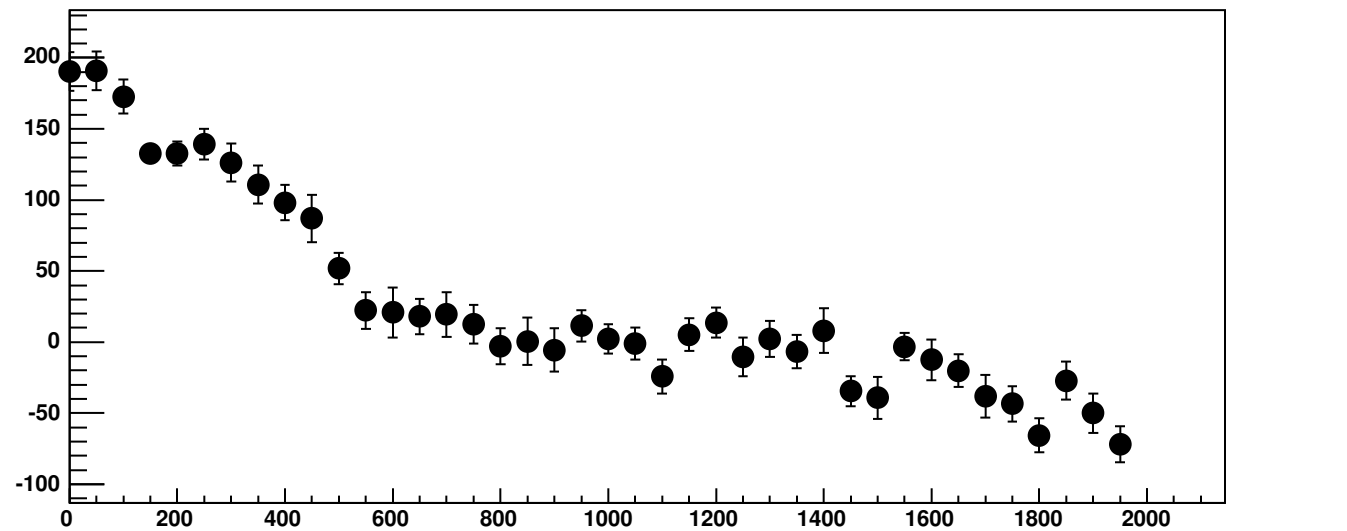
Chip 5, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC

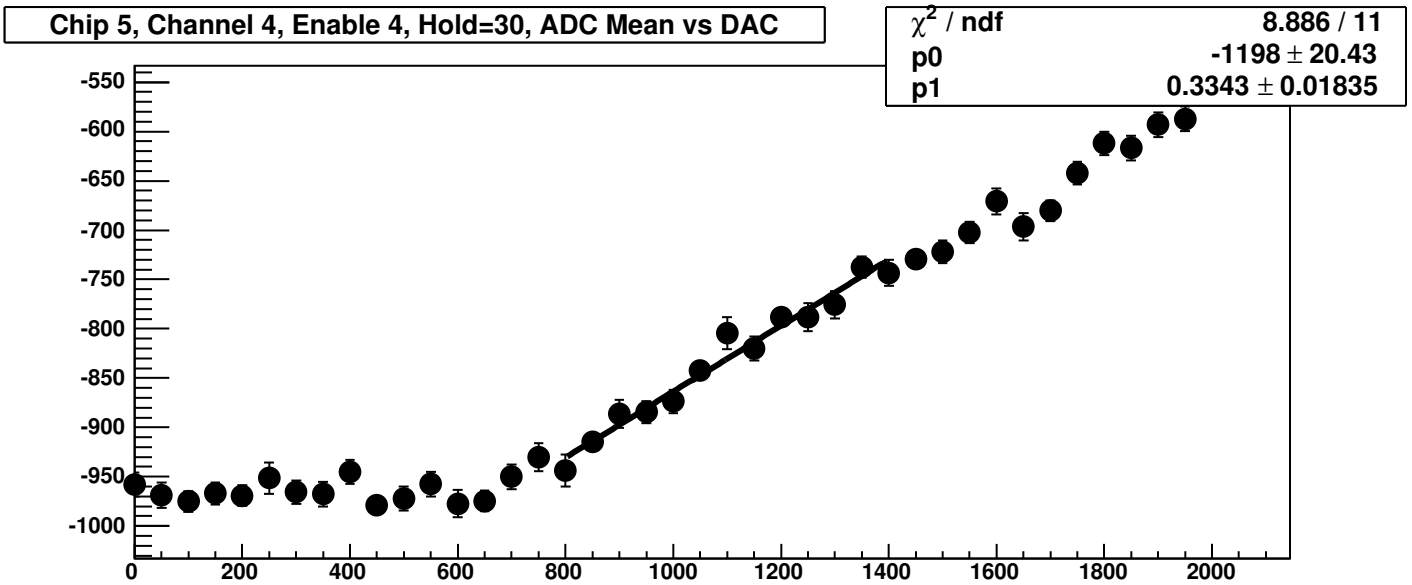


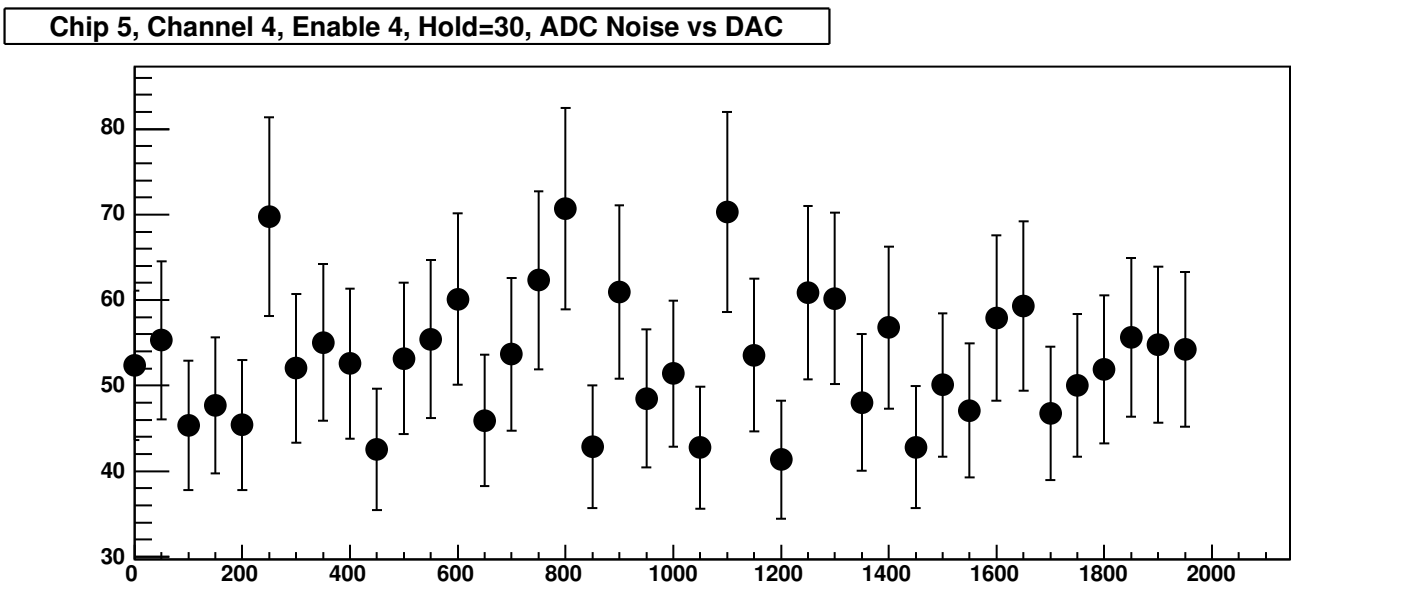
Chip 5, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC

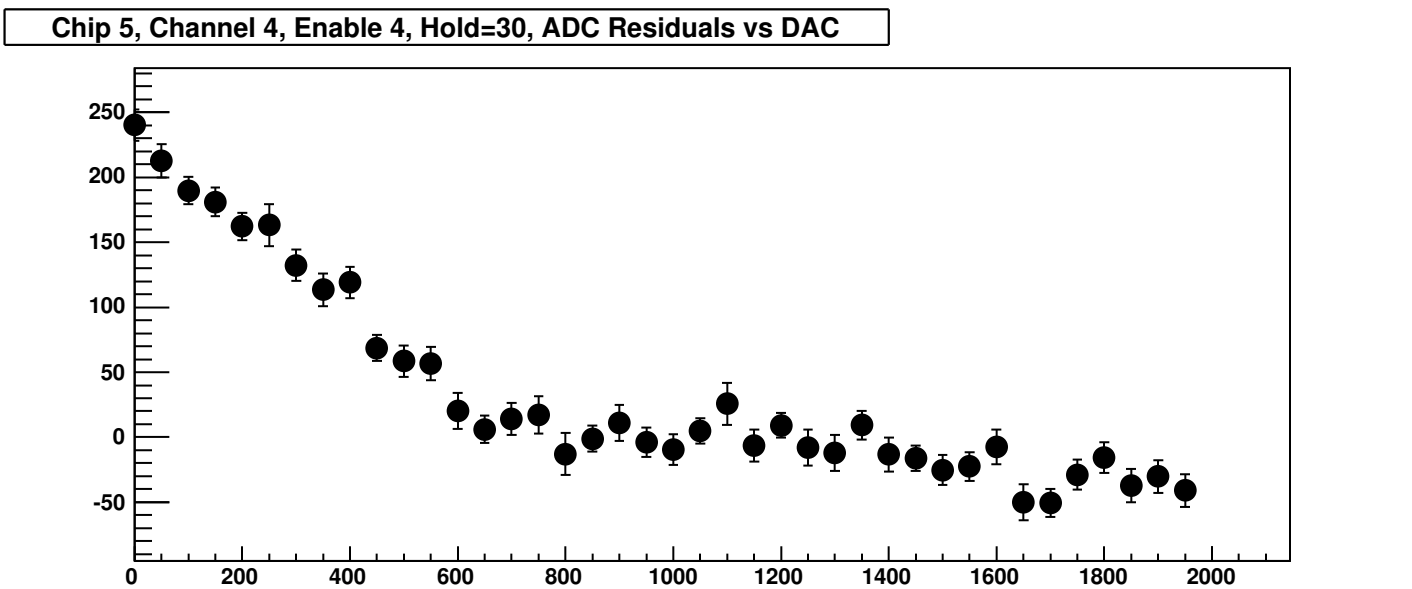


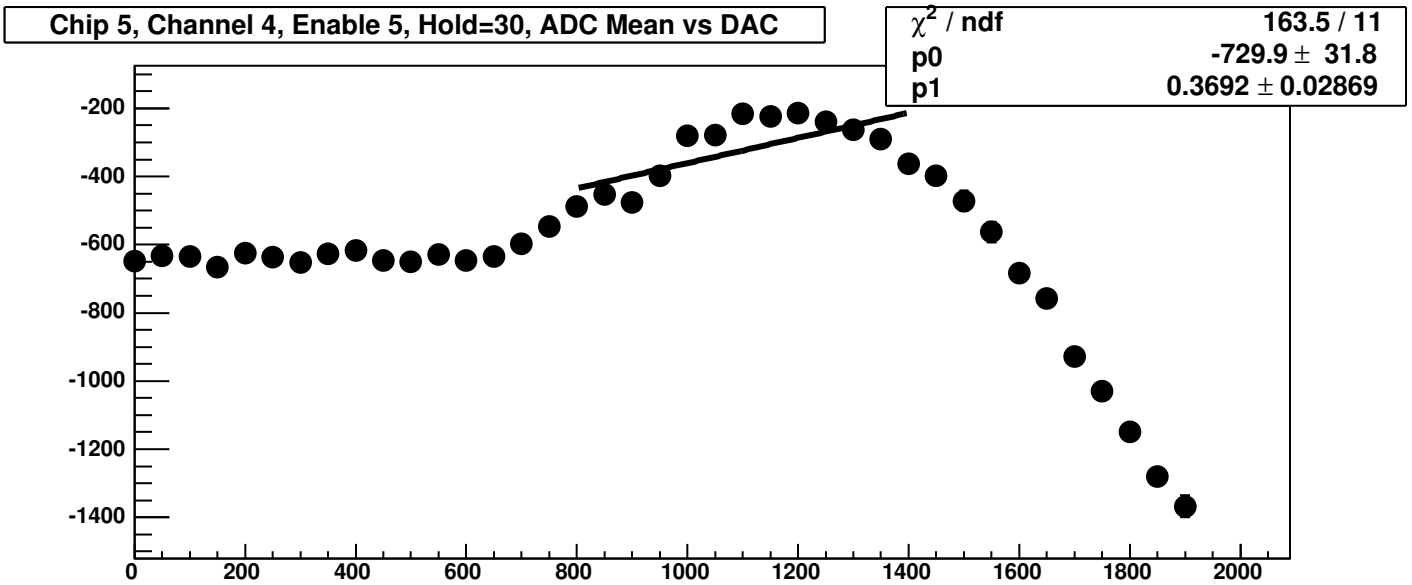
Chip 5, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC

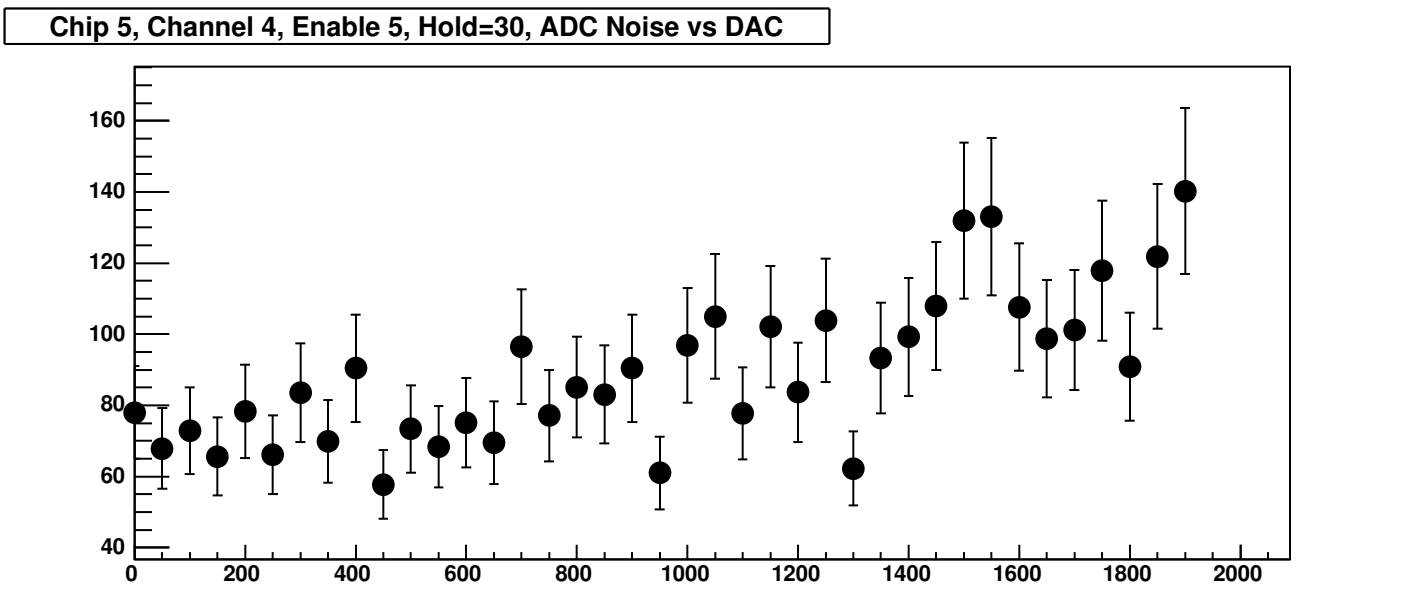


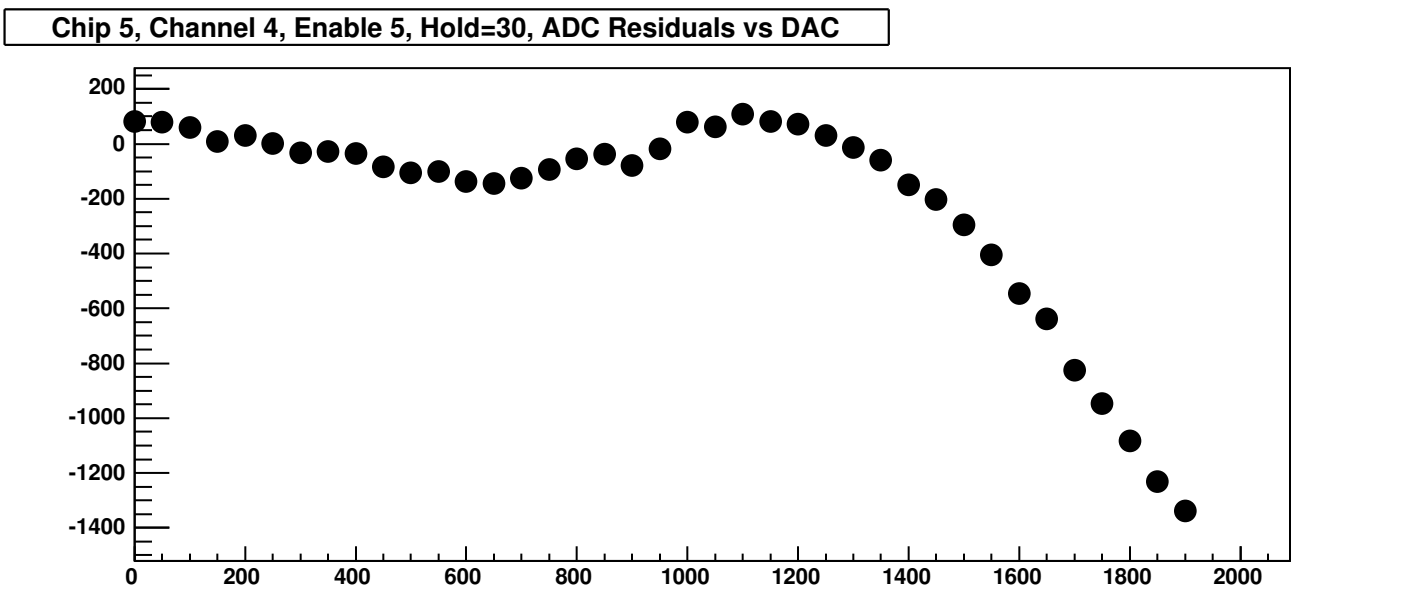

Chip 5, Channel 4, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 8.886 / 11
p0 -1198 ± 20.43
p1 0.3343 ± 0.01835
-550
-600
-650
-700
-750
-800
-850
-900
-950
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

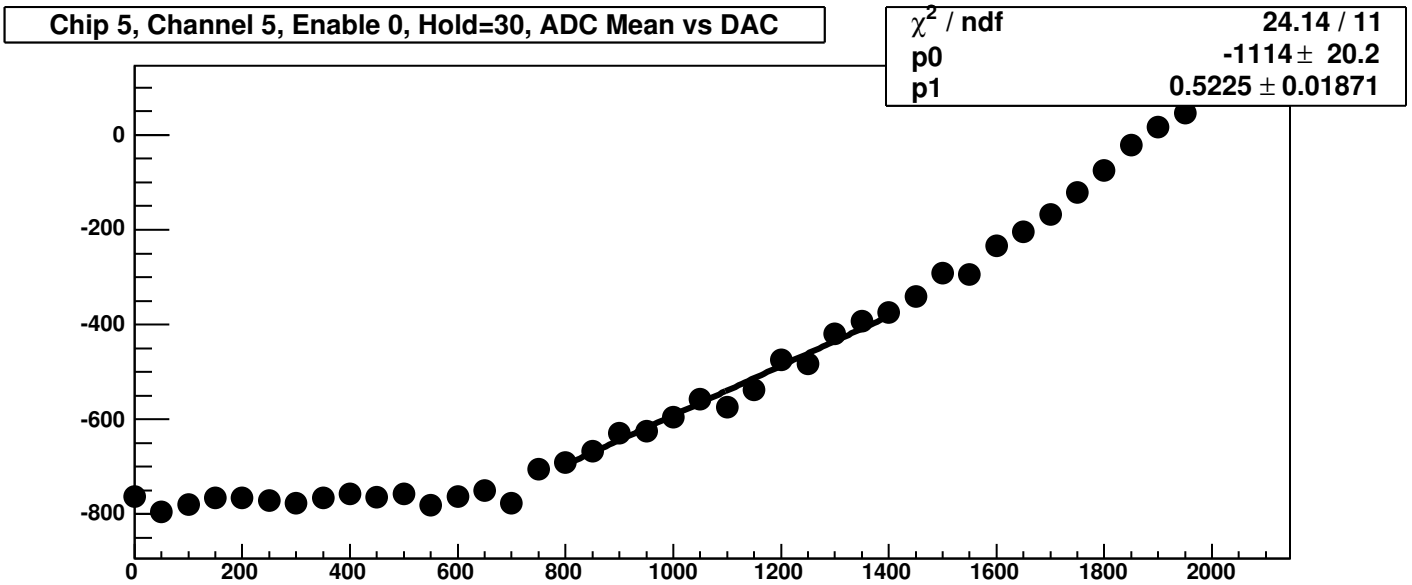

Chip 5, Channel 4, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

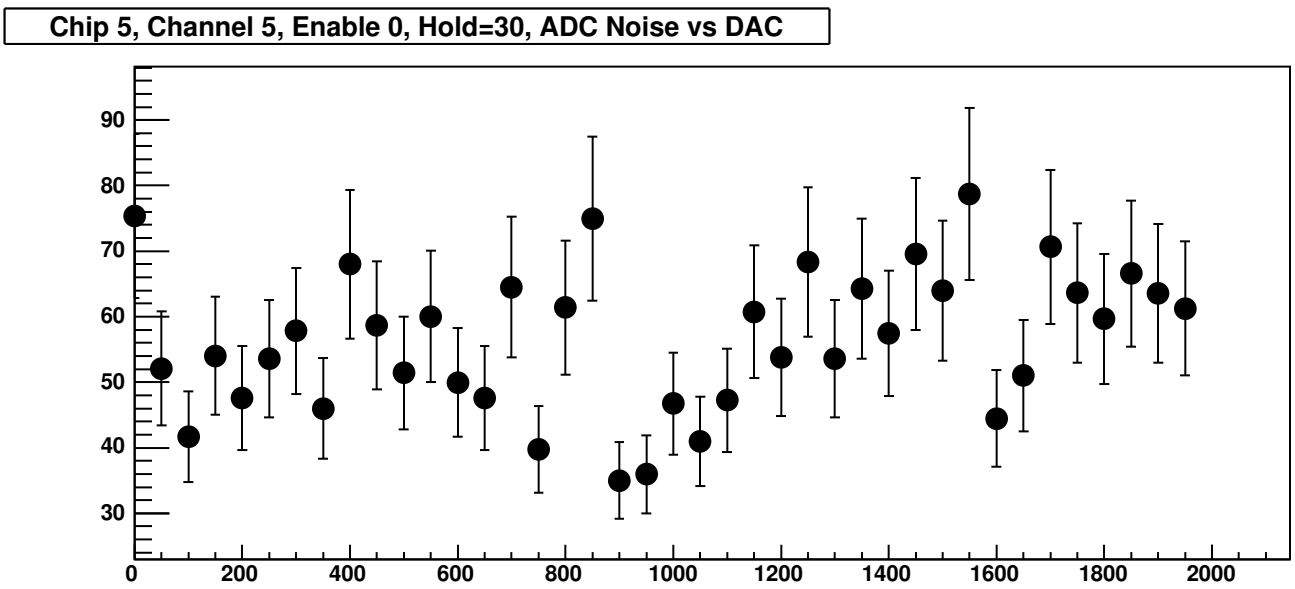

Chip 5, Channel 4, Enable 4, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

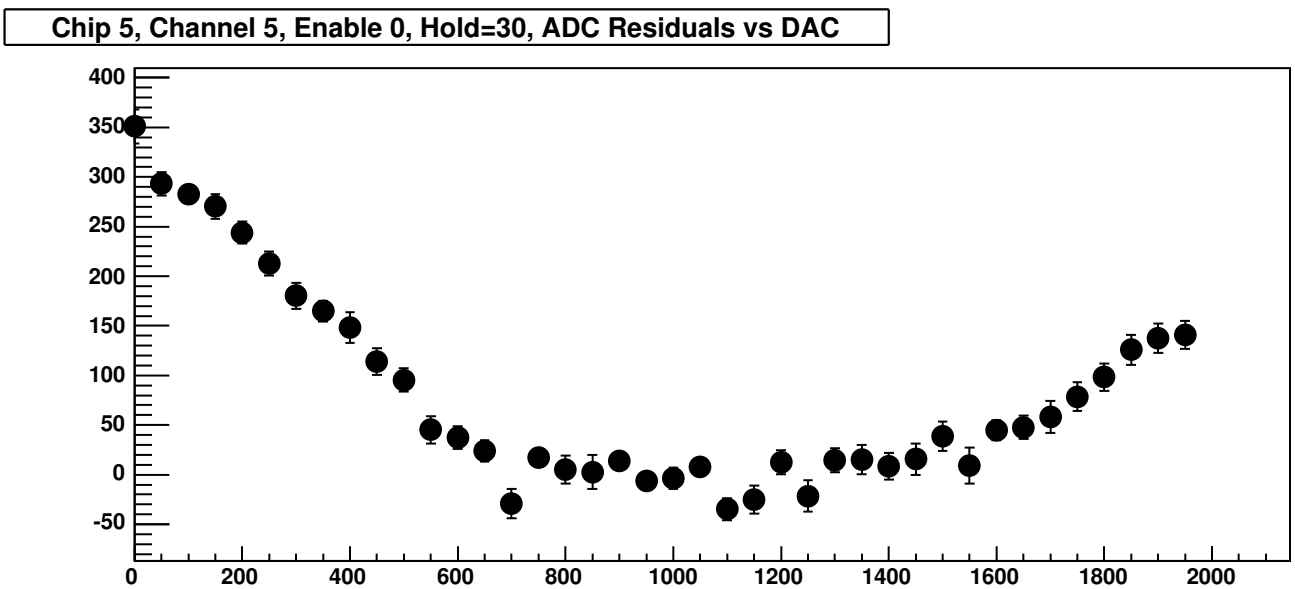

Chip 5, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 163.5 / 11
p0 -729.9 ± 31.8
p1 0.3692 ± 0.02869
-200
-400
-600
-800
-1000
-1200
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

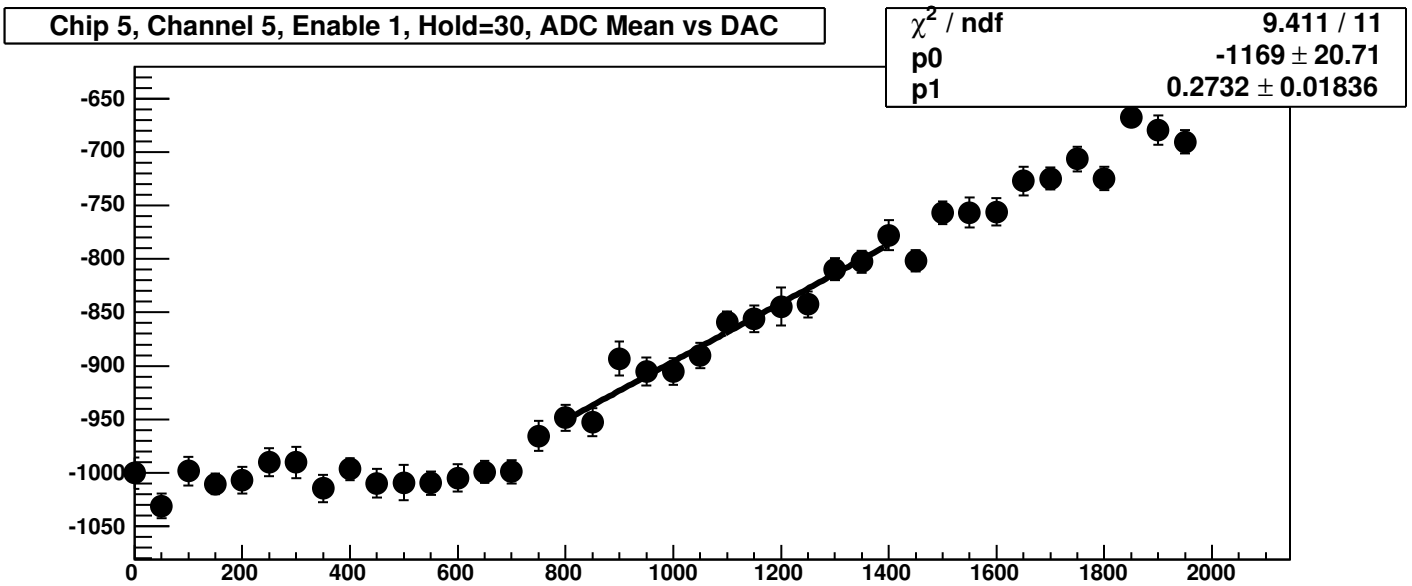

Chip 5, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

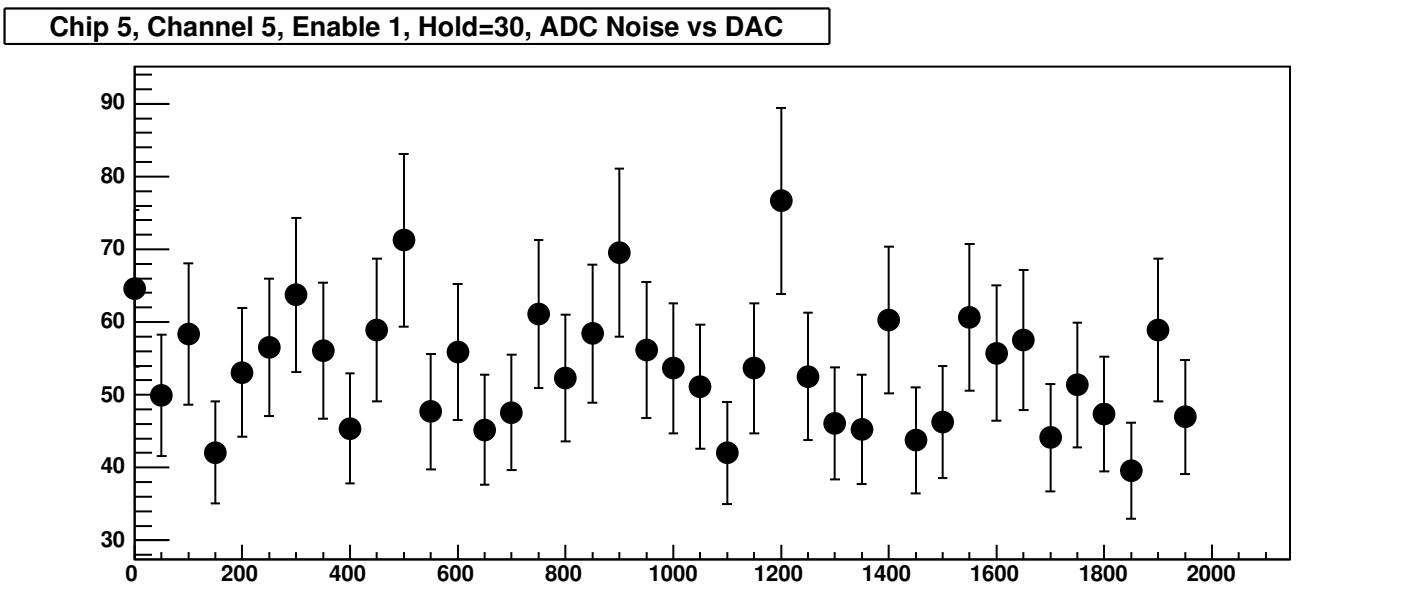

Chip 5, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

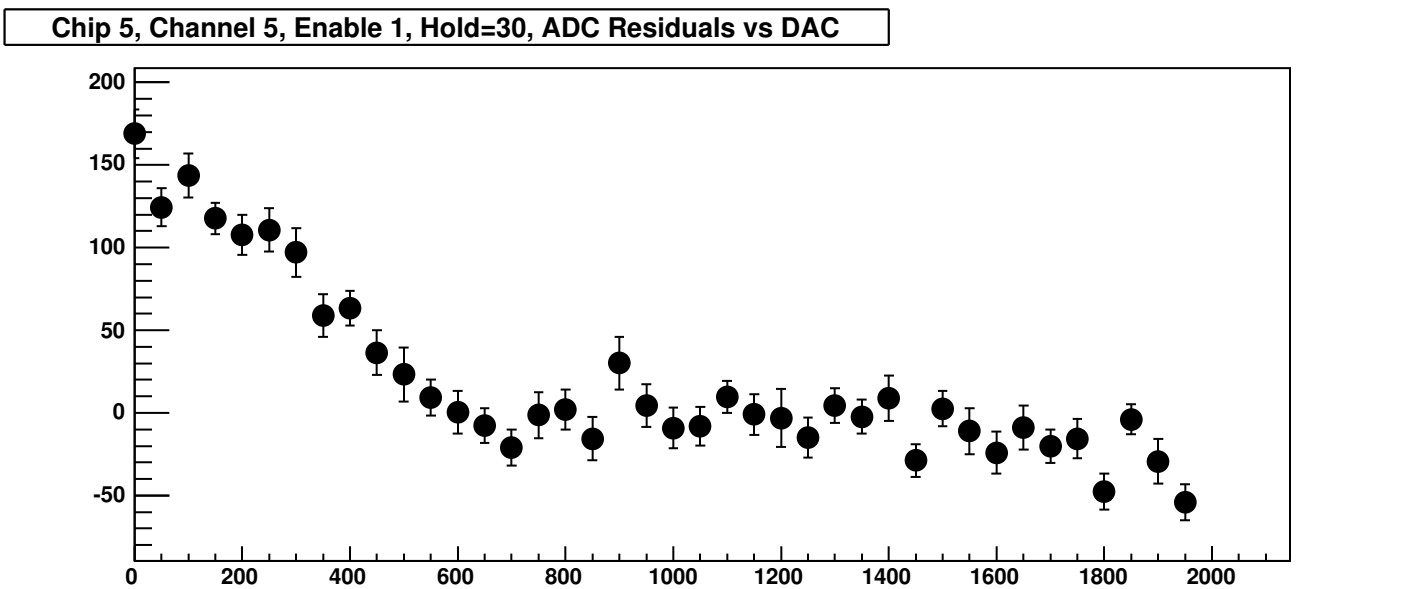

Chip 5, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 24.14 / 11
p0 -1114 ± 20.2
p1 0.5225 ± 0.01871
0
-200
-400
-600
-800
0 200 400 600 800 1000 1200 1400 1600 1800 2000

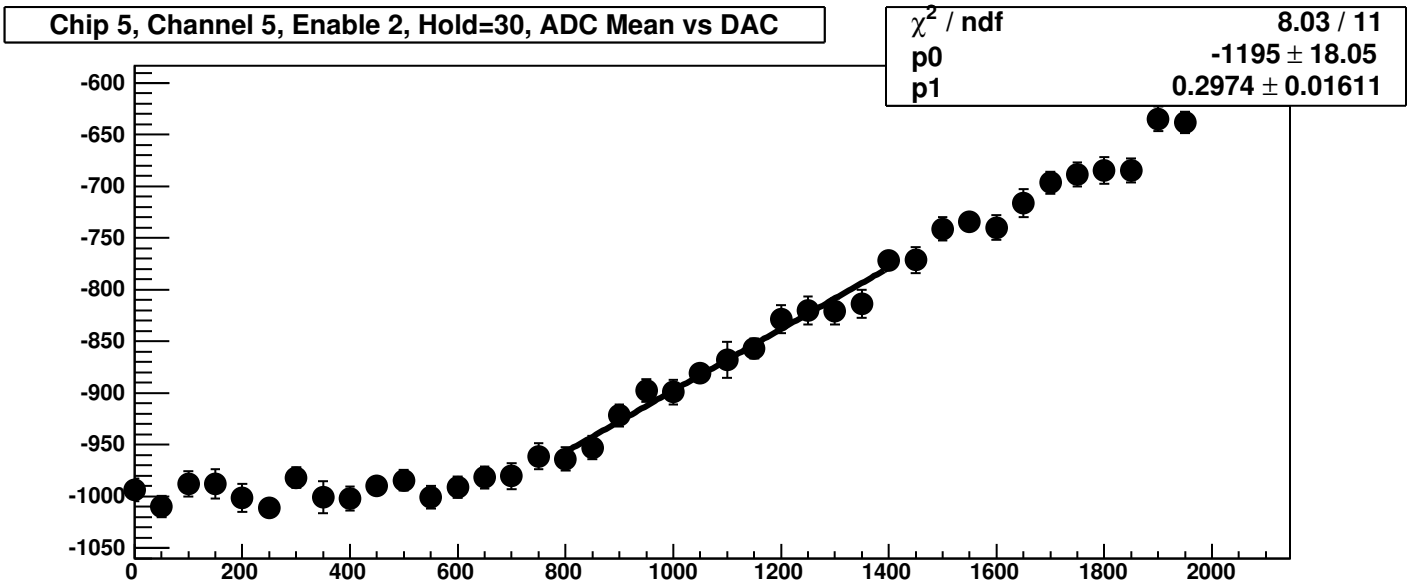

Chip 5, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0 200 400 600 800 1000 1200 1400 1600 1800 2000

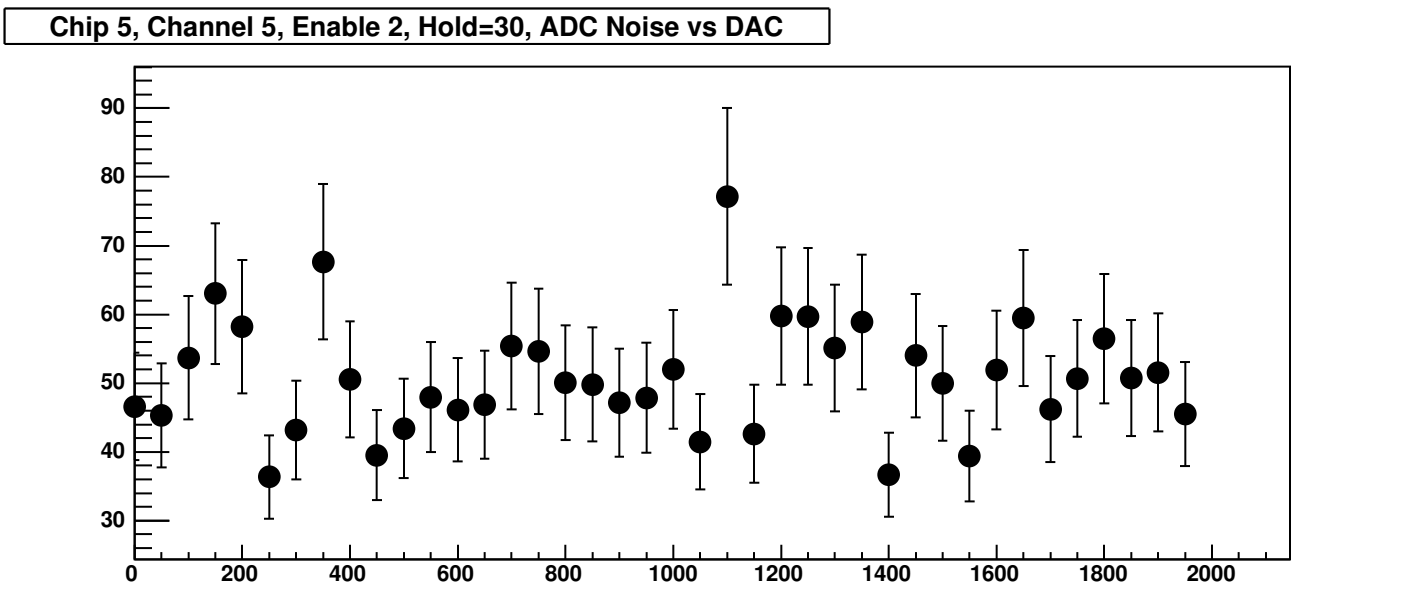

Chip 5, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC
400
350
300
250
200
150
100
50
0
-50
0 200 400 600 800 1000 1200 1400 1600 1800 2000

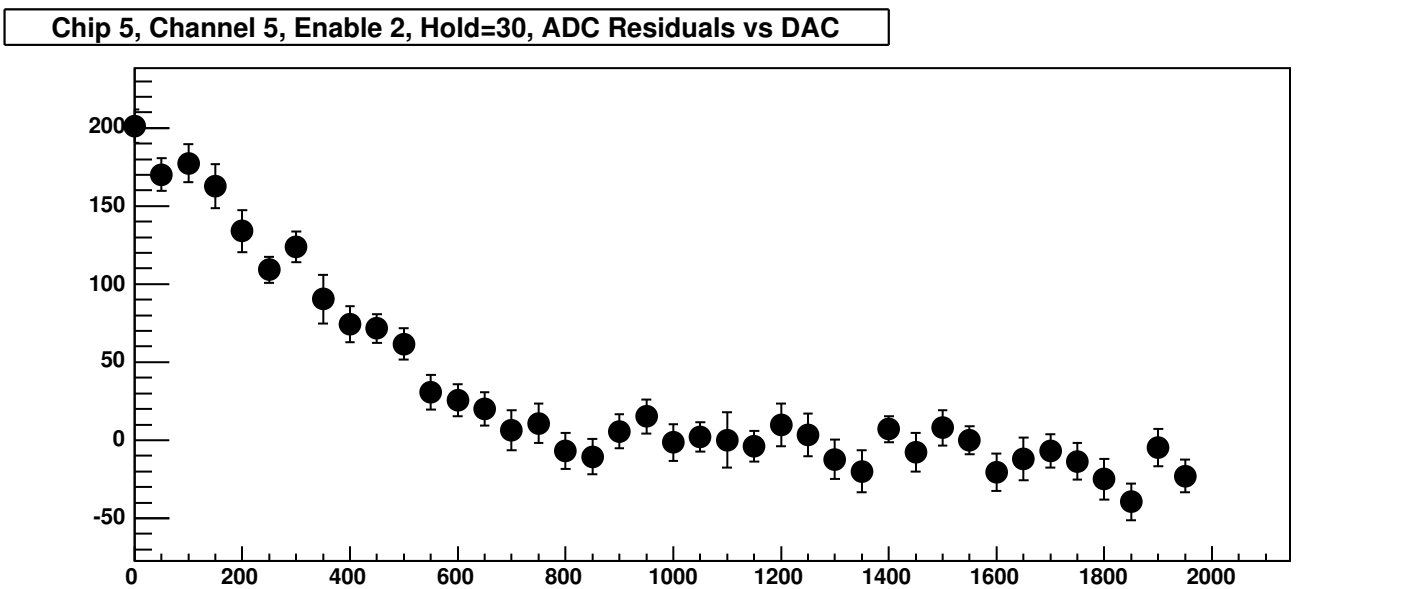

Chip 5, Channel 5, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 9.411 / 11
p0 -1169 ± 20.71
p1 0.2732 ± 0.01836
-650
-700
-750
-800
-850
-900
-950
-1000
-1050
0
200
400
600
800
1000
1200
1400
1600
1800
2000

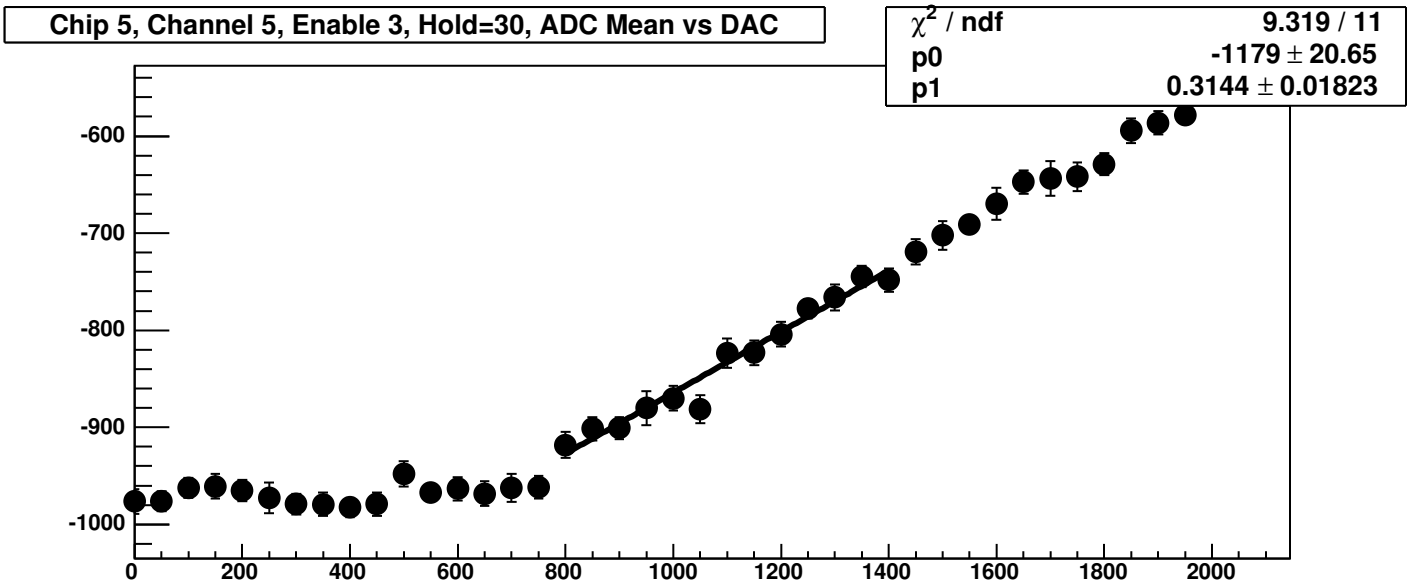

Chip 5, Channel 5, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

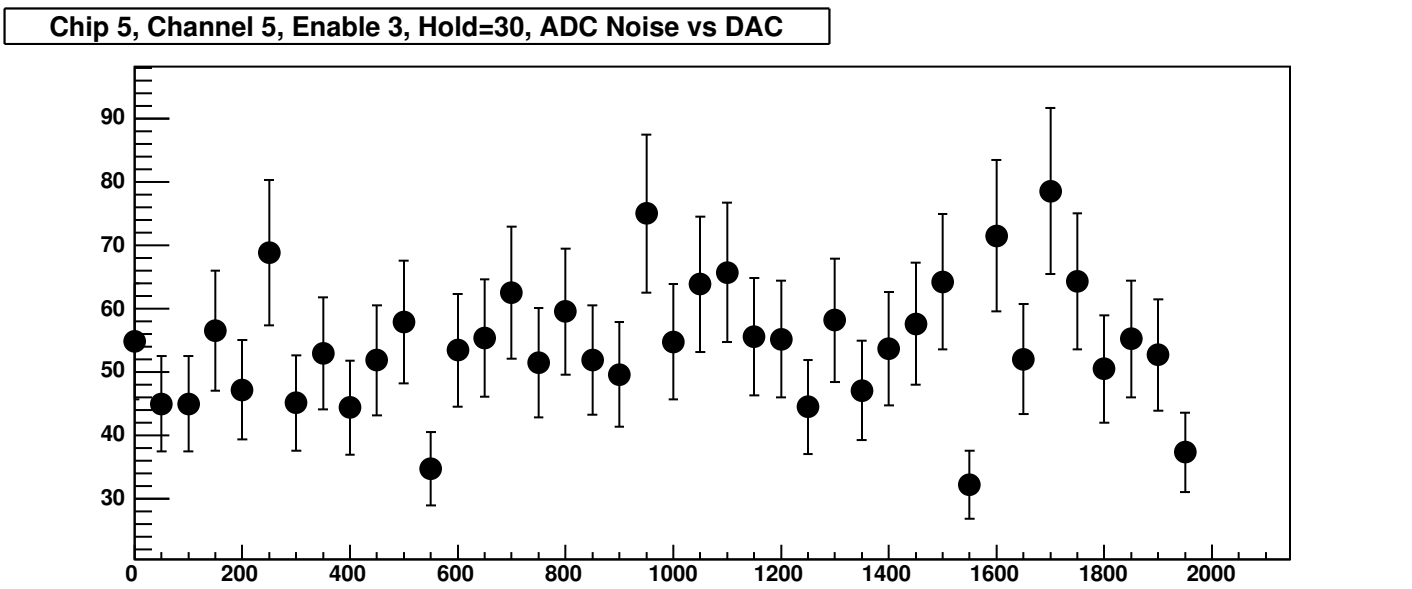

Chip 5, Channel 5, Enable 1, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

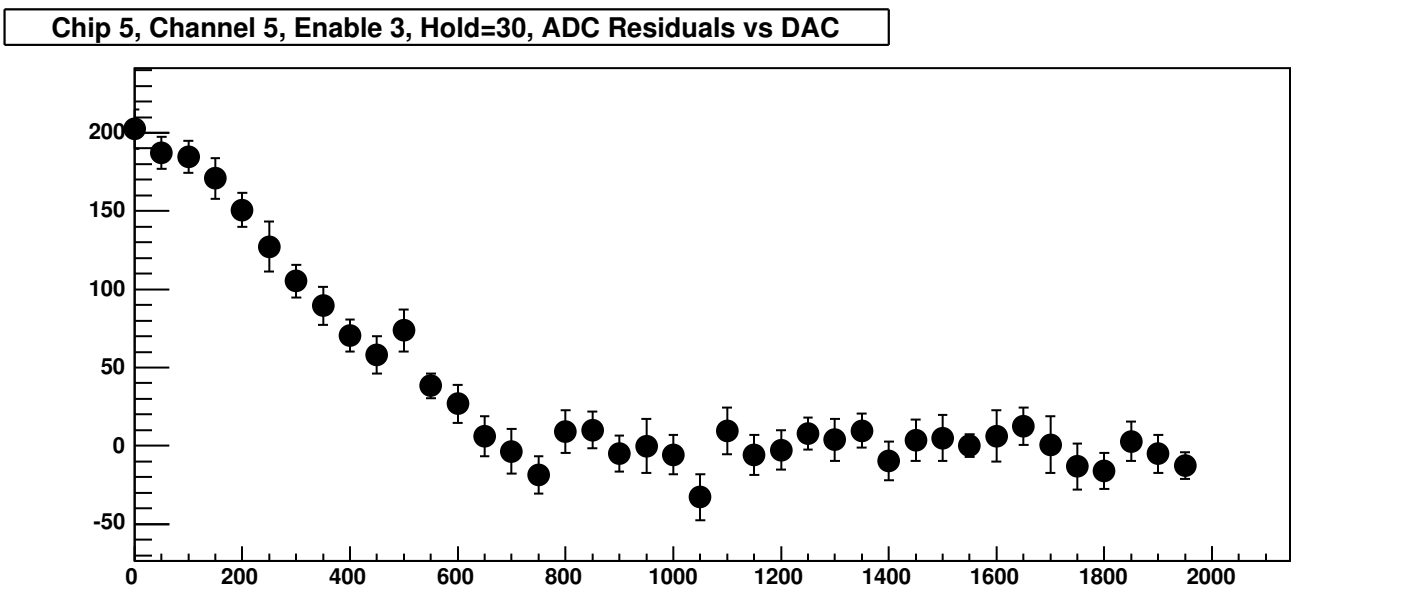

Chip 5, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 8.03 / 11
p0 -1195 ± 18.05
p1 0.2974 ± 0.01611
-600
-650
-700
-750
-800
-850
-900
-950
-1000
-1050
0
200
400
600
800
1000
1200
1400
1600
1800
2000

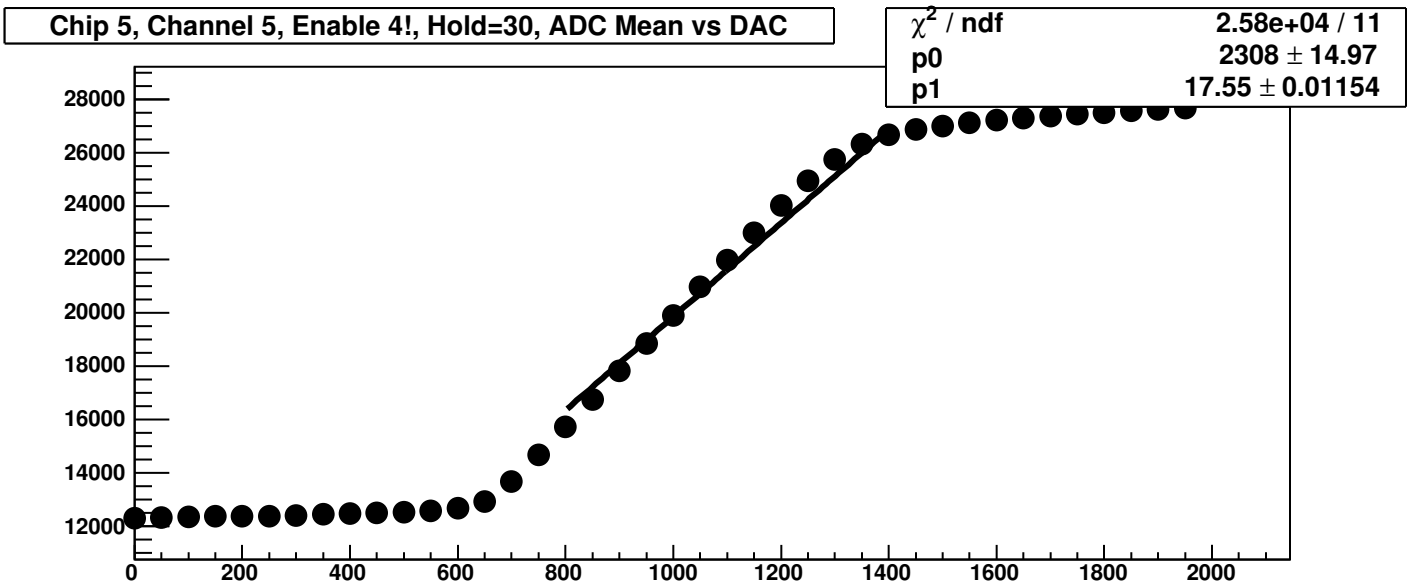

Chip 5, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

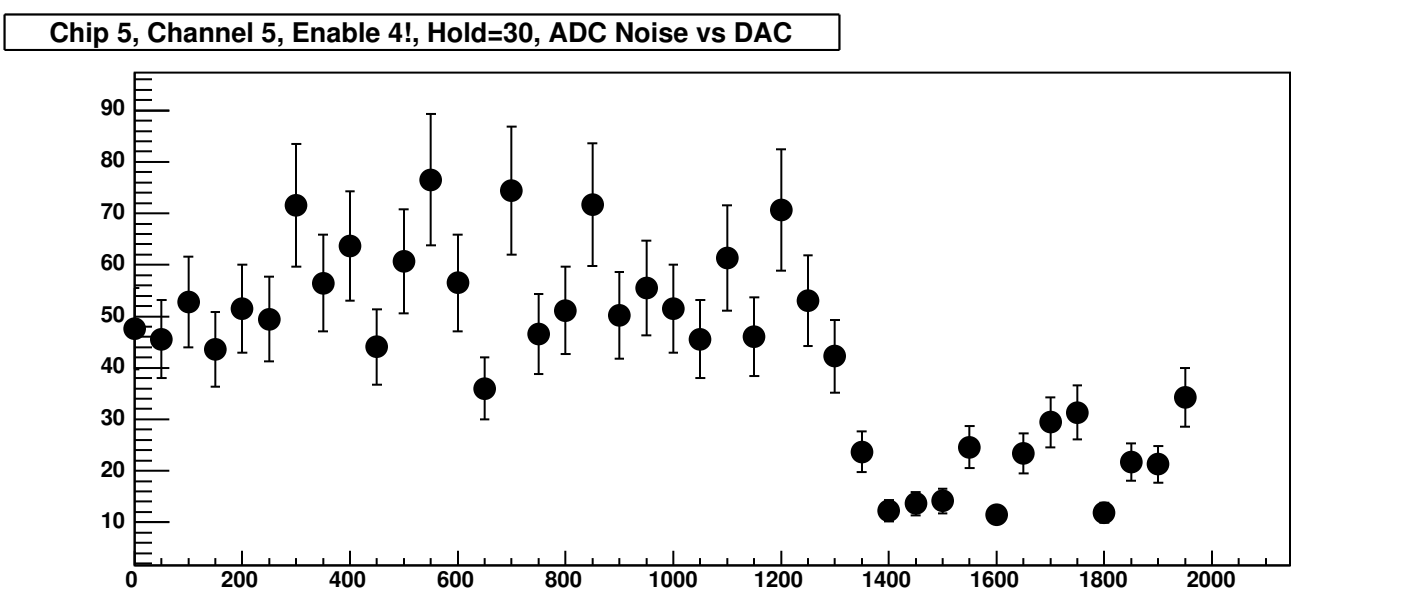

Chip 5, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

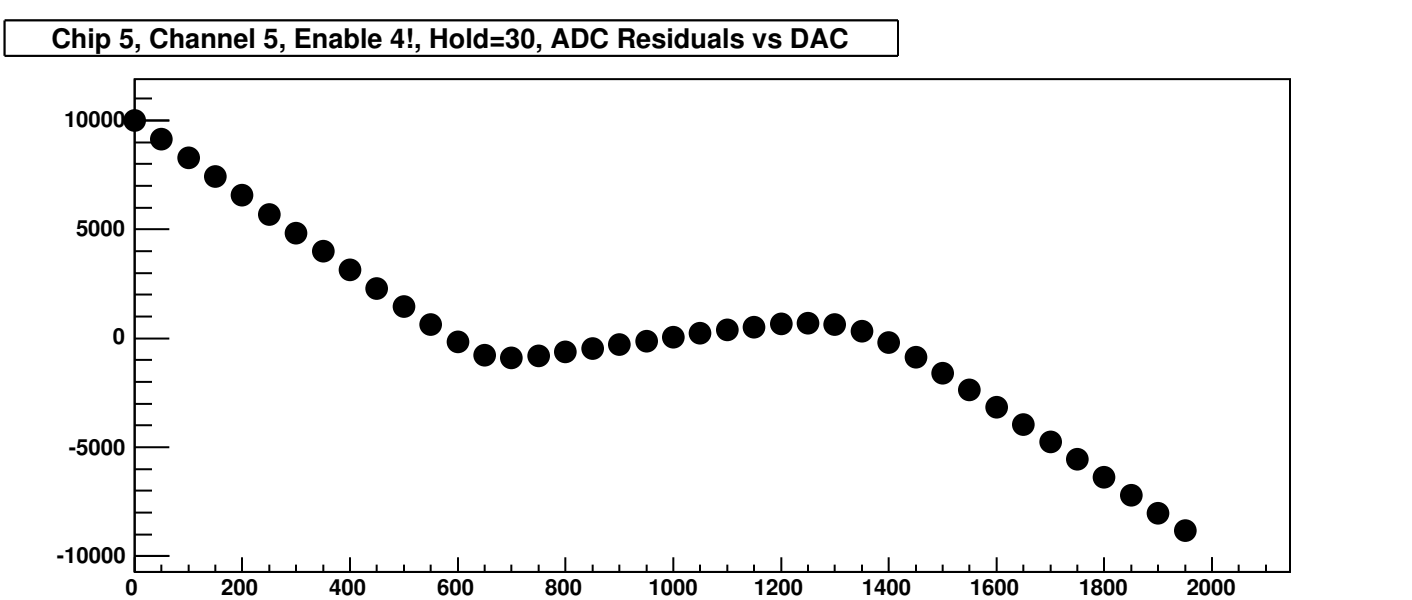

Chip 5, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 9.319 / 11
p0 -1179 ± 20.65
p1 0.3144 ± 0.01823
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

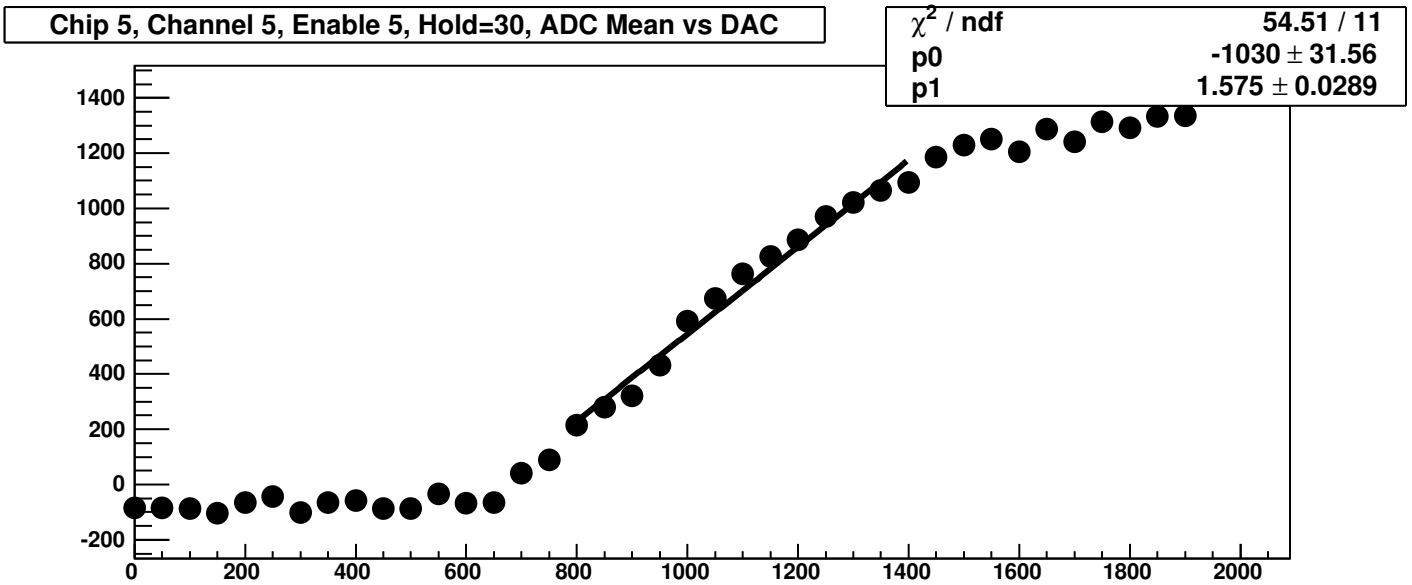

Chip 5, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

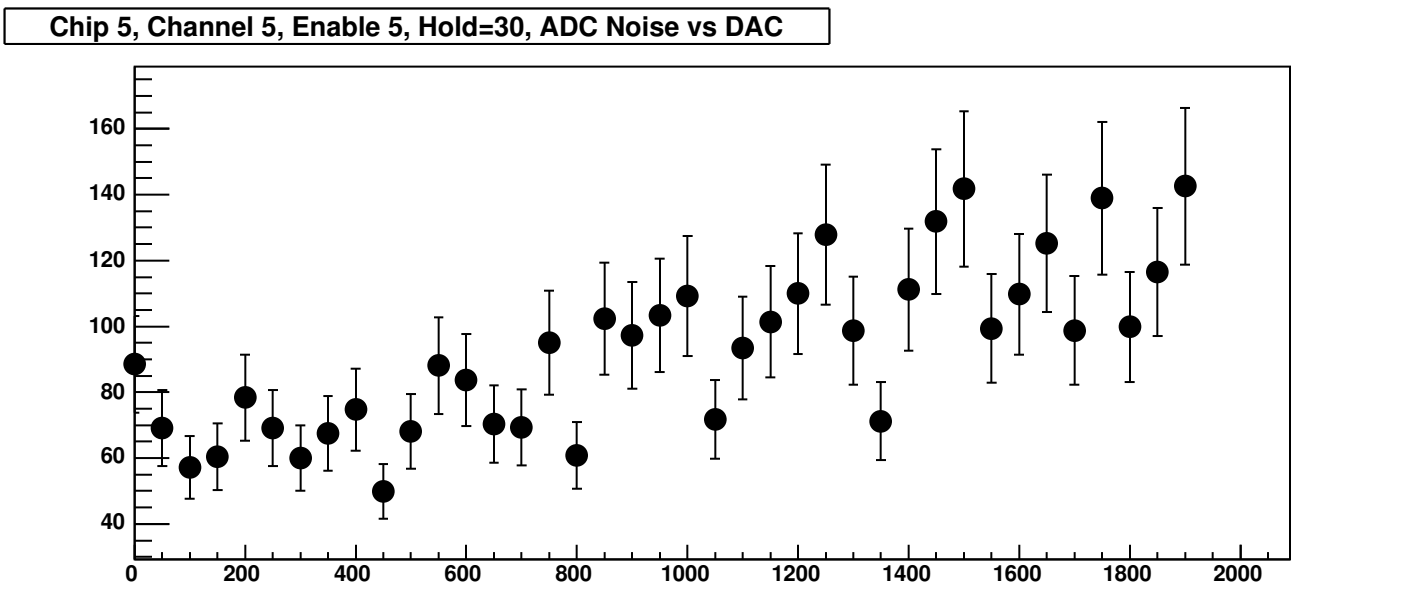

Chip 5, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

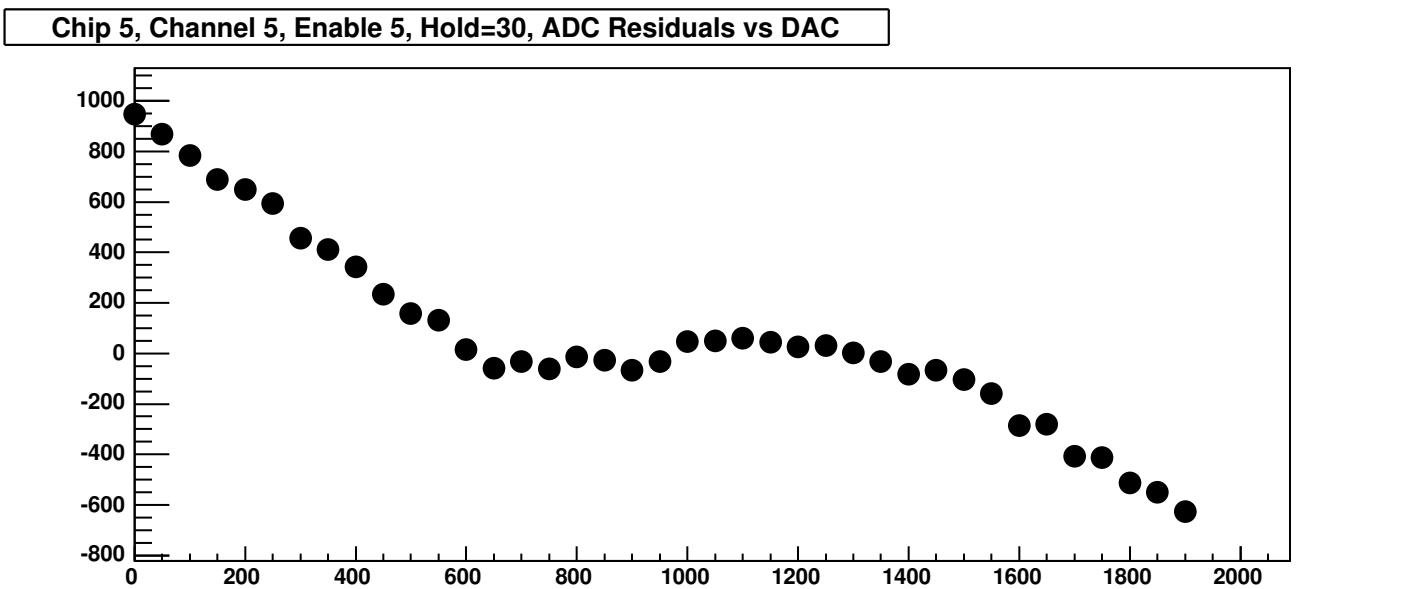

Chip 5, Channel 5, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 2.58e+04 / 11
p0 2308 ± 14.97
p1 17.55 ± 0.01154
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 5, Enable 4!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

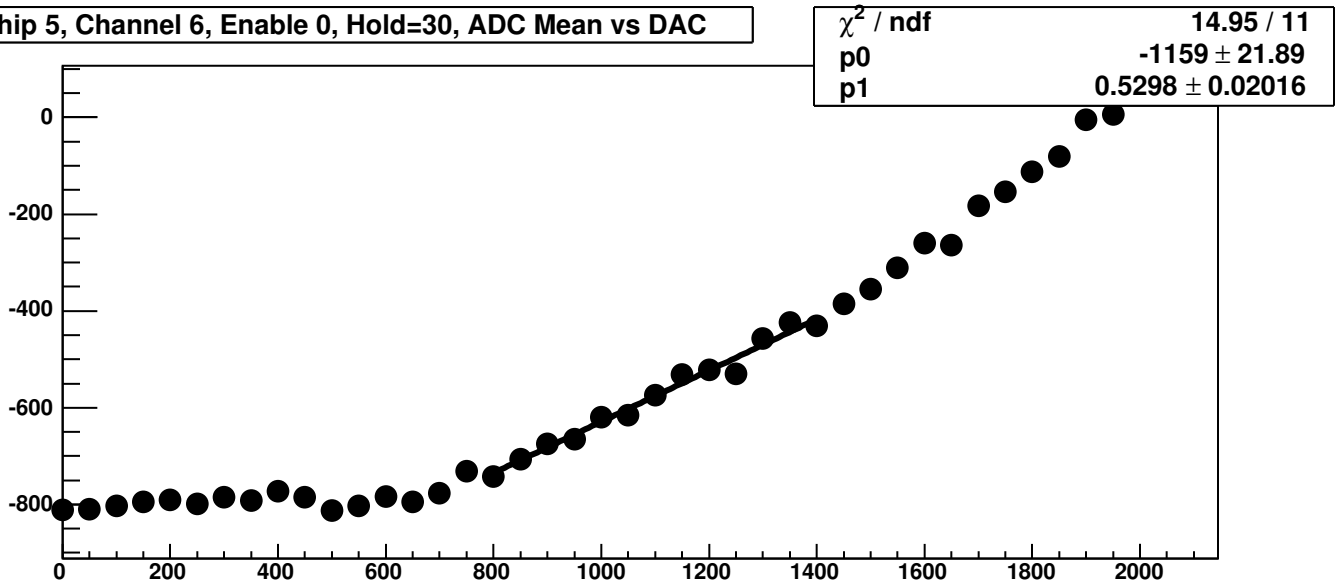

Chip 5, Channel 5, Enable 4!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 54.51 / 11
p0 -1030 ± 31.56
p1 1.575 ± 0.0289
1400
1200
1000
800
600
400
200
0
-200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

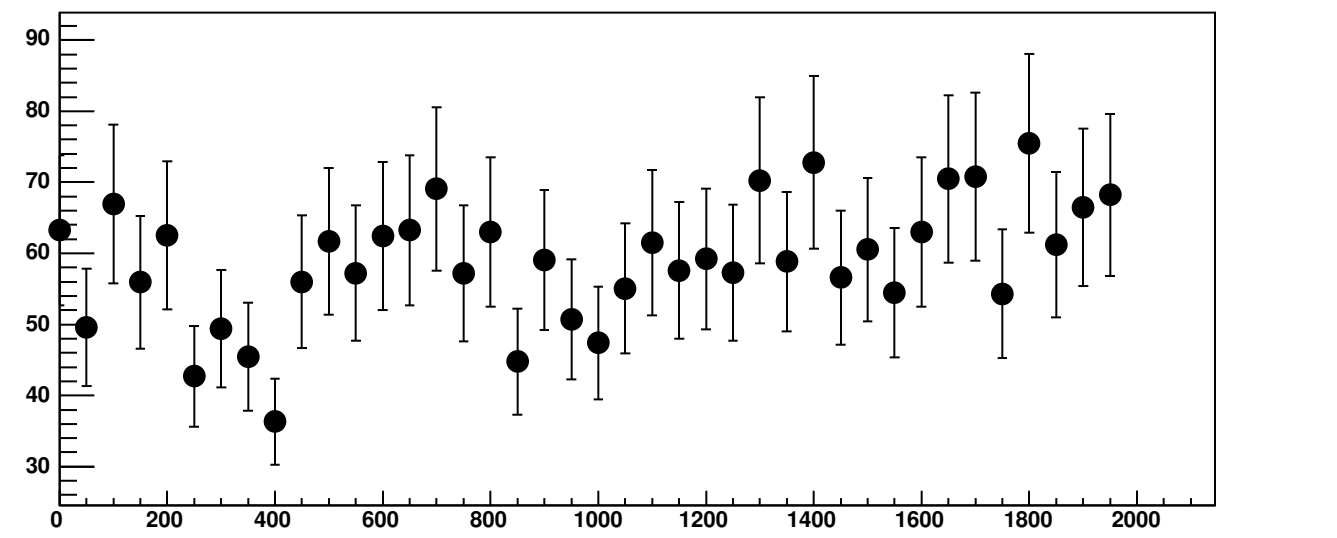

Chip 5, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

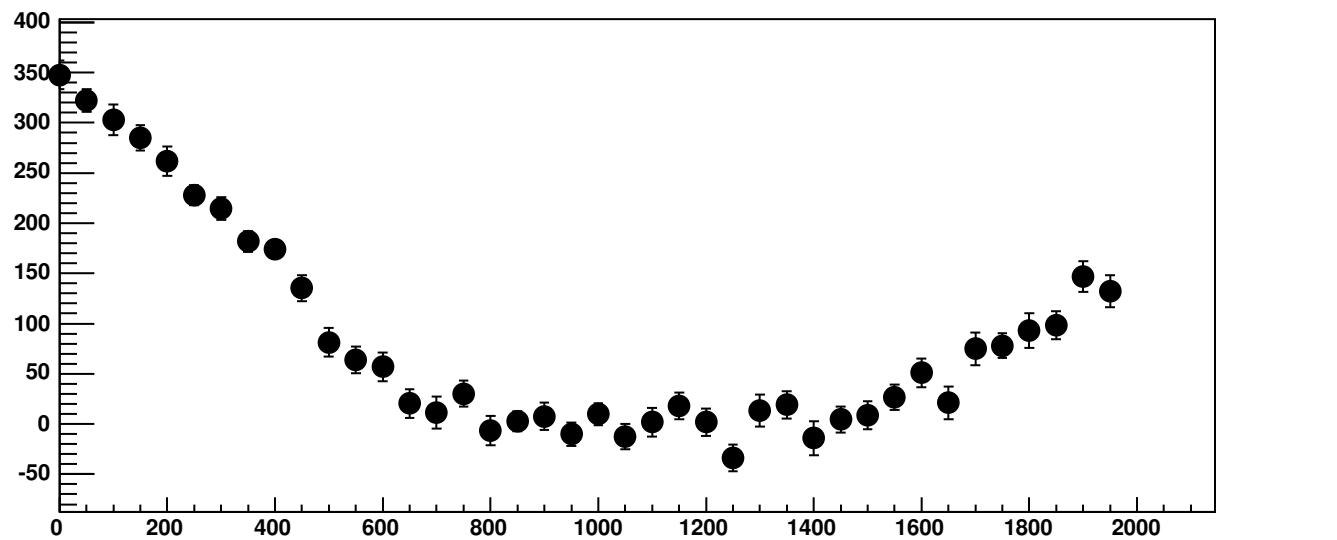
Chip 5, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC



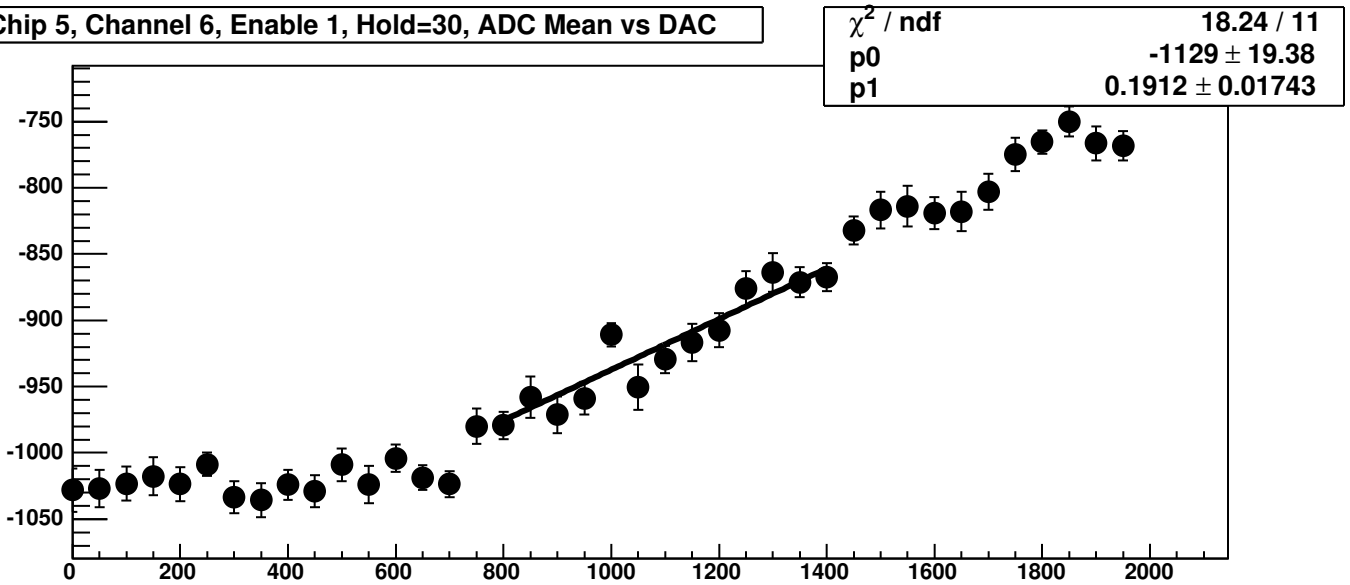
Chip 5, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC



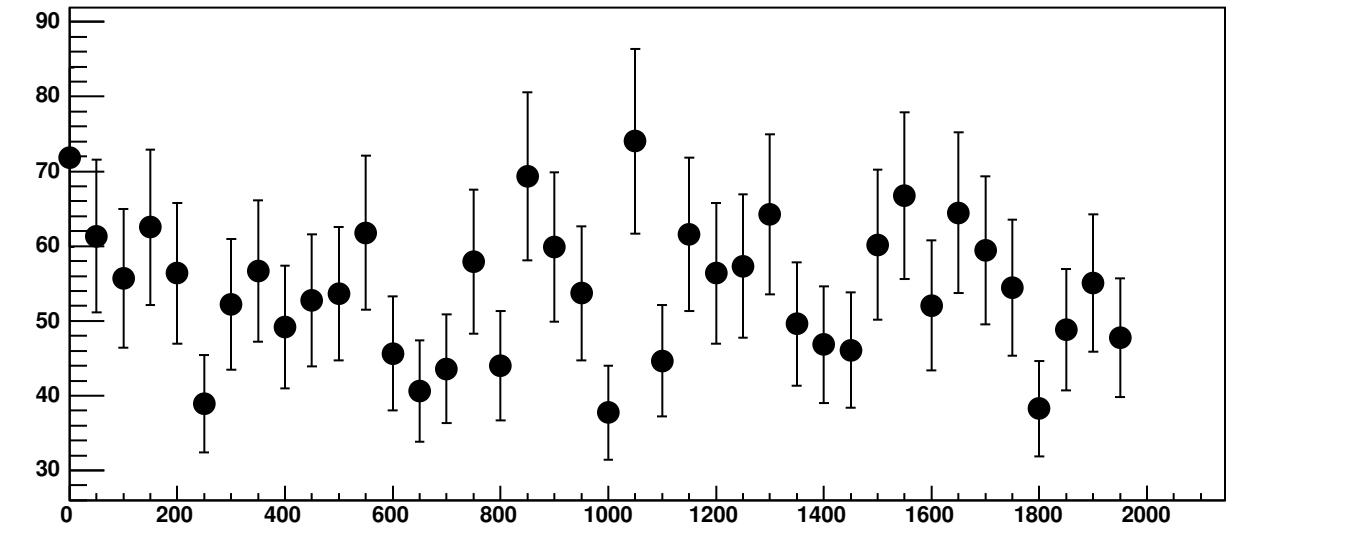
Chip 5, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC



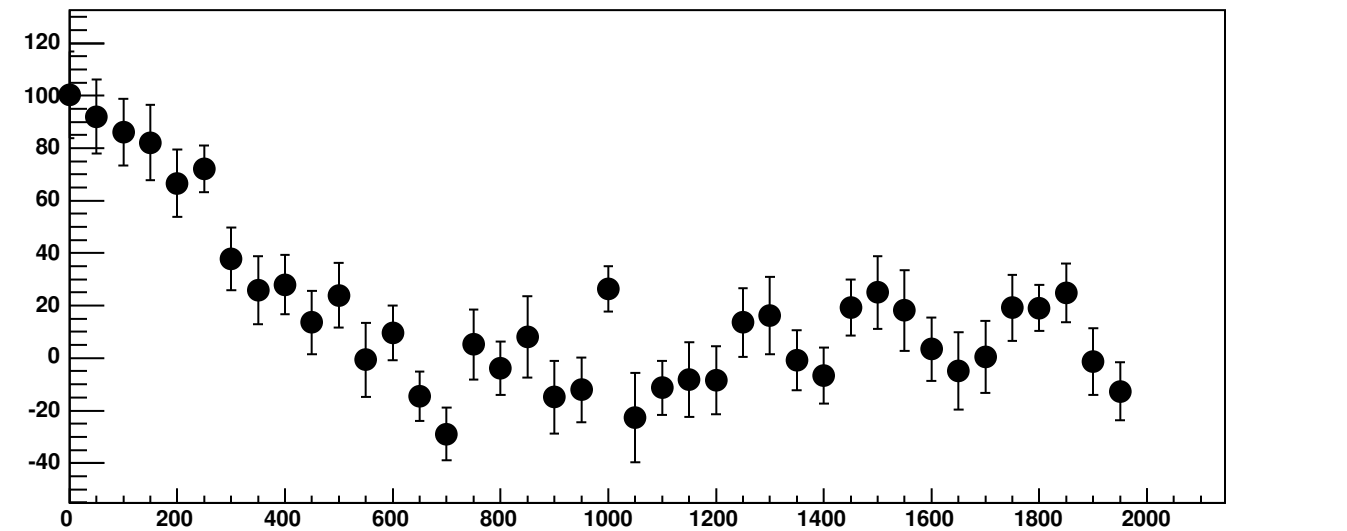
Chip 5, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC



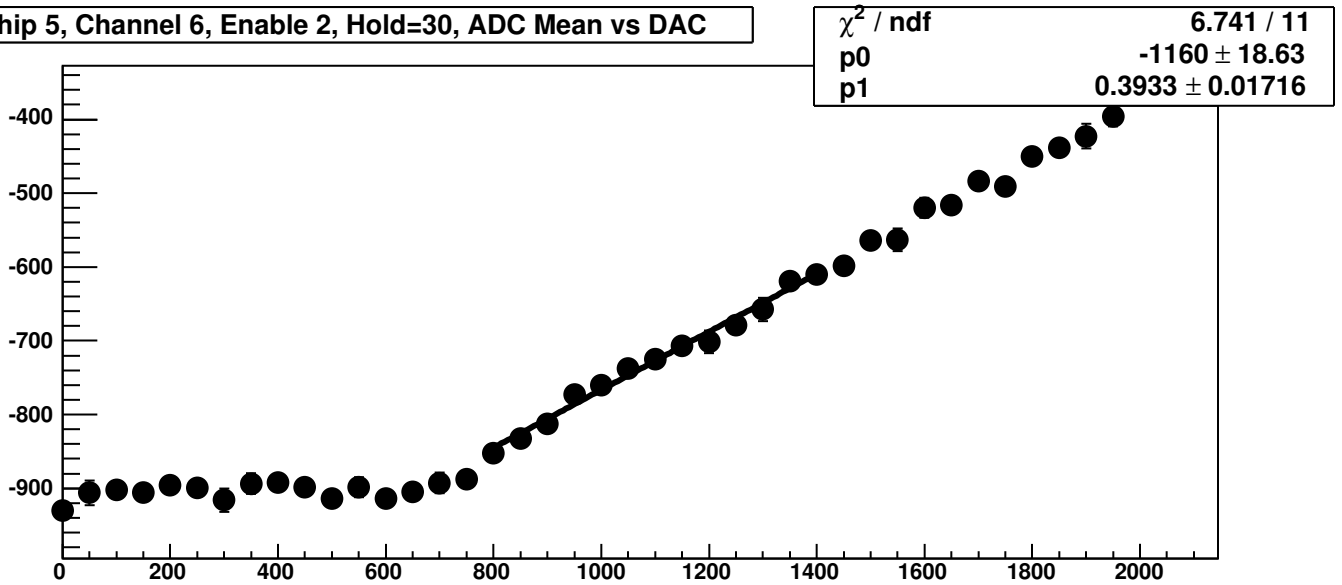
Chip 5, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC



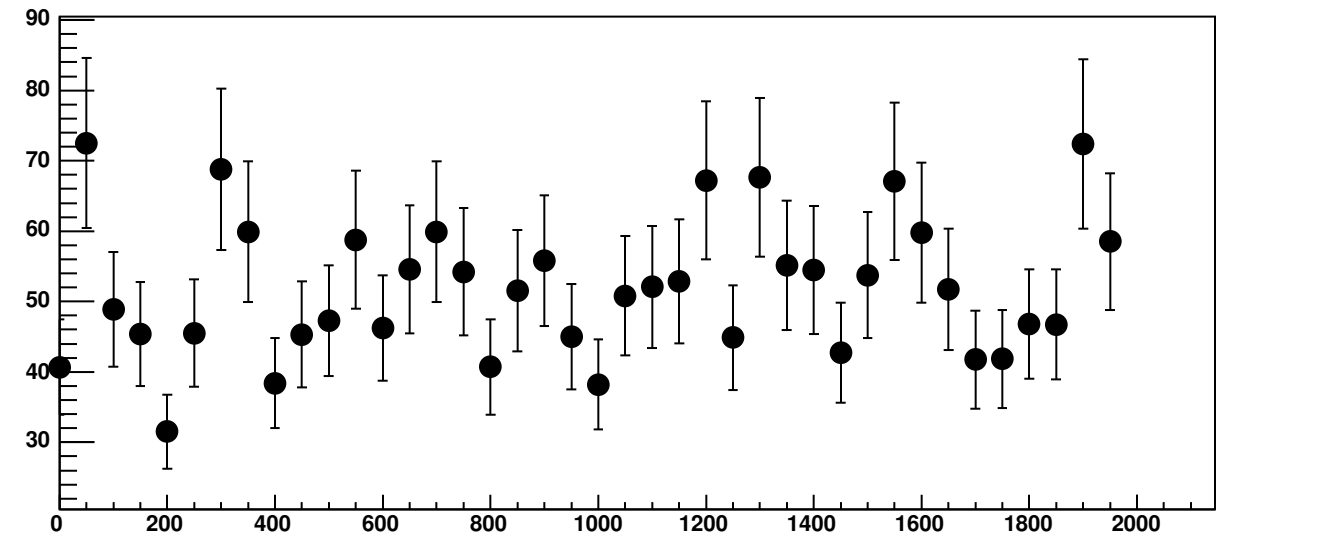
Chip 5, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC



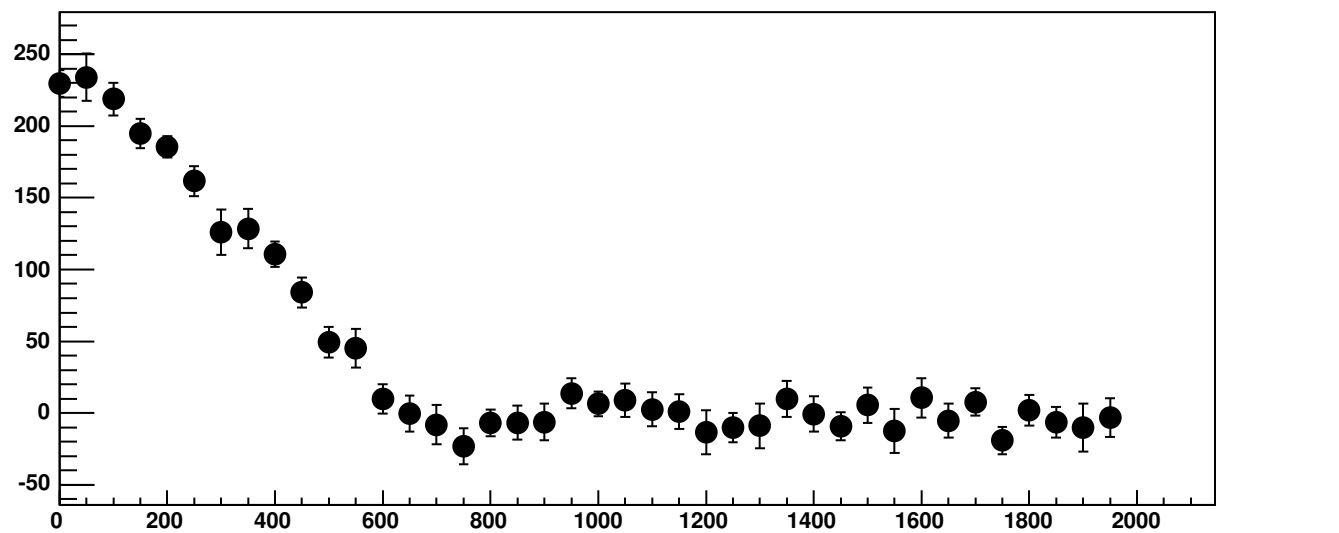
Chip 5, Channel 6, Enable 2, Hold=30, ADC Mean vs DAC

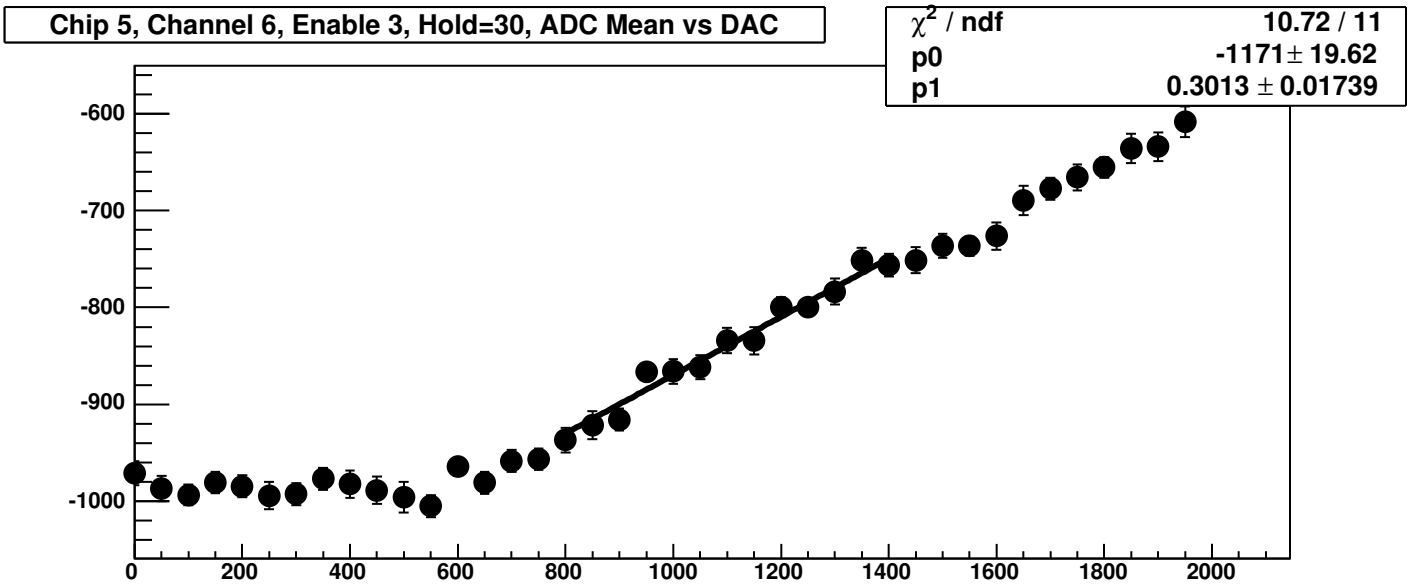


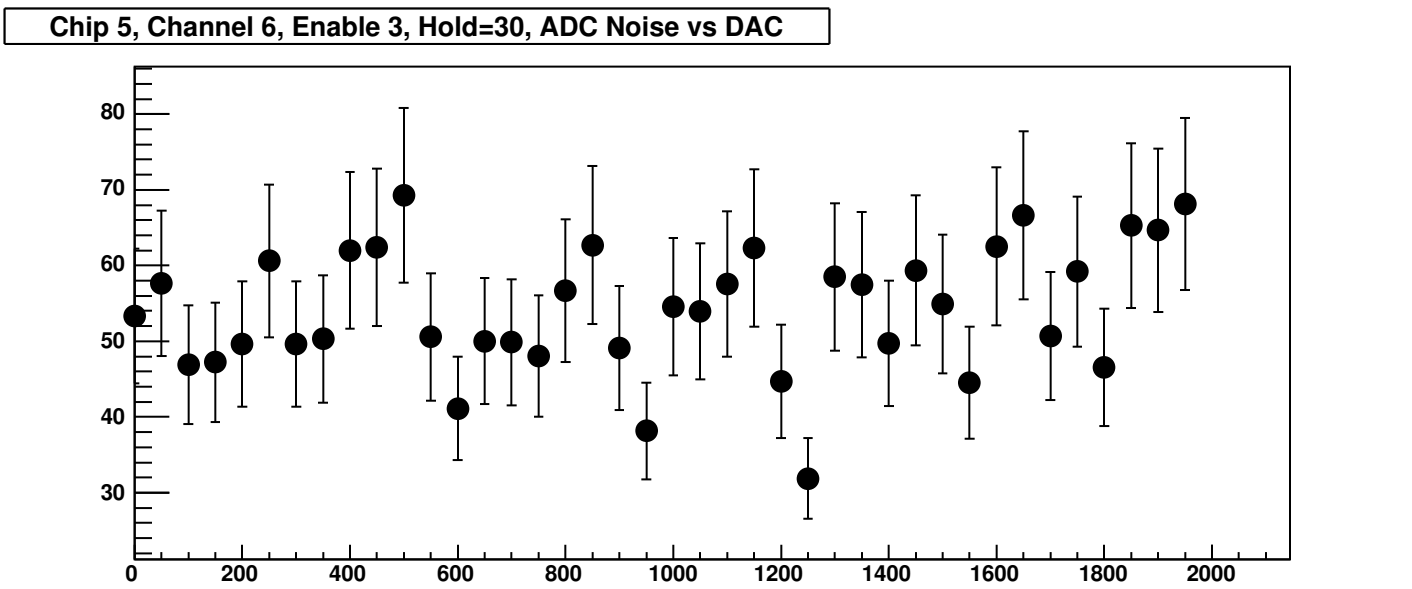
Chip 5, Channel 6, Enable 2, Hold=30, ADC Noise vs DAC

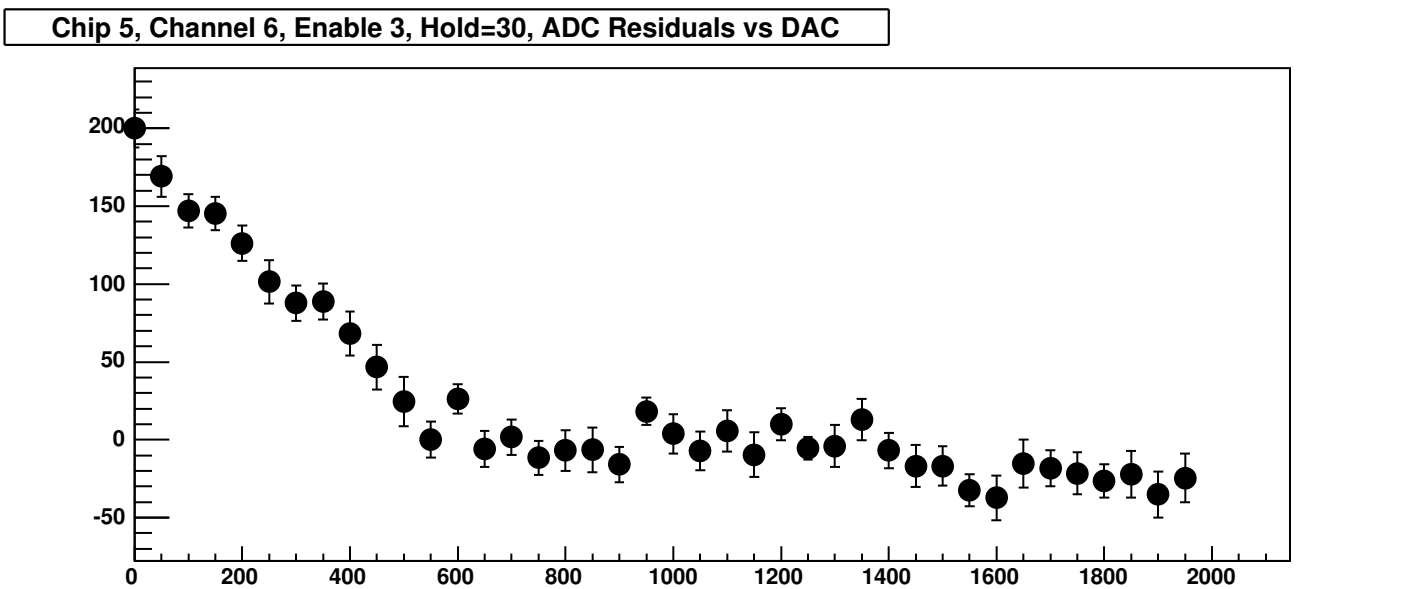


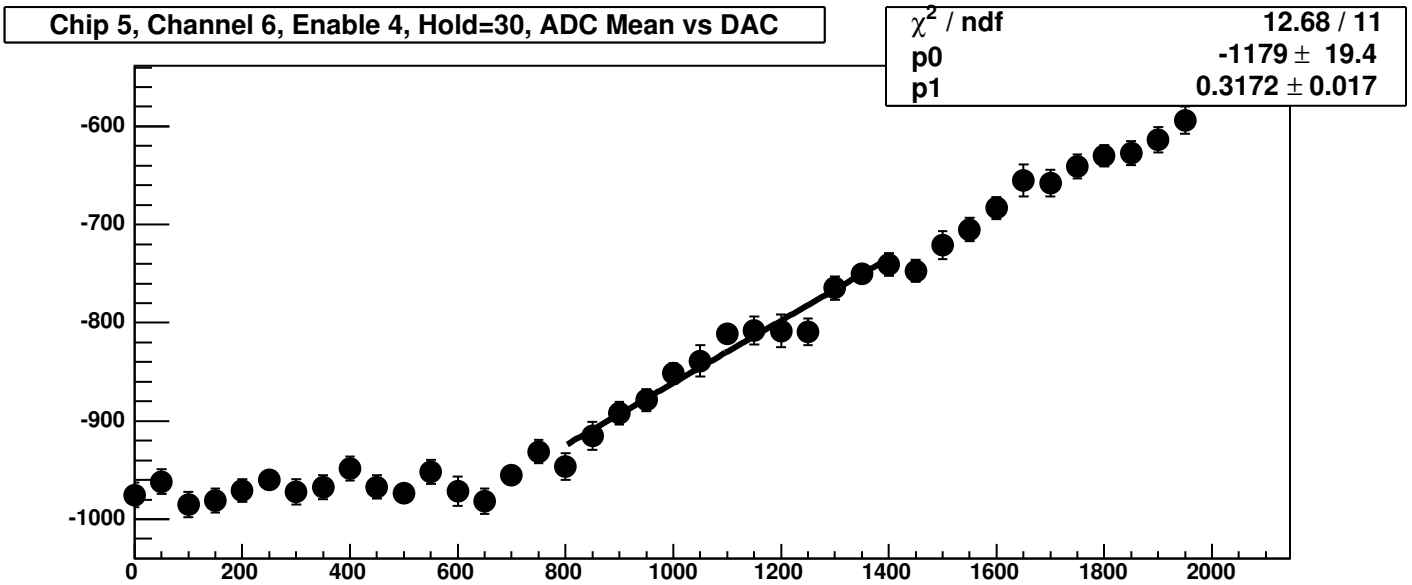
Chip 5, Channel 6, Enable 2, Hold=30, ADC Residuals vs DAC

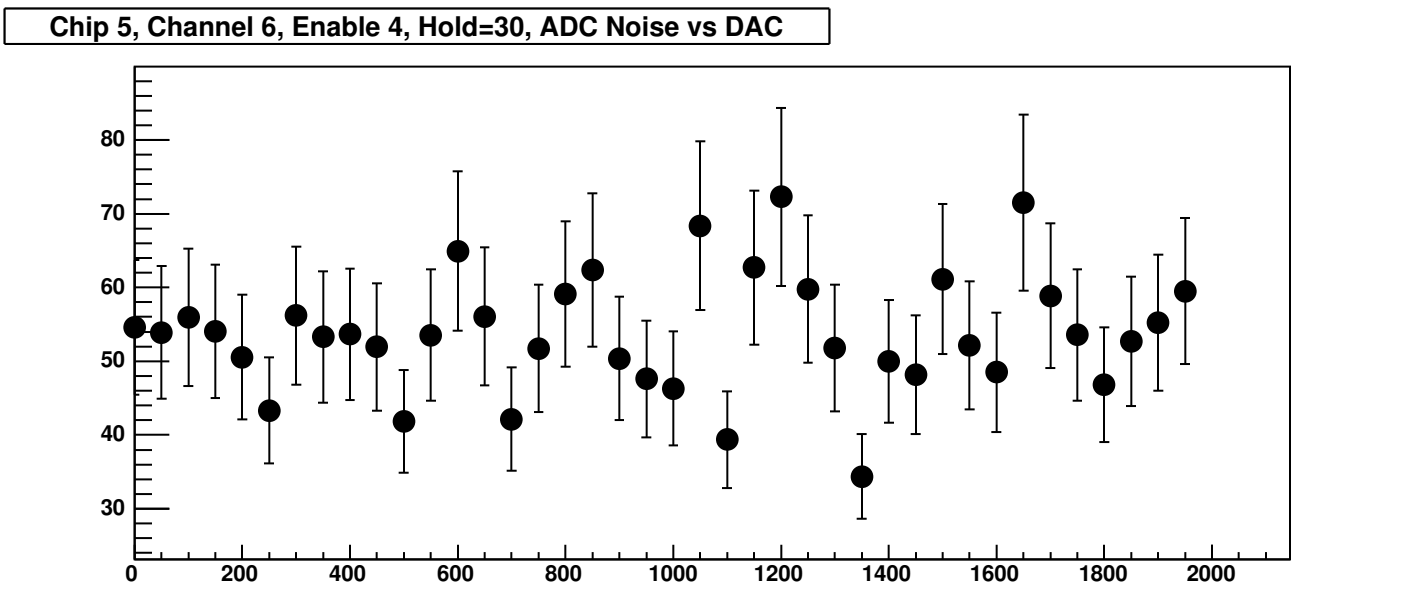


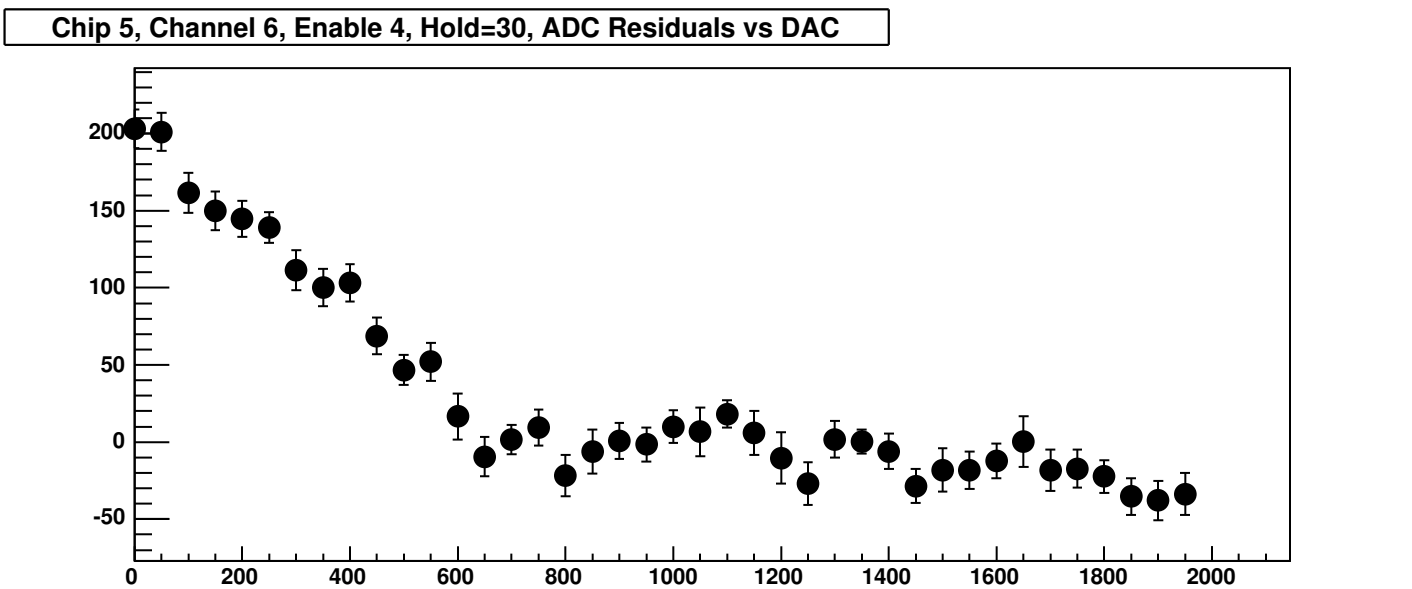

Chip 5, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 10.72 / 11
p0 -1171 ± 19.62
p1 0.3013 ± 0.01739
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

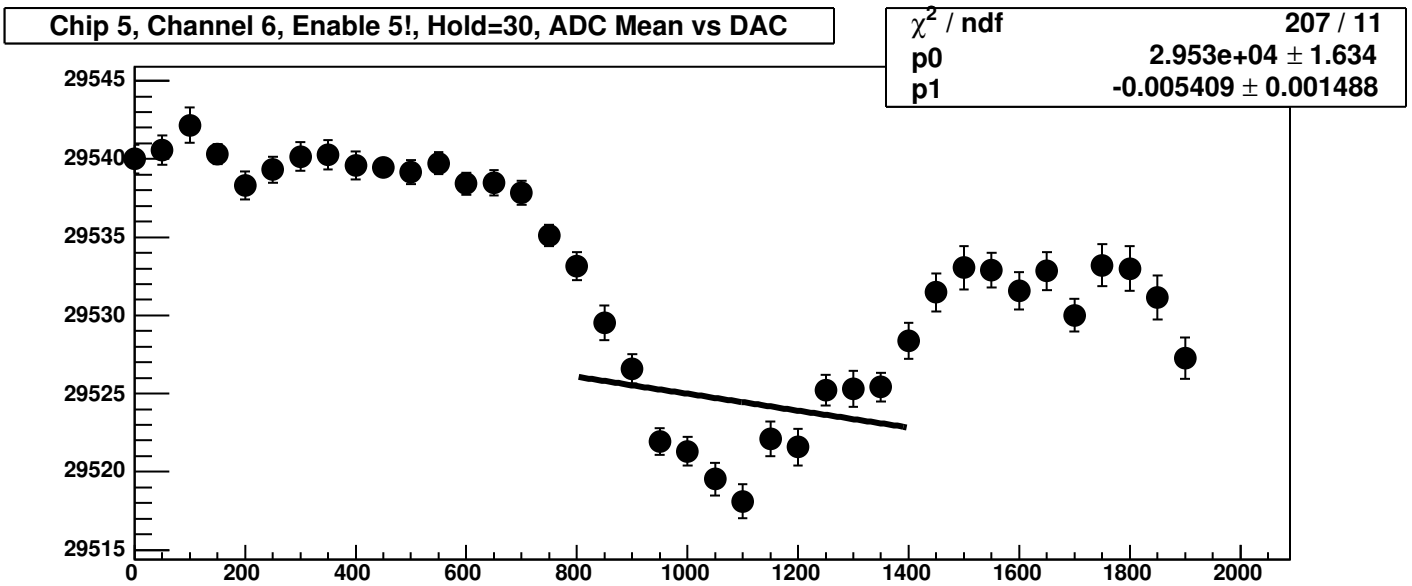

Chip 5, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

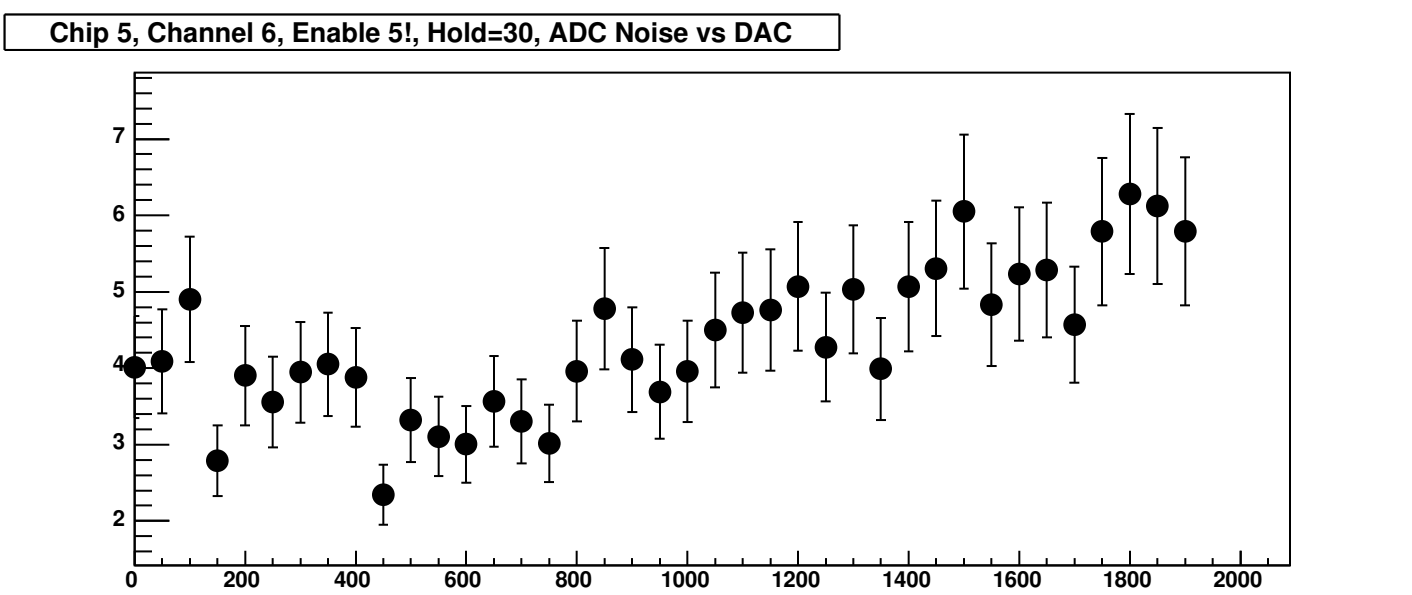

Chip 5, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

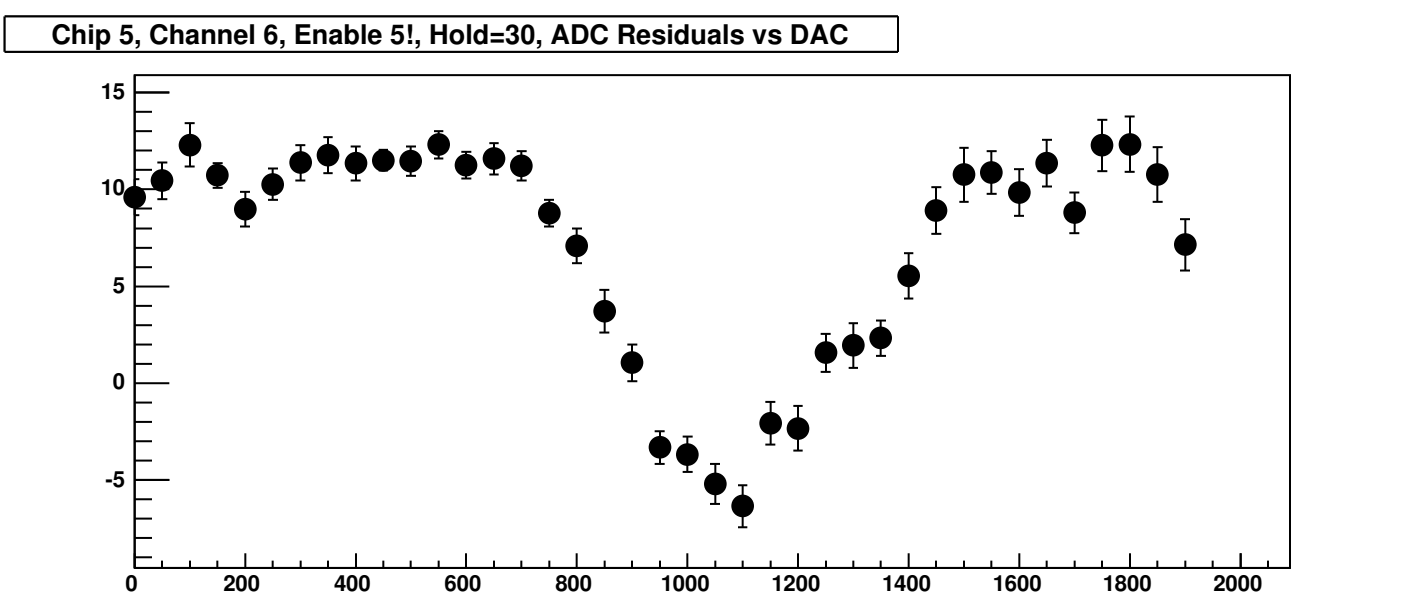

Chip 5, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 12.68 / 11
p0 -1179 ± 19.4
p1 0.3172 ± 0.017
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

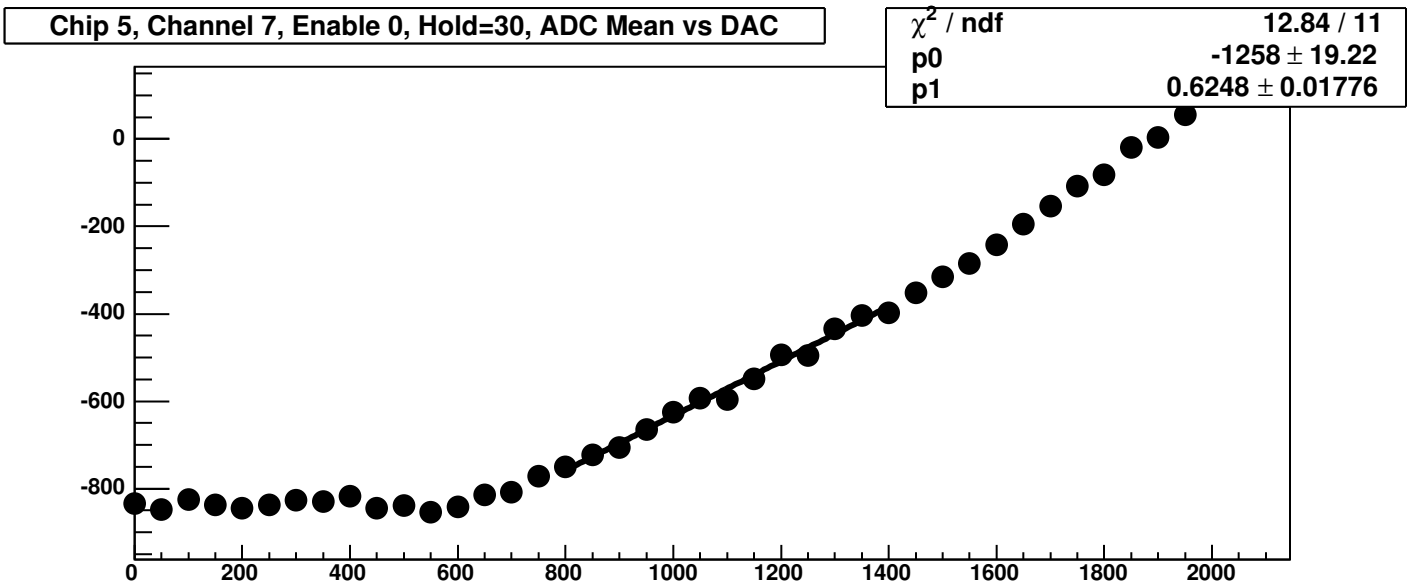

Chip 5, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

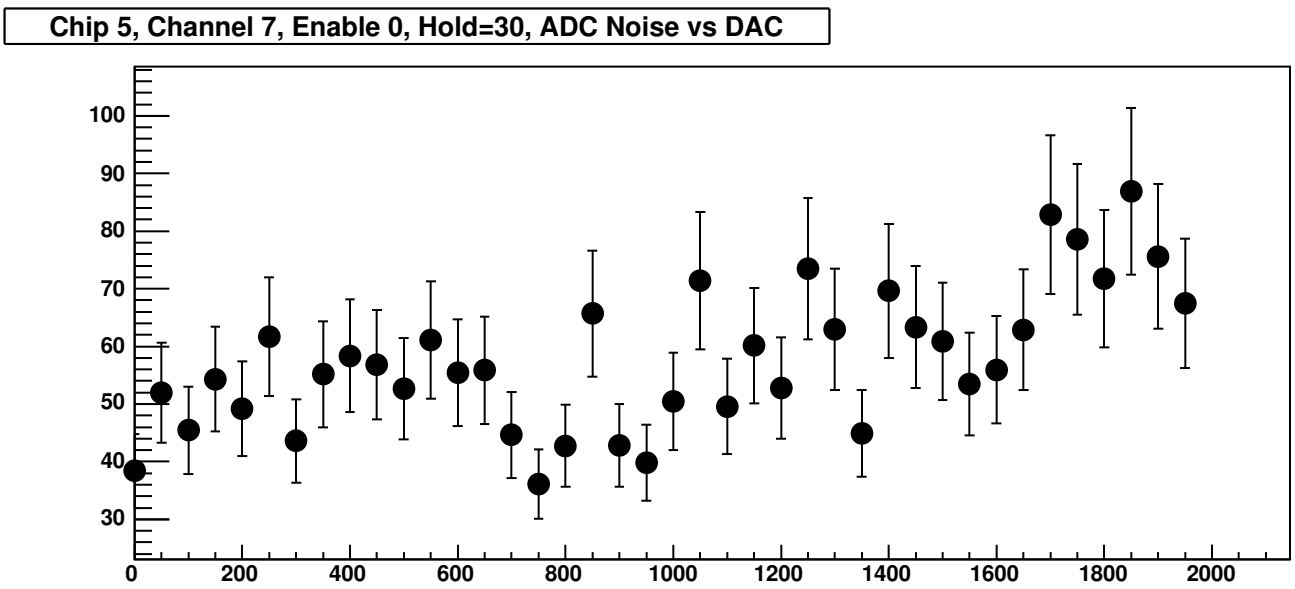

Chip 5, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

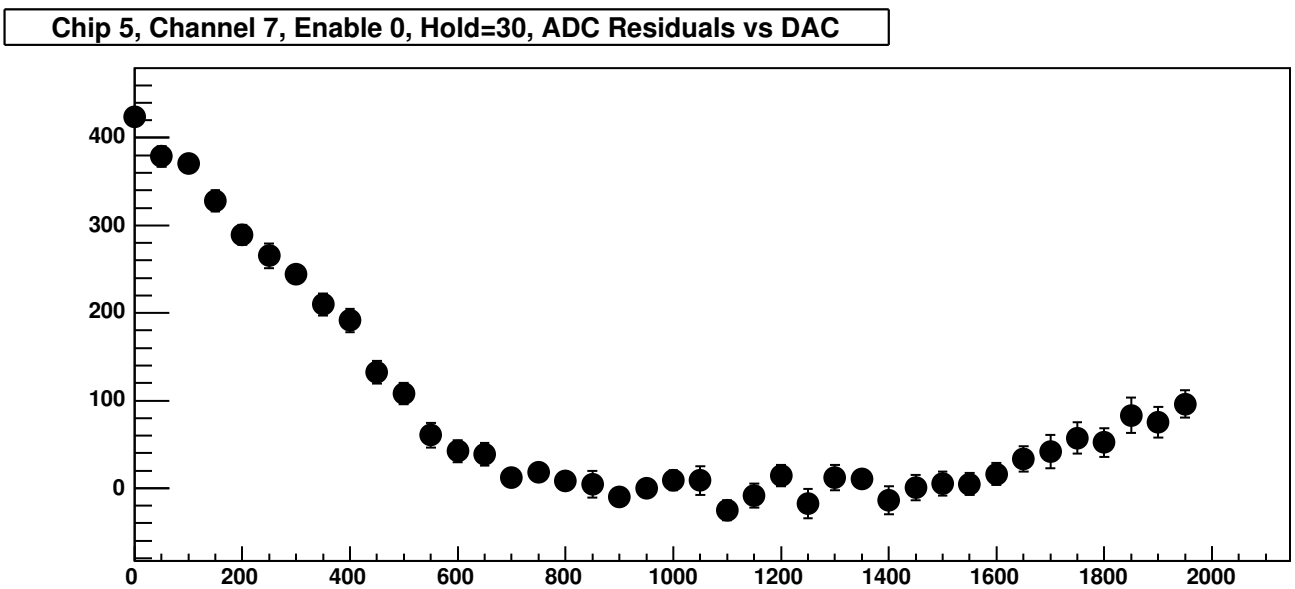

Chip 5, Channel 6, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 207 / 11
p0 2.953e+04 ± 1.634
p1 -0.005409 ± 0.001488
29545
29540
29535
29530
29525
29520
29515
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 6, Enable 5!, Hold=30, ADC Noise vs DAC
7
6
5
4
3
2
0
200
400
600
800
1000
1200
1400
1600
1800
2000

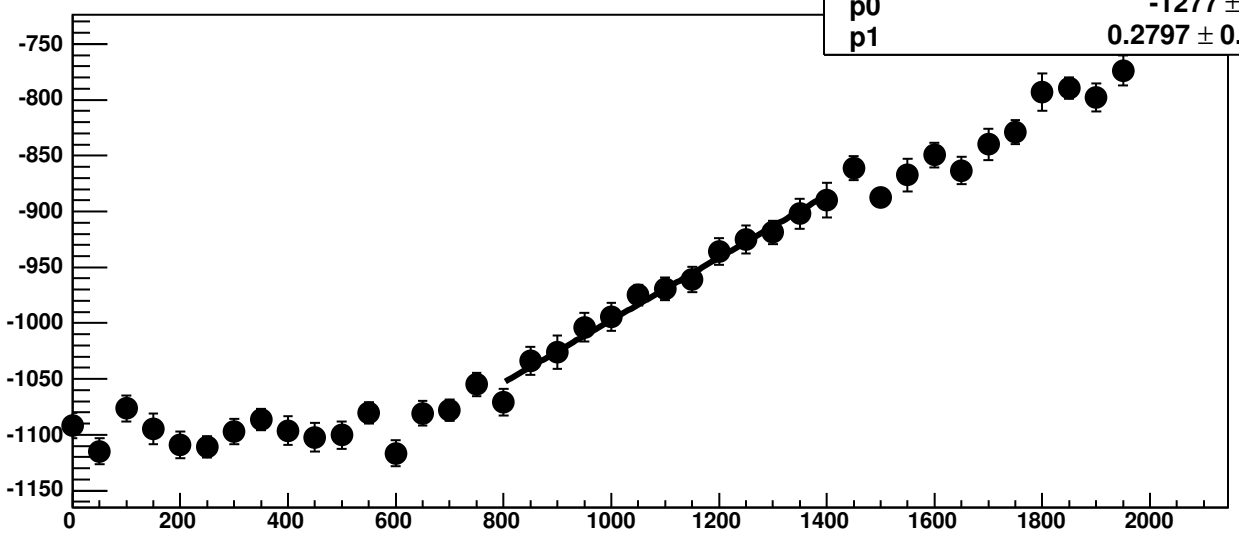

Chip 5, Channel 6, Enable 5!, Hold=30, ADC Residuals vs DAC
15
10
5
0
-5
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 12.84 / 11
p0 -1258 ± 19.22
p1 0.6248 ± 0.01776
0
-200
-400
-600
-800
0 200 400 600 800 1000 1200 1400 1600 1800 2000

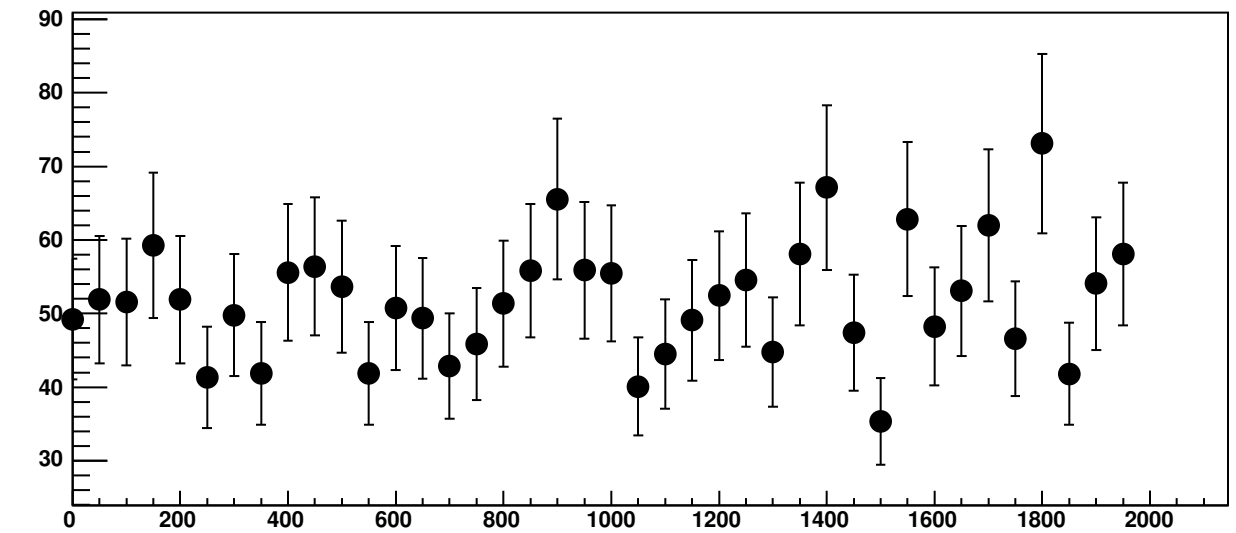

Chip 5, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0 200 400 600 800 1000 1200 1400 1600 1800 2000


Chip 5, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC
400
300
200
100
0
0 200 400 600 800 1000 1200 1400 1600 1800 2000

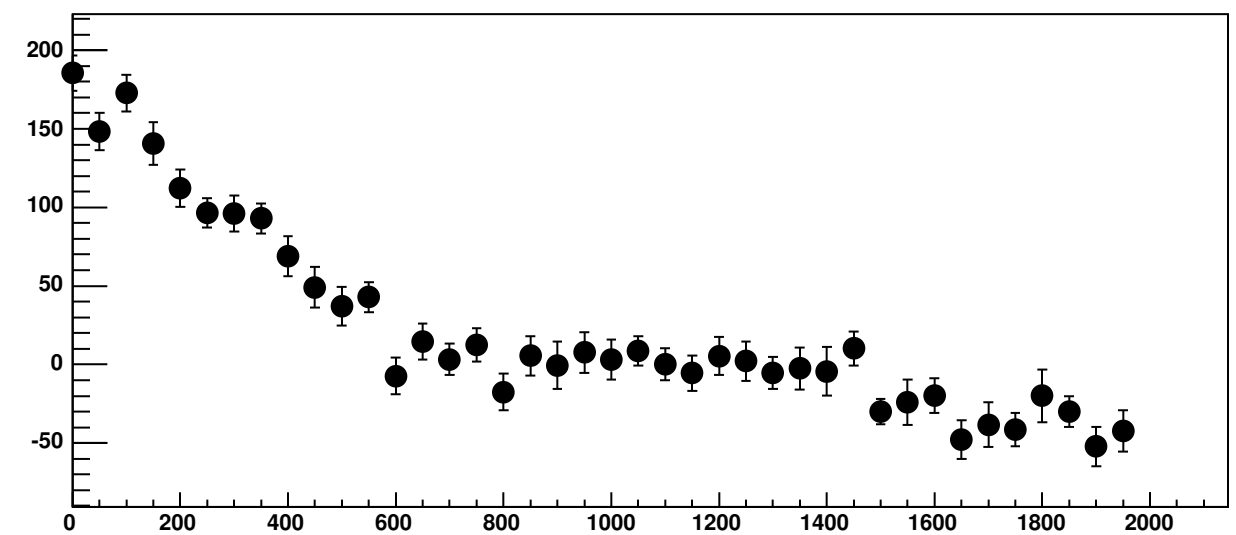
Chip 5, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC

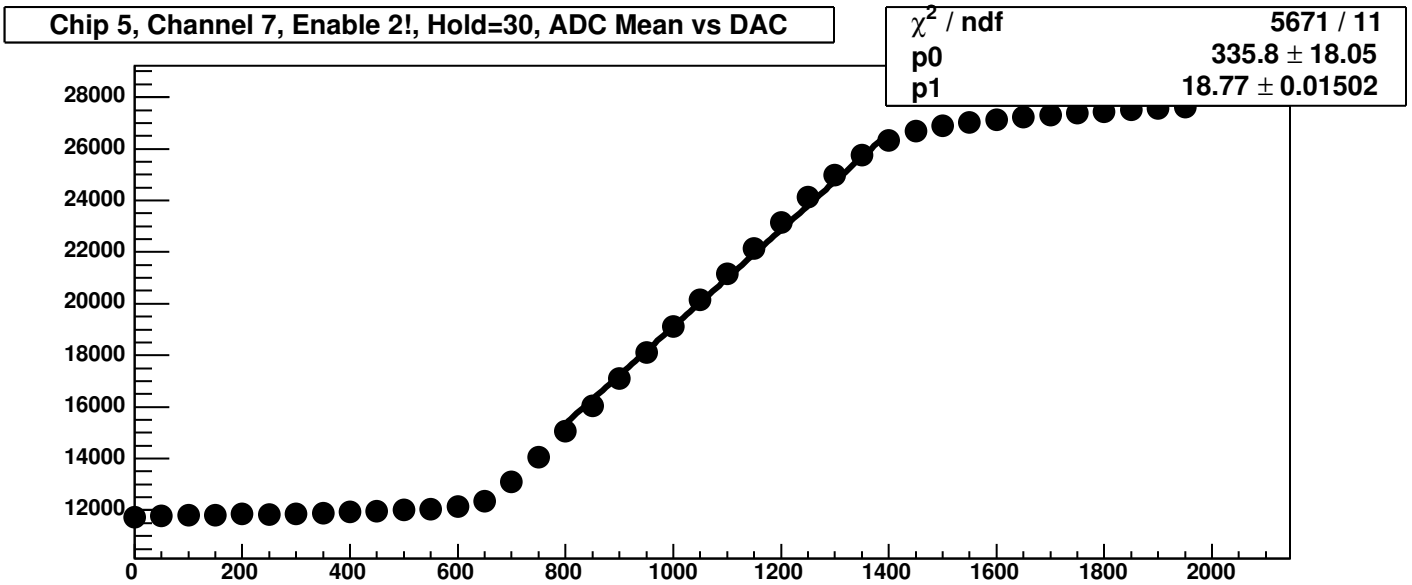


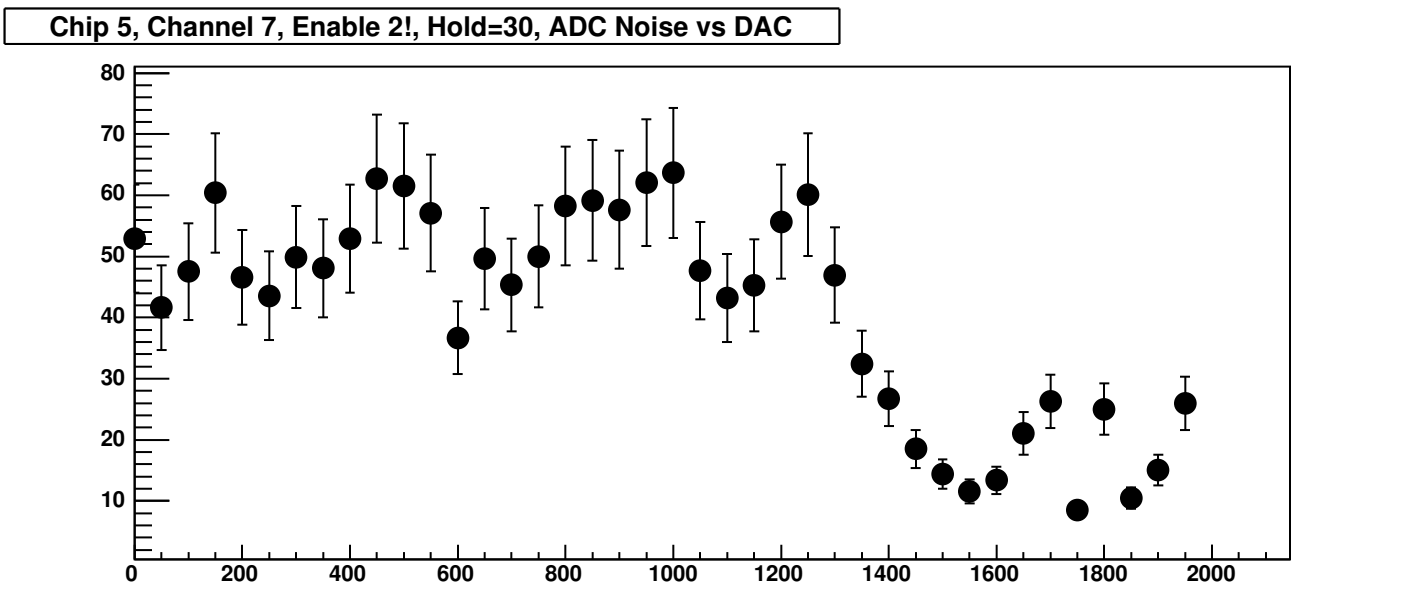
Chip 5, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC

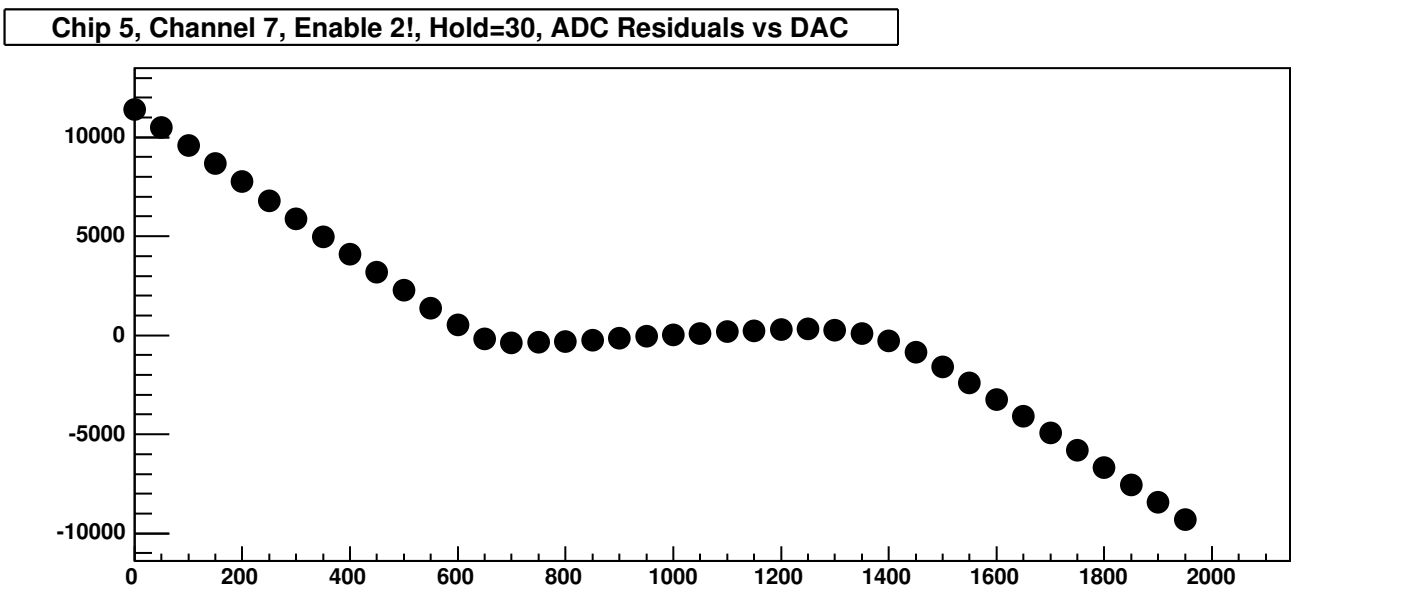


Chip 5, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC

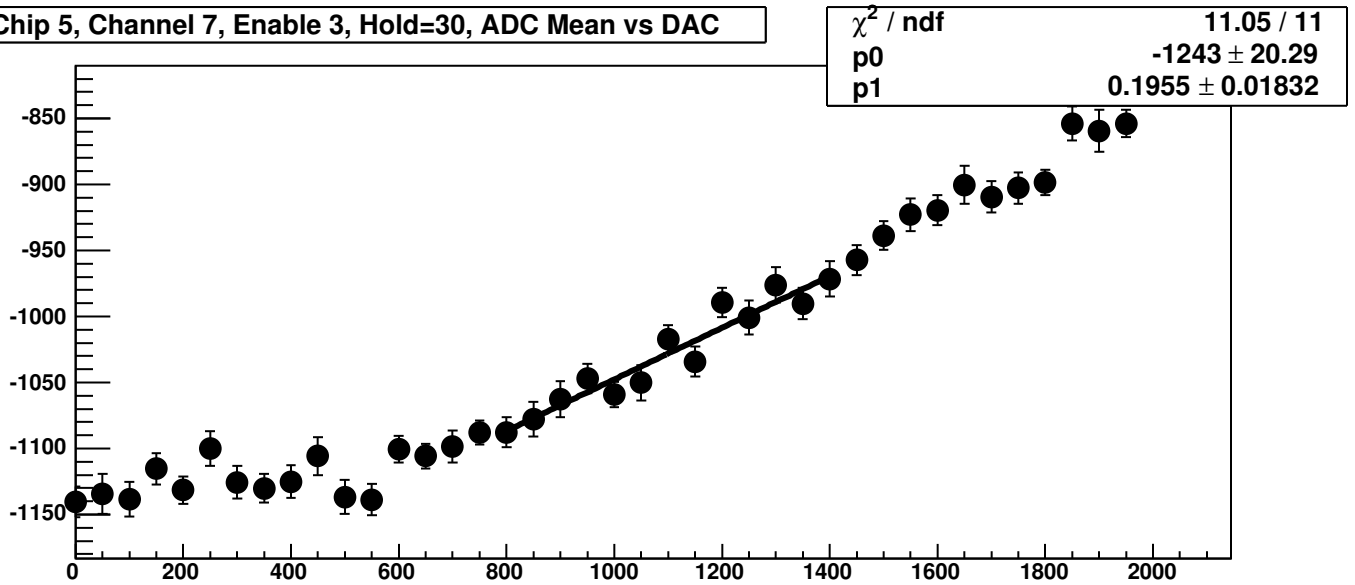



Chip 5, Channel 7, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 5671 / 11
p0 335.8 ± 18.05
p1 18.77 ± 0.01502
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

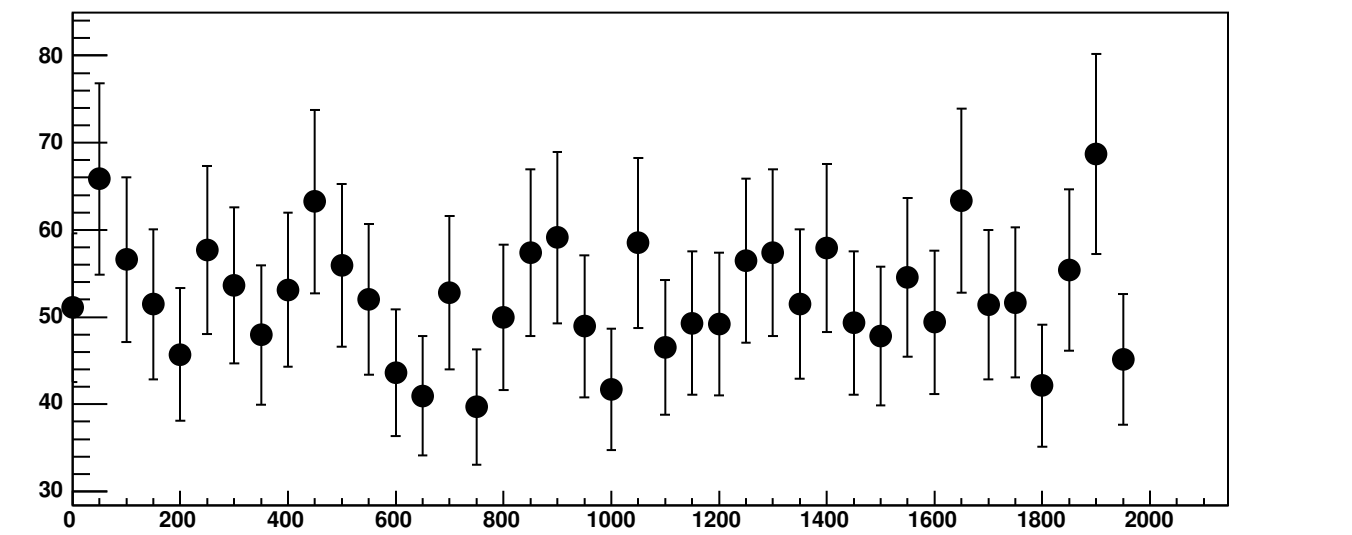

Chip 5, Channel 7, Enable 2!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 7, Enable 2!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

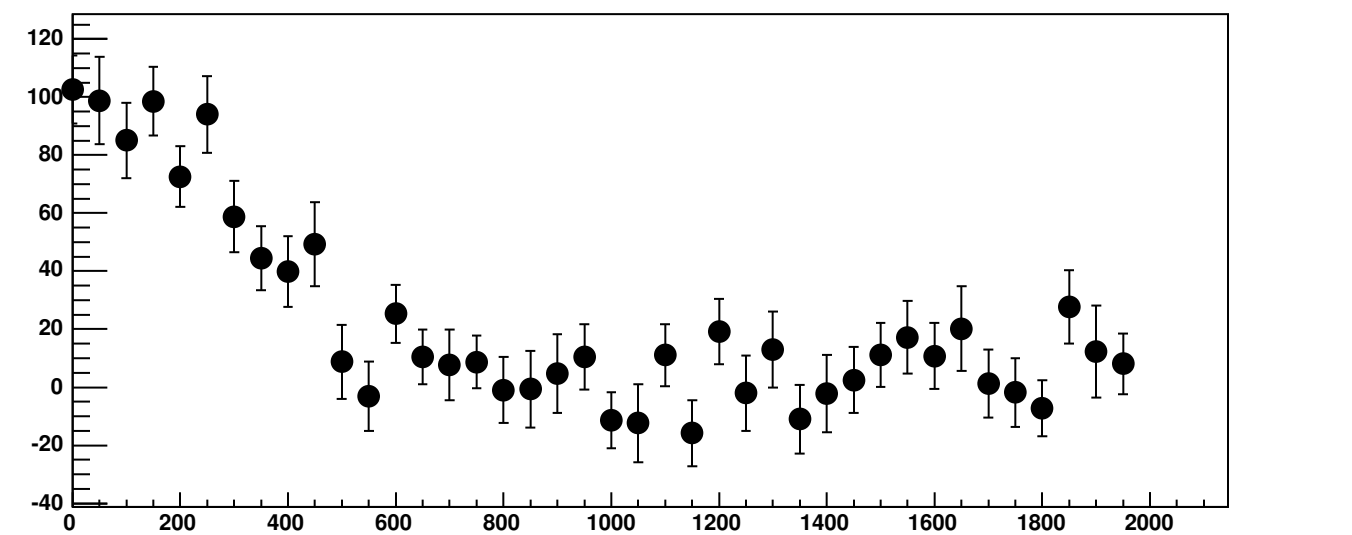
Chip 5, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC



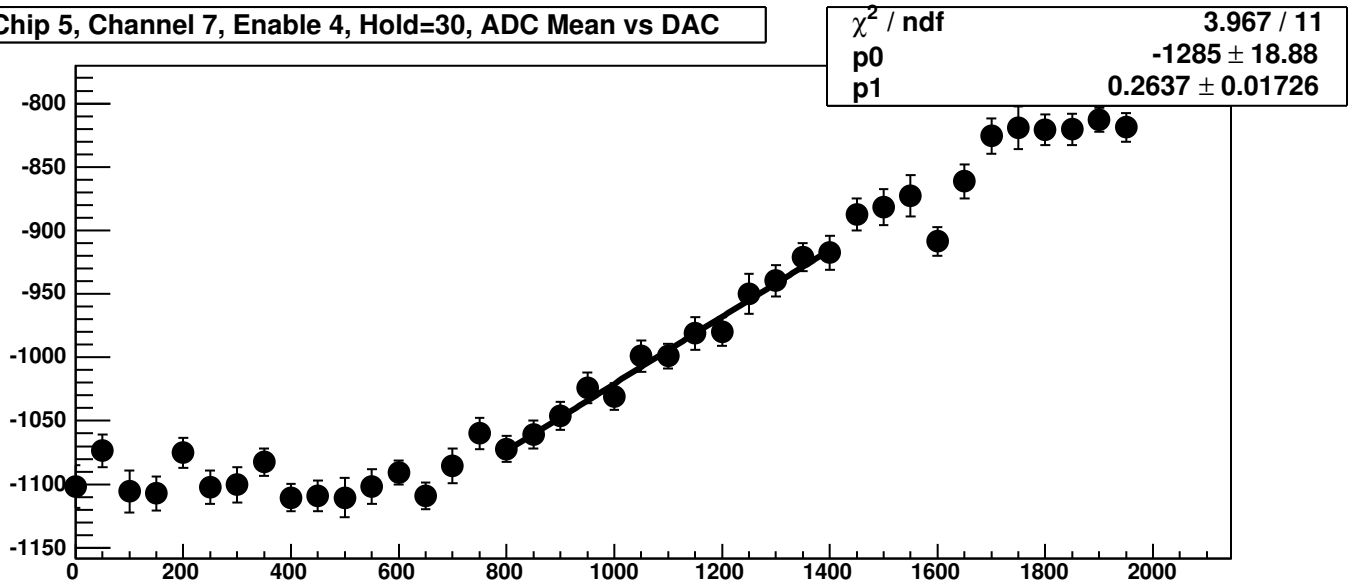
Chip 5, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC



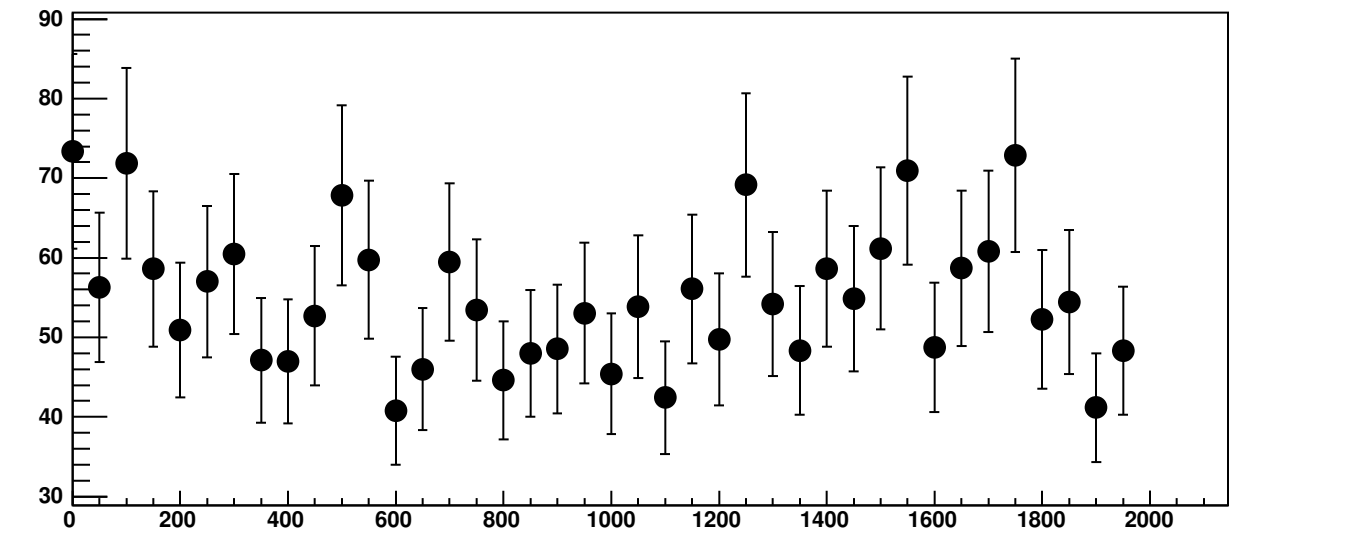
Chip 5, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC



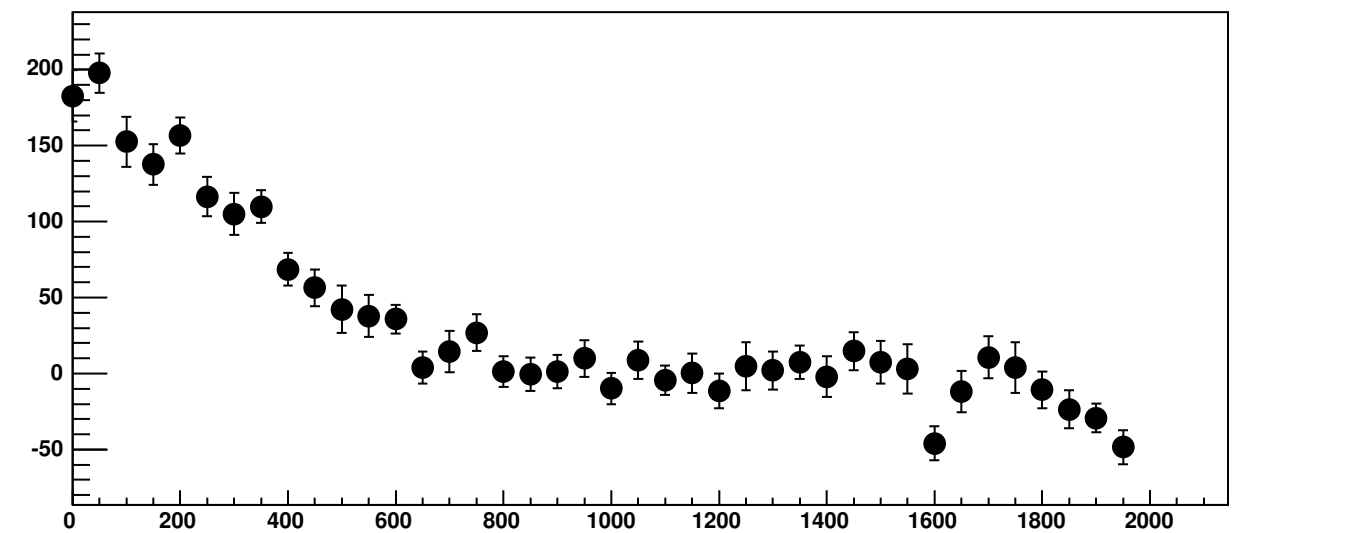
Chip 5, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC

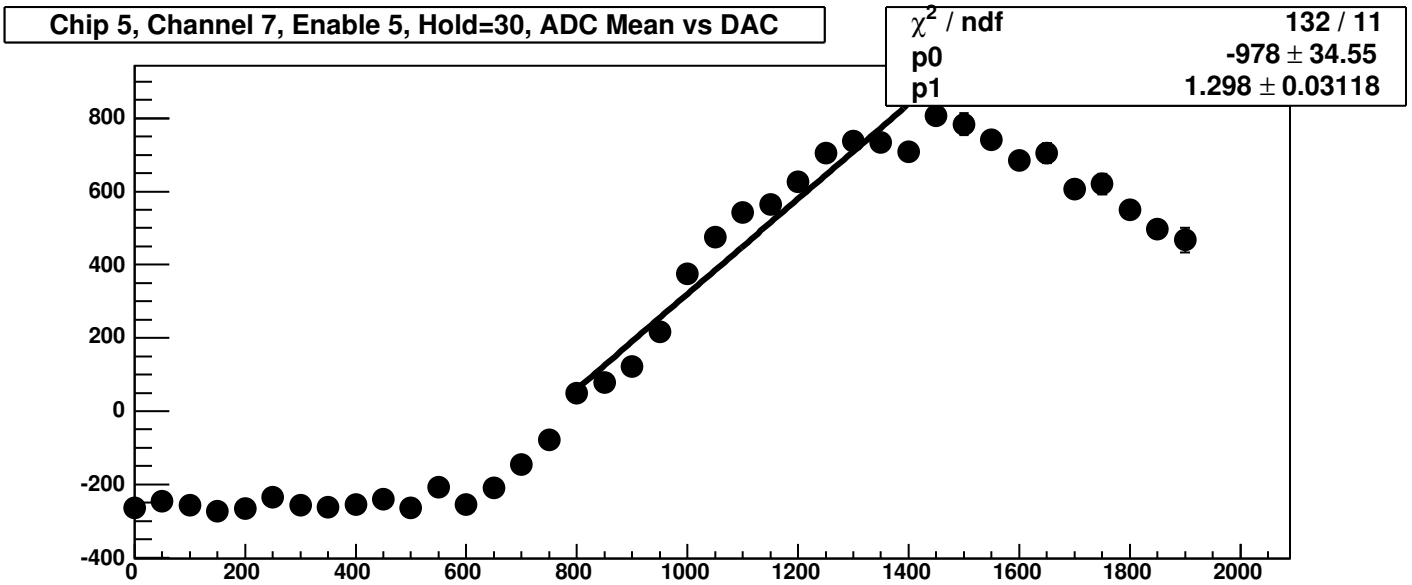


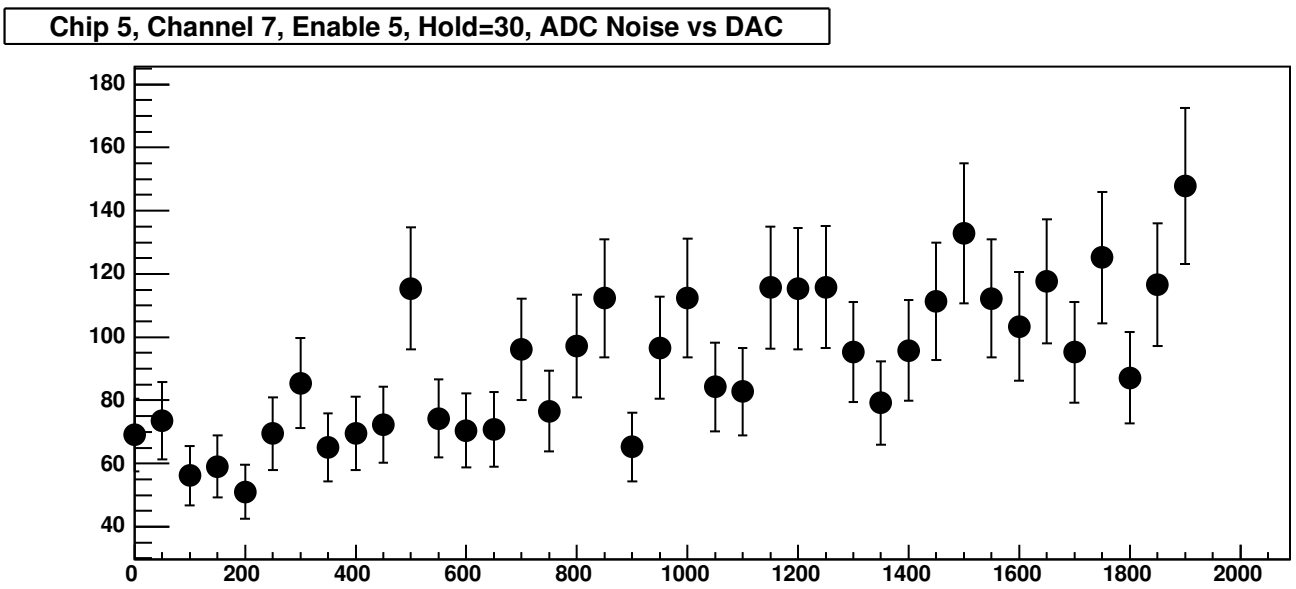
Chip 5, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC

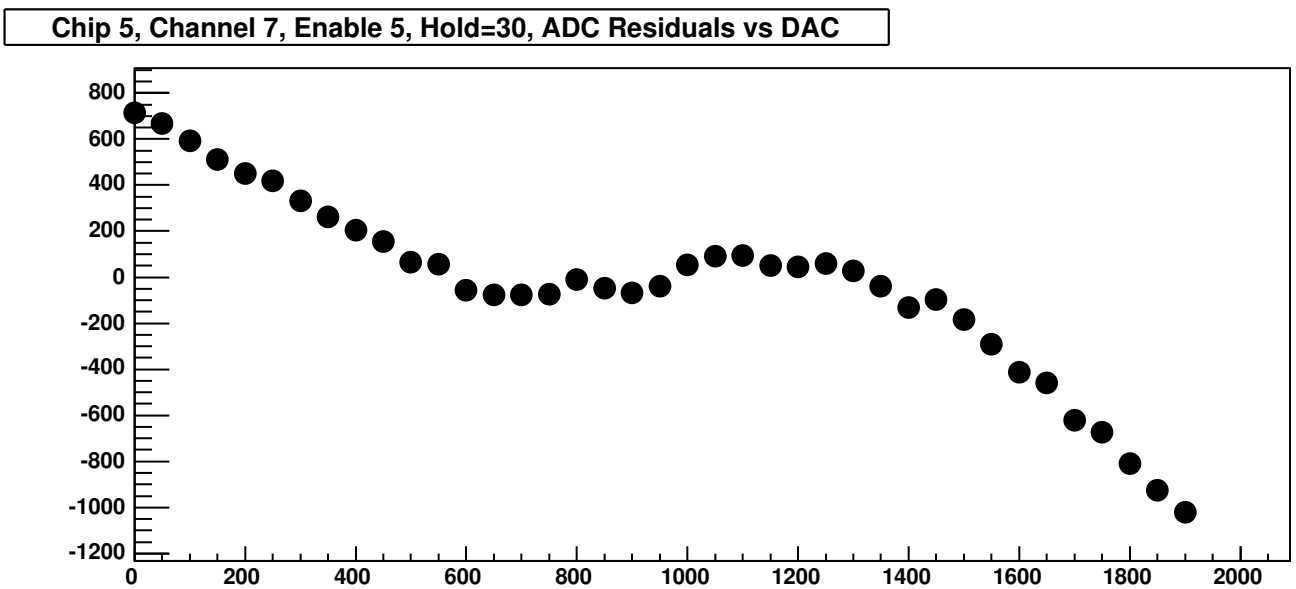


Chip 5, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC

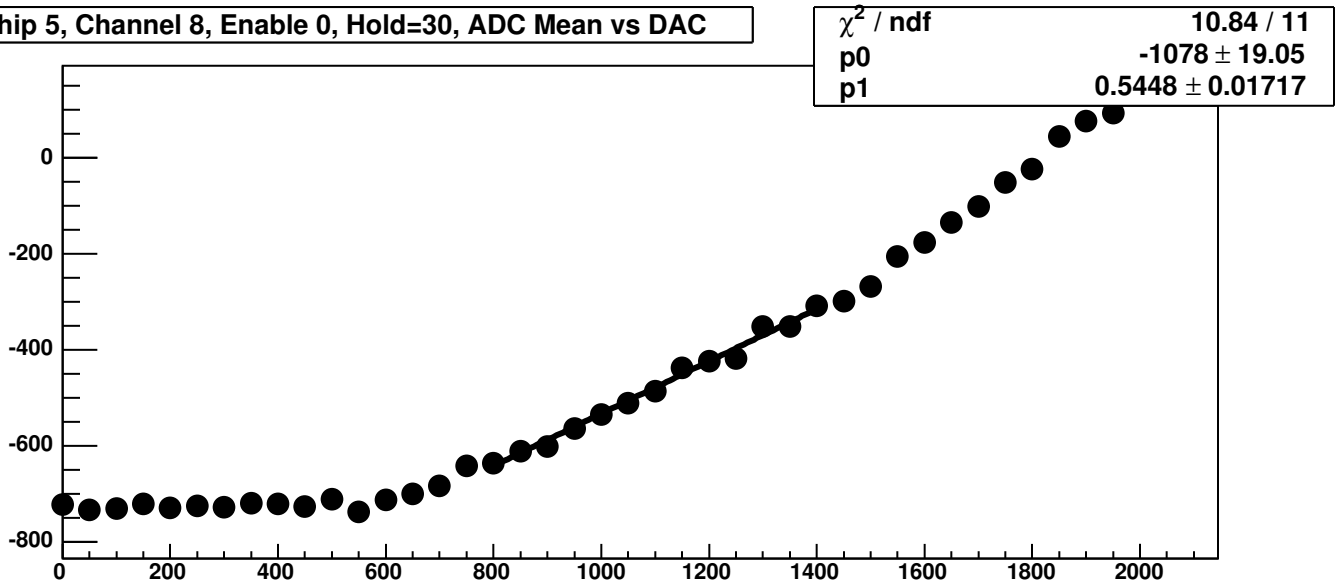



Chip 5, Channel 7, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 132 / 11
p0 -978 ± 34.55
p1 1.298 ± 0.03118
800
600
400
200
0
-200
-400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

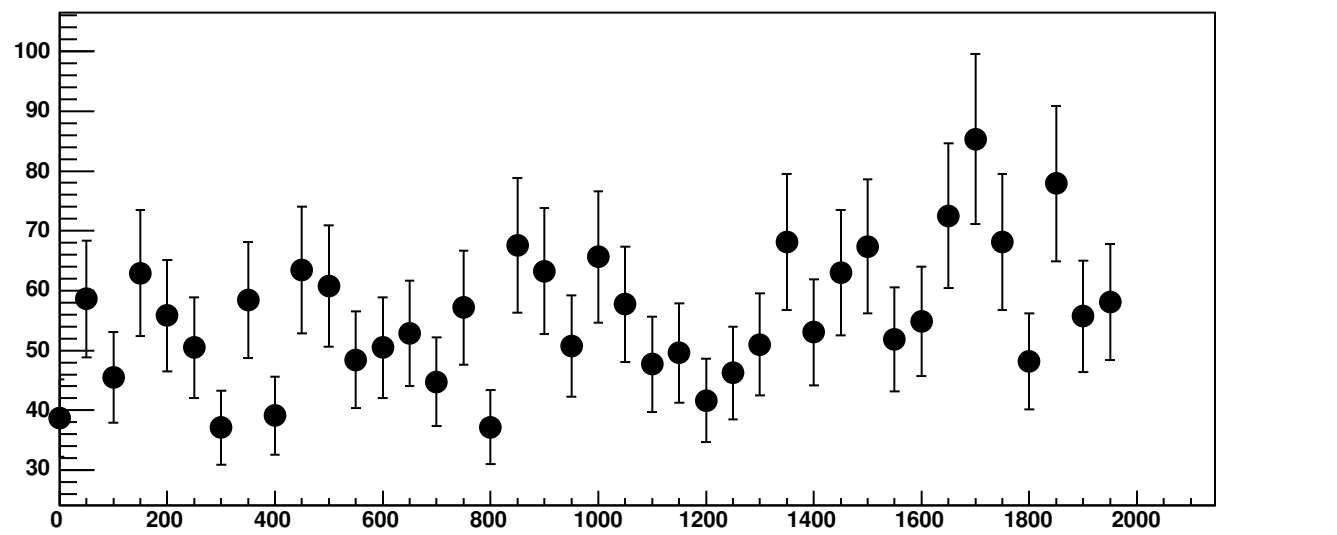

Chip 5, Channel 7, Enable 5, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 7, Enable 5, Hold=30, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

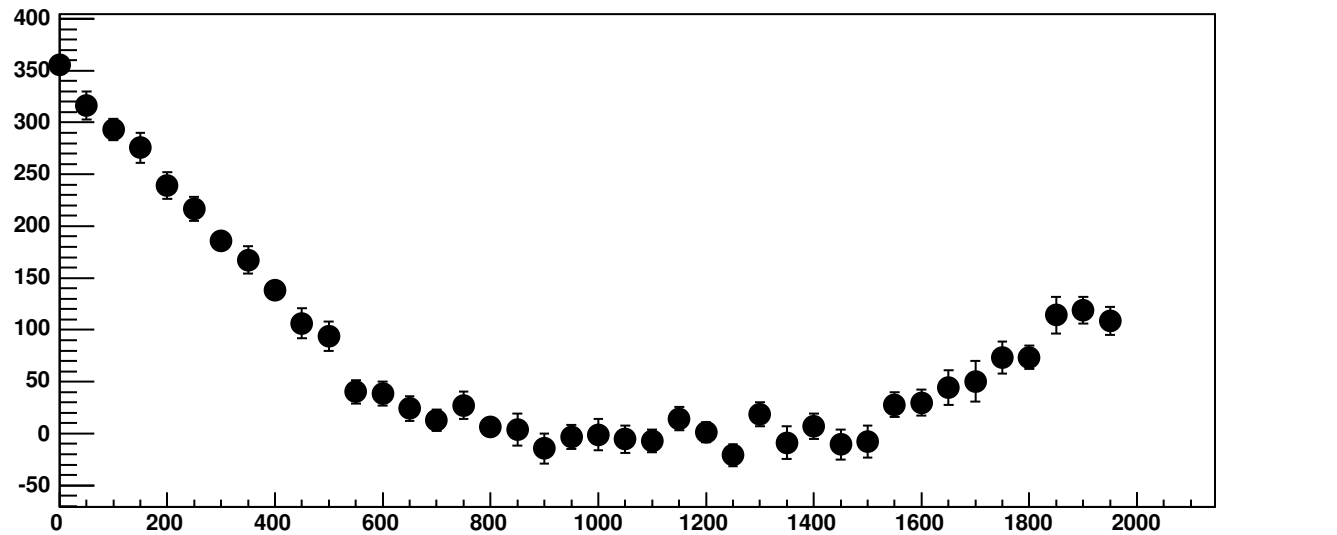
Chip 5, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC

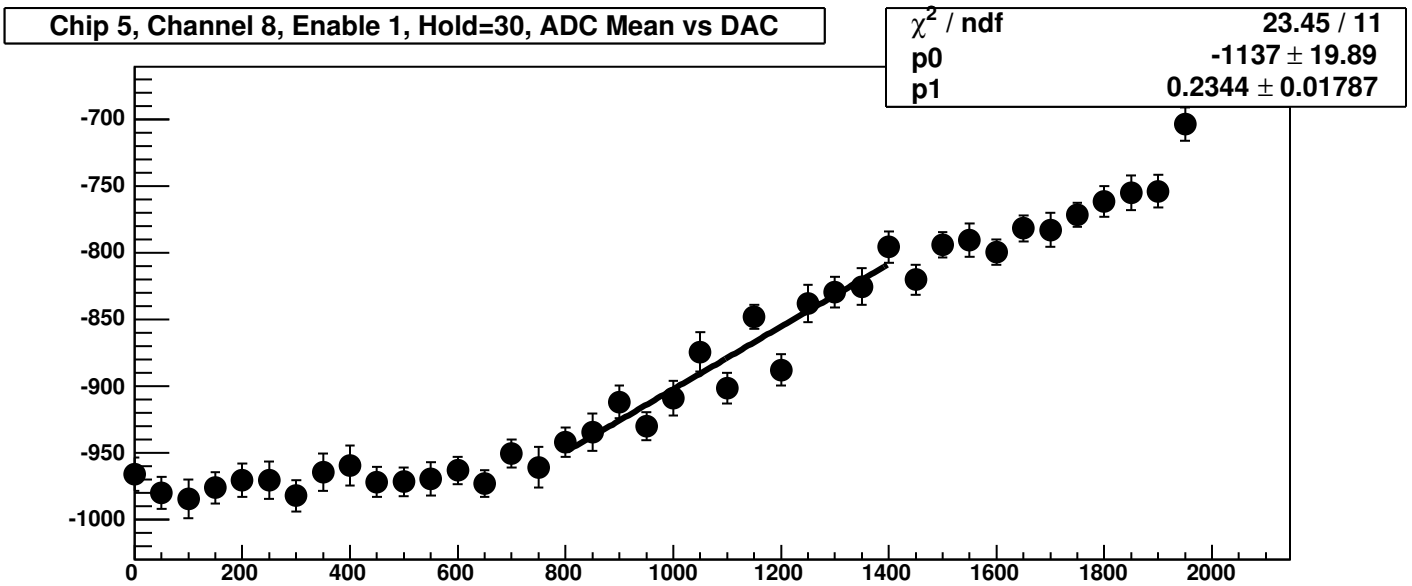


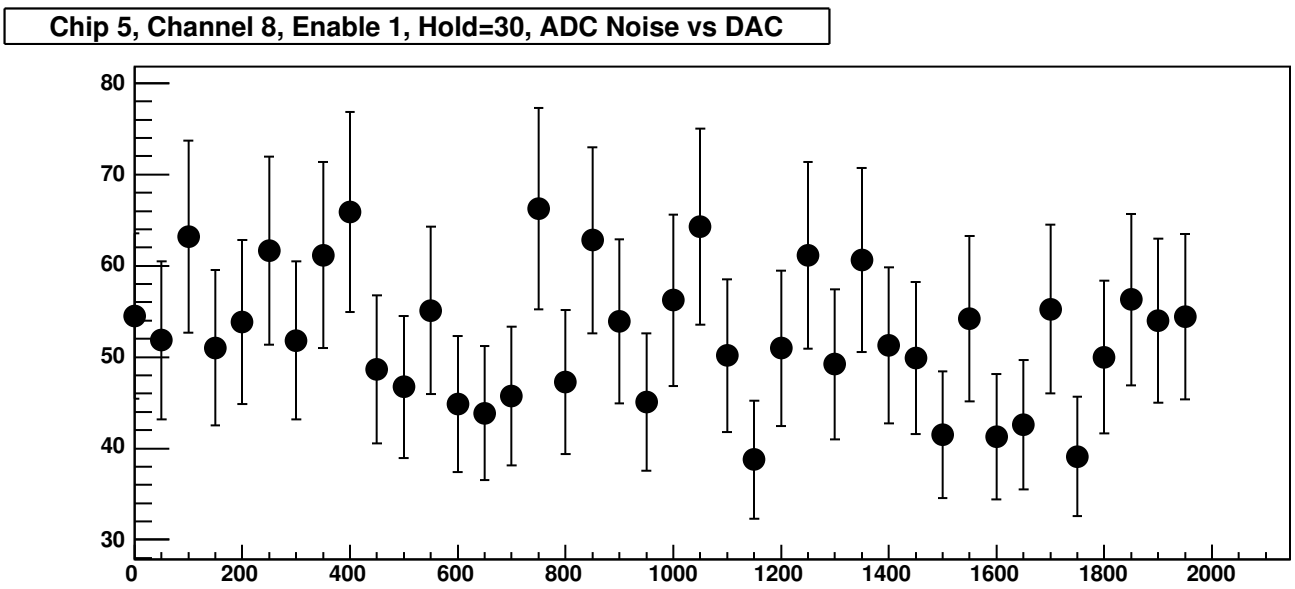
Chip 5, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC

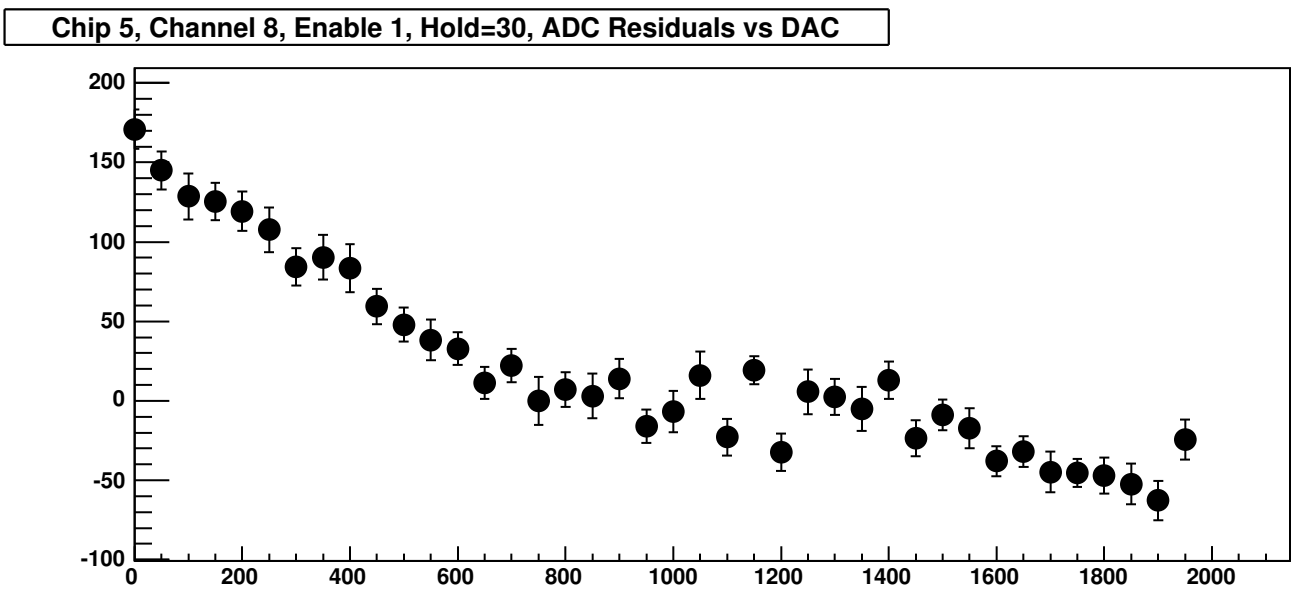


Chip 5, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC

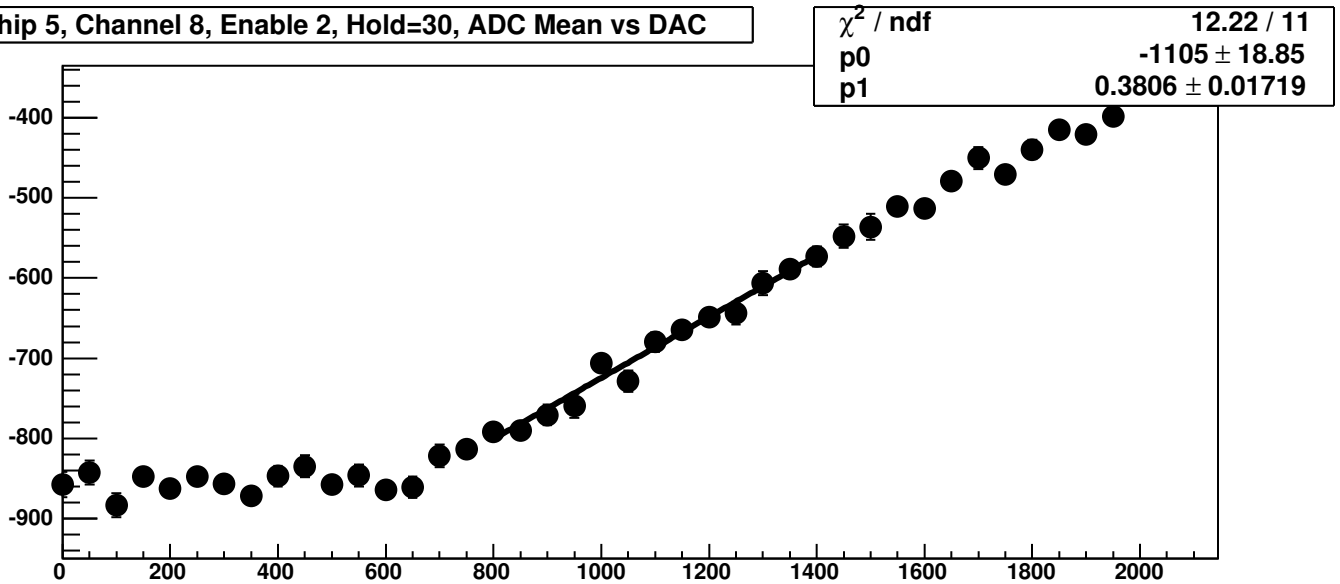



Chip 5, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 23.45 / 11
p0 -1137 ± 19.89
p1 0.2344 ± 0.01787
-700
-750
-800
-850
-900
-950
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

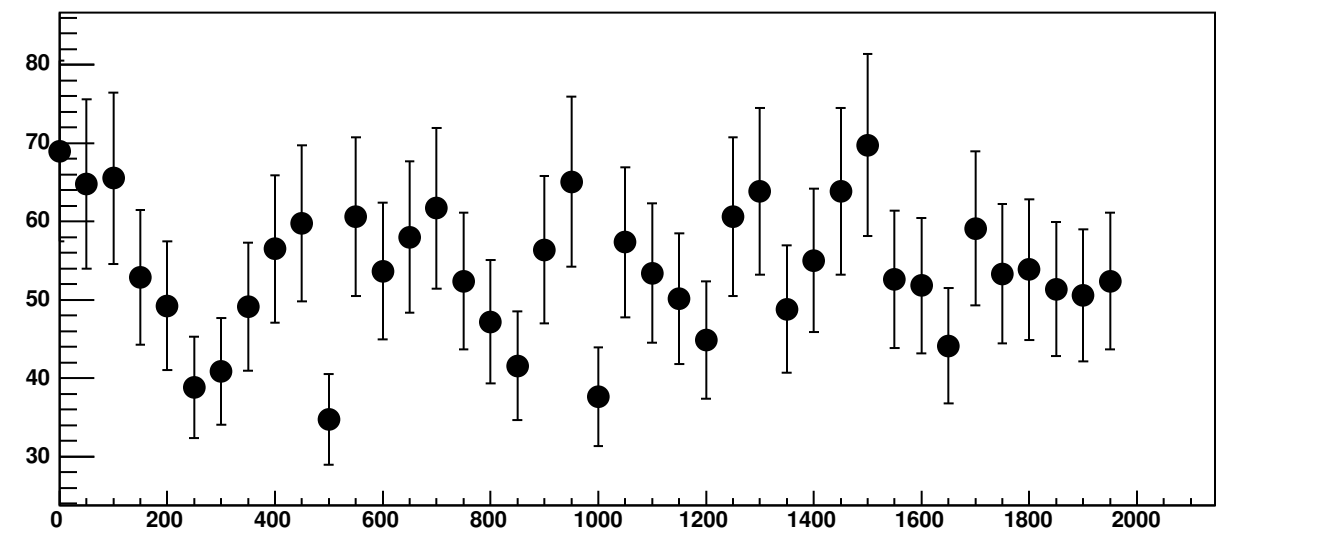

Chip 5, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

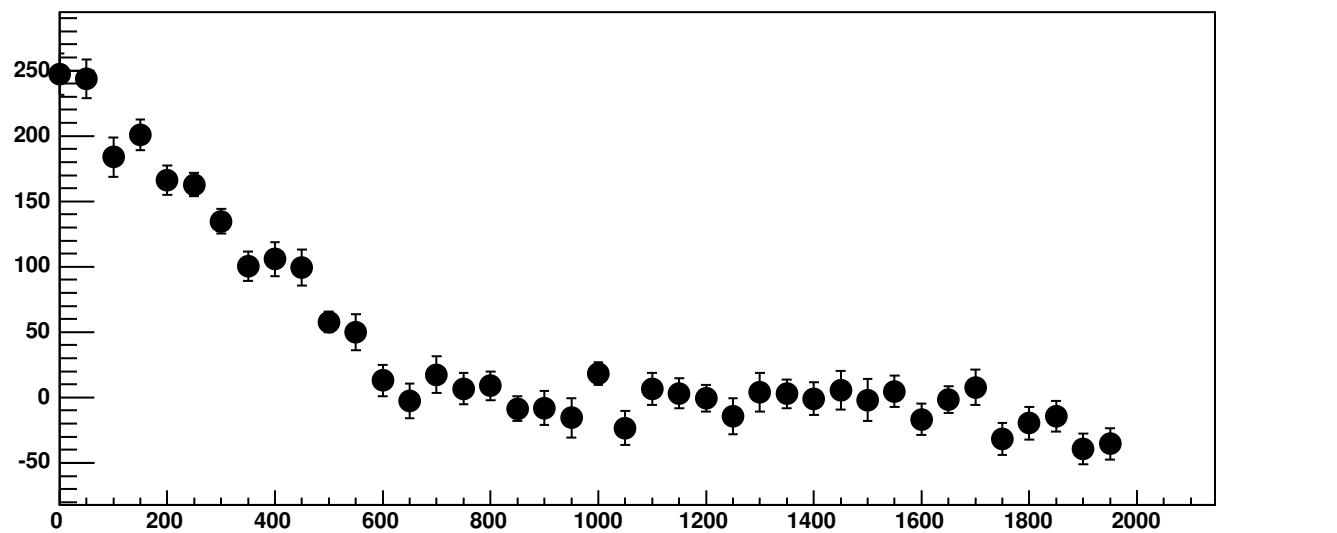
Chip 5, Channel 8, Enable 2, Hold=30, ADC Mean vs DAC

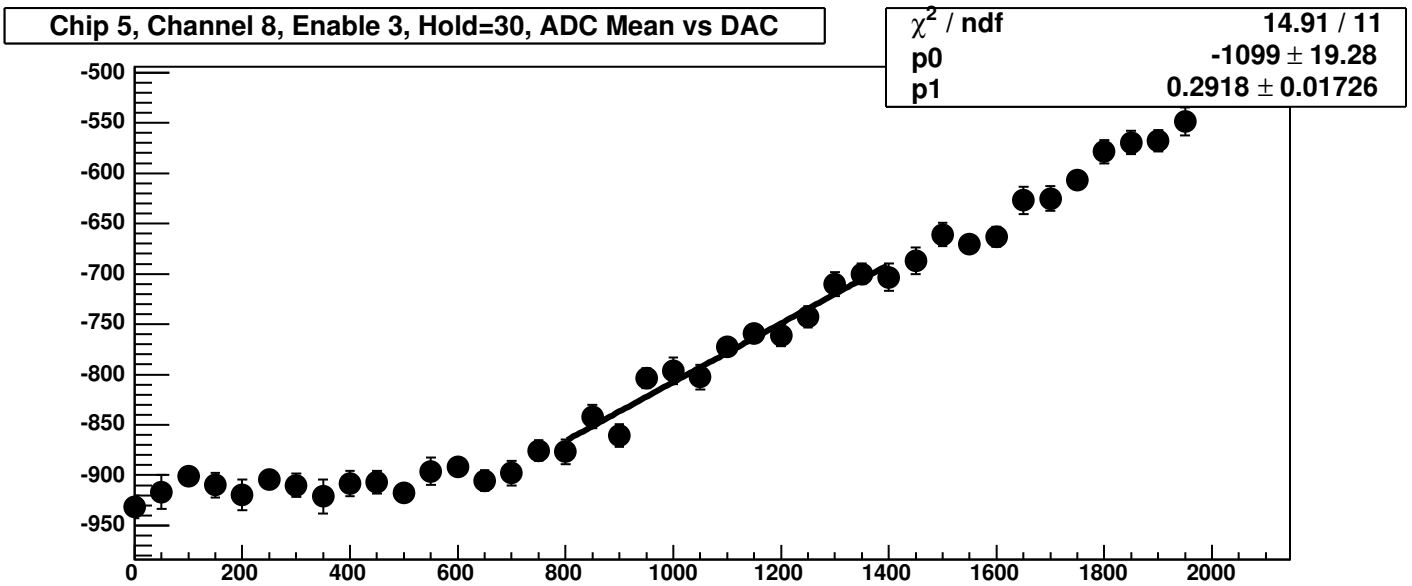


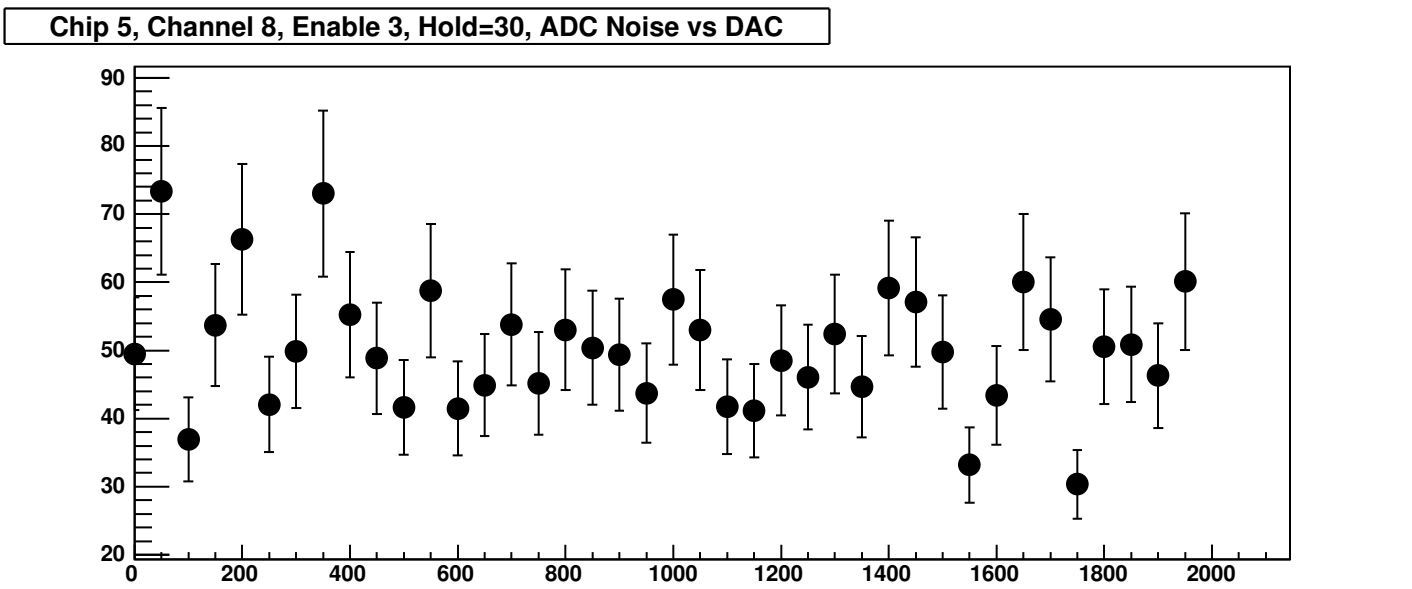
Chip 5, Channel 8, Enable 2, Hold=30, ADC Noise vs DAC

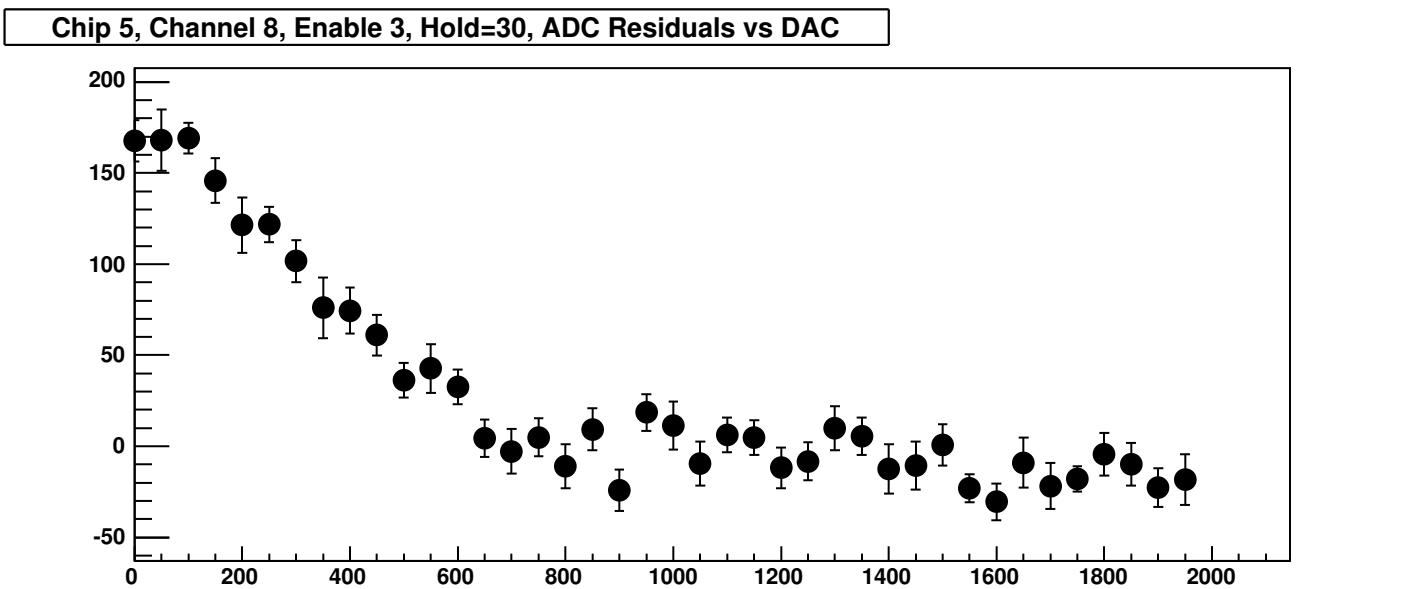


Chip 5, Channel 8, Enable 2, Hold=30, ADC Residuals vs DAC

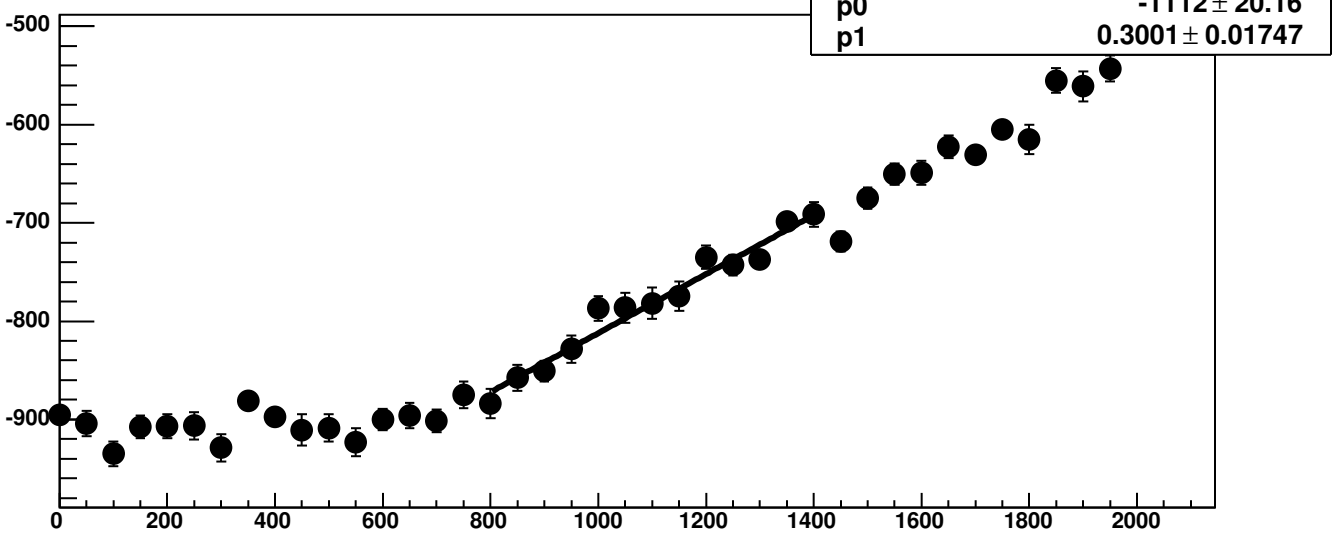



Chip 5, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 14.91 / 11
p0 -1099 ± 19.28
p1 0.2918 ± 0.01726
-500
-550
-600
-650
-700
-750
-800
-850
-900
-950
0
200
400
600
800
1000
1200
1400
1600
1800
2000

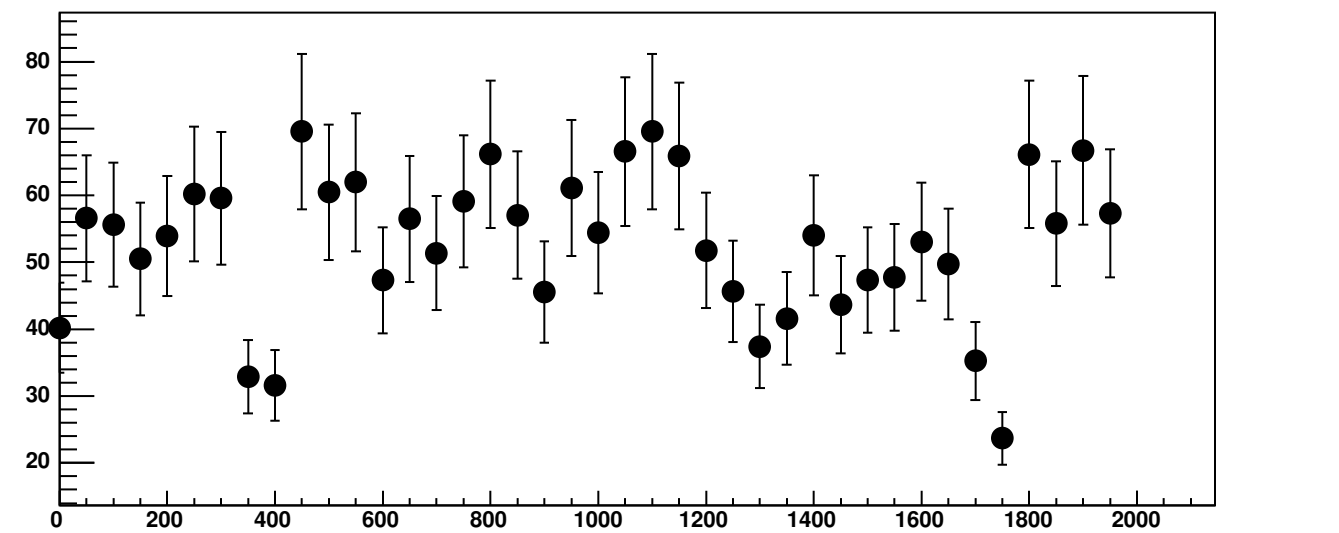

Chip 5, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

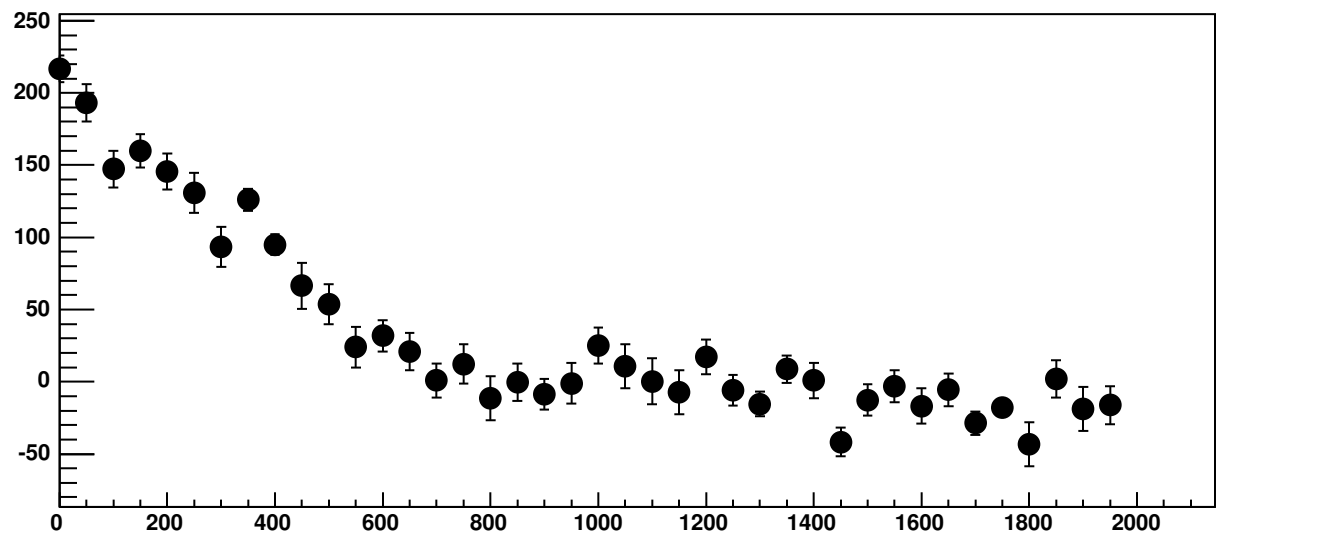
Chip 5, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC



Chip 5, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC

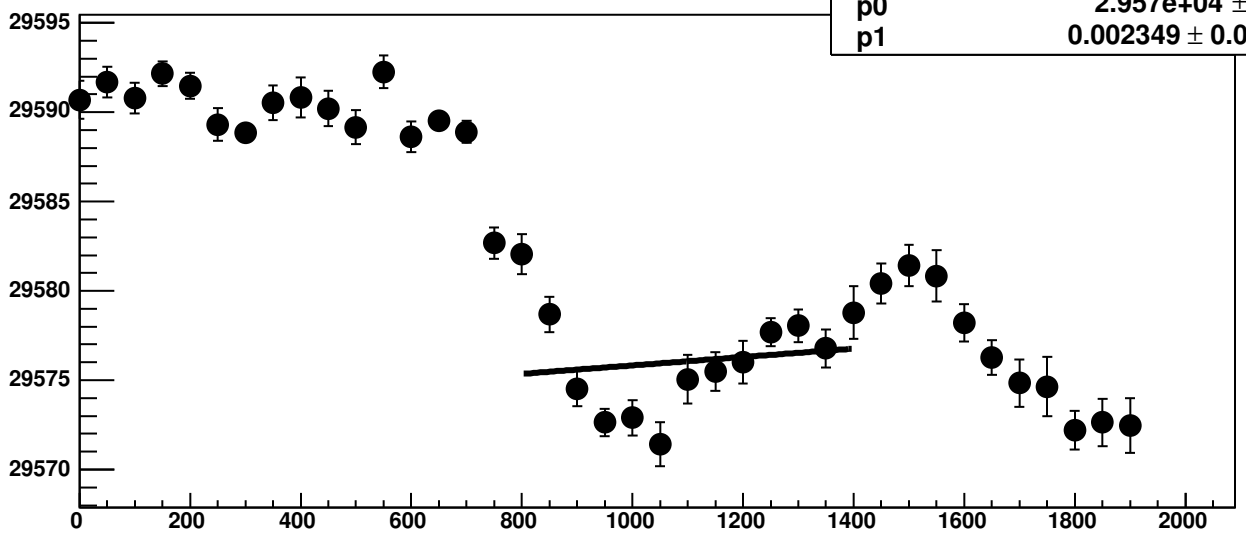


Chip 5, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC

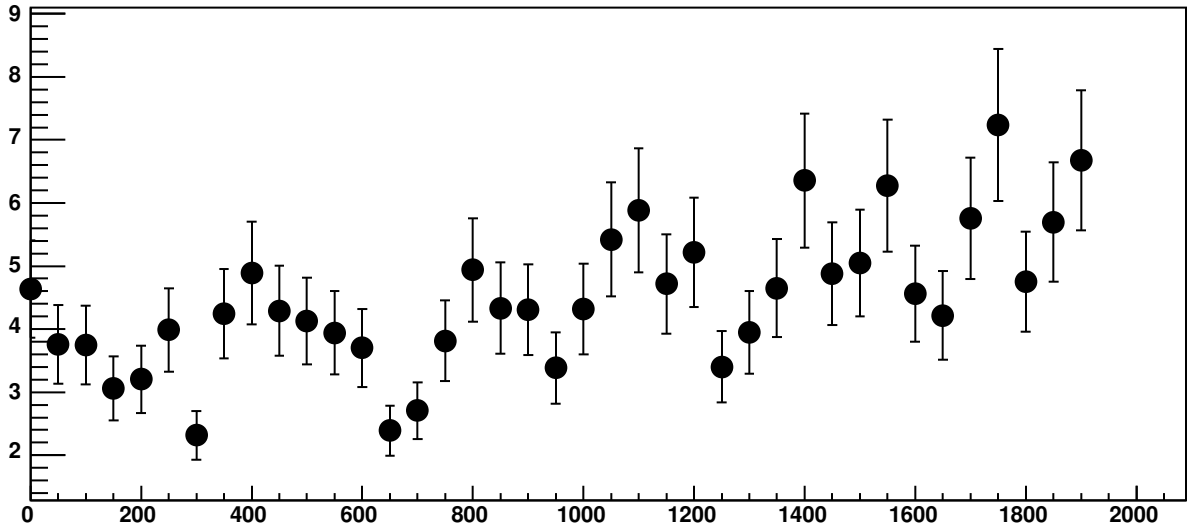


Chip 5, Channel 8, Enable 5!, Hold=30, ADC Mean vs DAC

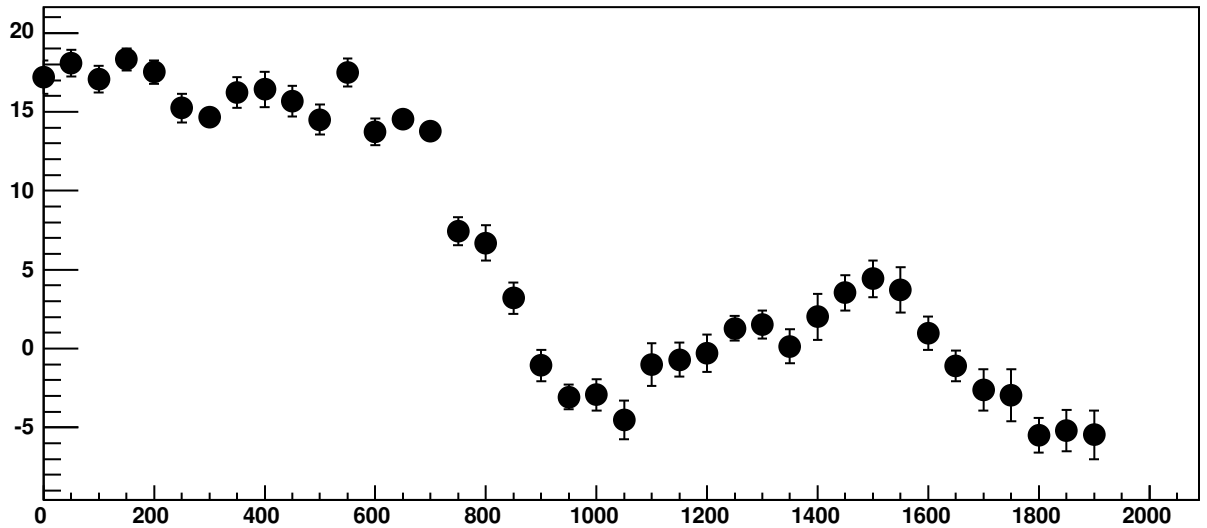
χ^2 / ndf	92.65 / 11
p0	2.957e+04 ± 1.719
p1	0.002349 ± 0.001554

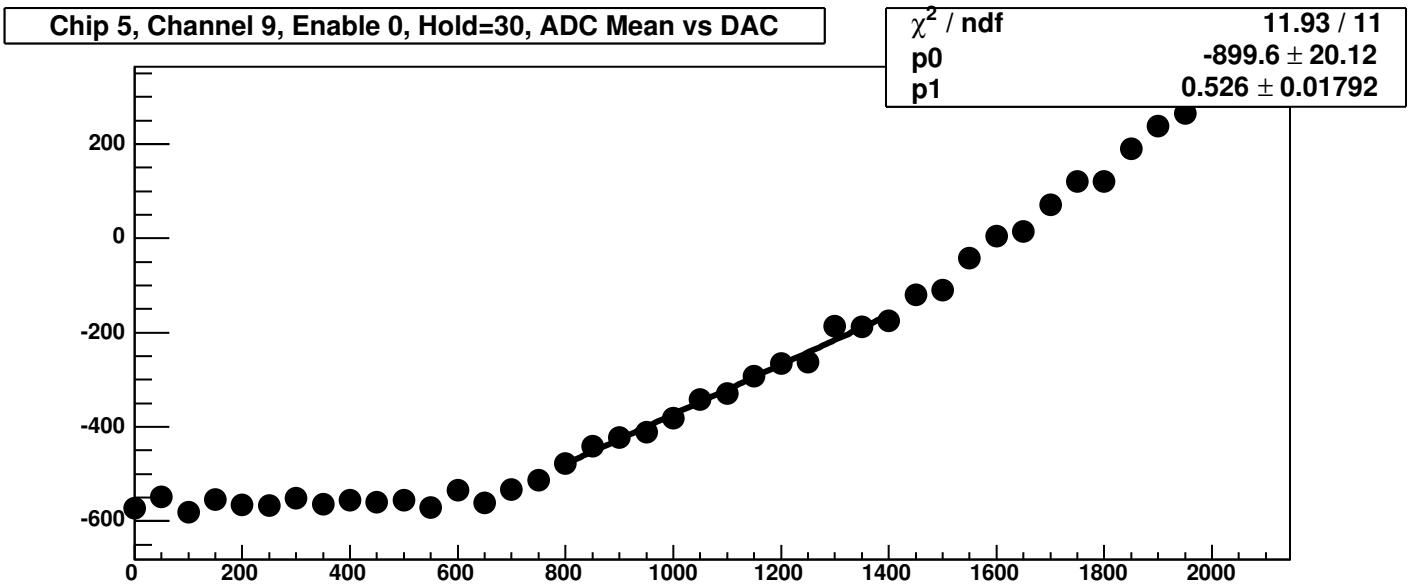


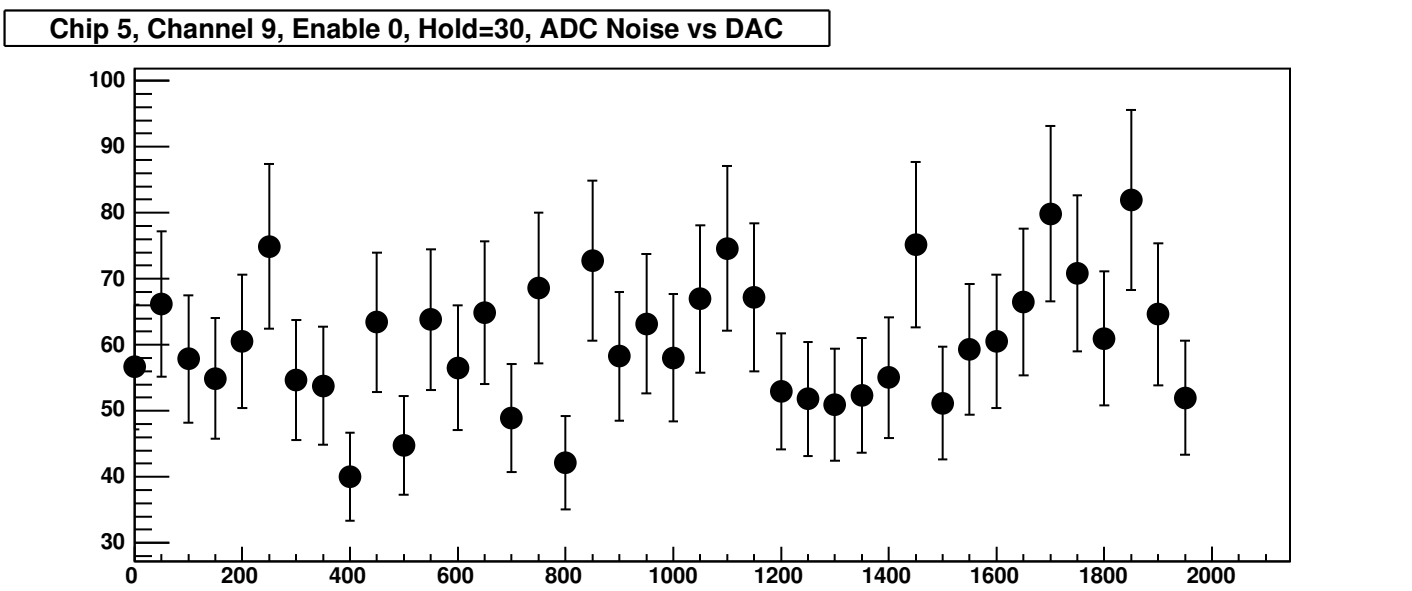
Chip 5, Channel 8, Enable 5!, Hold=30, ADC Noise vs DAC

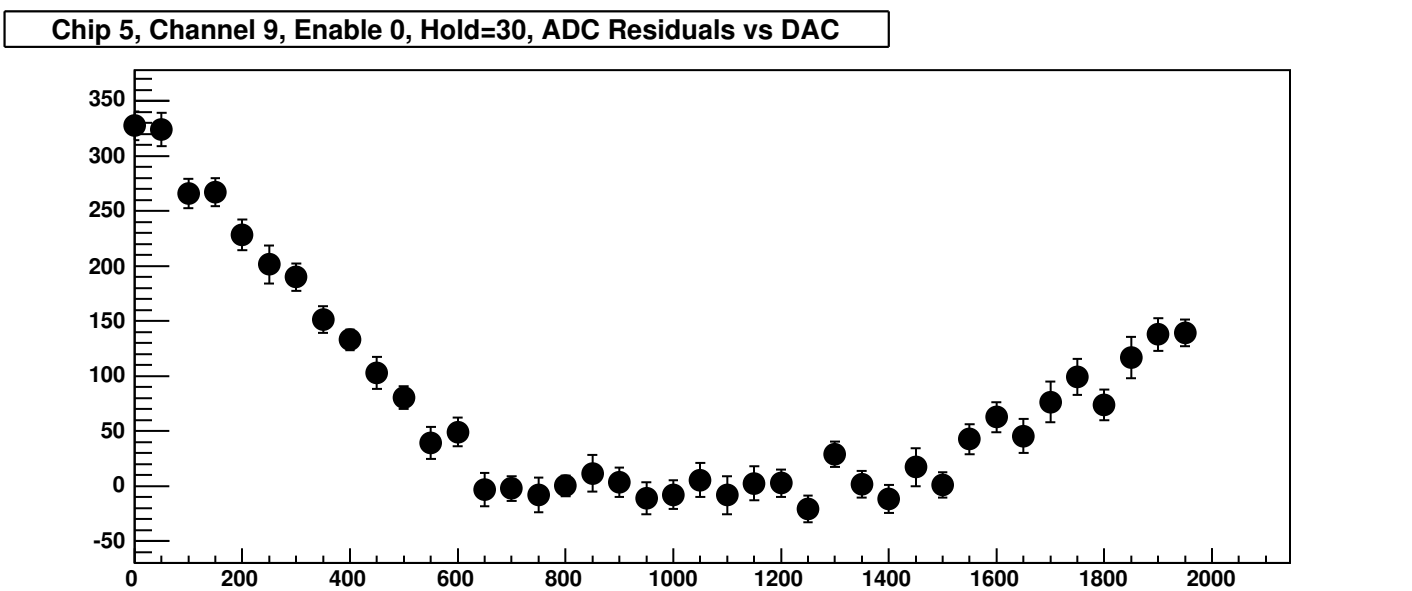


Chip 5, Channel 8, Enable 5!, Hold=30, ADC Residuals vs DAC

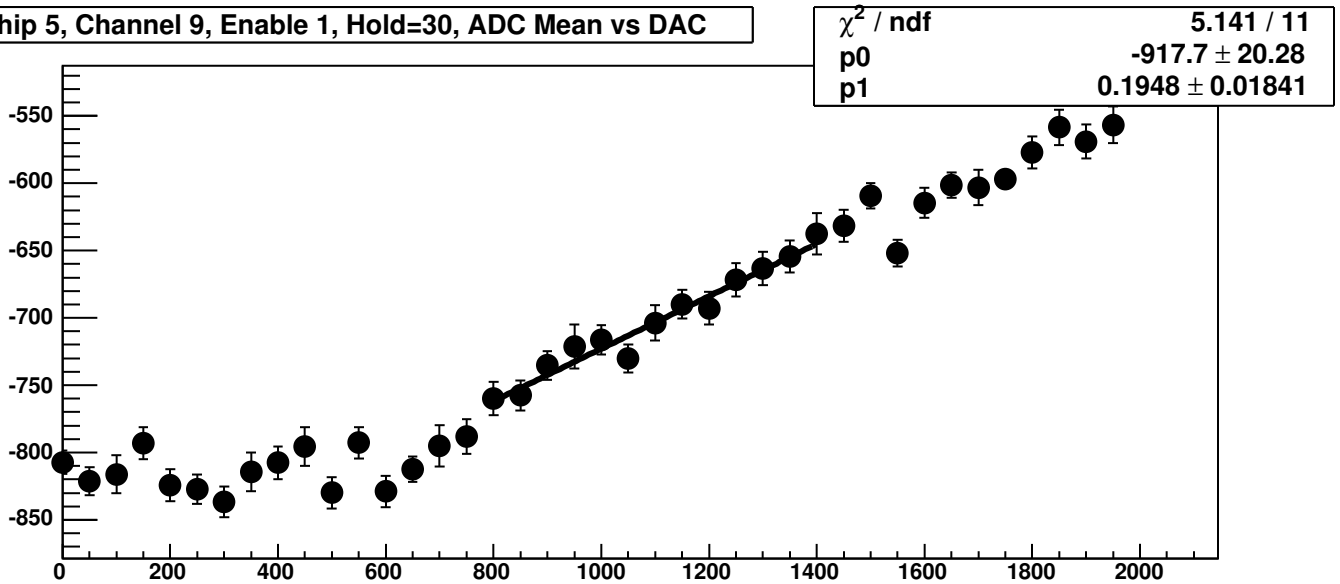



Chip 5, Channel 9, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 11.93 / 11
p0 -899.6 ± 20.12
p1 0.526 ± 0.01792
200
0
-200
-400
-600
0 200 400 600 800 1000 1200 1400 1600 1800 2000

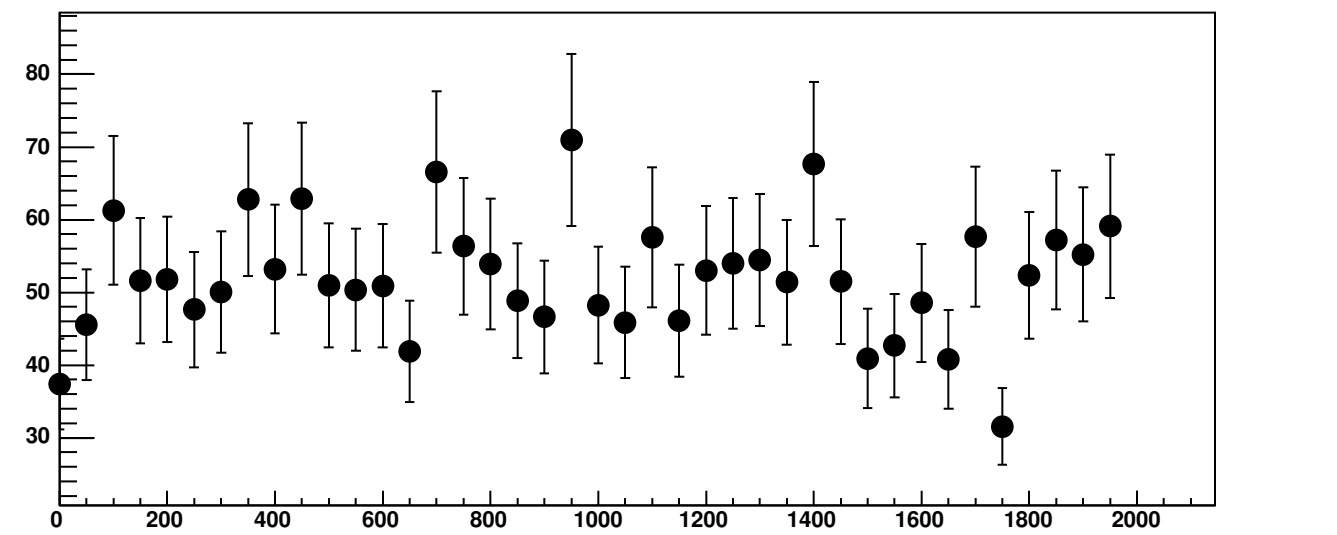

Chip 5, Channel 9, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0 200 400 600 800 1000 1200 1400 1600 1800 2000


Chip 5, Channel 9, Enable 0, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0 200 400 600 800 1000 1200 1400 1600 1800 2000

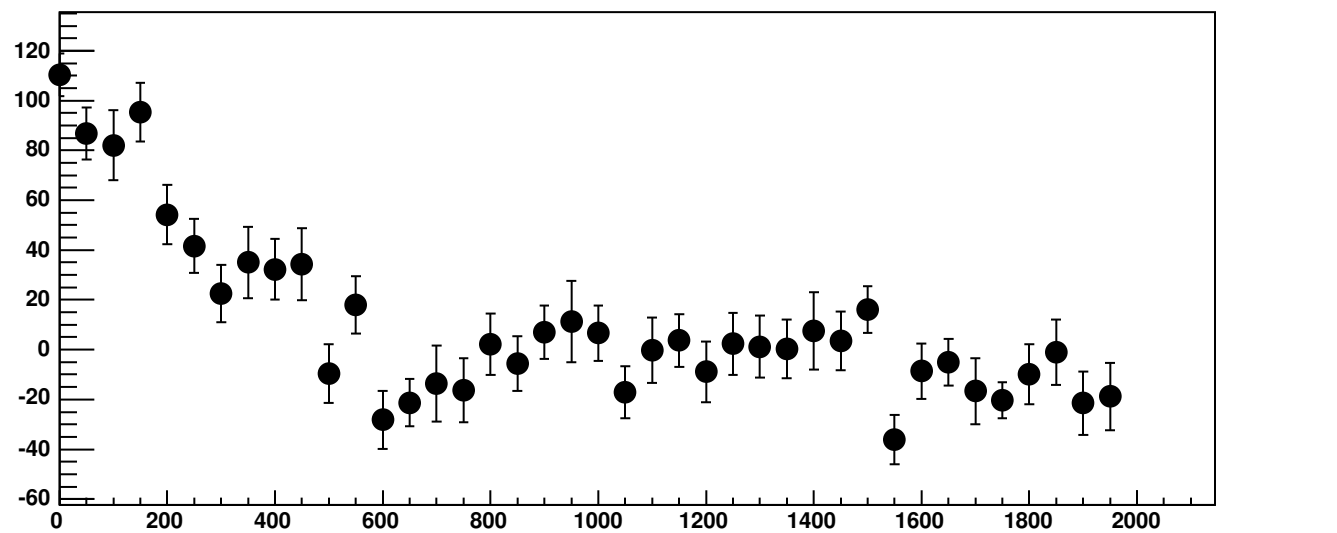
Chip 5, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC

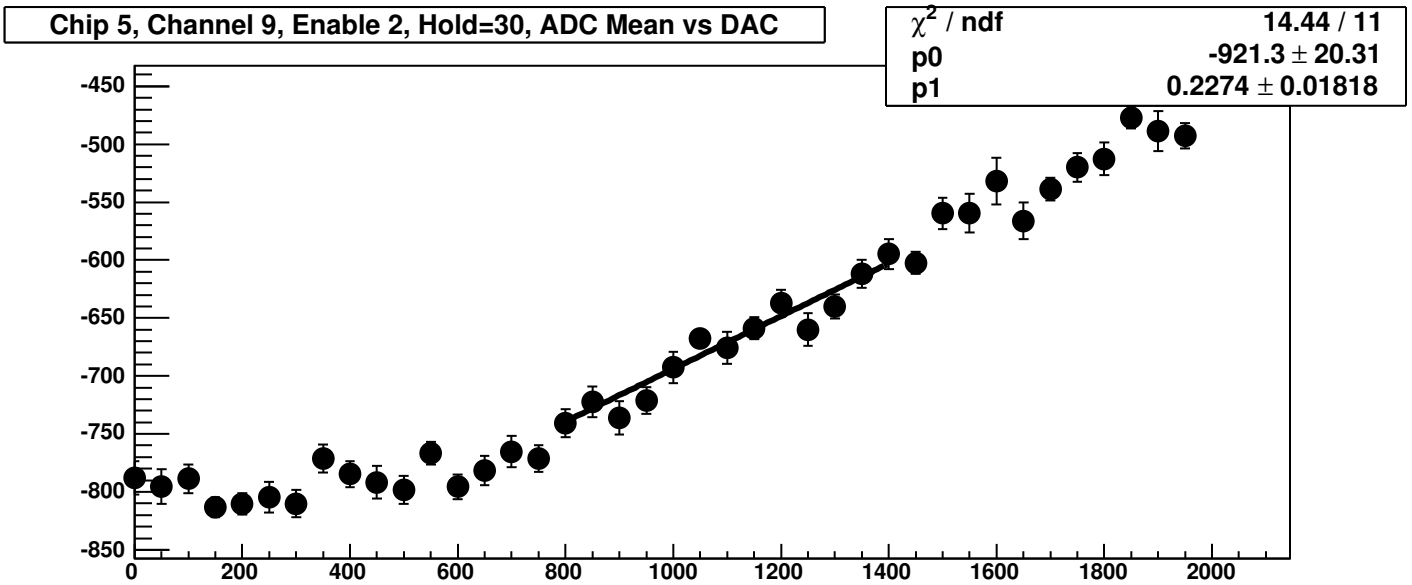


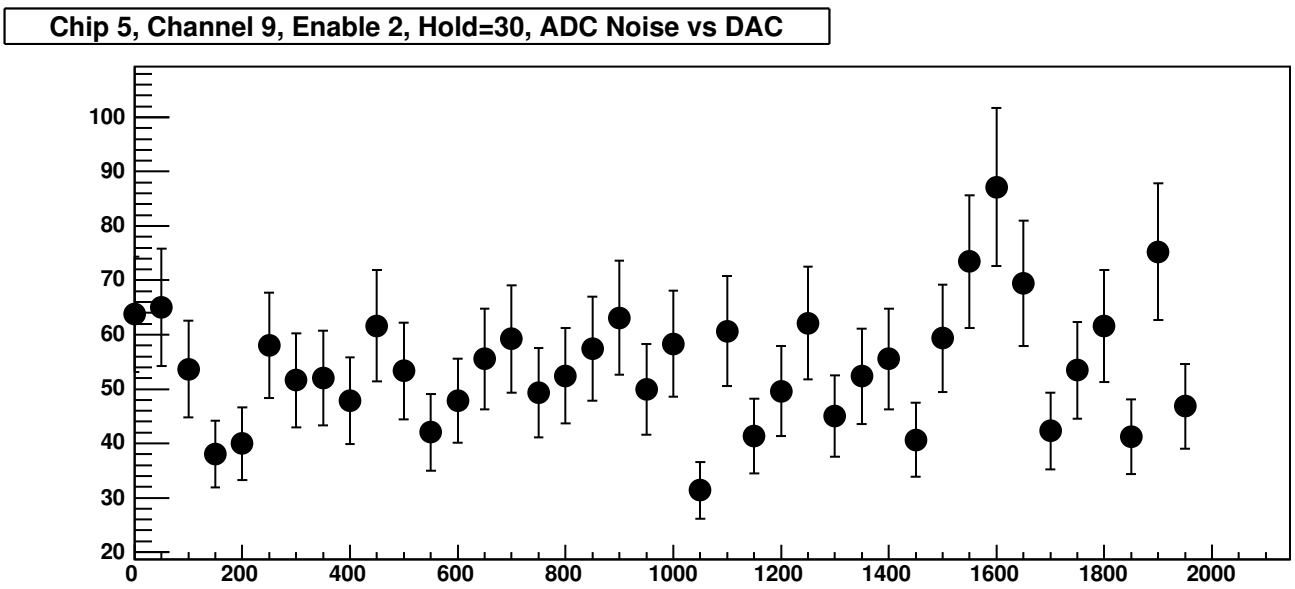
Chip 5, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC

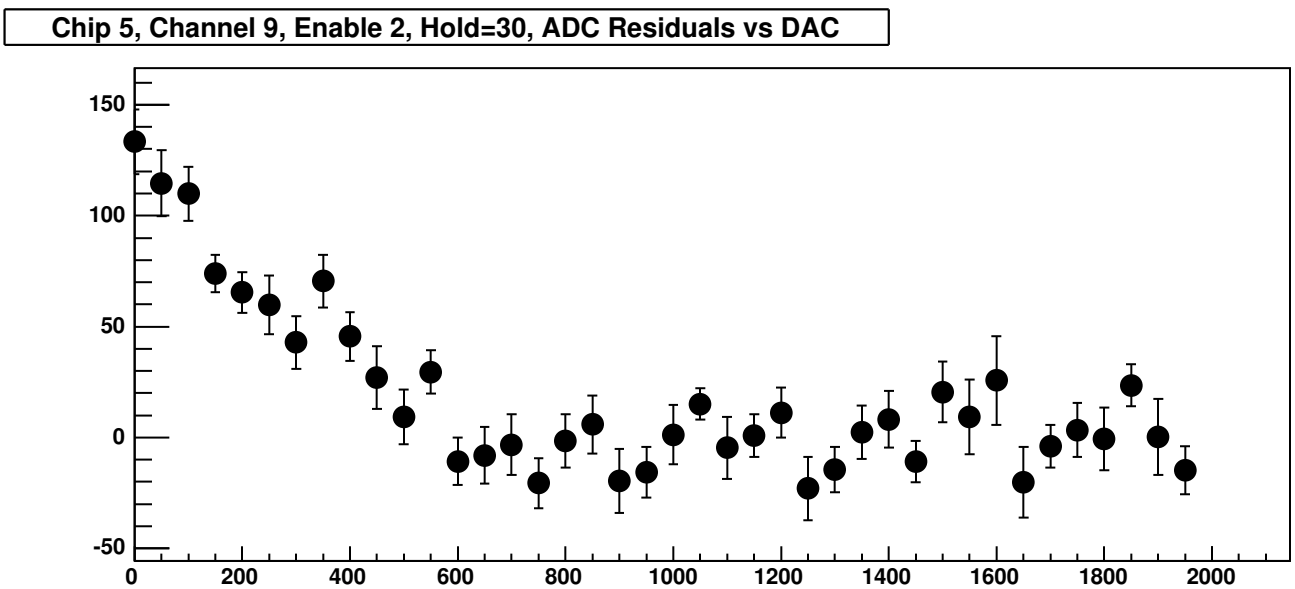


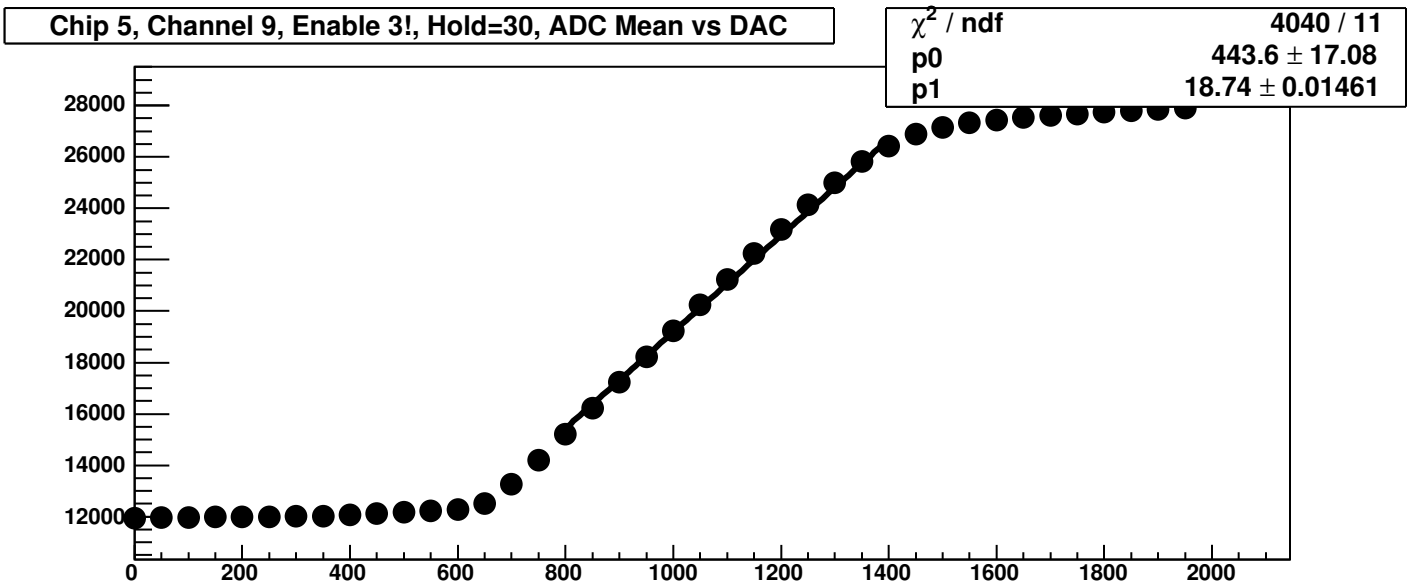
Chip 5, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC

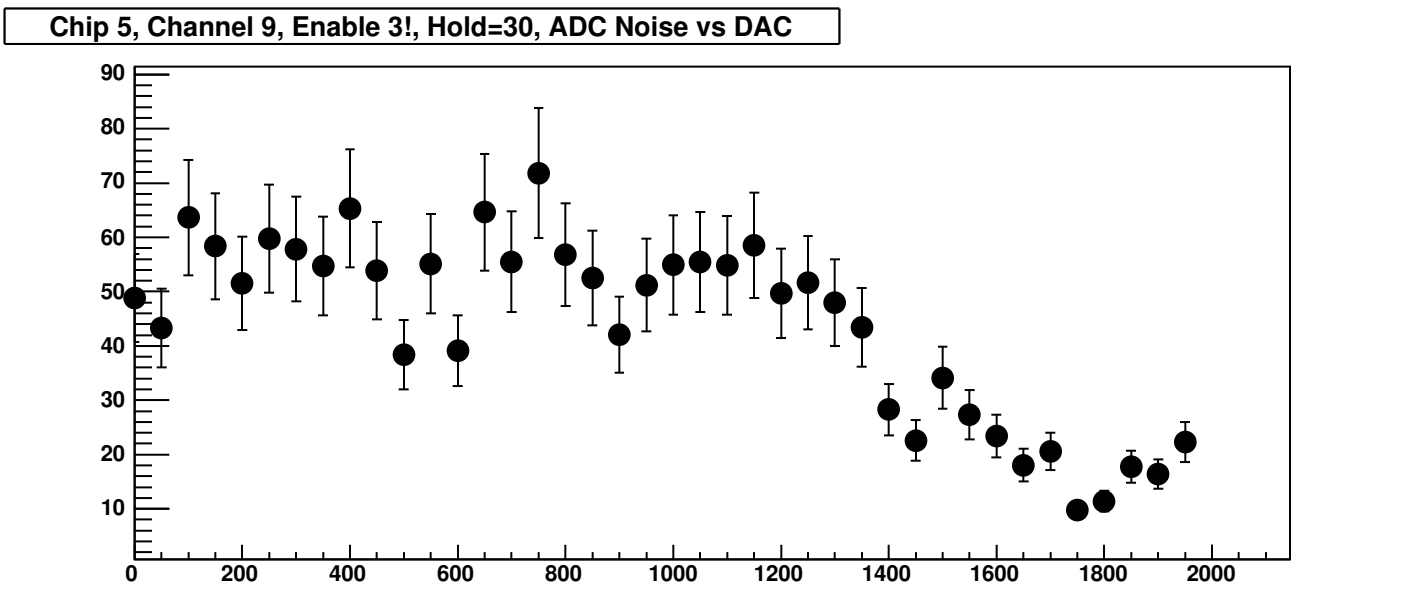


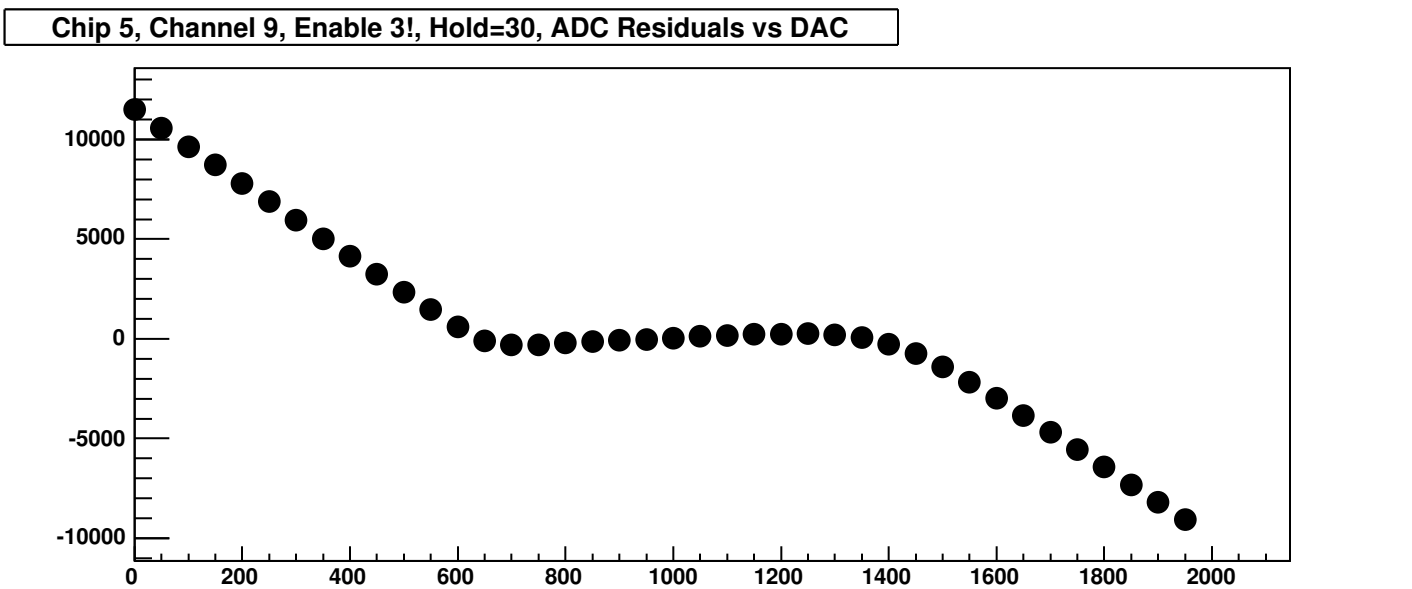

Chip 5, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 14.44 / 11
p0 -921.3 ± 20.31
p1 0.2274 ± 0.01818
-450
-500
-550
-600
-650
-700
-750
-800
-850
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

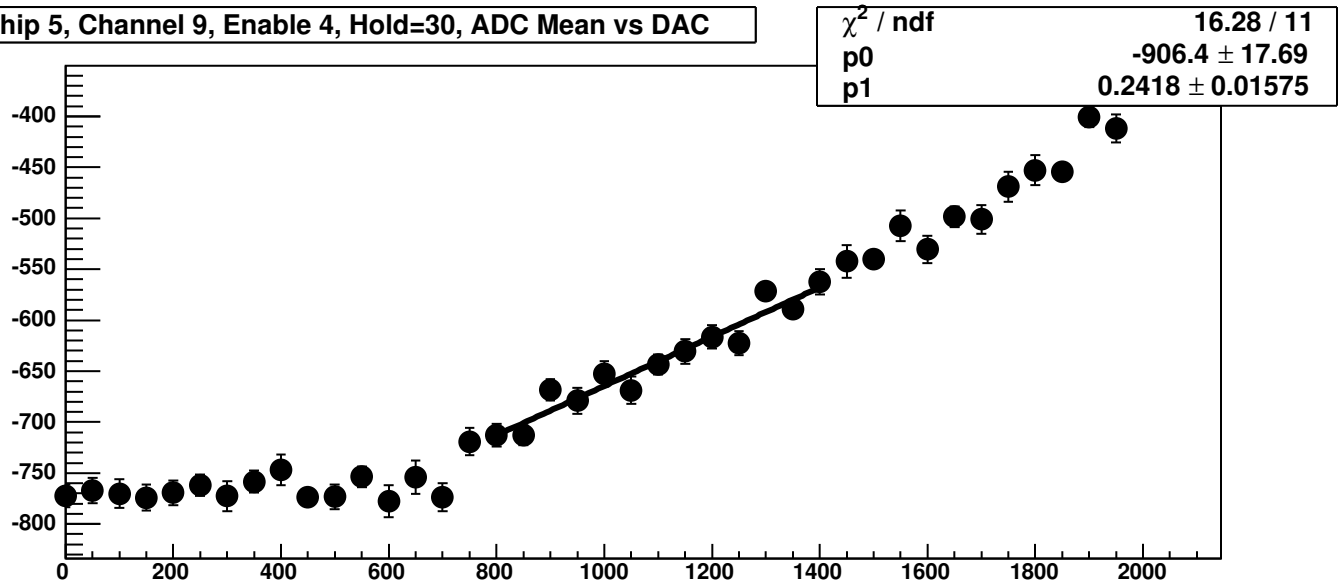

Chip 5, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 9, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 4040 / 11
p0 443.6 ± 17.08
p1 18.74 ± 0.01461
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

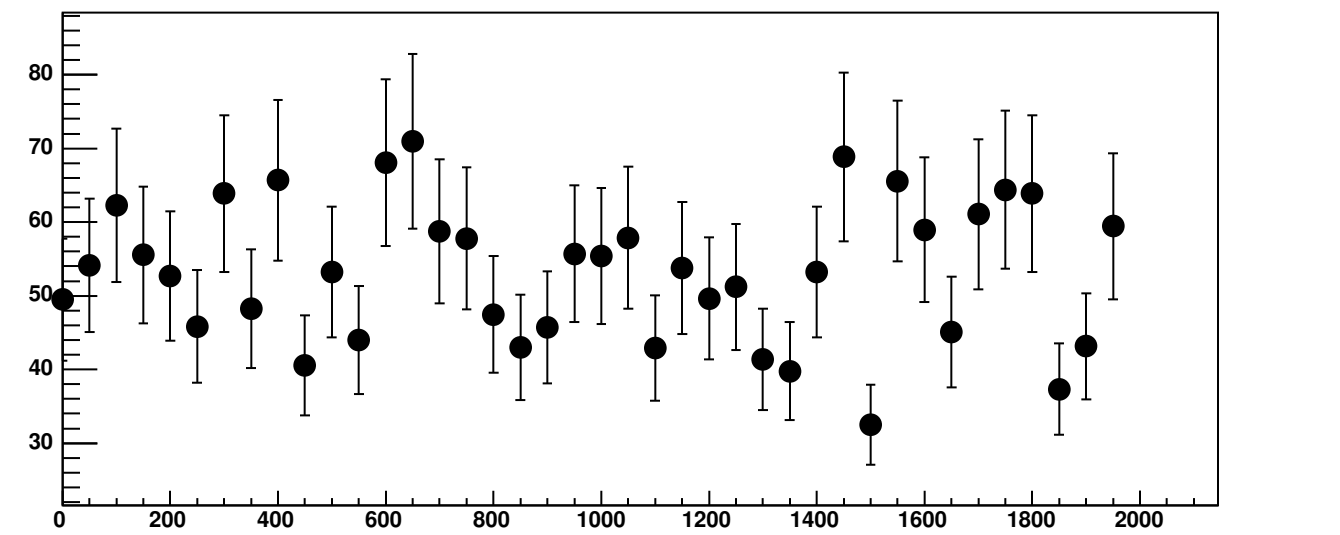

Chip 5, Channel 9, Enable 3!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 9, Enable 3!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

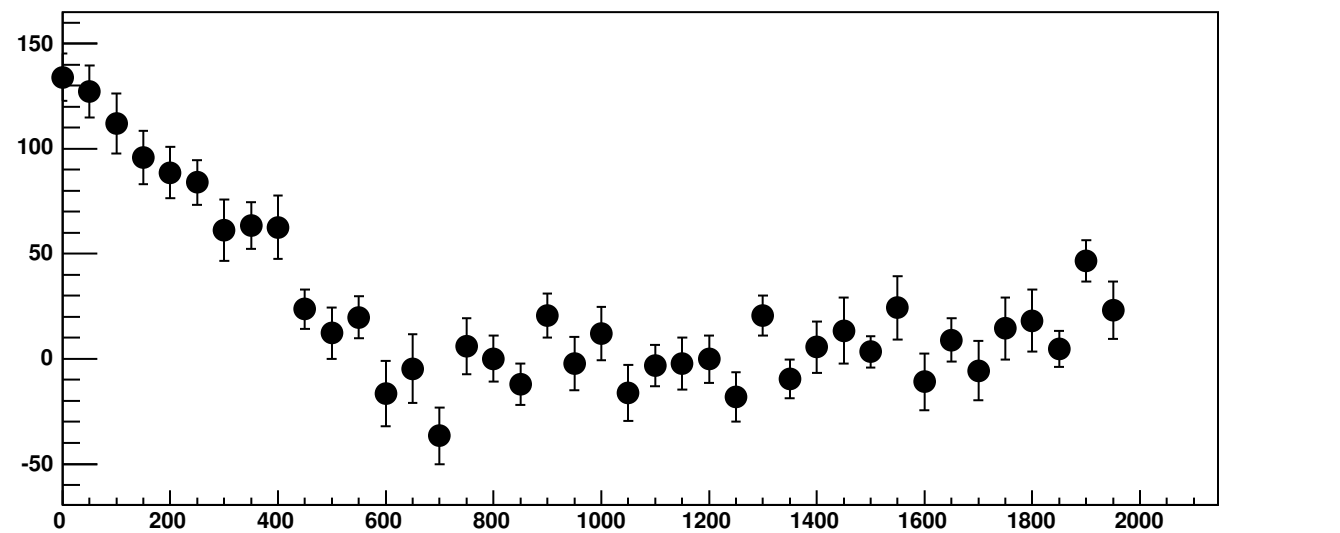
Chip 5, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC

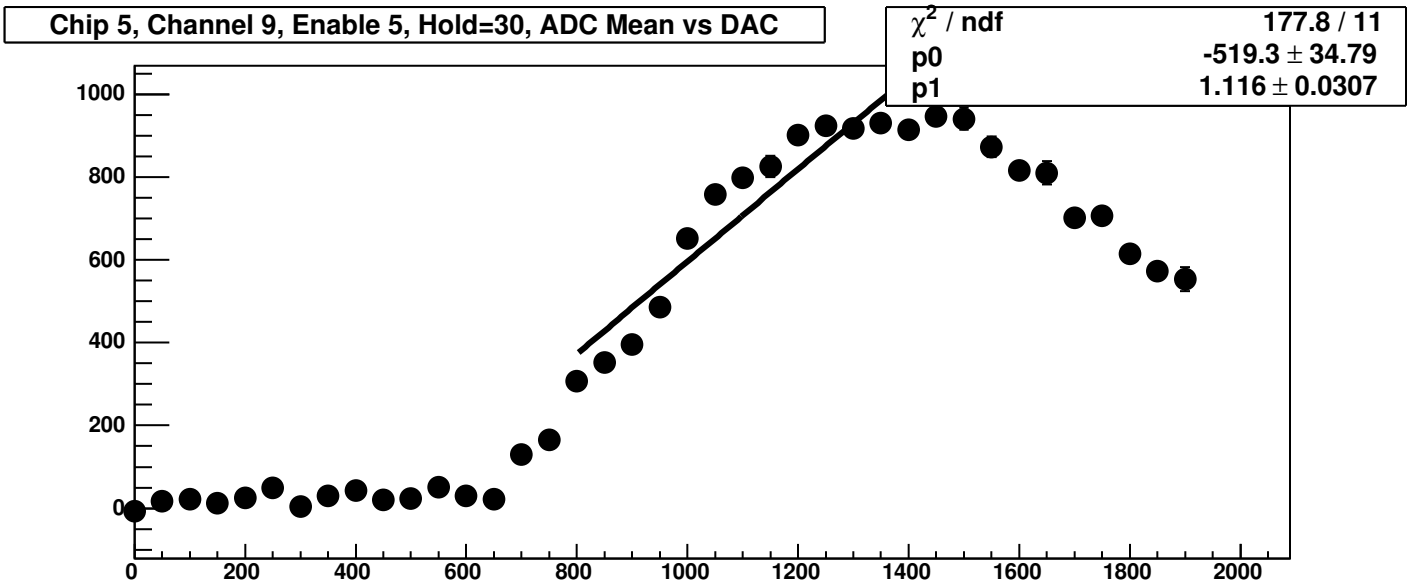


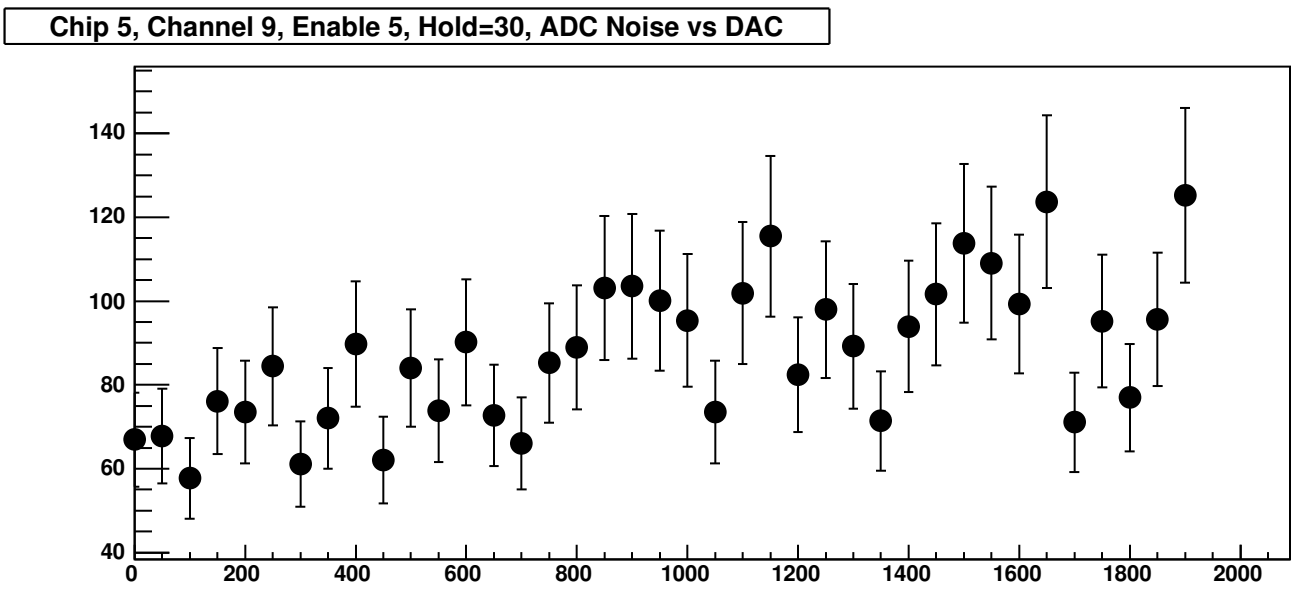
Chip 5, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC

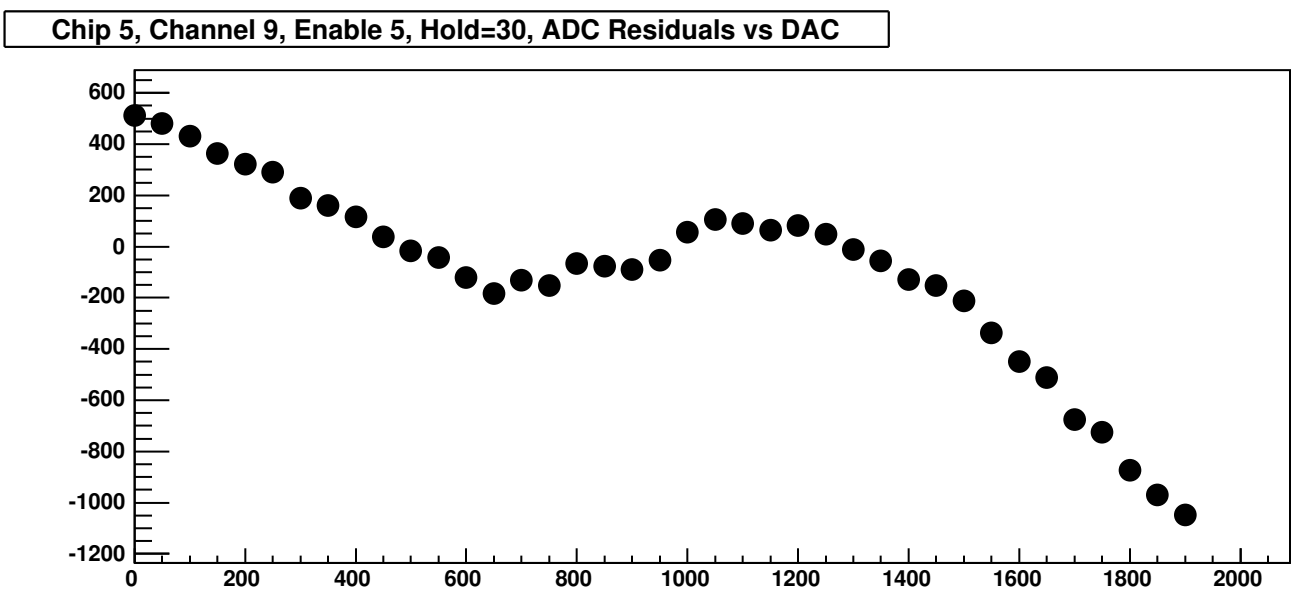


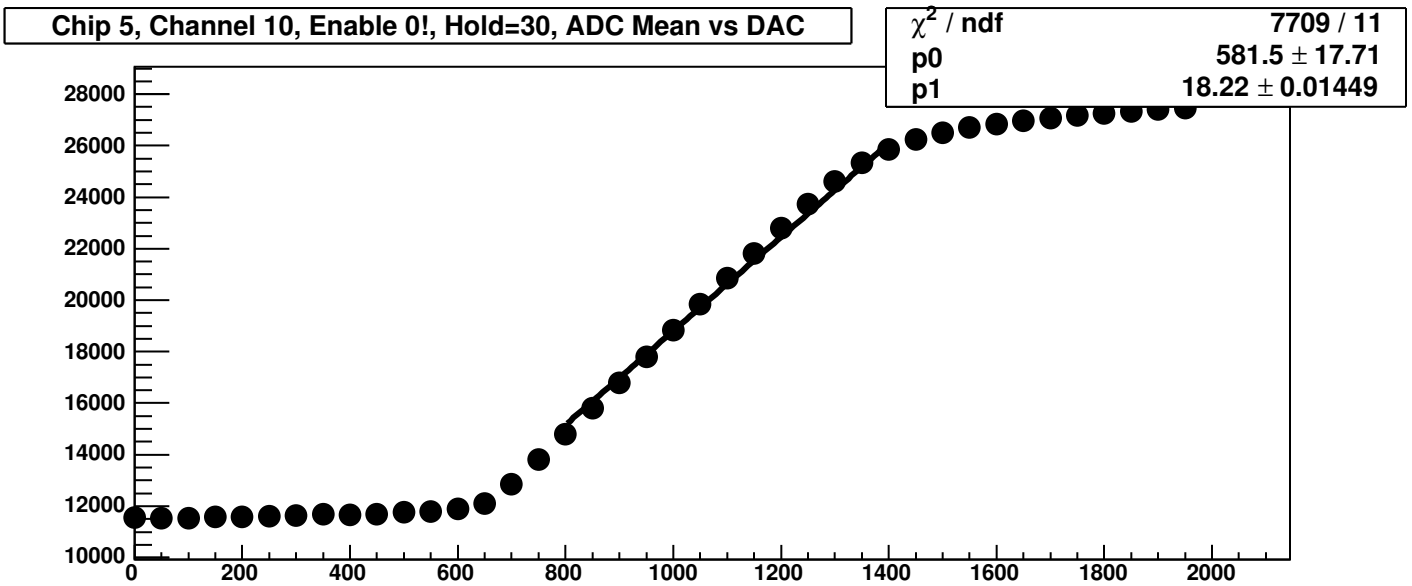
Chip 5, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC

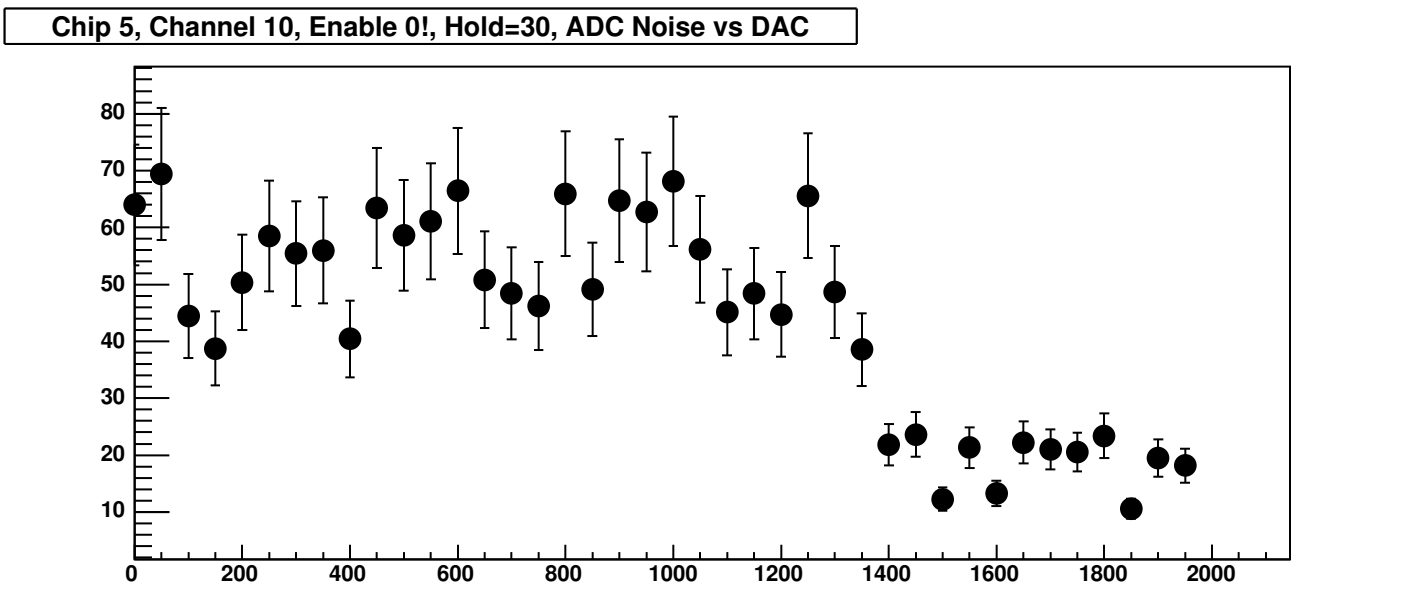


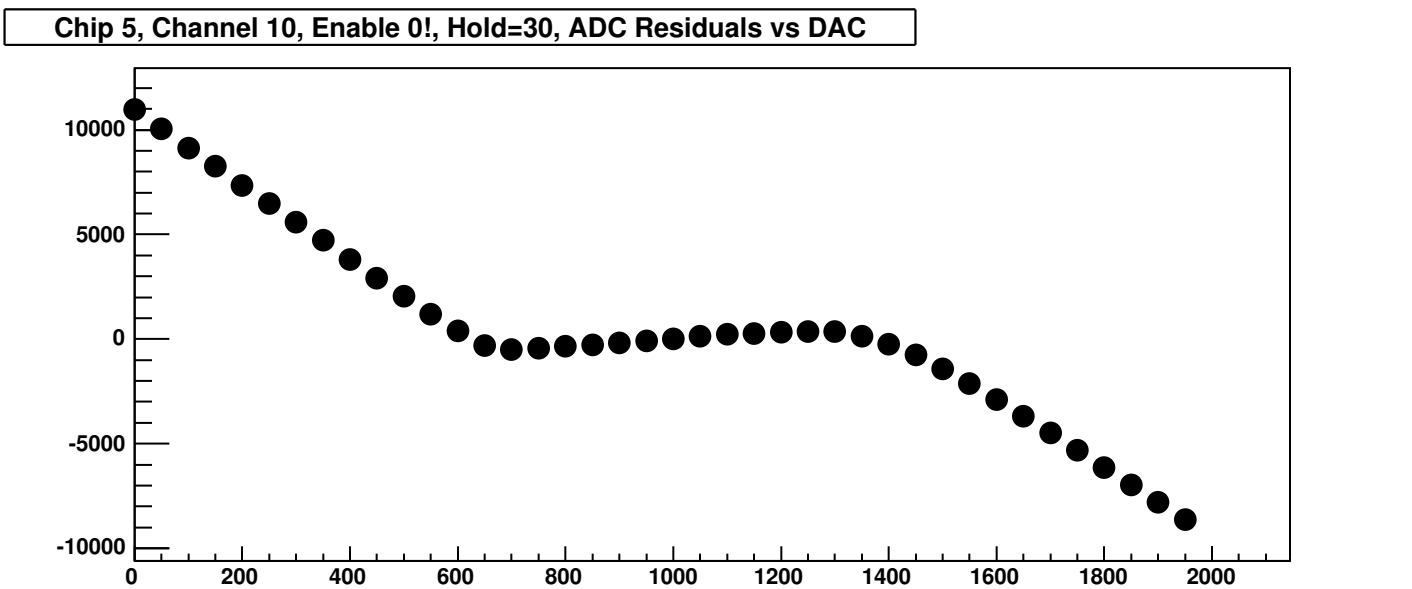

Chip 5, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 177.8 / 11
p0 -519.3 ± 34.79
p1 1.116 ± 0.0307
1000
800
600
400
200
0
0 200 400 600 800 1000 1200 1400 1600 1800 2000

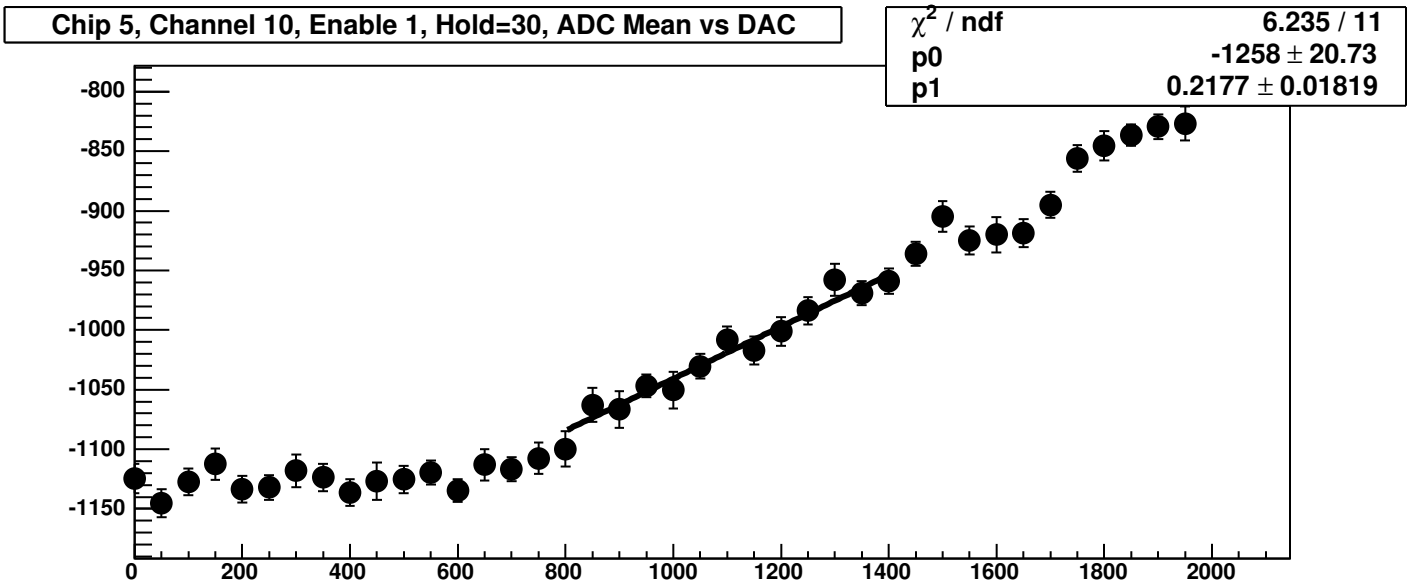

Chip 5, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0 200 400 600 800 1000 1200 1400 1600 1800 2000

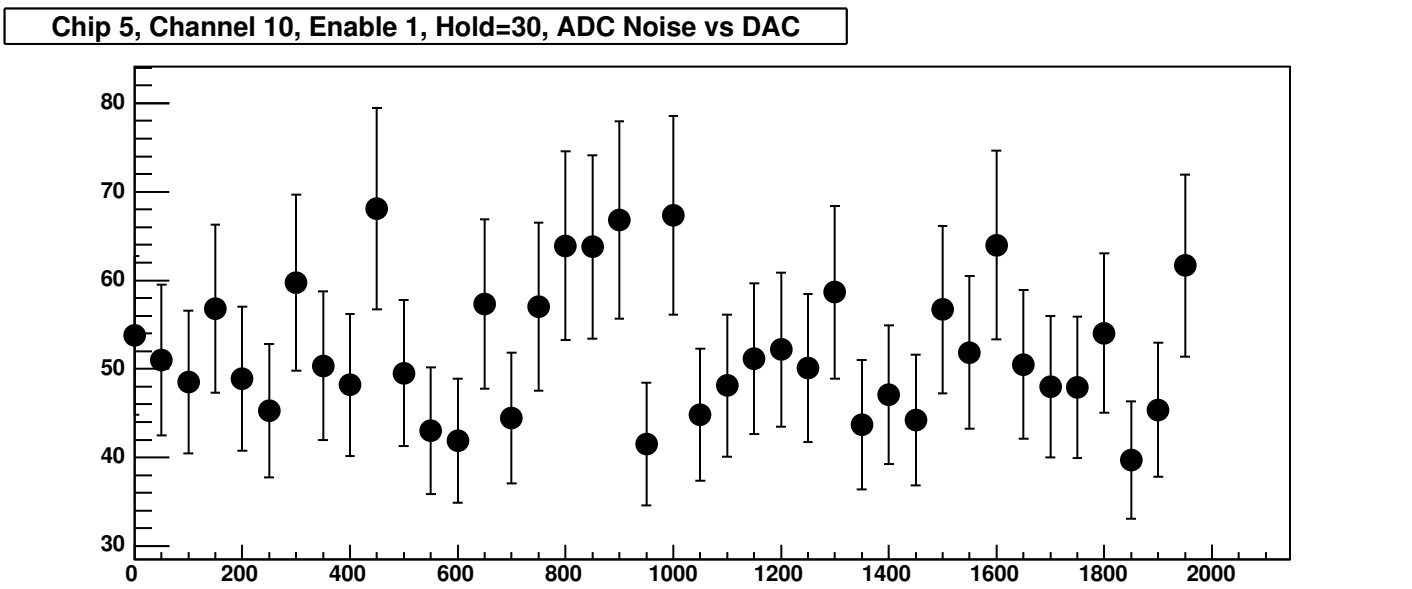

Chip 5, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
600
400
200
0
-200
-400
-600
-800
-1000
-1200
0 200 400 600 800 1000 1200 1400 1600 1800 2000

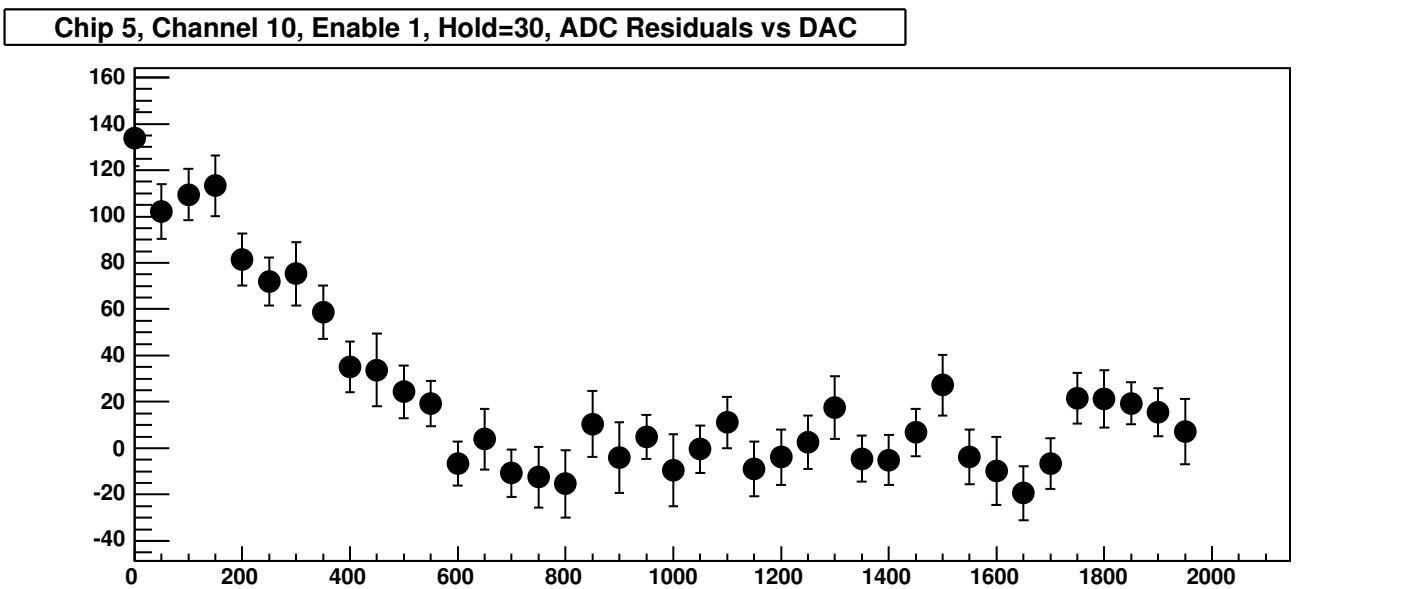

Chip 5, Channel 10, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 7709 / 11
p0 581.5 ± 17.71
p1 18.22 ± 0.01449
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 10, Enable 0!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

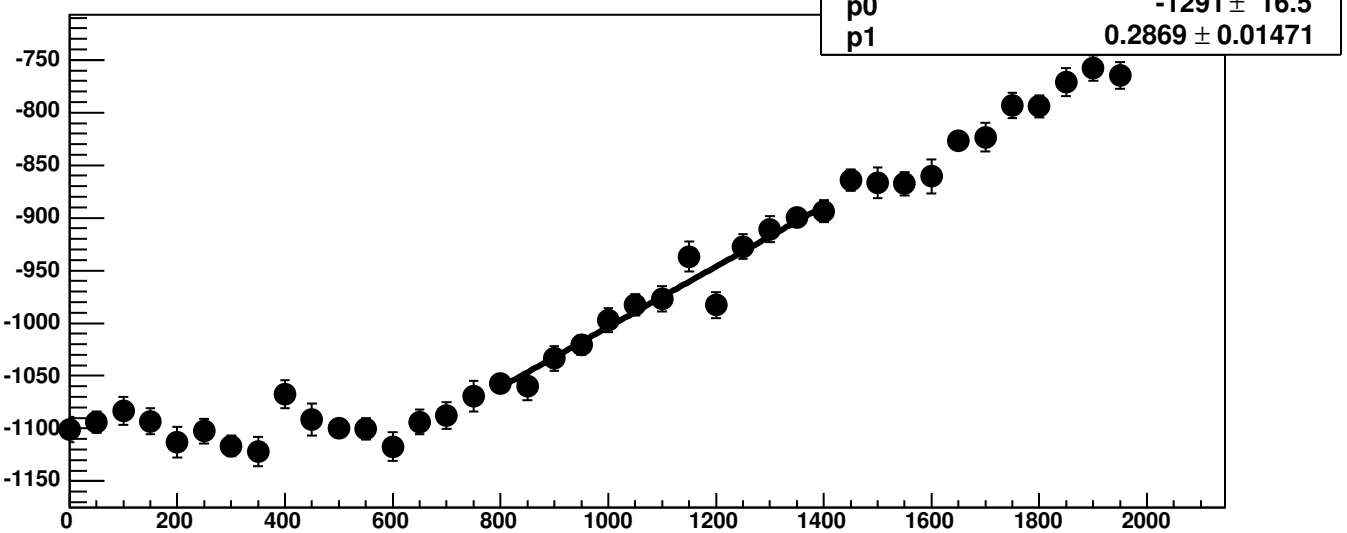

Chip 5, Channel 10, Enable 0!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 6.235 / 11
p0 -1258 ± 20.73
p1 0.2177 ± 0.01819
-800
-850
-900
-950
-1000
-1050
-1100
-1150
0
200
400
600
800
1000
1200
1400
1600
1800
2000

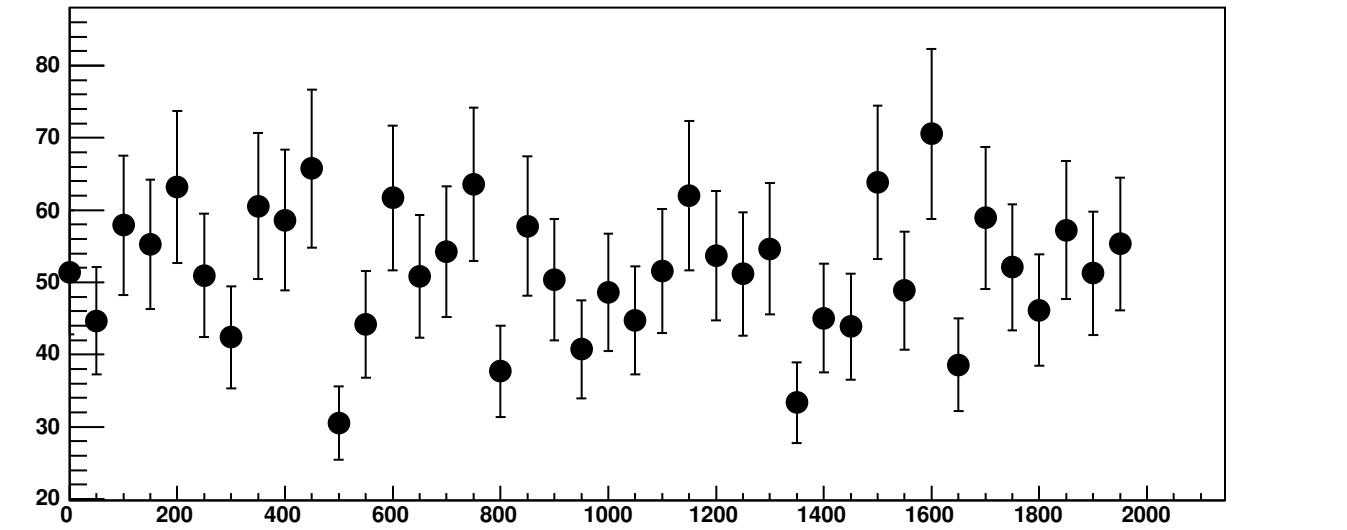

Chip 5, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
160
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

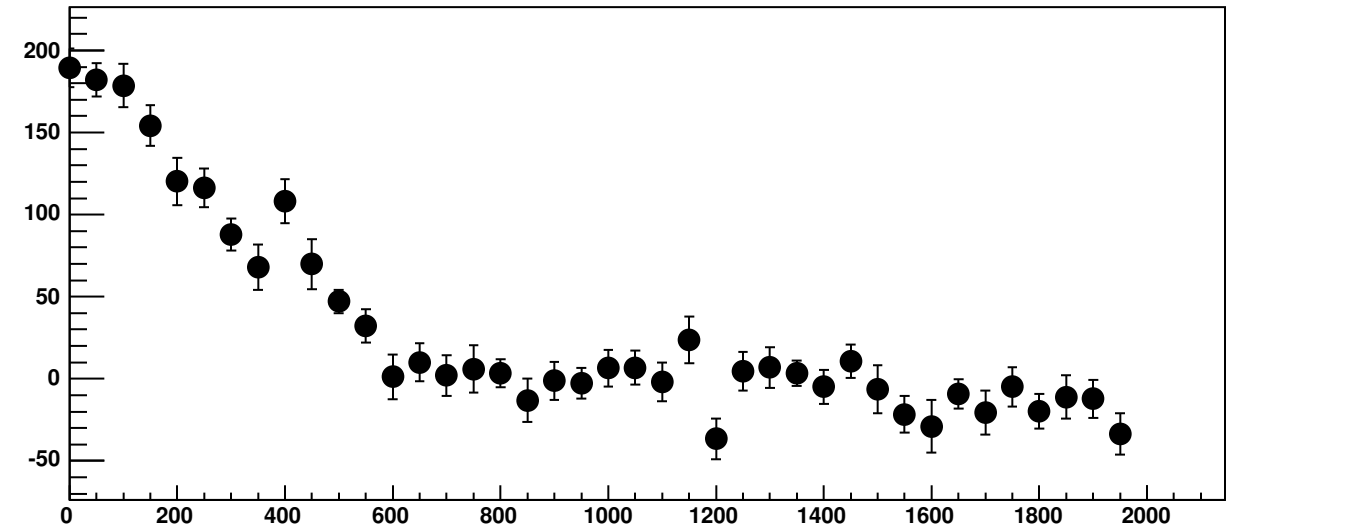
Chip 5, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC

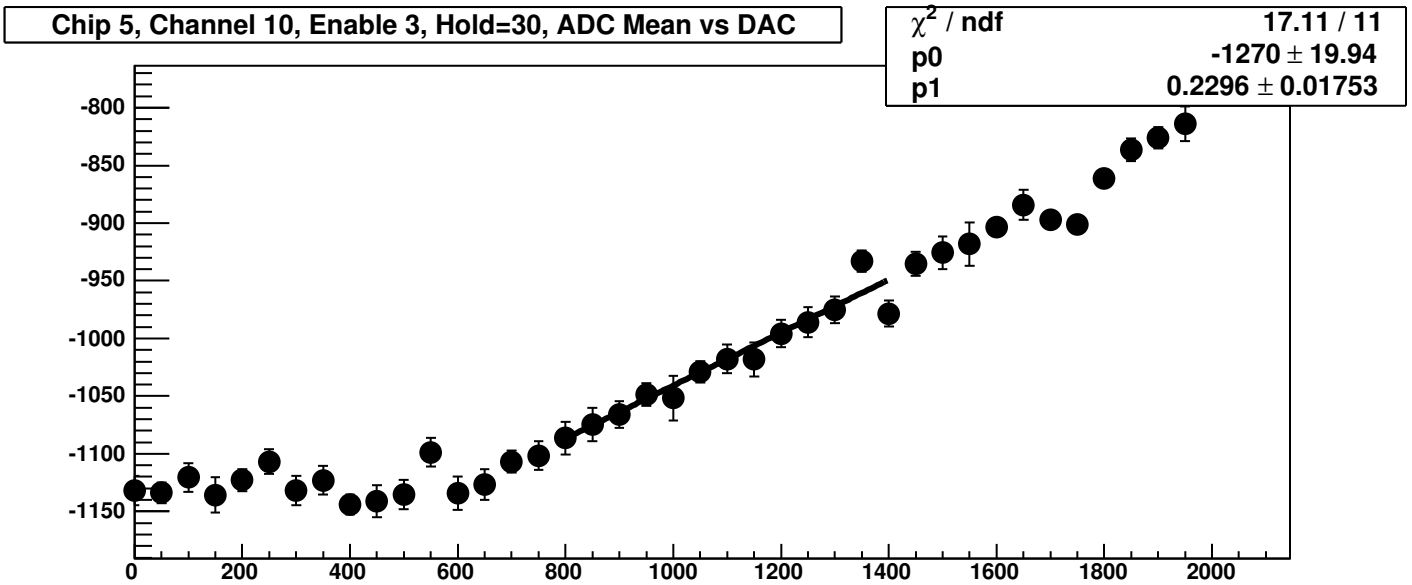


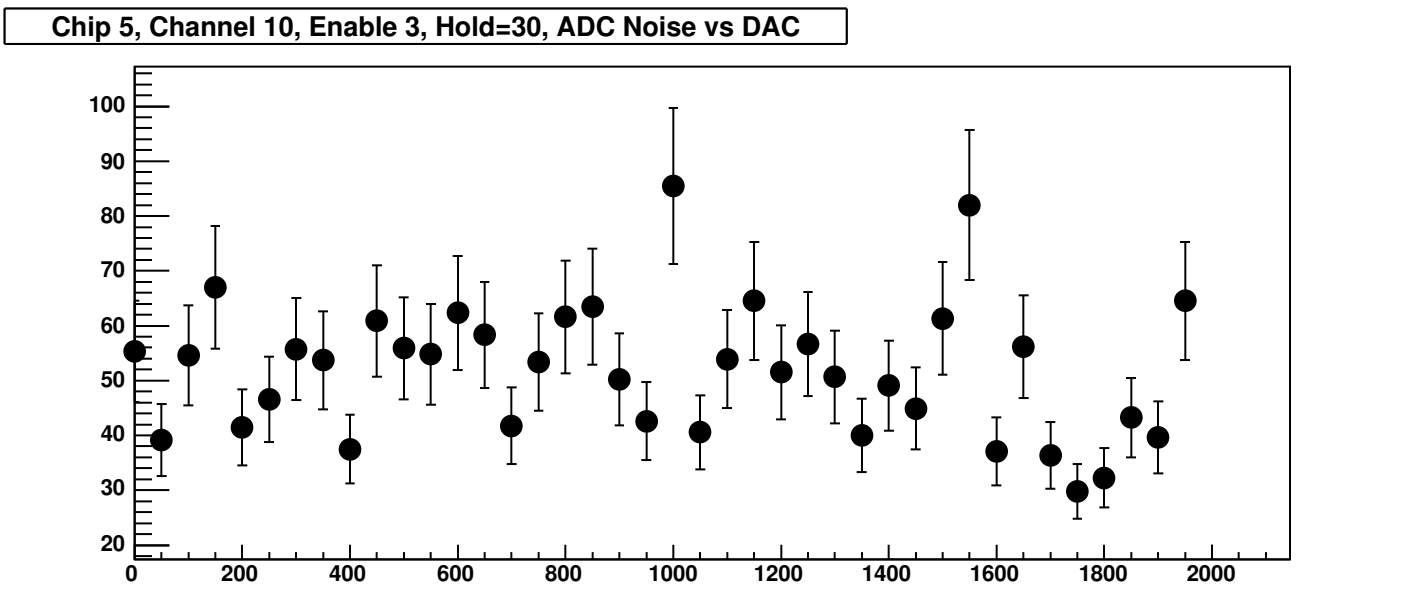
Chip 5, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC

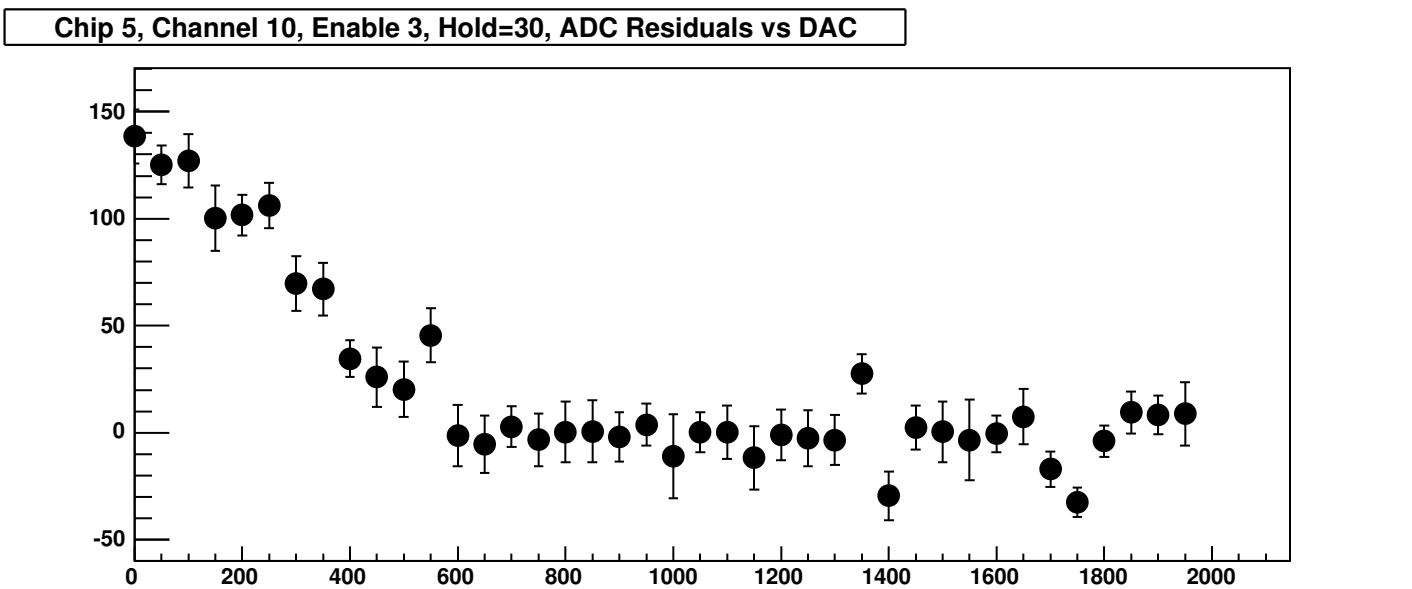


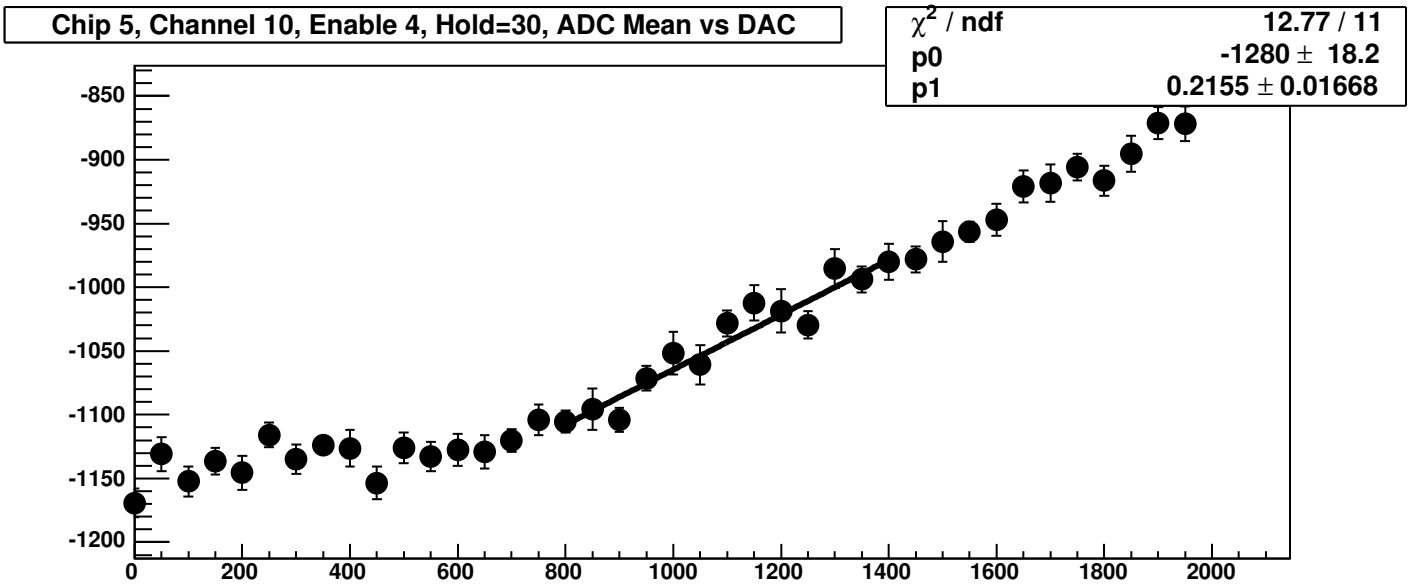
Chip 5, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC

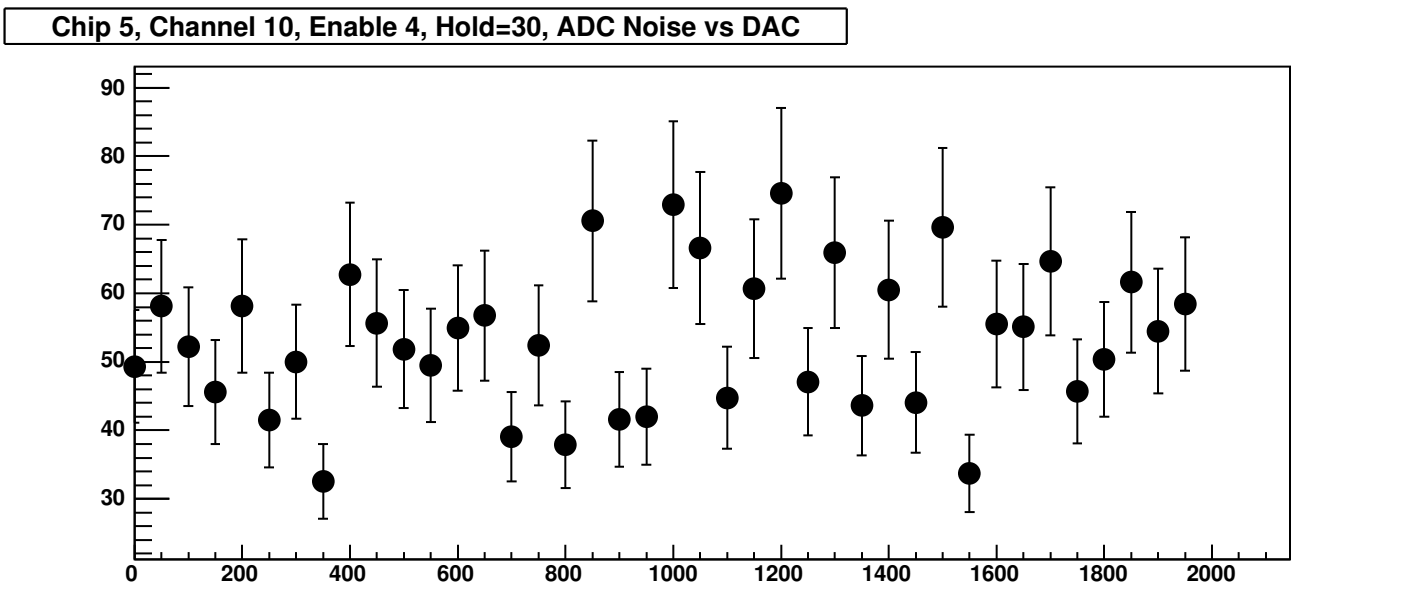


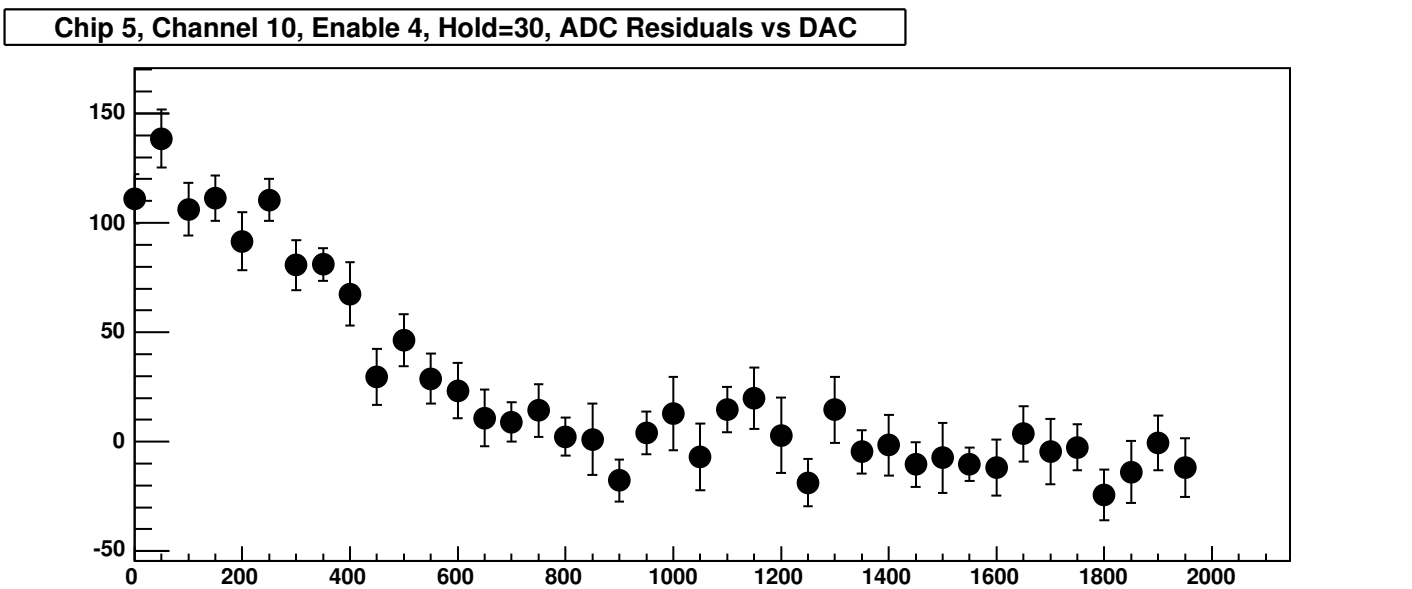

Chip 5, Channel 10, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 17.11 / 11
p0 -1270 ± 19.94
p1 0.2296 ± 0.01753
-800
-850
-900
-950
-1000
-1050
-1100
-1150
0
200
400
600
800
1000
1200
1400
1600
1800
2000

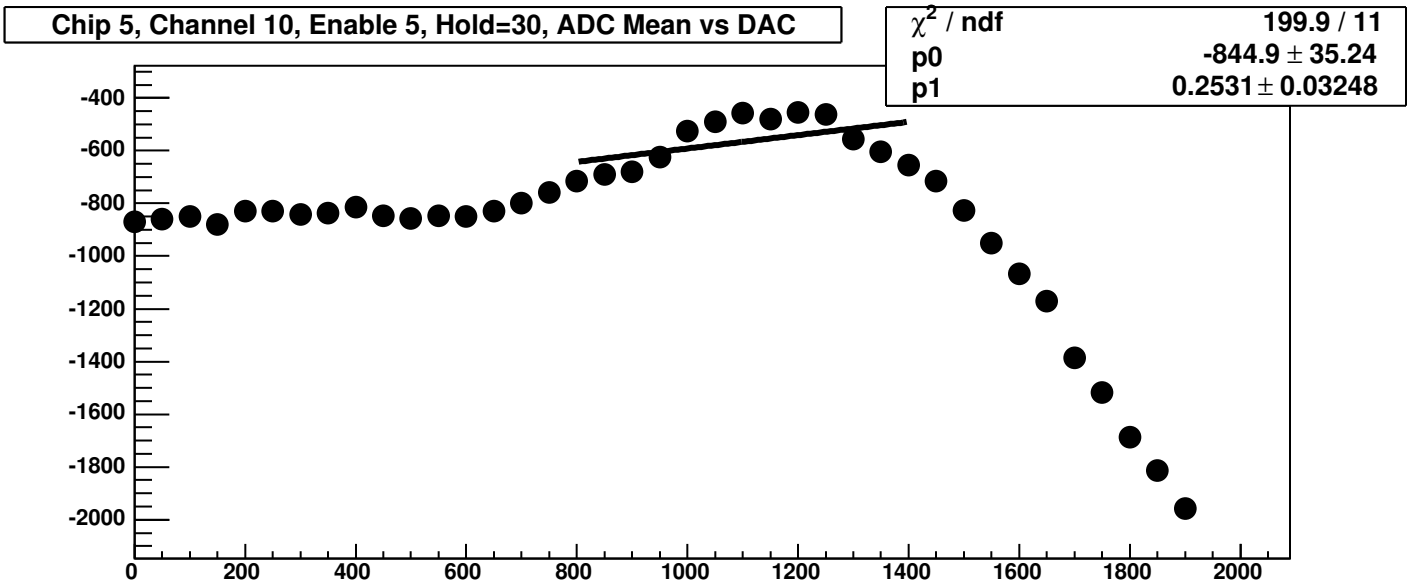

Chip 5, Channel 10, Enable 3, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

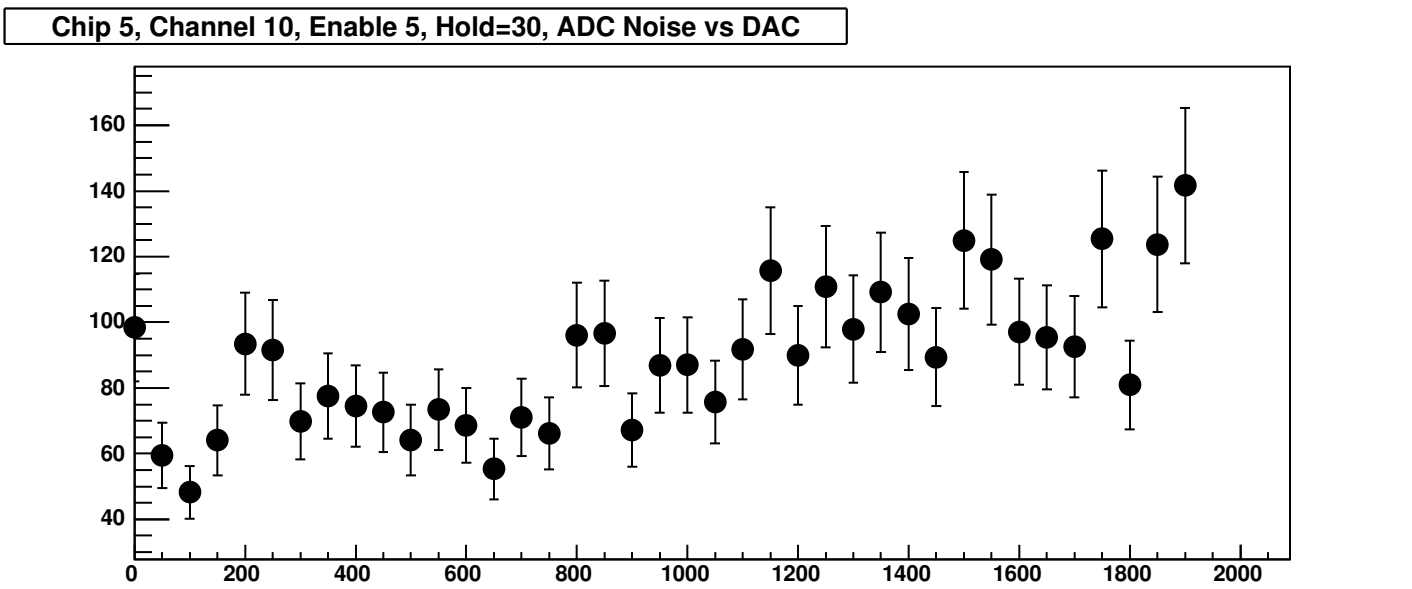

Chip 5, Channel 10, Enable 3, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

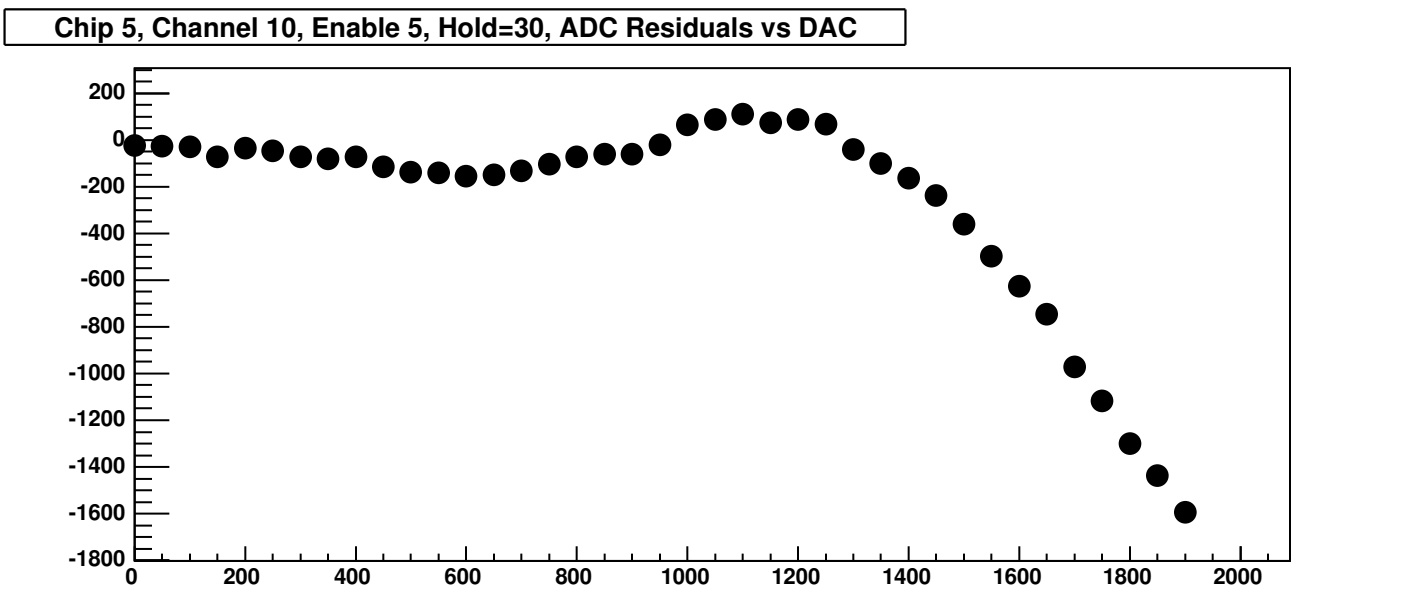

Chip 5, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 12.77 / 11
p0 -1280 ± 18.2
p1 0.2155 ± 0.01668
-850
-900
-950
-1000
-1050
-1100
-1150
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

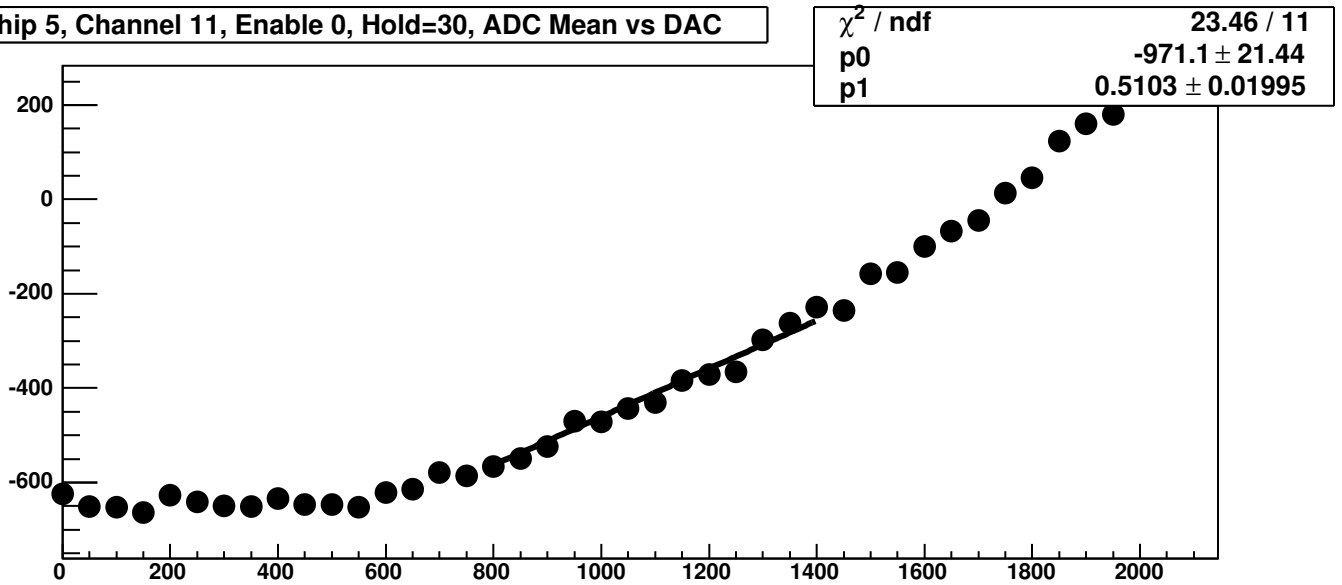

Chip 5, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 199.9 / 11
p0 -844.9 ± 35.24
p1 0.2531 ± 0.03248
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
-2000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

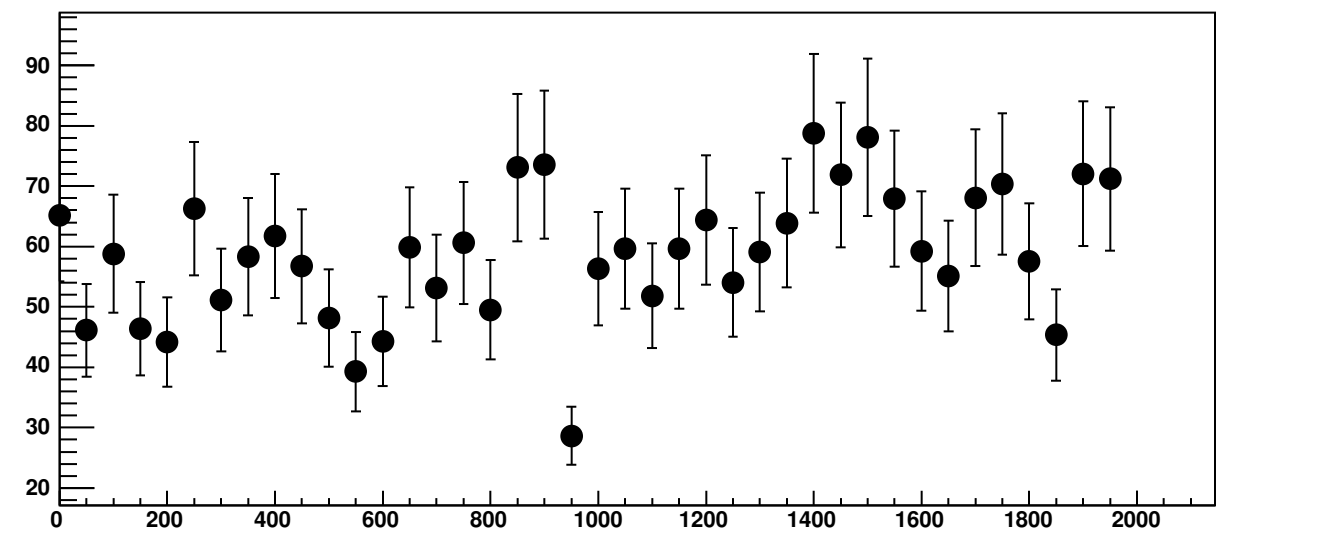

Chip 5, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

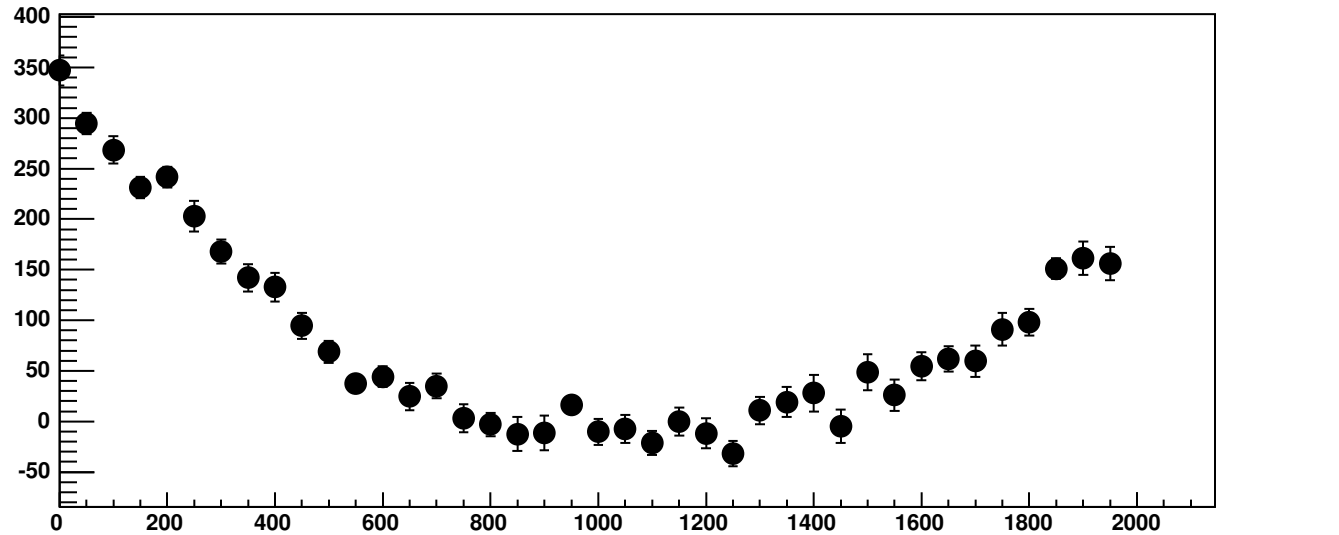
Chip 5, Channel 11, Enable 0, Hold=30, ADC Mean vs DAC



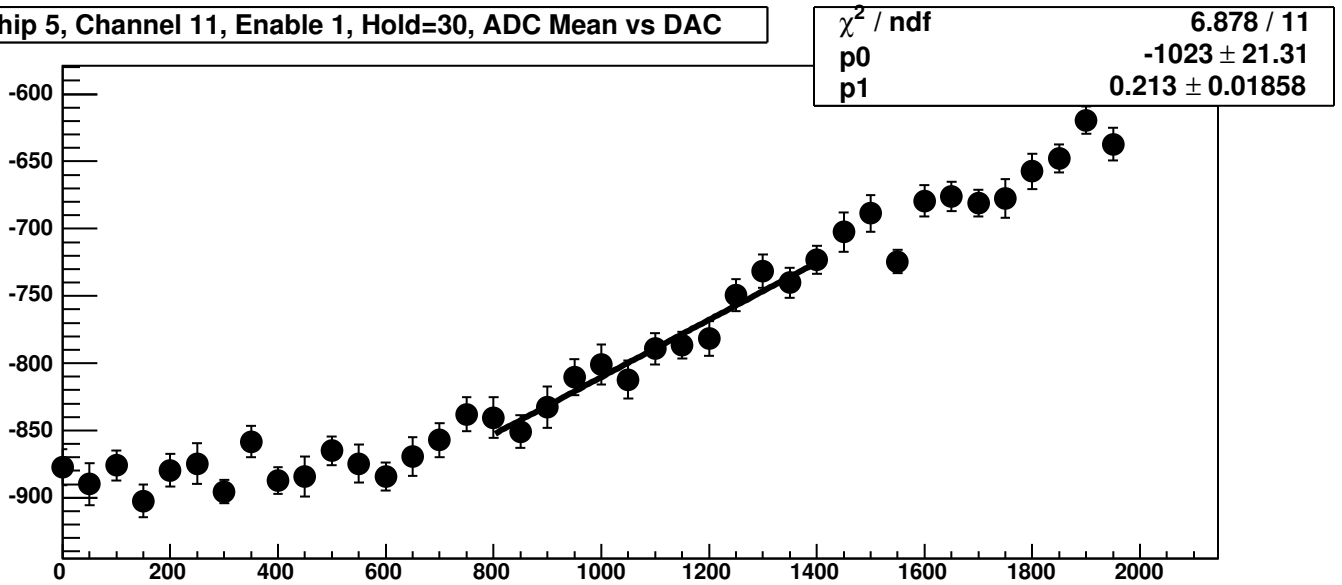
Chip 5, Channel 11, Enable 0, Hold=30, ADC Noise vs DAC



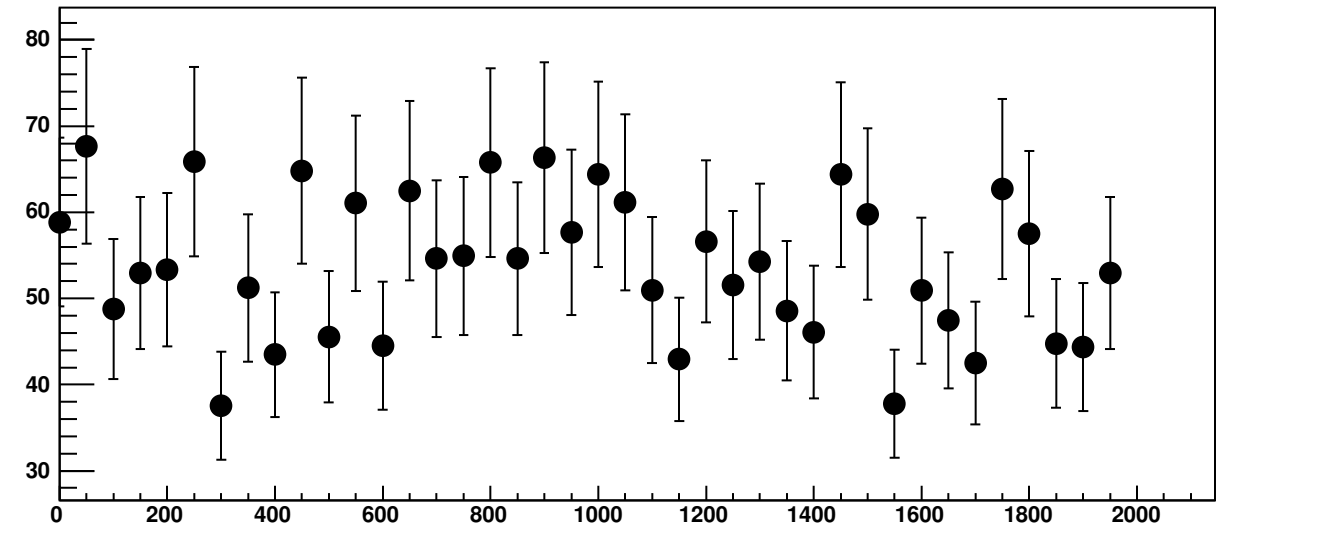
Chip 5, Channel 11, Enable 0, Hold=30, ADC Residuals vs DAC



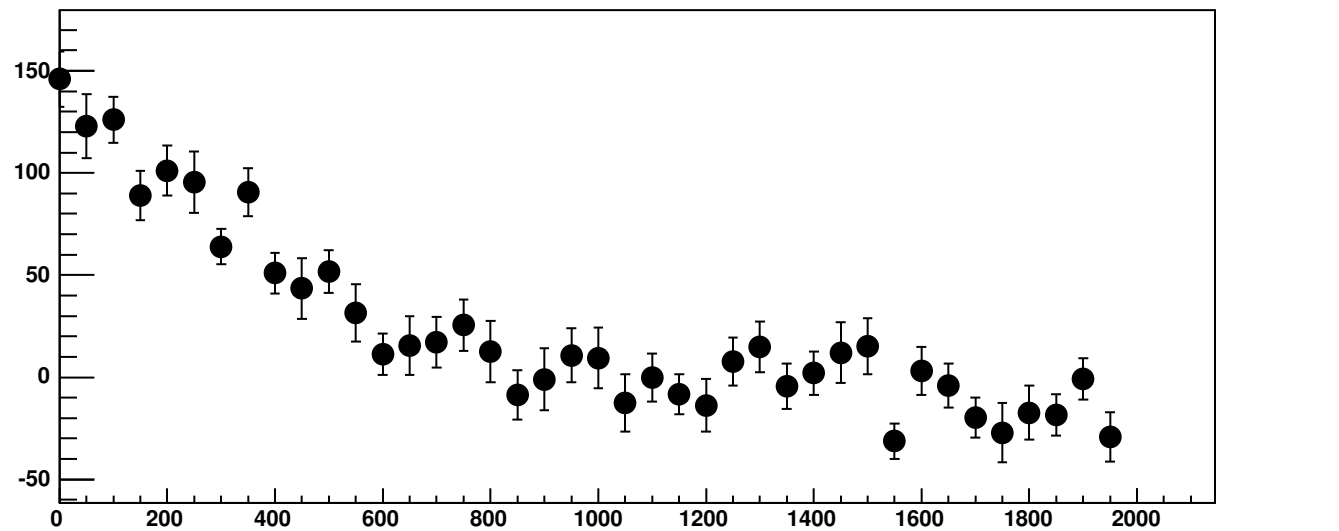
Chip 5, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC

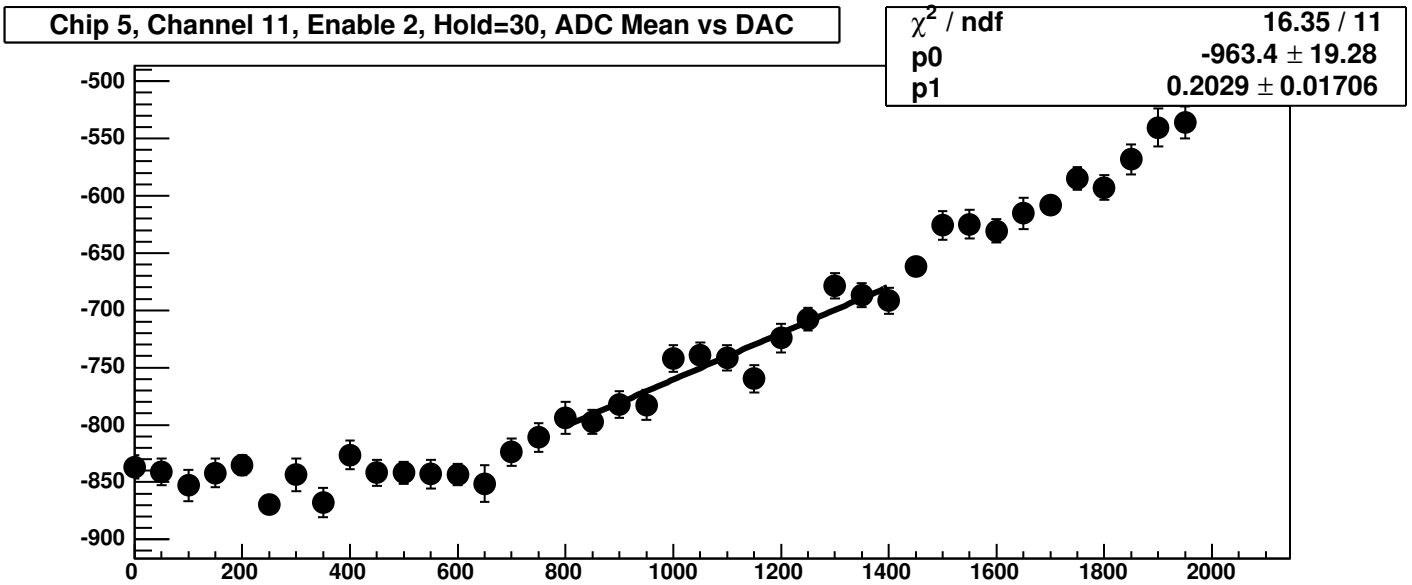


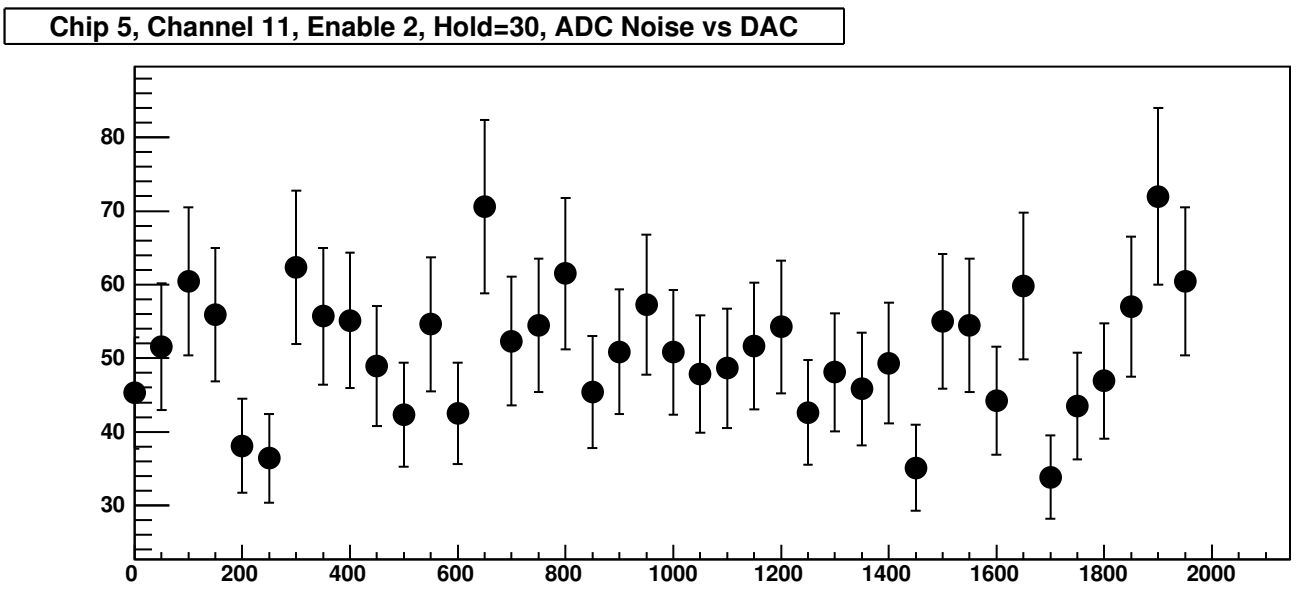
Chip 5, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC

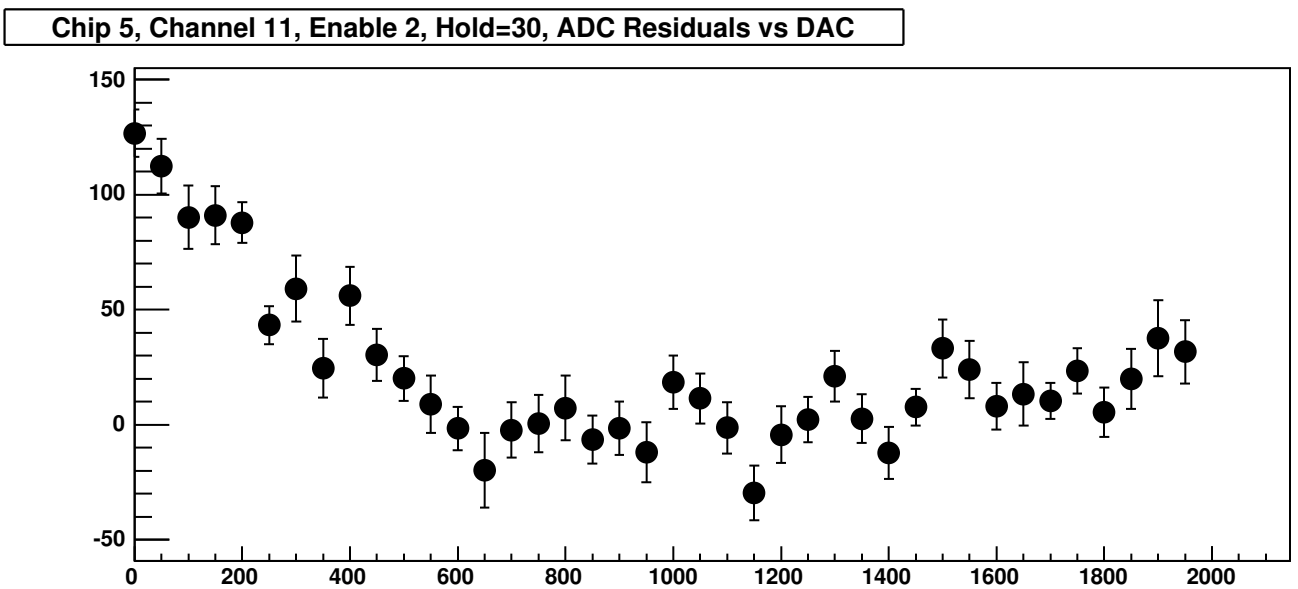


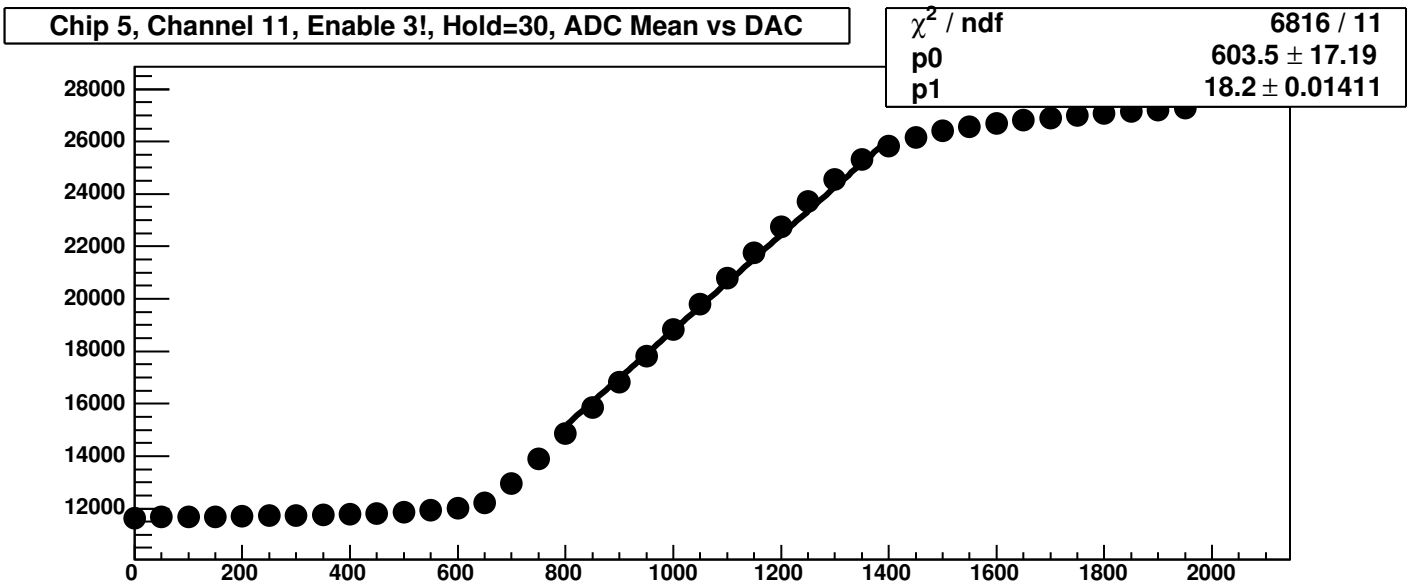
Chip 5, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC

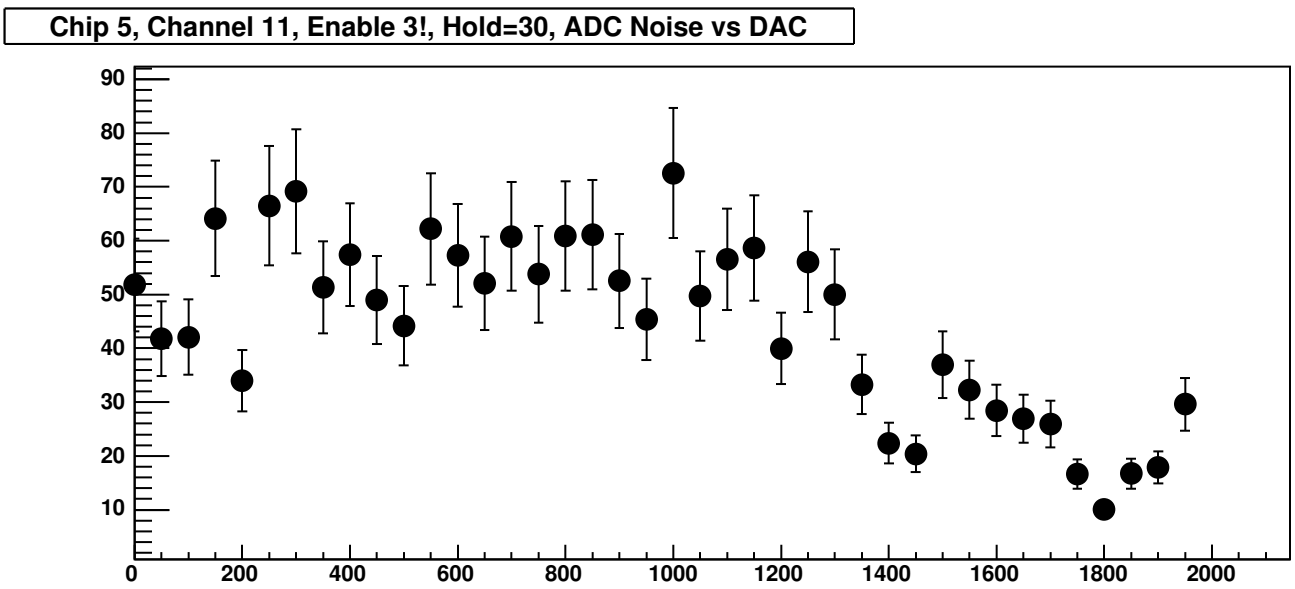


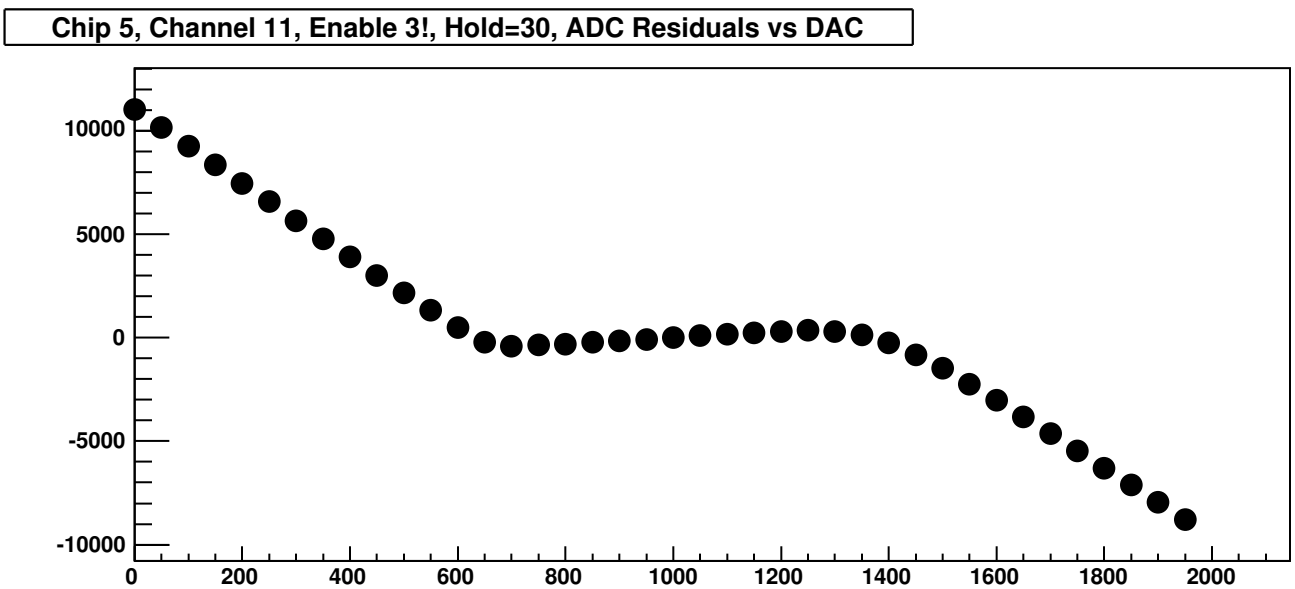

Chip 5, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 16.35 / 11
p0 -963.4 ± 19.28
p1 0.2029 ± 0.01706
-500
-550
-600
-650
-700
-750
-800
-850
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

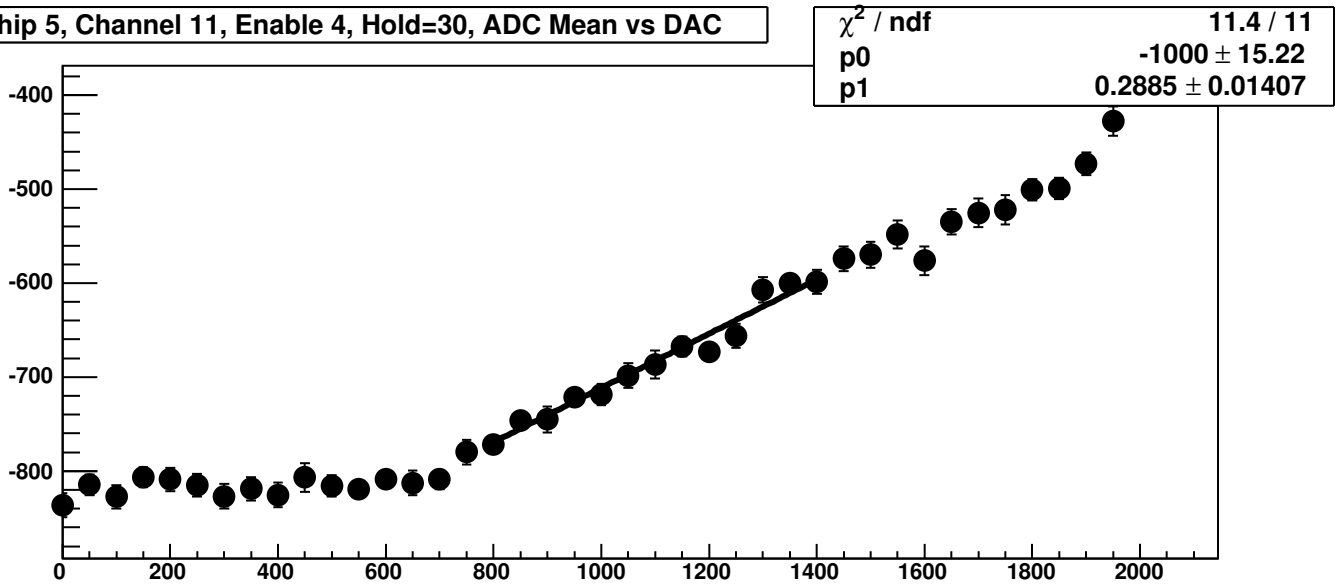

Chip 5, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 11, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 6816 / 11
p0 603.5 ± 17.19
p1 18.2 ± 0.01411
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

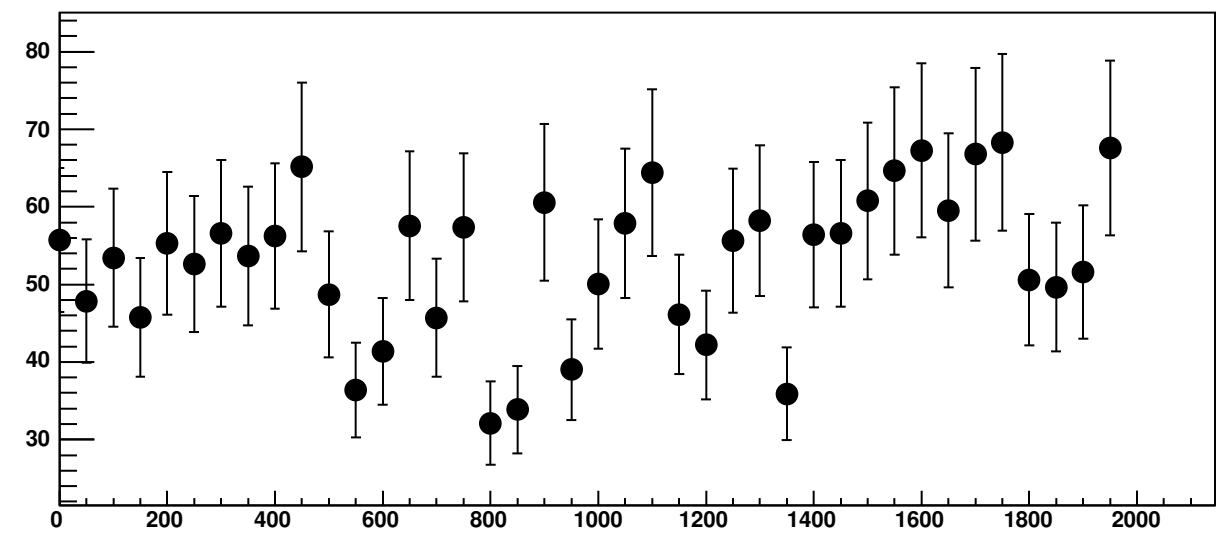

Chip 5, Channel 11, Enable 3!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 11, Enable 3!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

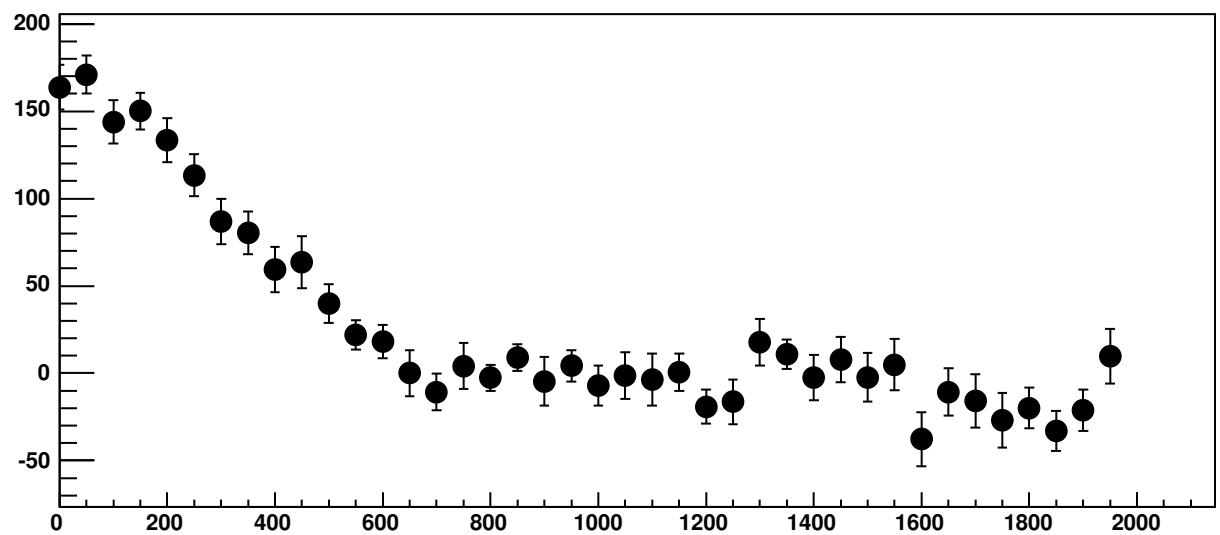
Chip 5, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC



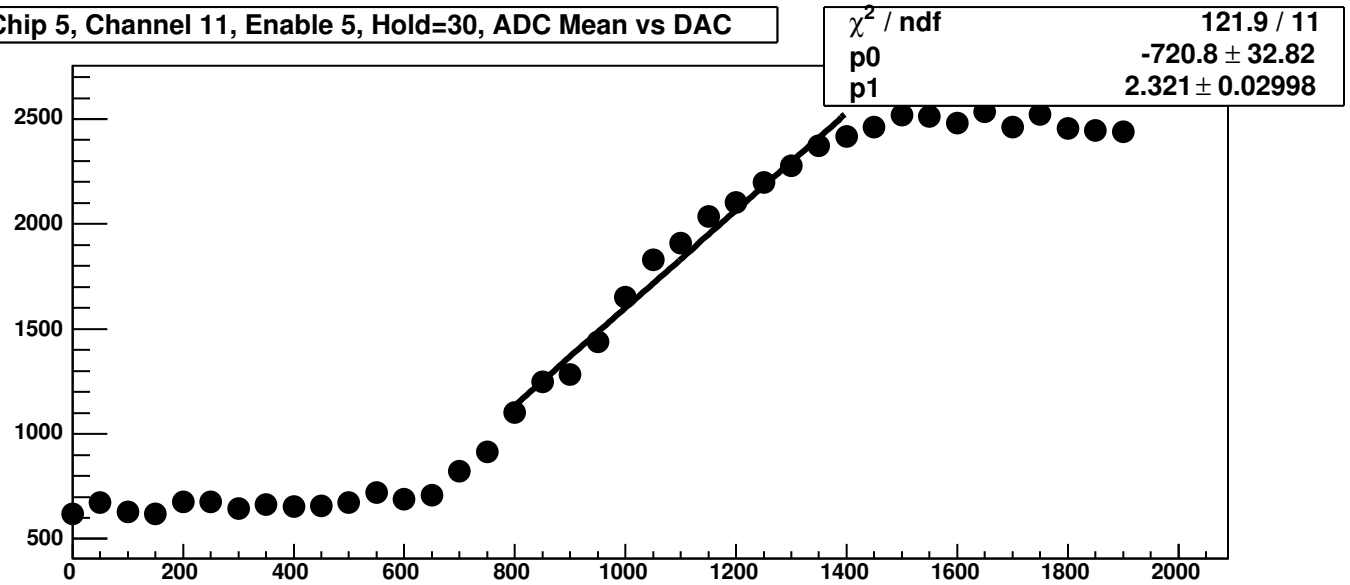
Chip 5, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC



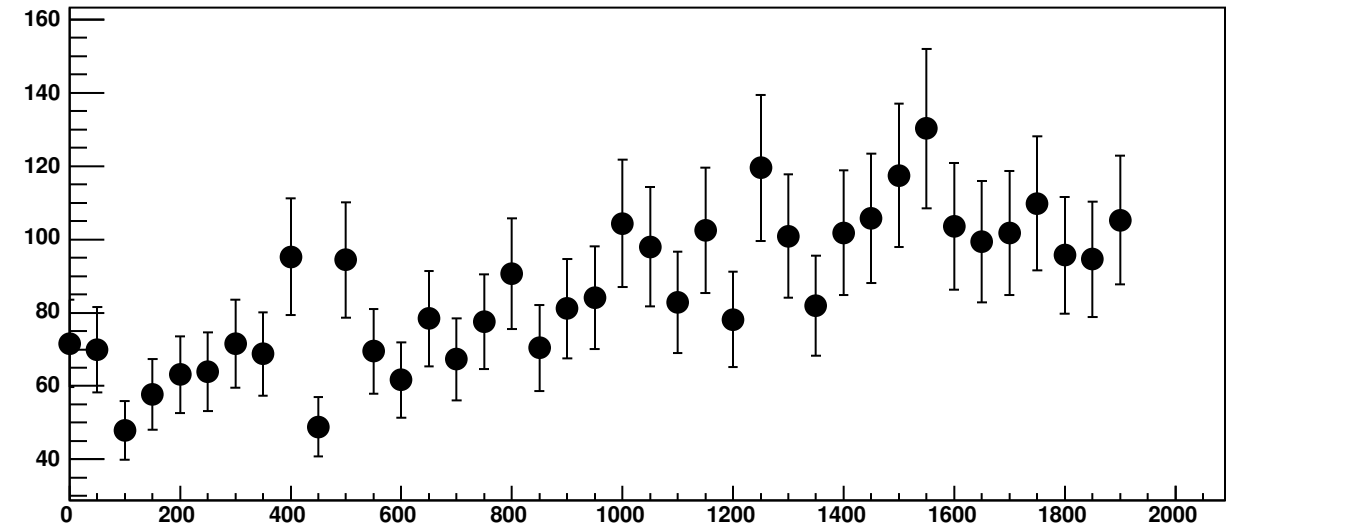
Chip 5, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC



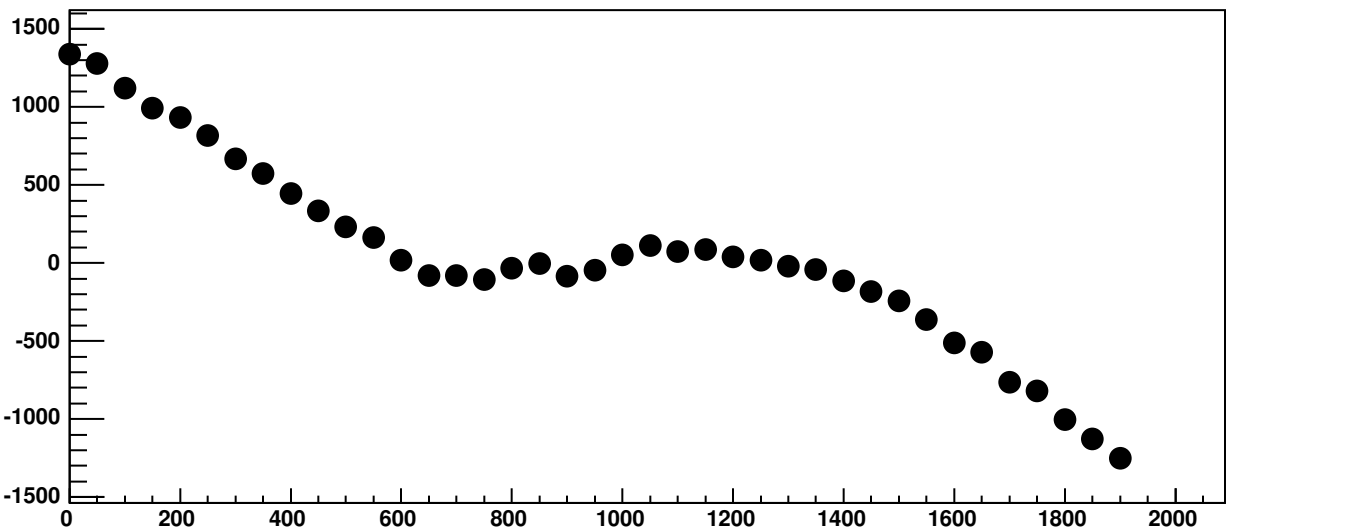
Chip 5, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC

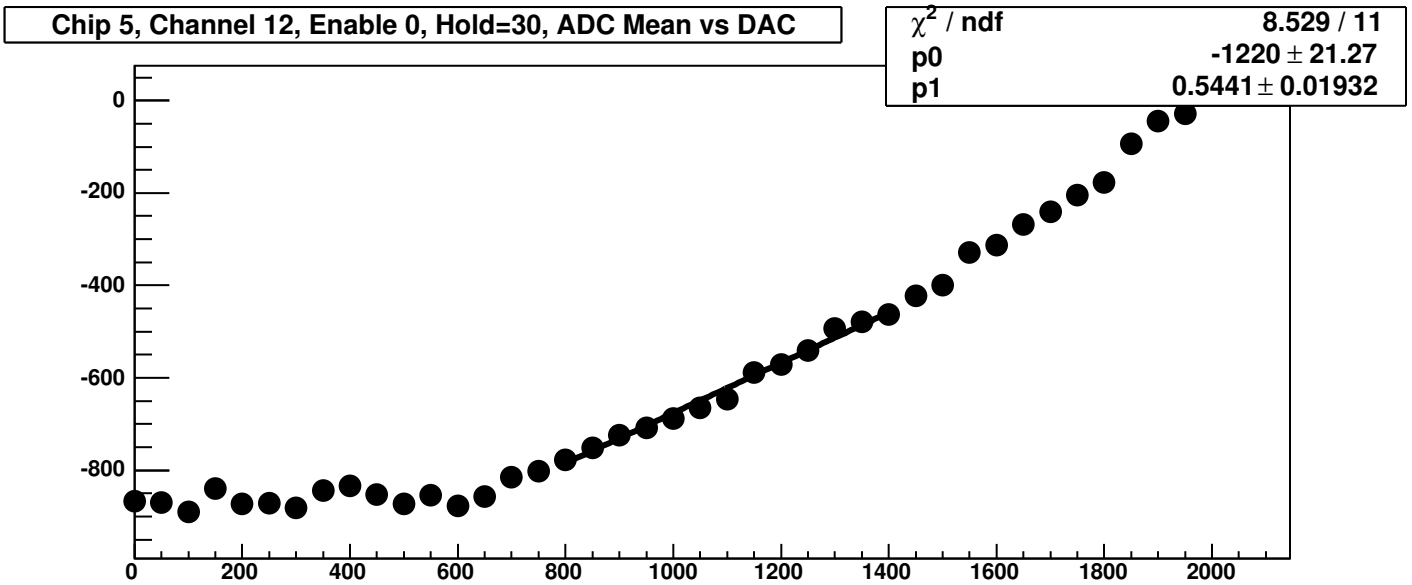


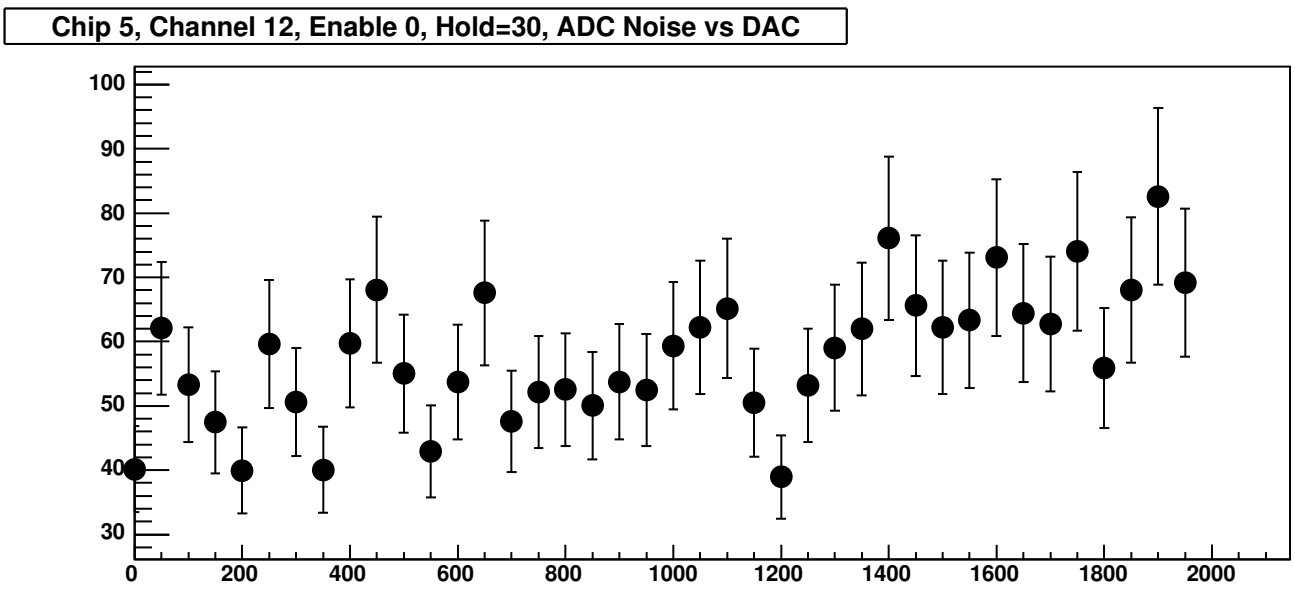
Chip 5, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC

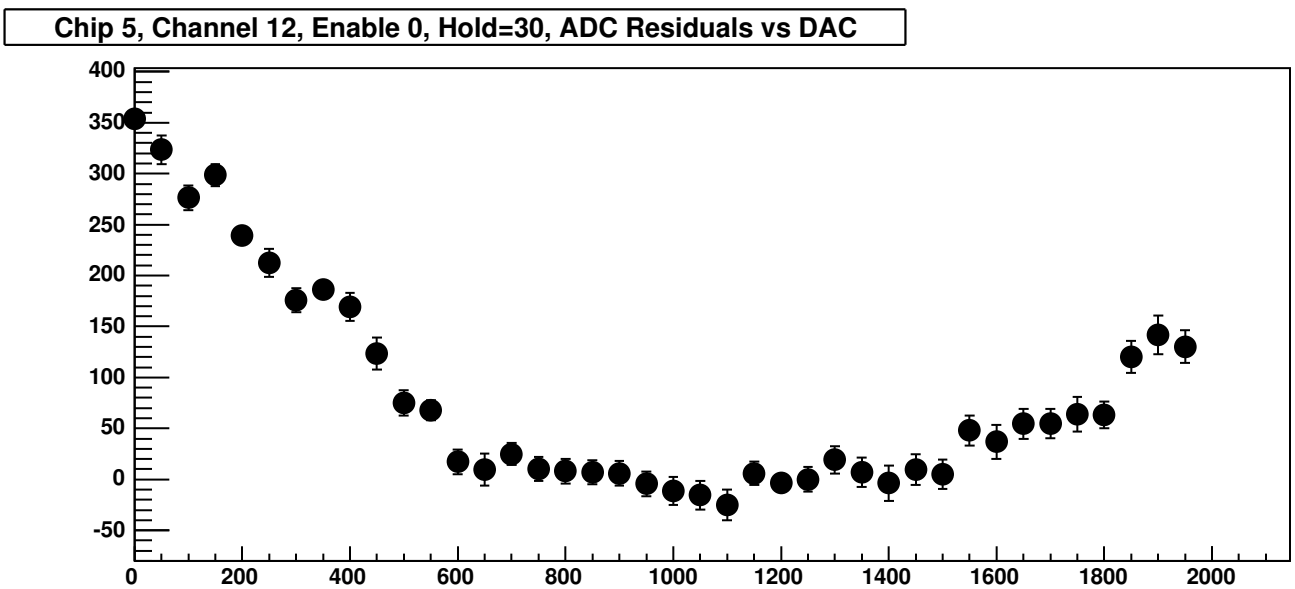


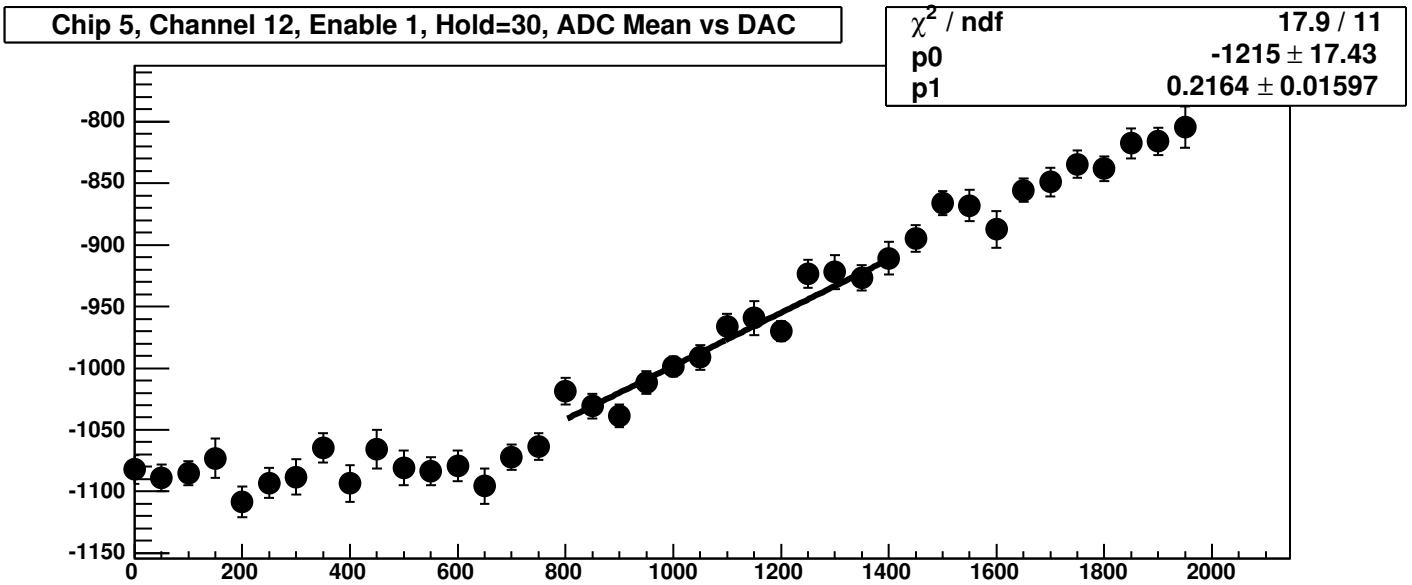
Chip 5, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC

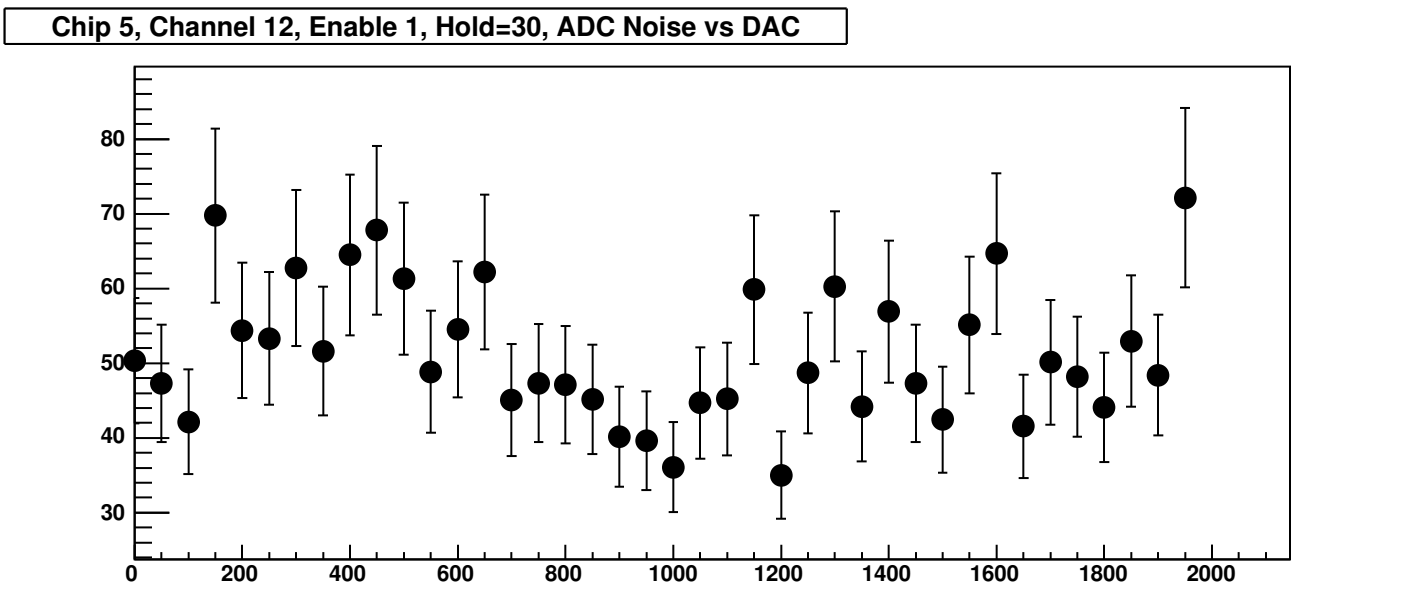


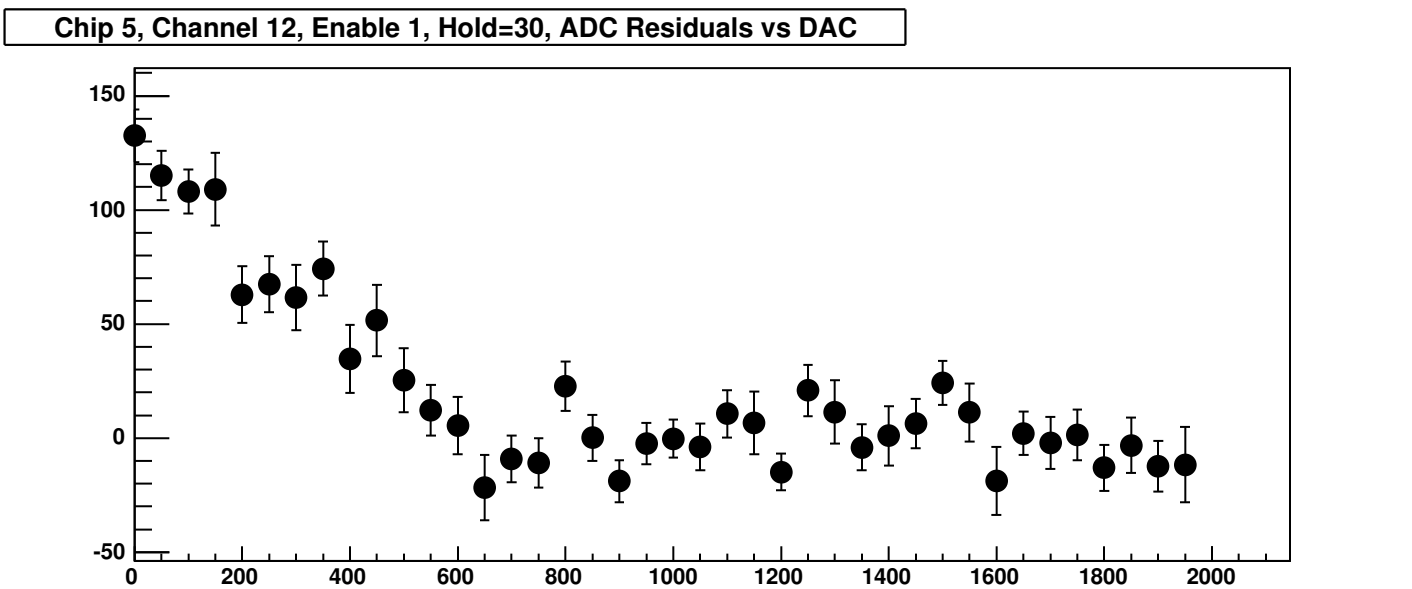

Chip 5, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 8.529 / 11
p0 -1220 ± 21.27
p1 0.5441 ± 0.01932
0
-200
-400
-600
-800
0 200 400 600 800 1000 1200 1400 1600 1800 2000

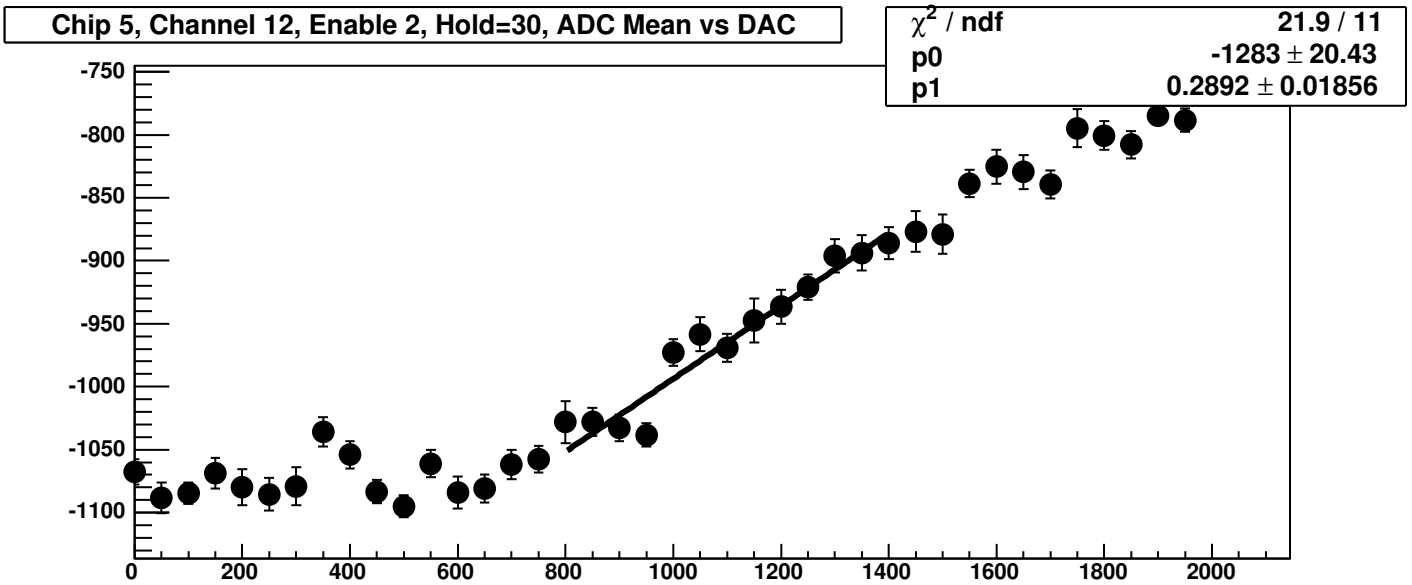

Chip 5, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0 200 400 600 800 1000 1200 1400 1600 1800 2000

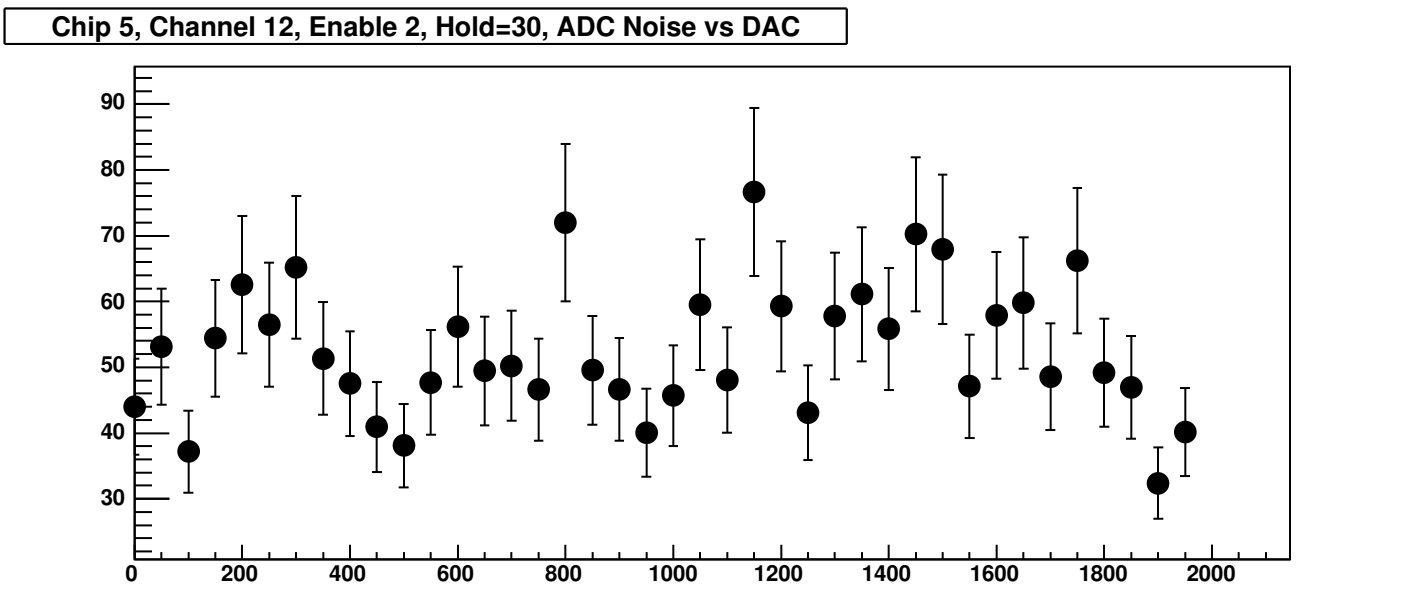

Chip 5, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC
400
350
300
250
200
150
100
50
0
-50
0 200 400 600 800 1000 1200 1400 1600 1800 2000

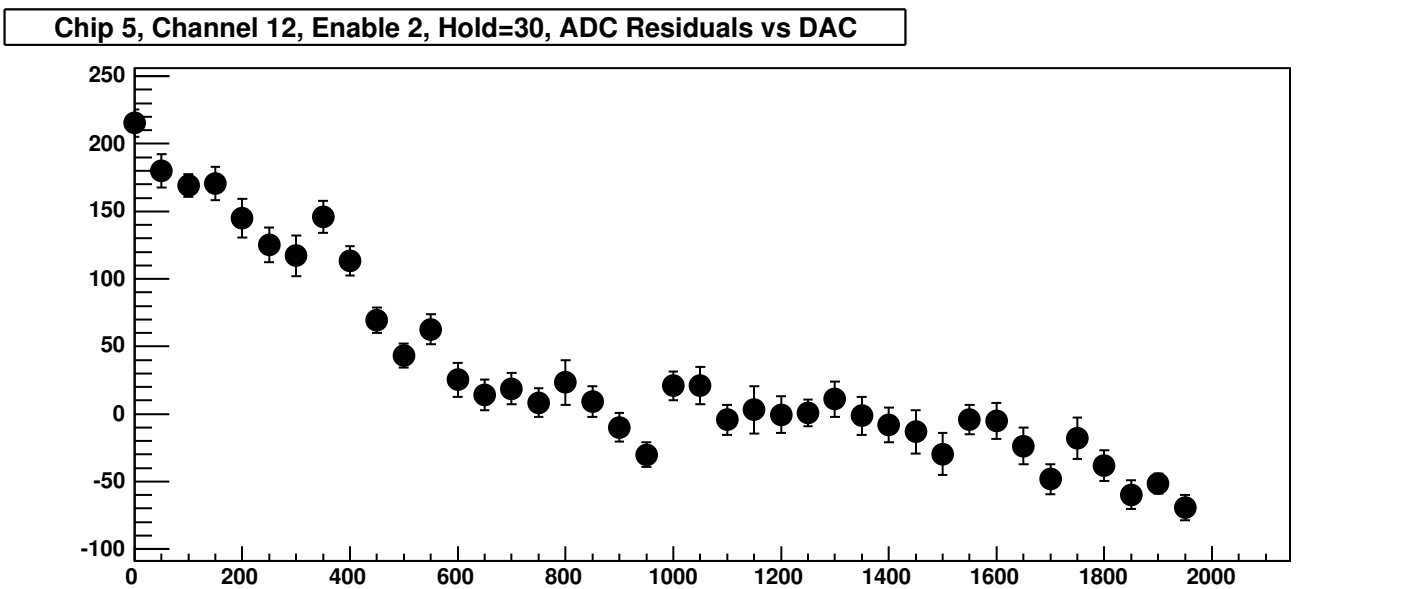

Chip 5, Channel 12, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 17.9 / 11
p0 -1215 ± 17.43
p1 0.2164 ± 0.01597
-800
-850
-900
-950
-1000
-1050
-1100
-1150
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 12, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

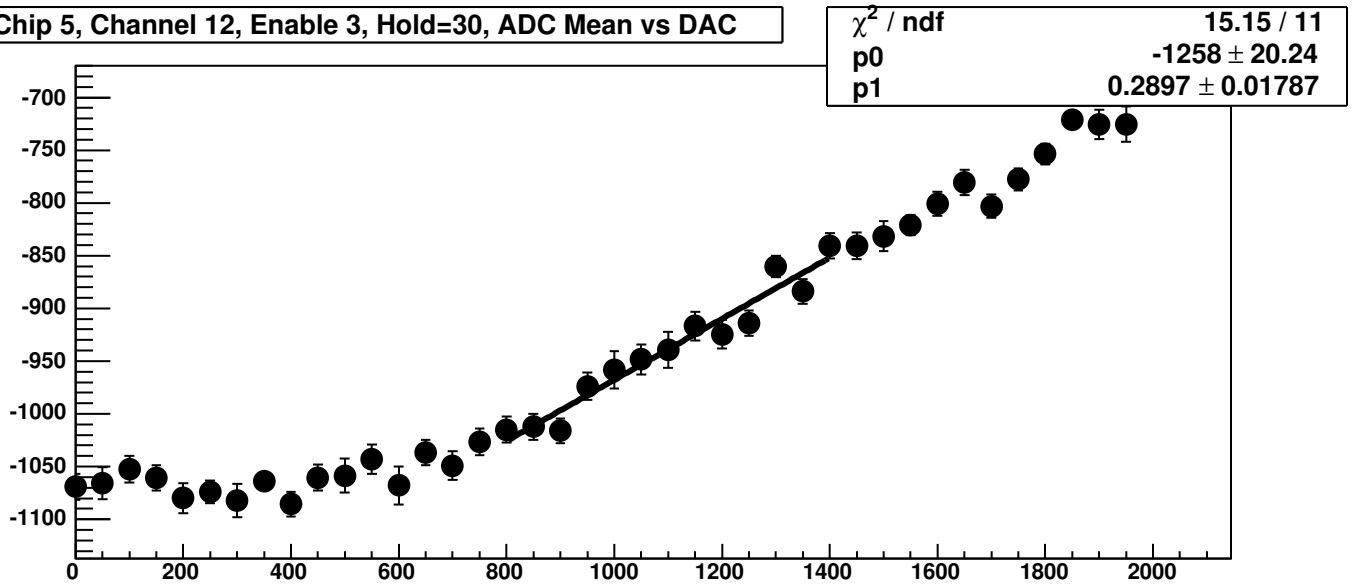

Chip 5, Channel 12, Enable 1, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 21.9 / 11
p0 -1283 ± 20.43
p1 0.2892 ± 0.01856
-750
-800
-850
-900
-950
-1000
-1050
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

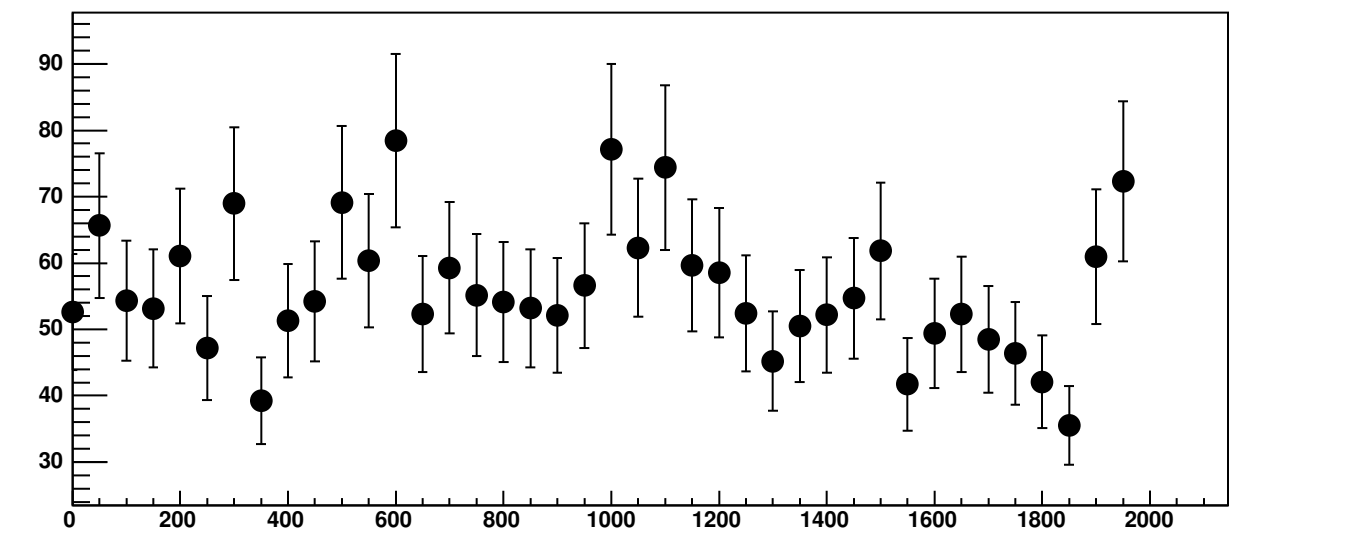

Chip 5, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

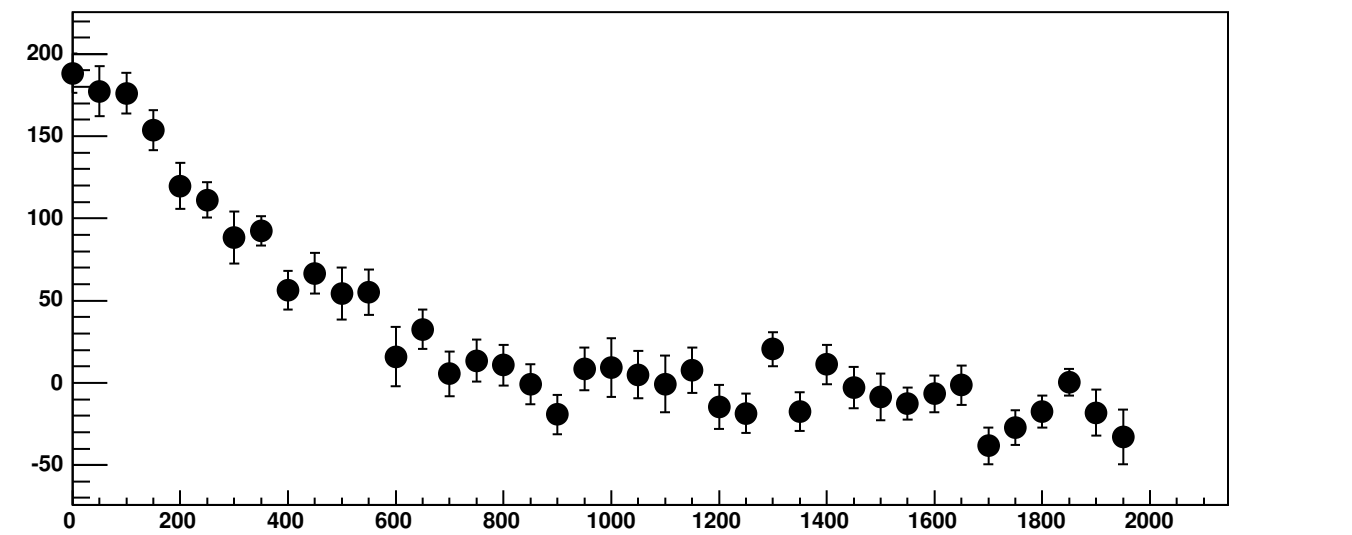
Chip 5, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC

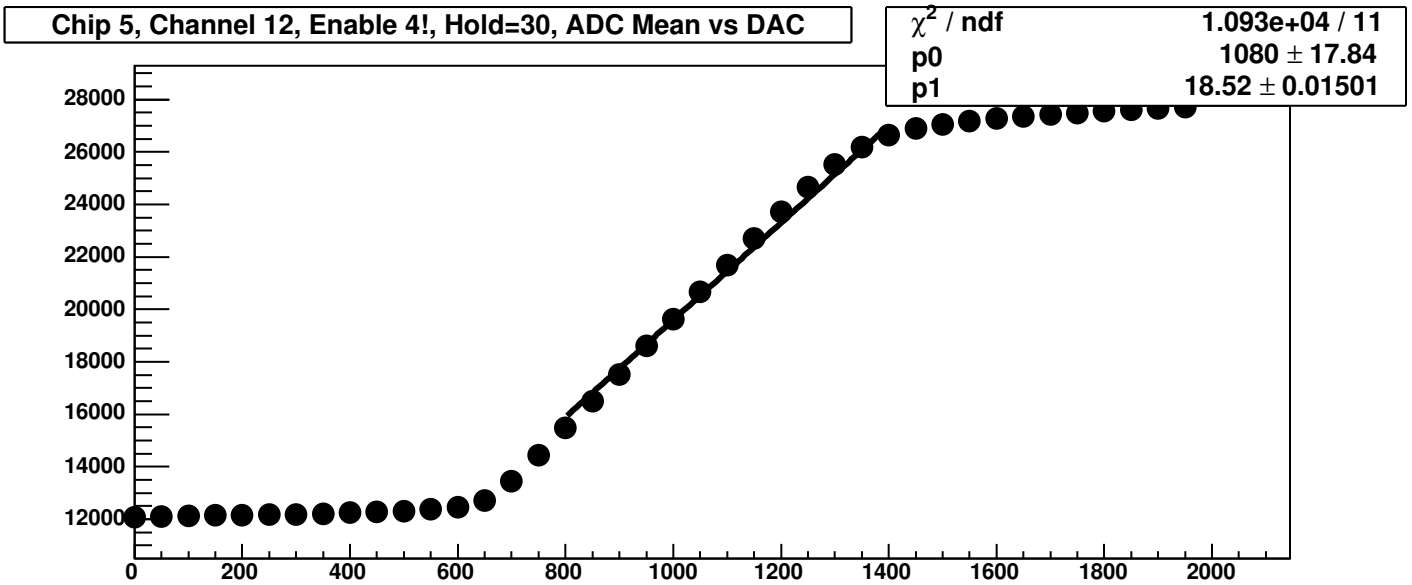


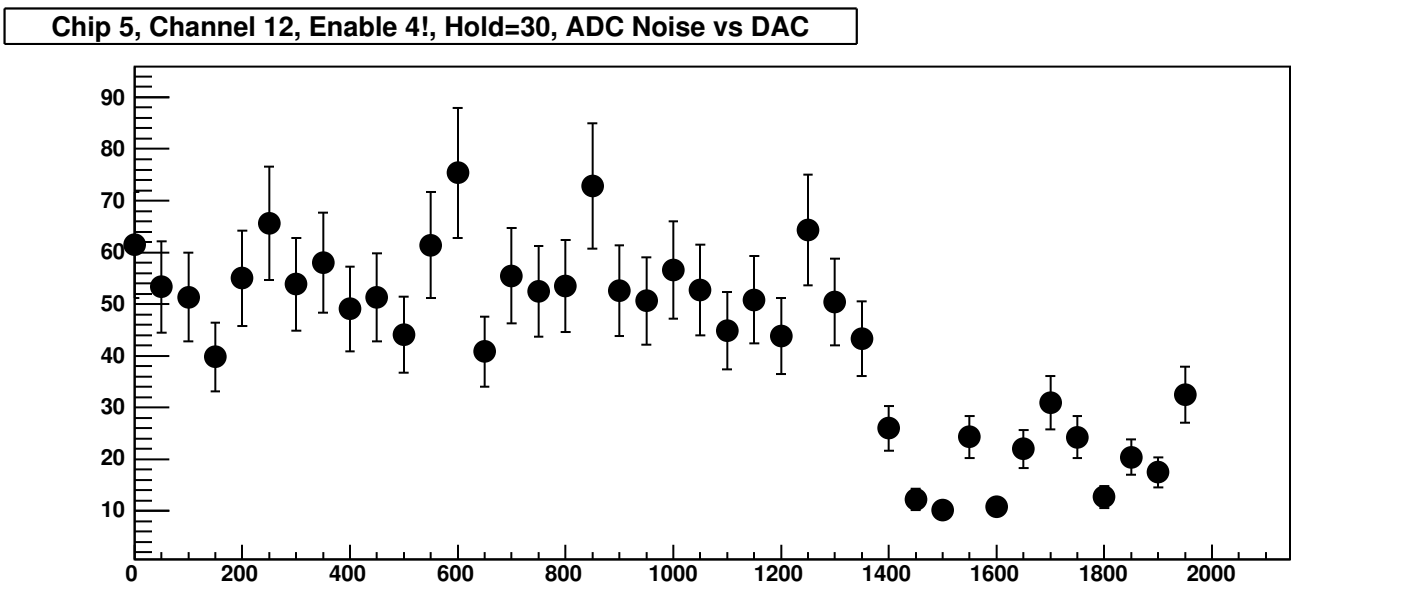
Chip 5, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC

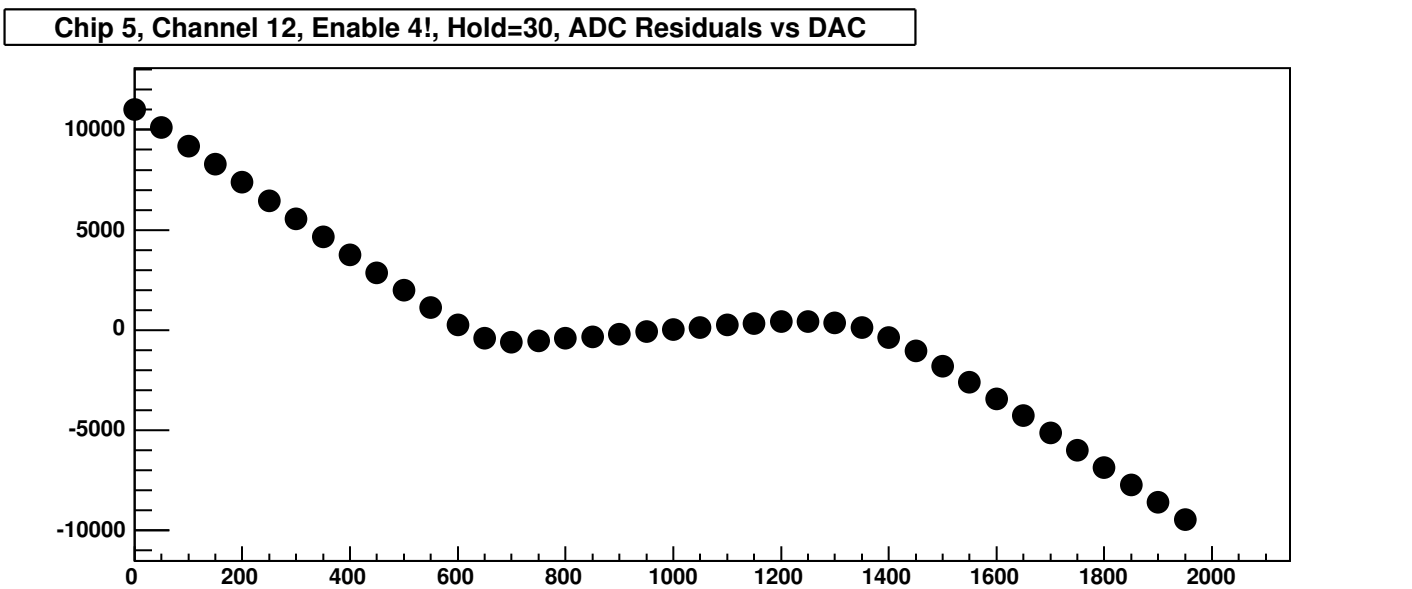


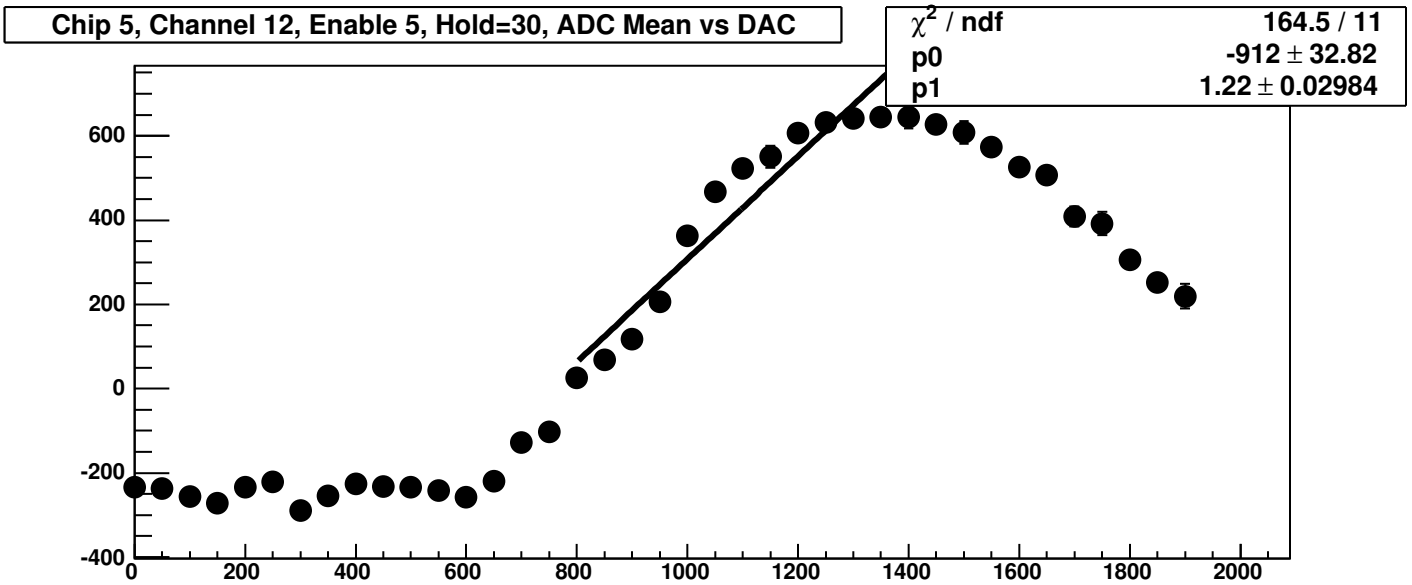
Chip 5, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC

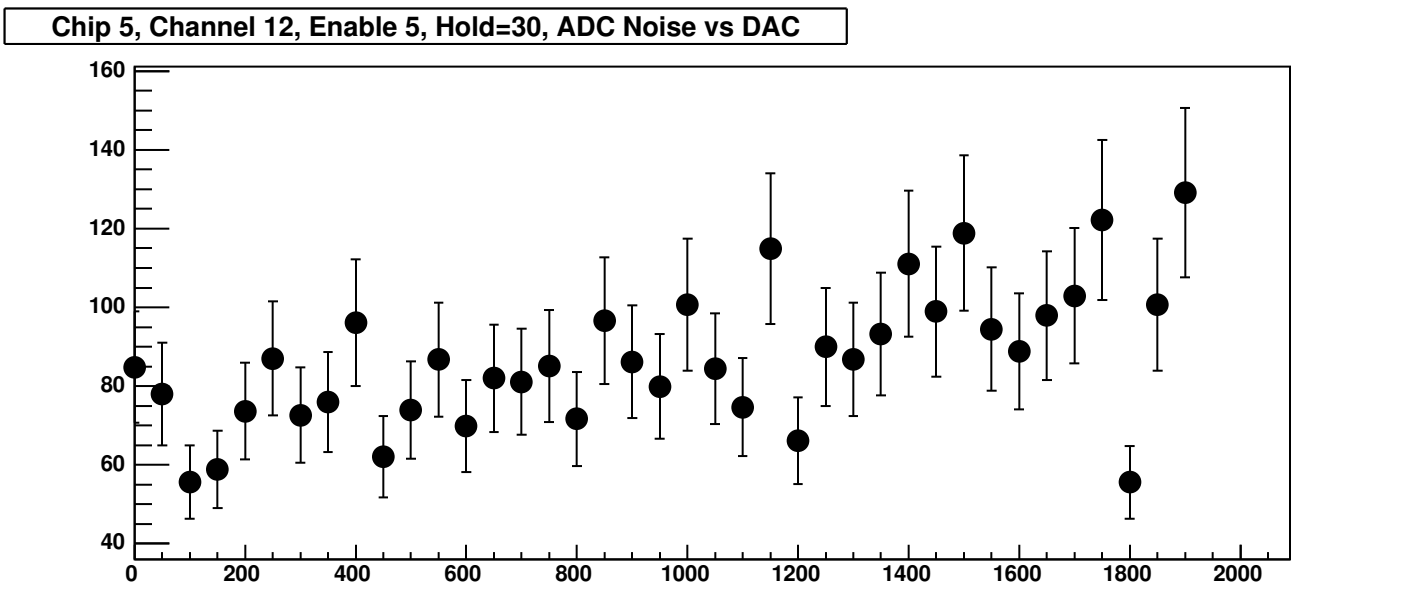


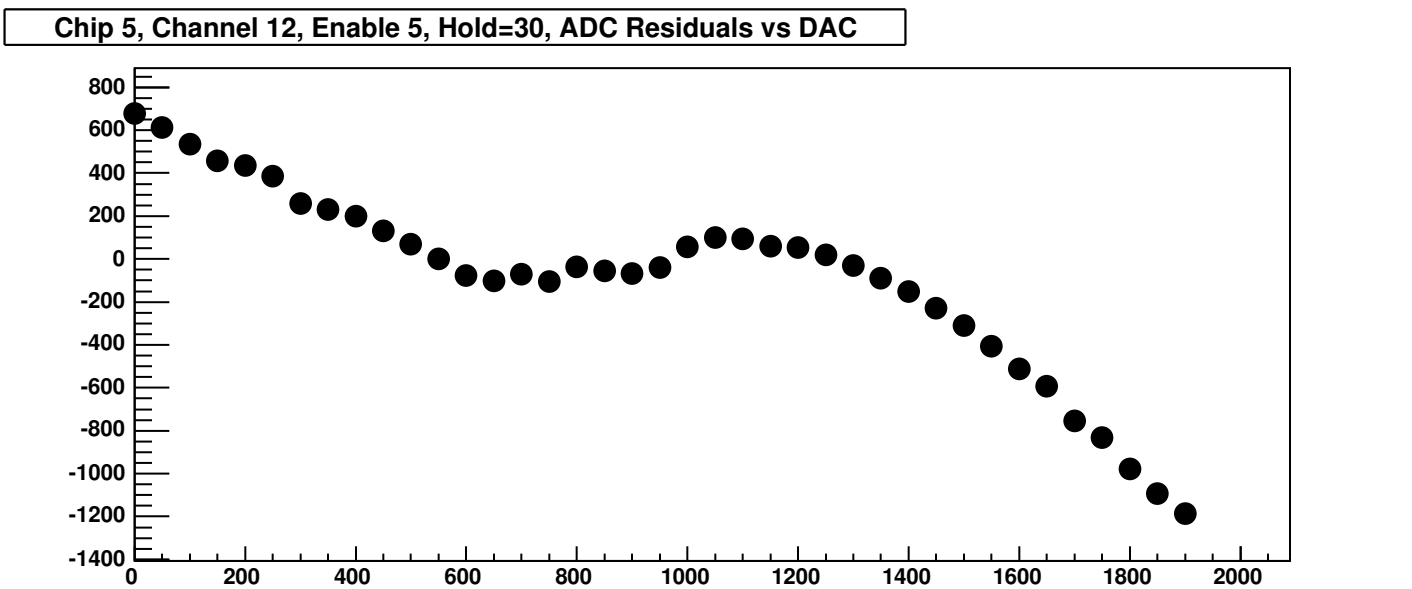

Chip 5, Channel 12, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.093e+04 / 11
p0 1080 ± 17.84
p1 18.52 ± 0.01501
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

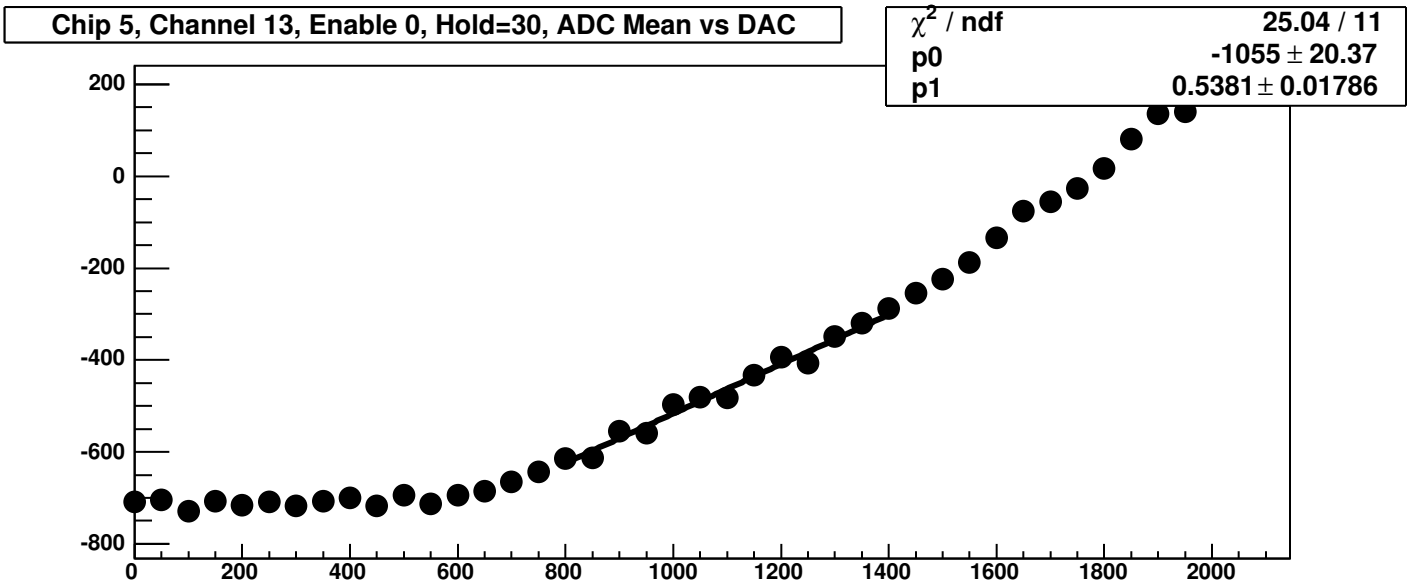

Chip 5, Channel 12, Enable 4!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

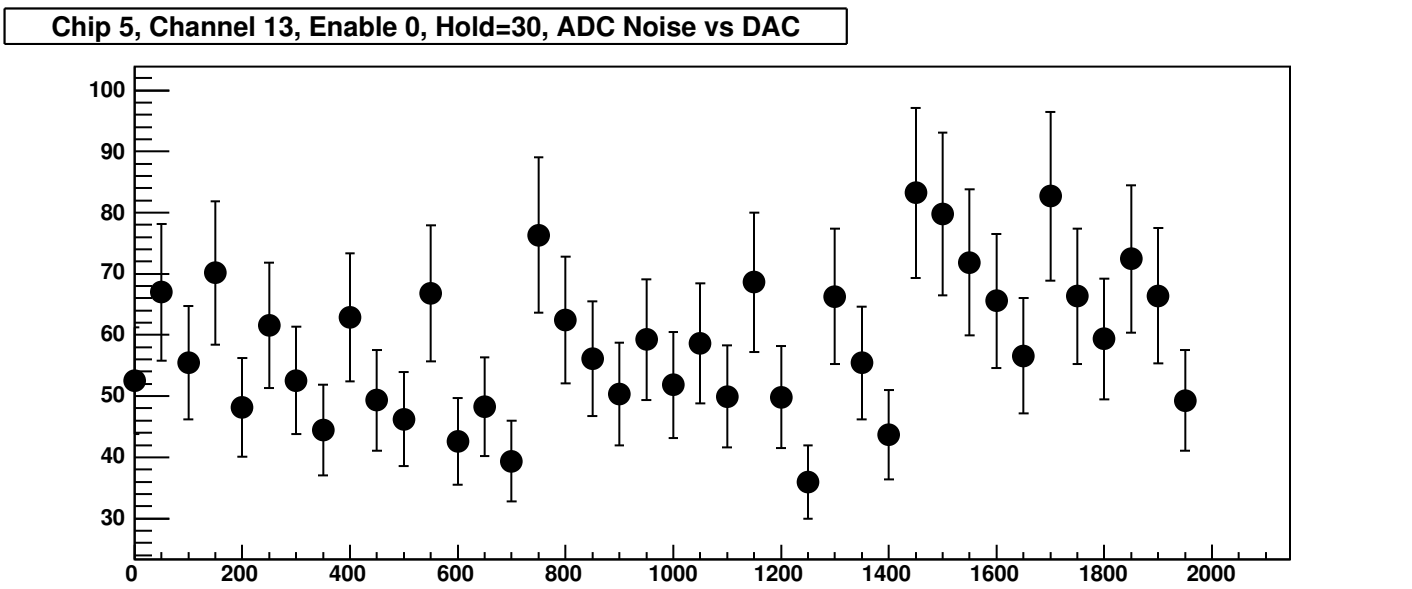

Chip 5, Channel 12, Enable 4!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

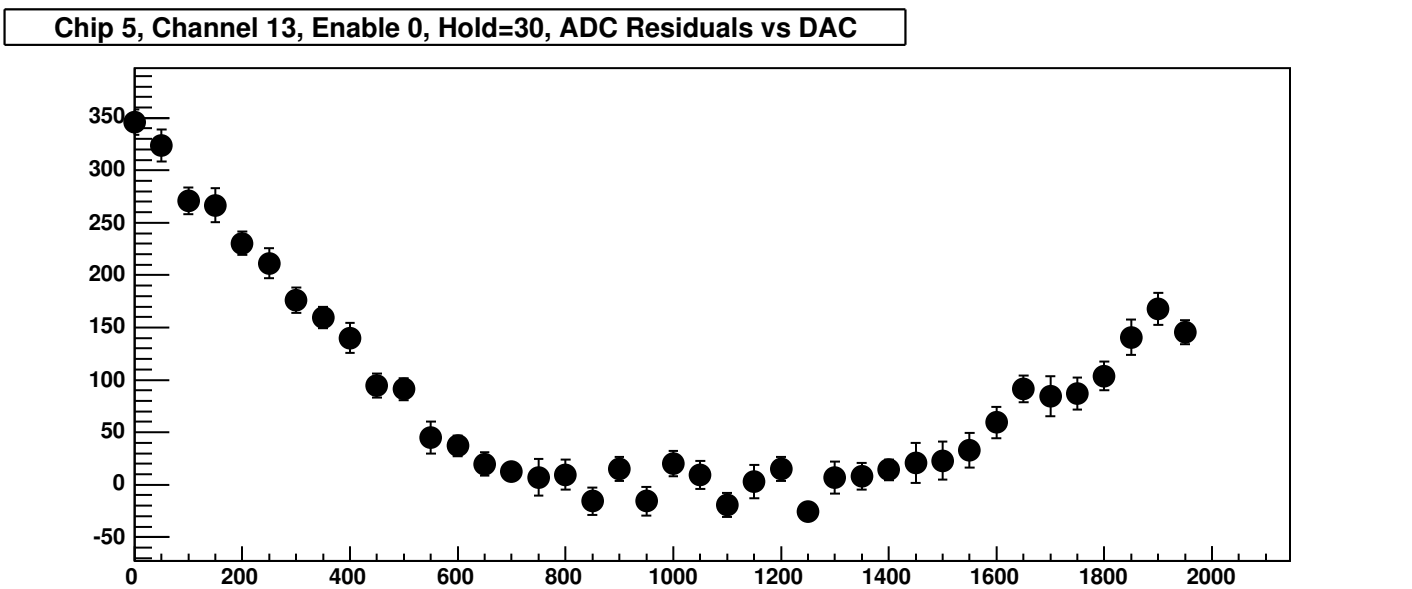

Chip 5, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 164.5 / 11
p0 -912 ± 32.82
p1 1.22 ± 0.02984
600
400
200
0
-200
-400
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

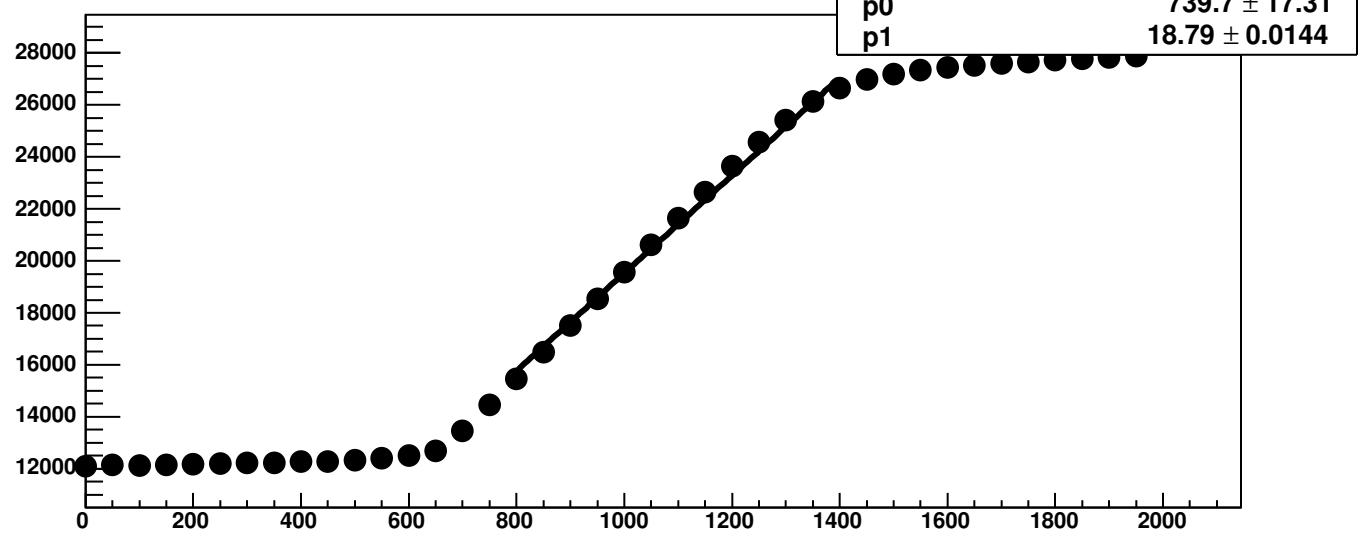

Chip 5, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 25.04 / 11
p0 -1055 ± 20.37
p1 0.5381 ± 0.01786
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

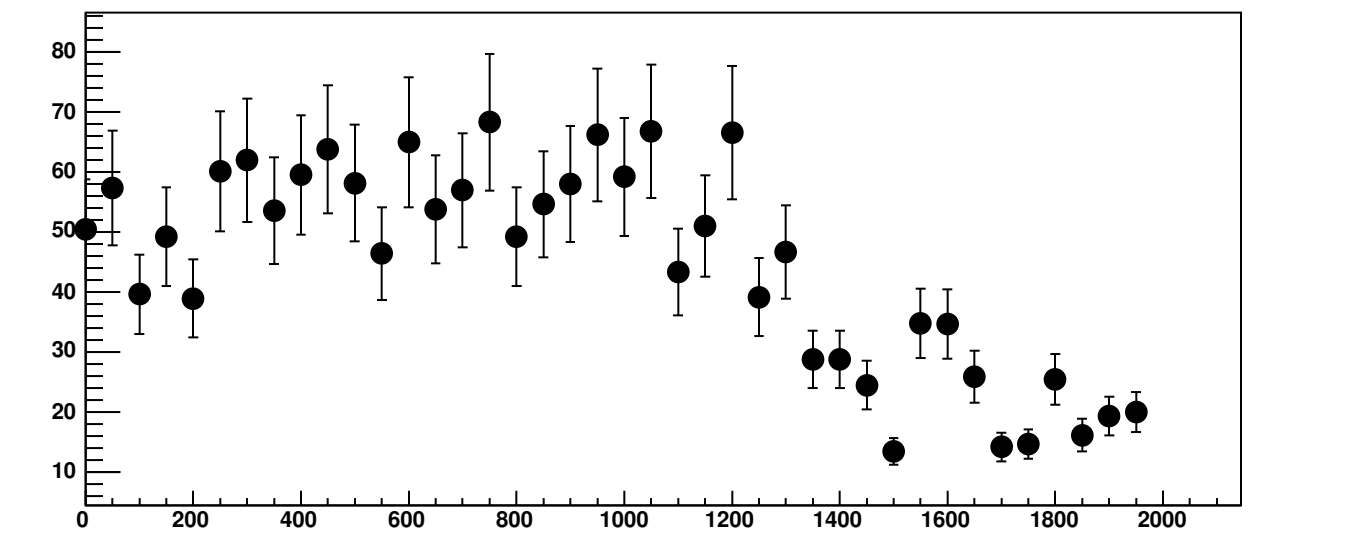

Chip 5, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

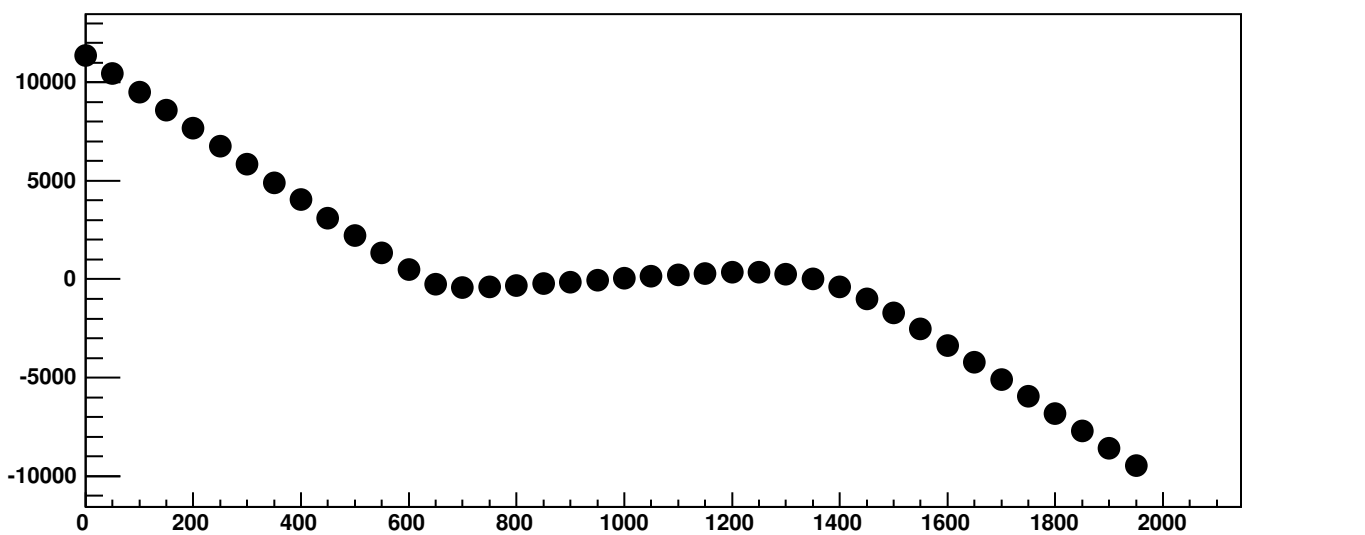
Chip 5, Channel 13, Enable 1!, Hold=30, ADC Mean vs DAC

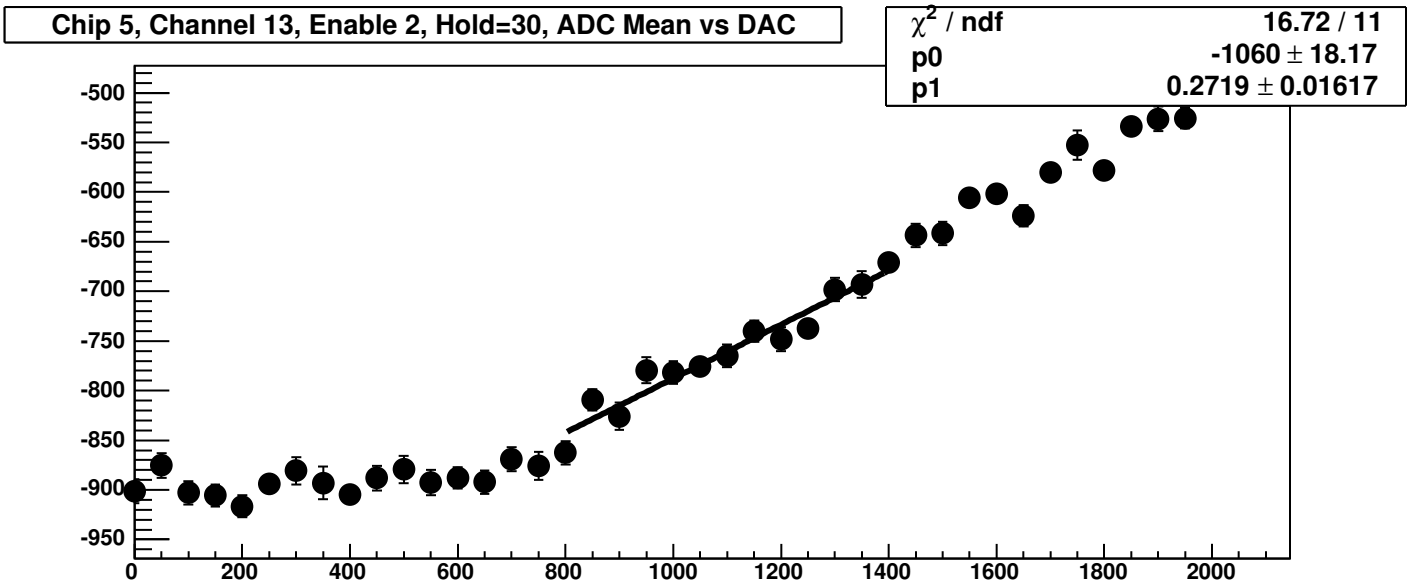


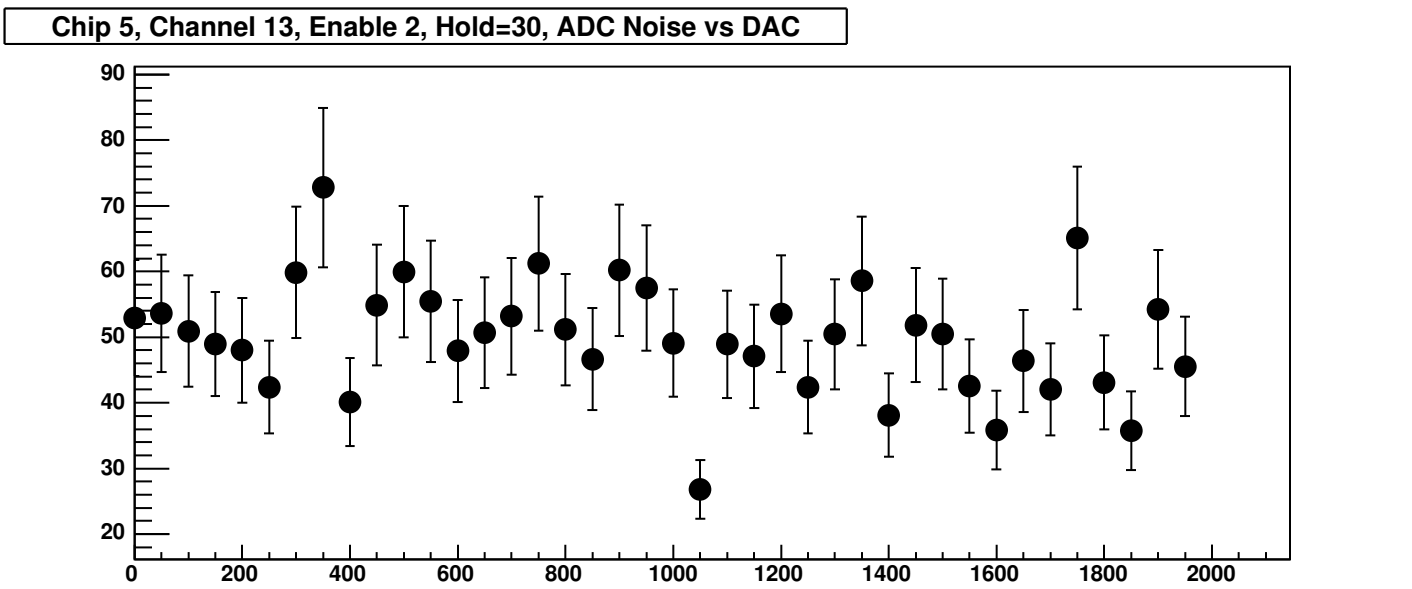
Chip 5, Channel 13, Enable 1!, Hold=30, ADC Noise vs DAC

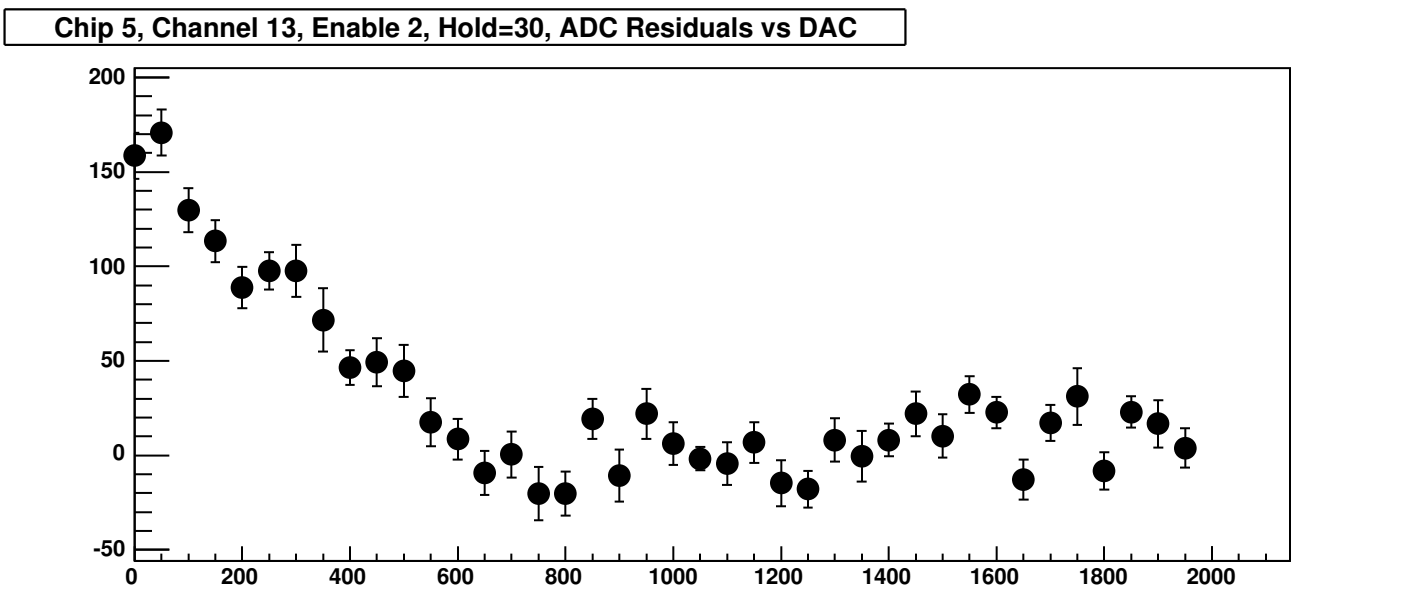


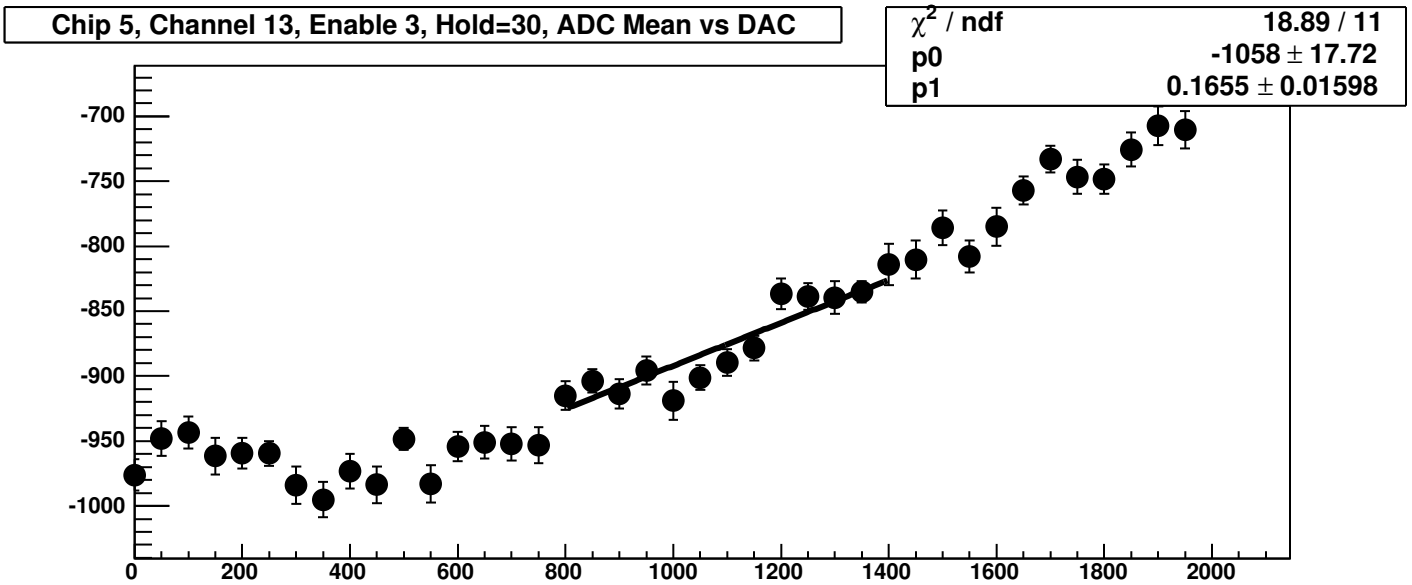
Chip 5, Channel 13, Enable 1!, Hold=30, ADC Residuals vs DAC

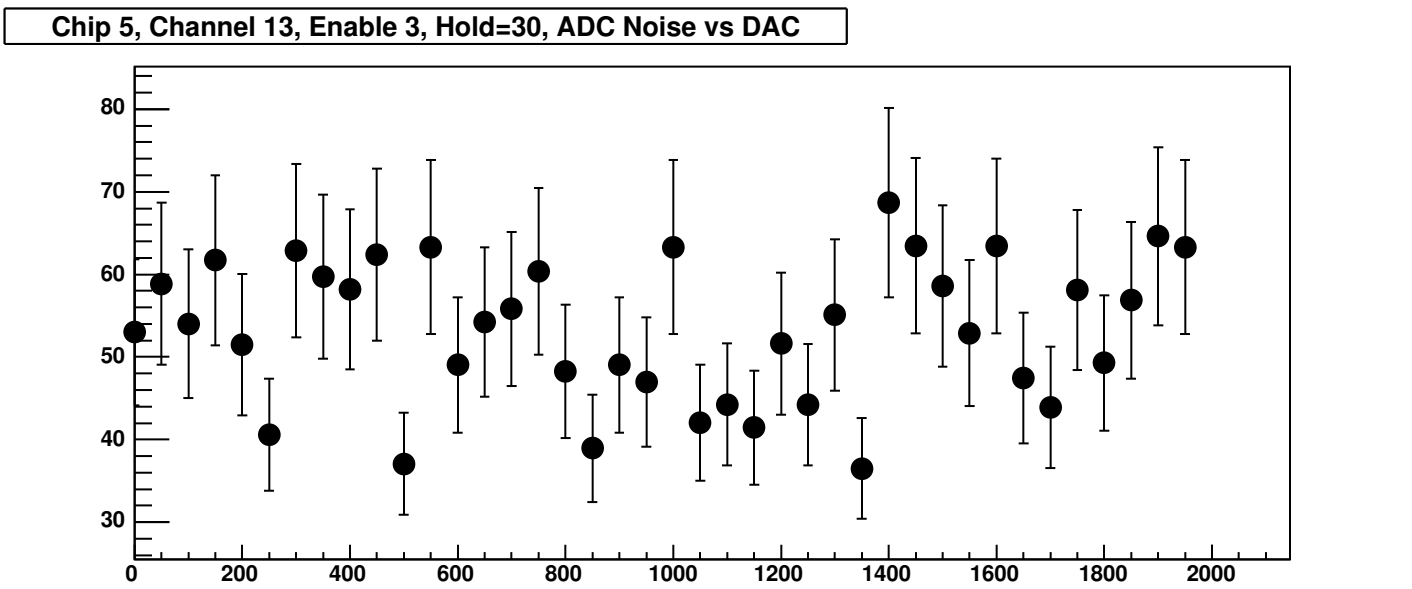


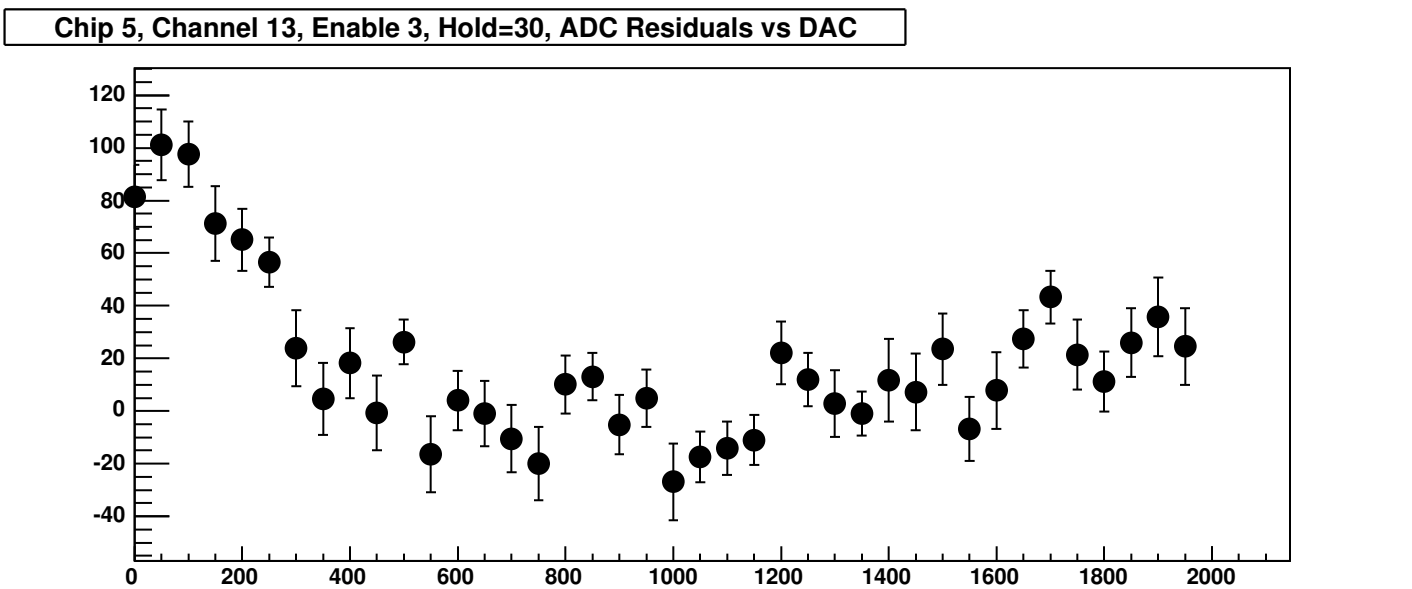

Chip 5, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 16.72 / 11
p0 -1060 ± 18.17
p1 0.2719 ± 0.01617
-500
-550
-600
-650
-700
-750
-800
-850
-900
-950
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

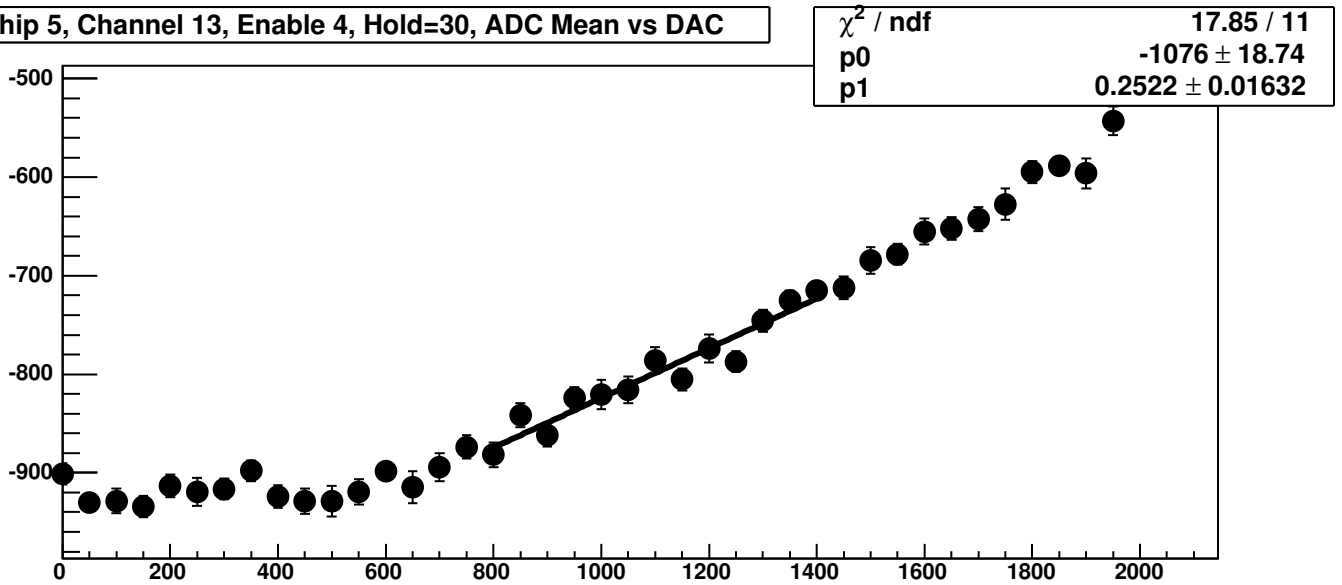

Chip 5, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 18.89 / 11
p0 -1058 ± 17.72
p1 0.1655 ± 0.01598
-700
-750
-800
-850
-900
-950
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

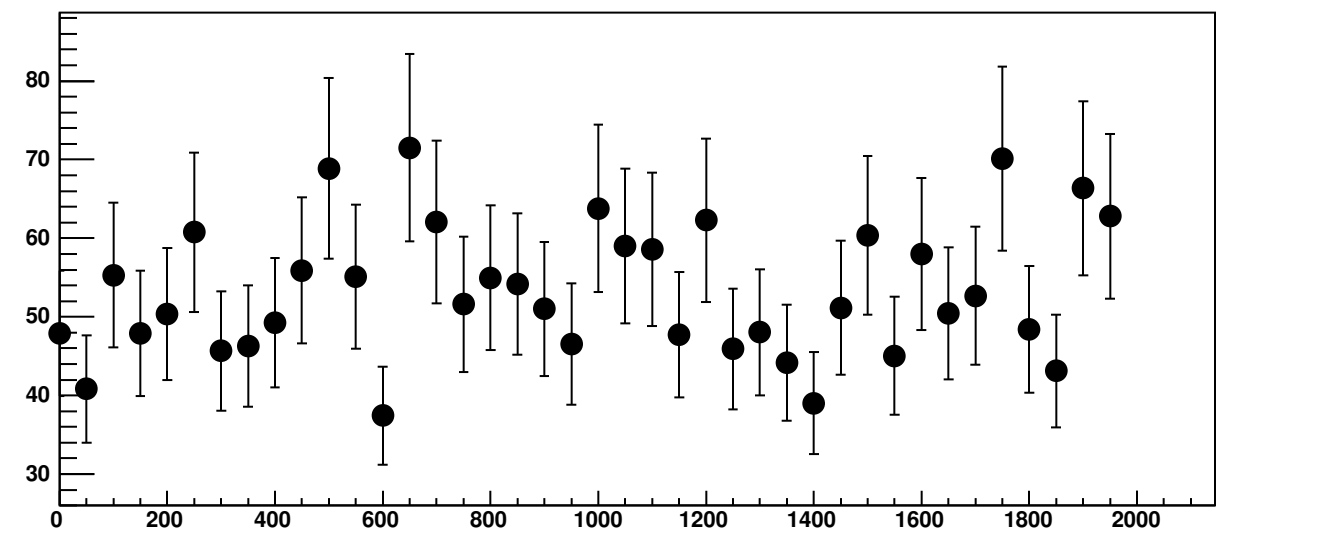

Chip 5, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

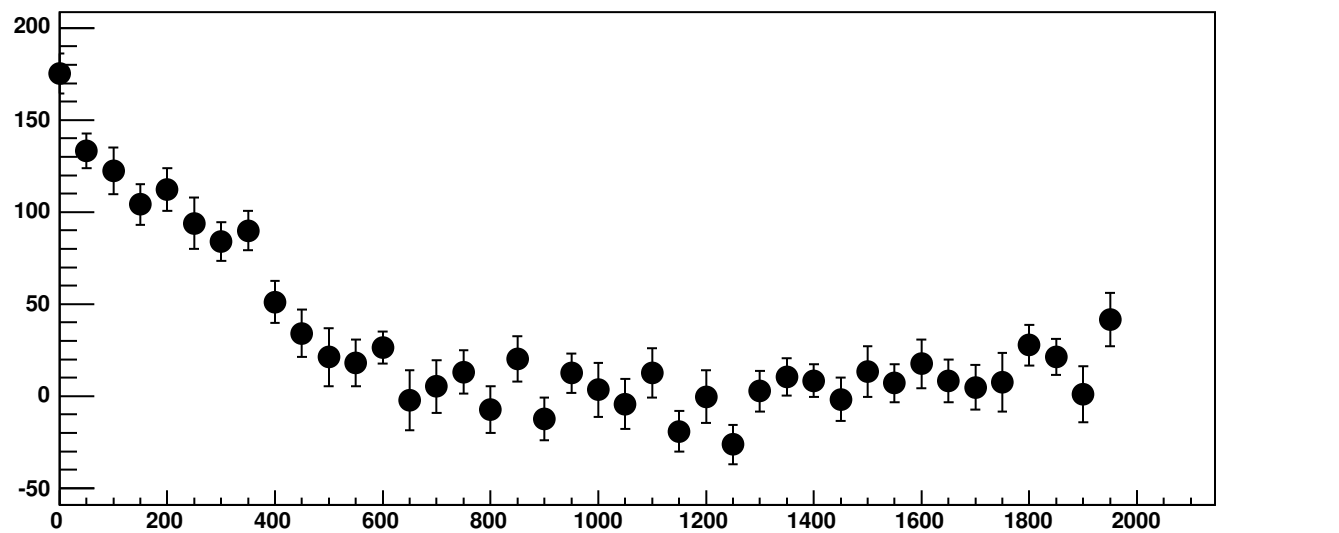
Chip 5, Channel 13, Enable 4, Hold=30, ADC Mean vs DAC

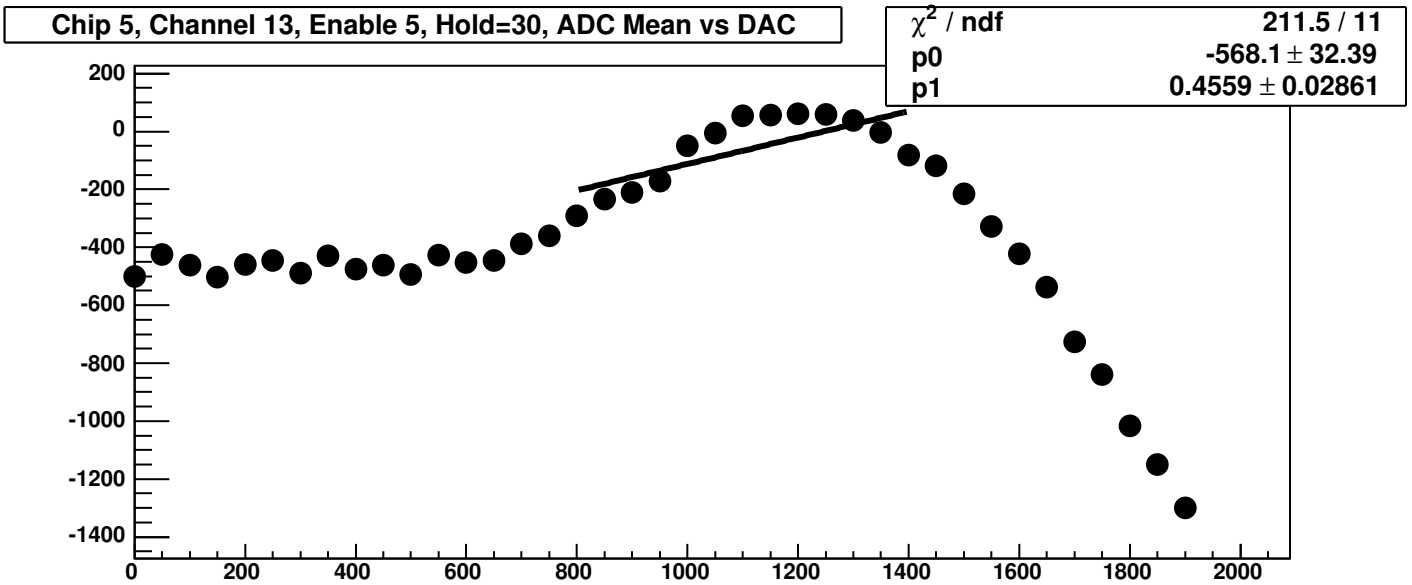


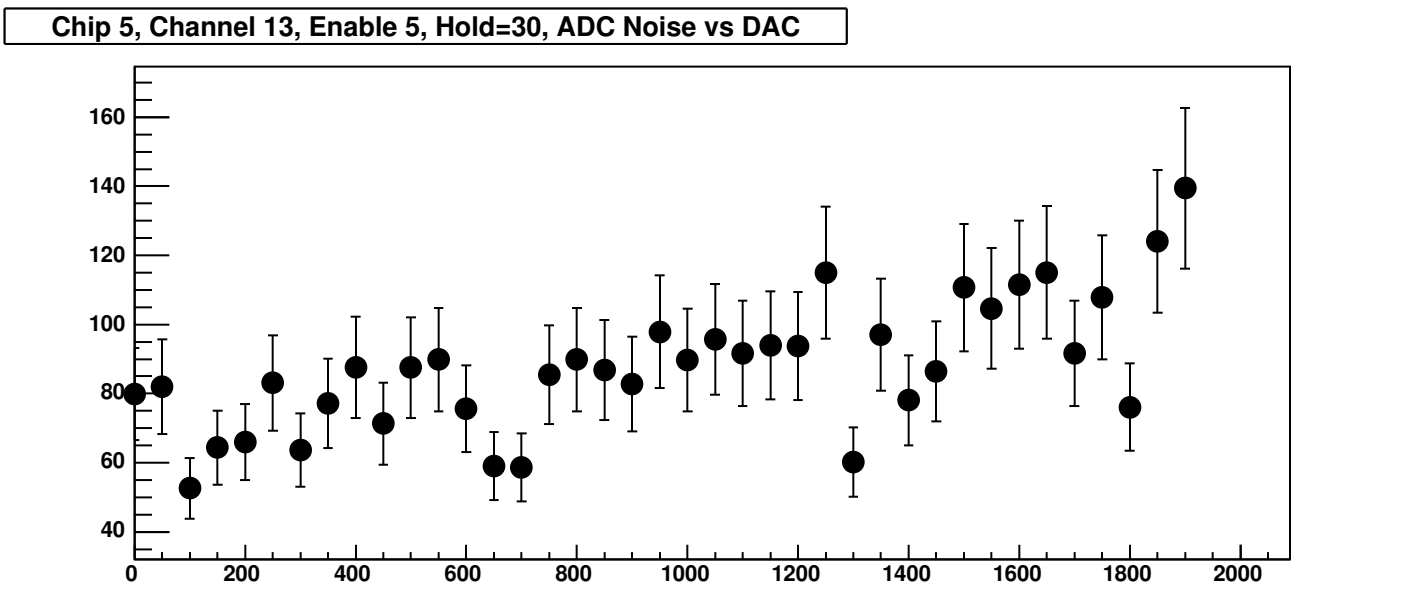
Chip 5, Channel 13, Enable 4, Hold=30, ADC Noise vs DAC

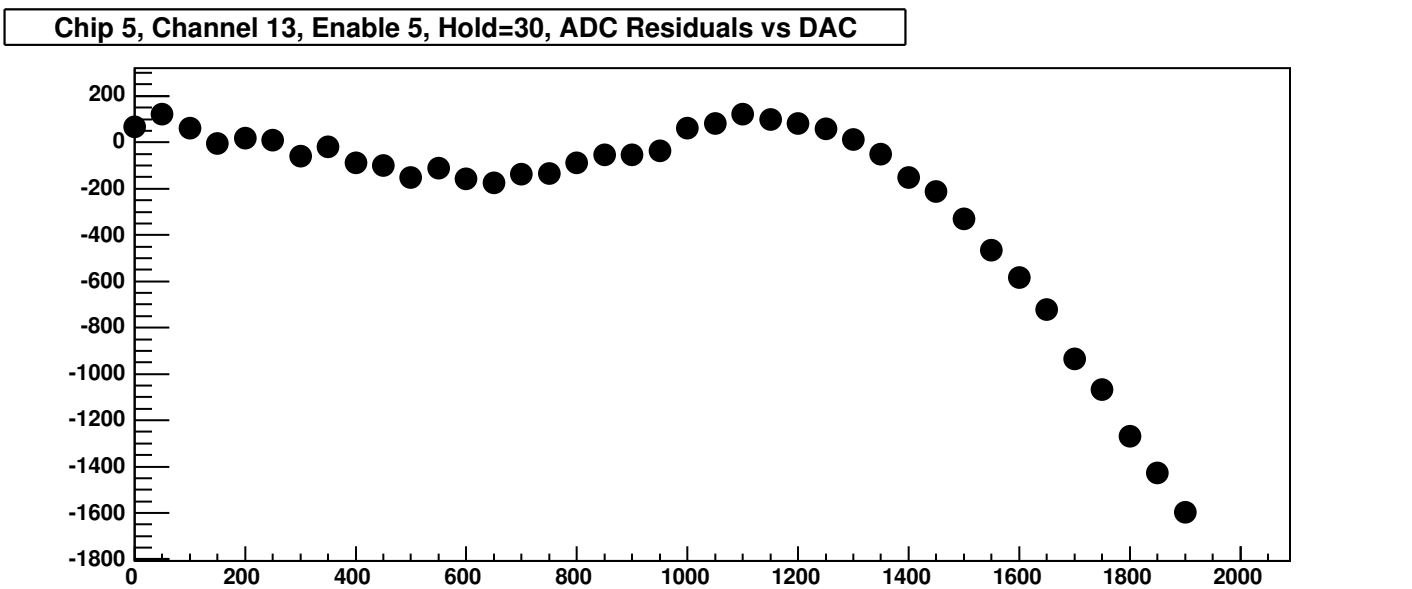


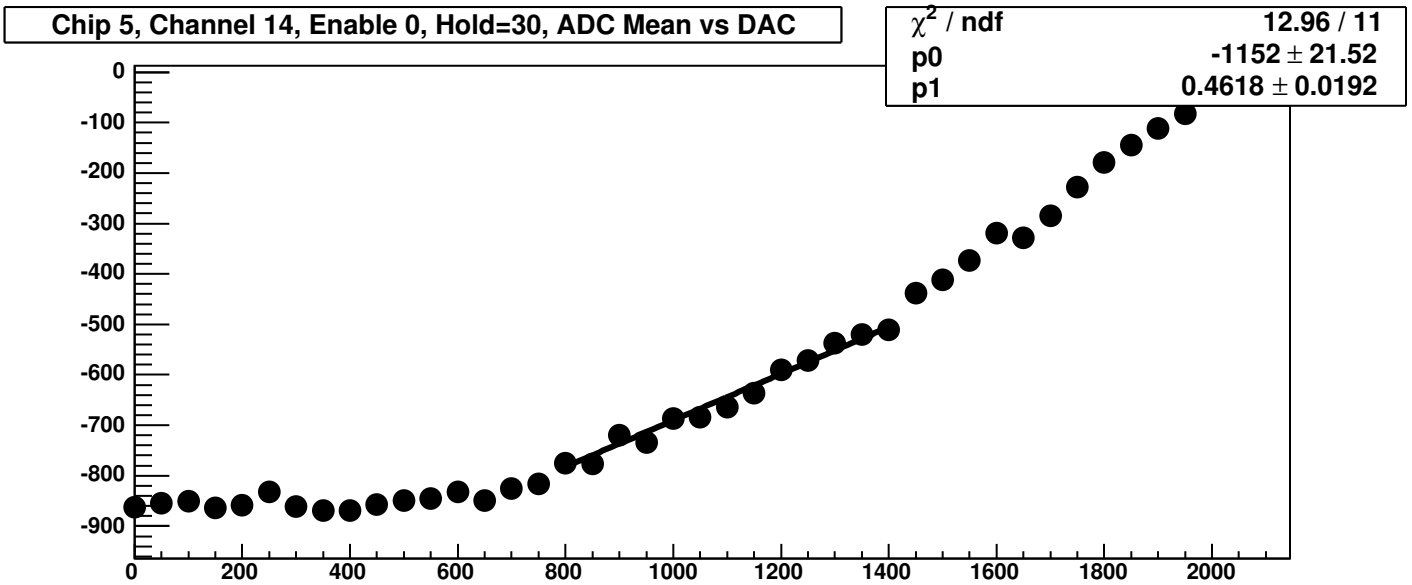
Chip 5, Channel 13, Enable 4, Hold=30, ADC Residuals vs DAC

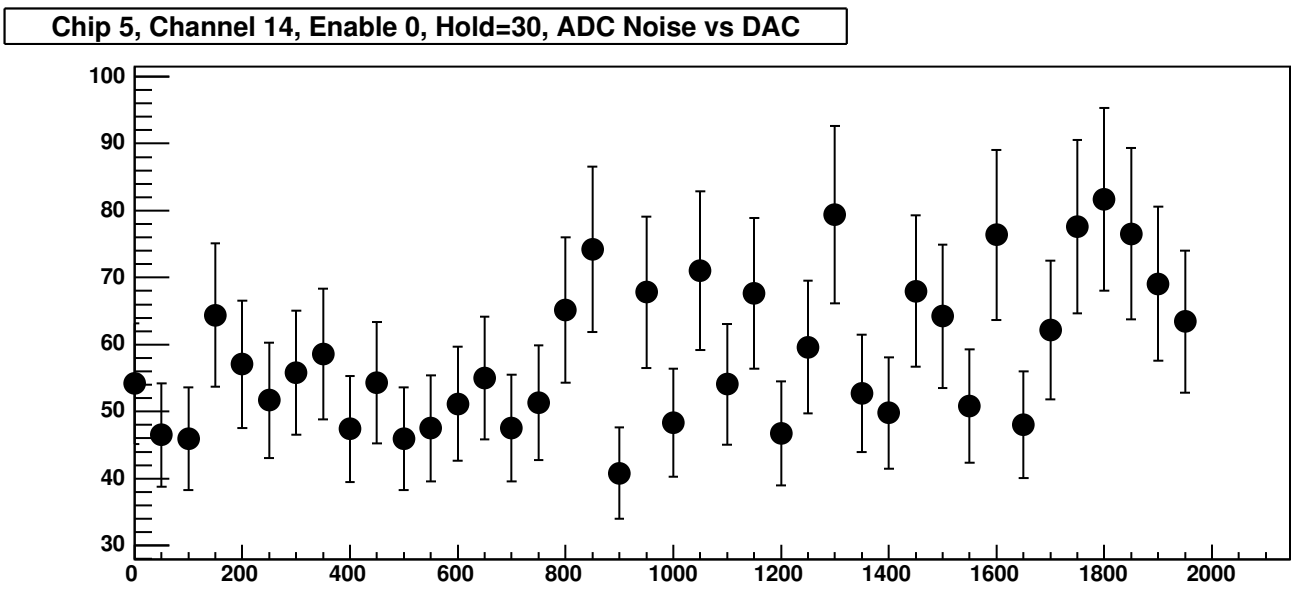


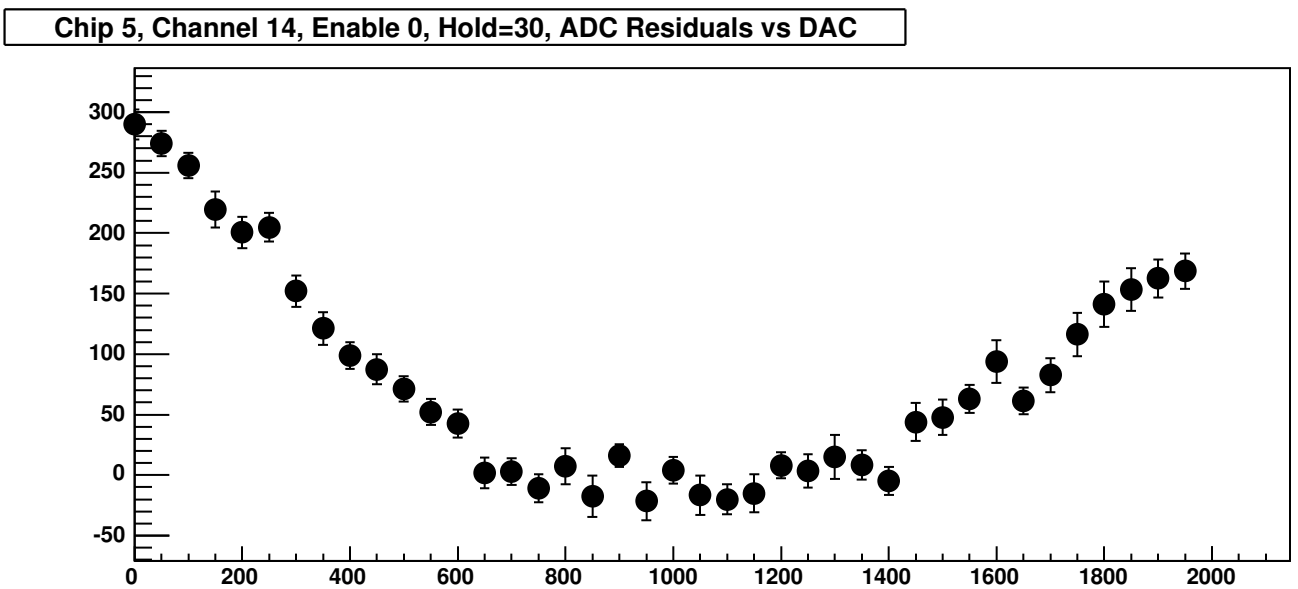

Chip 5, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 211.5 / 11
p0 -568.1 ± 32.39
p1 0.4559 ± 0.02861
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

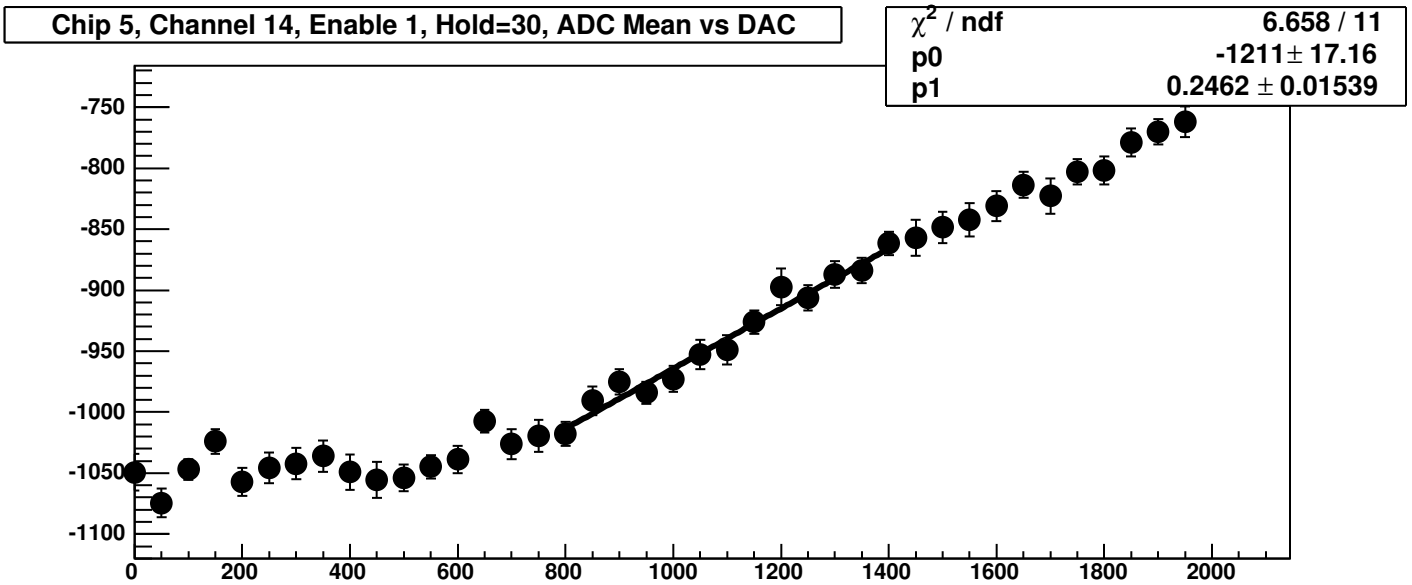

Chip 5, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

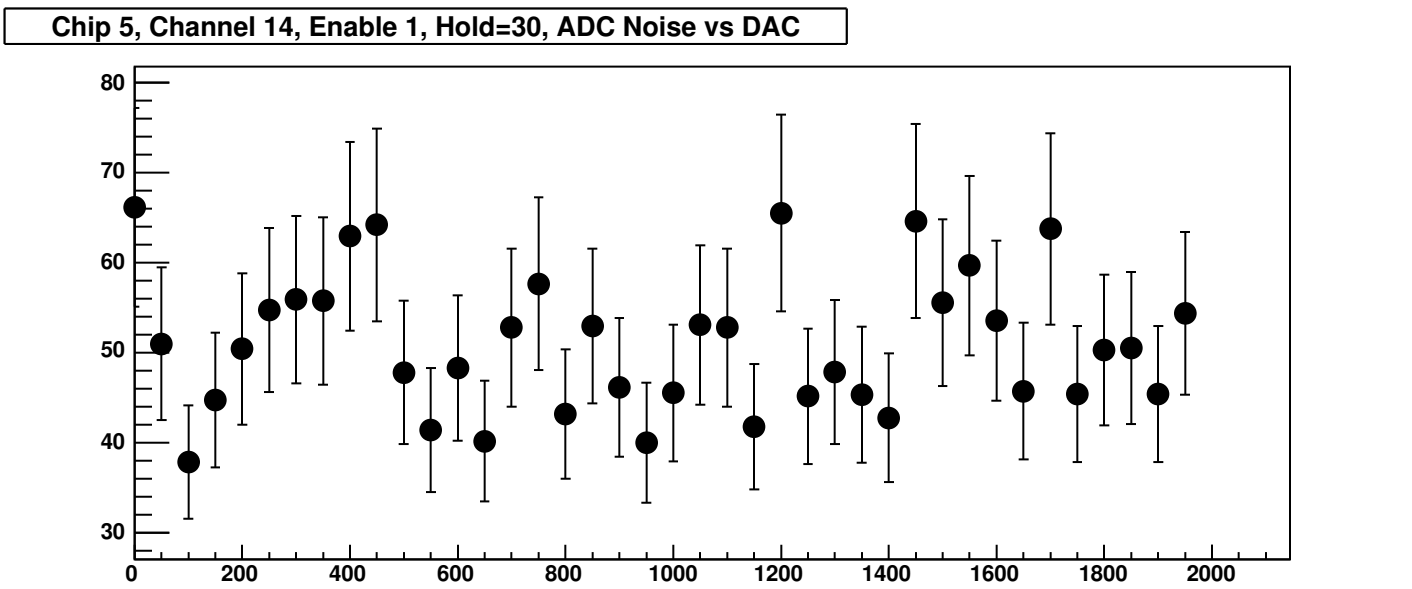

Chip 5, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

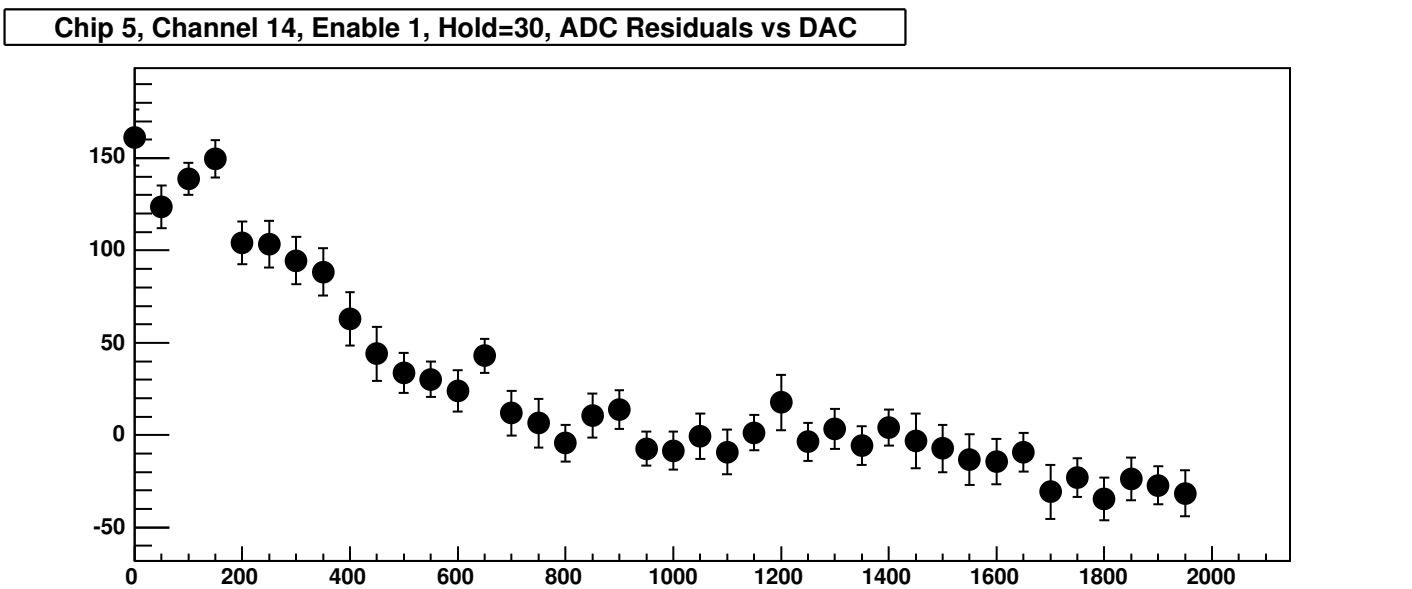

Chip 5, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 12.96 / 11
p0 -1152 ± 21.52
p1 0.4618 ± 0.0192
0
-100
-200
-300
-400
-500
-600
-700
-800
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

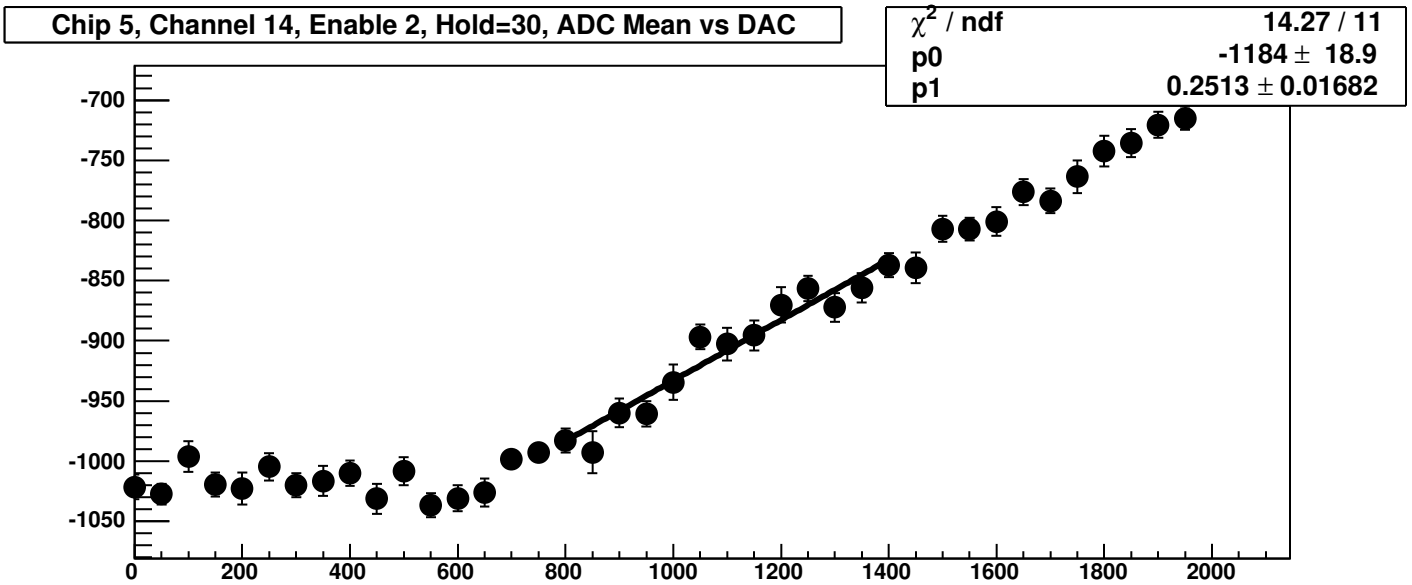

Chip 5, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

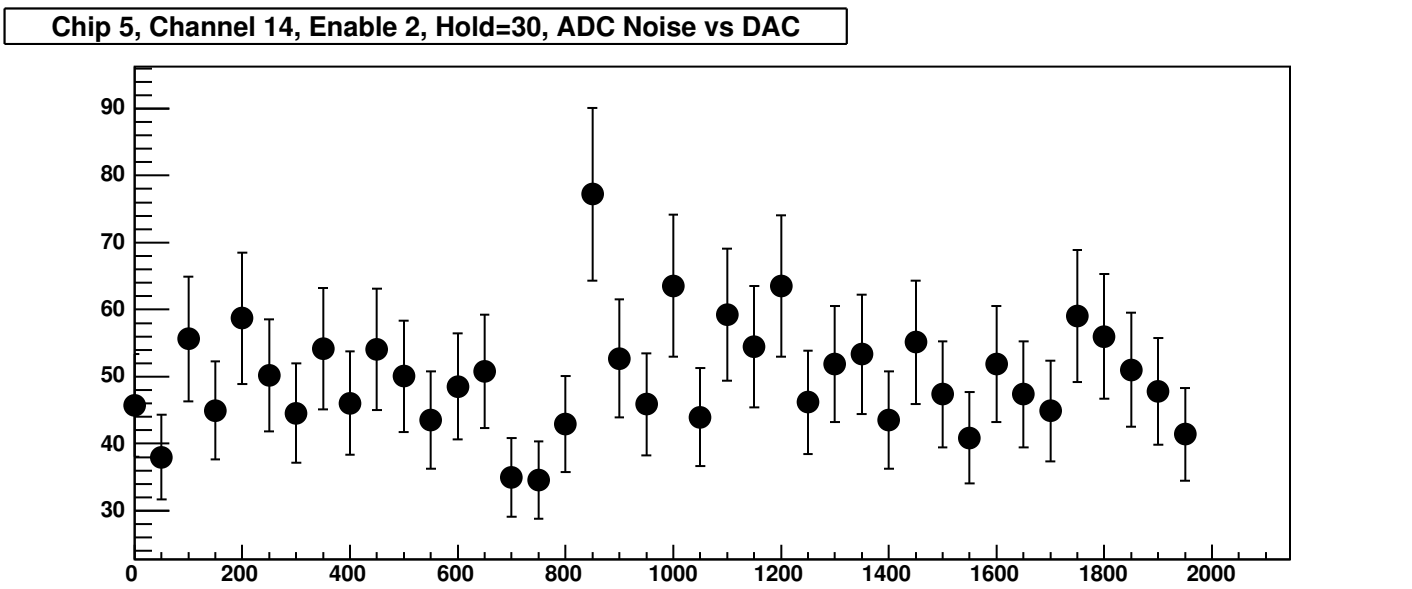

Chip 5, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

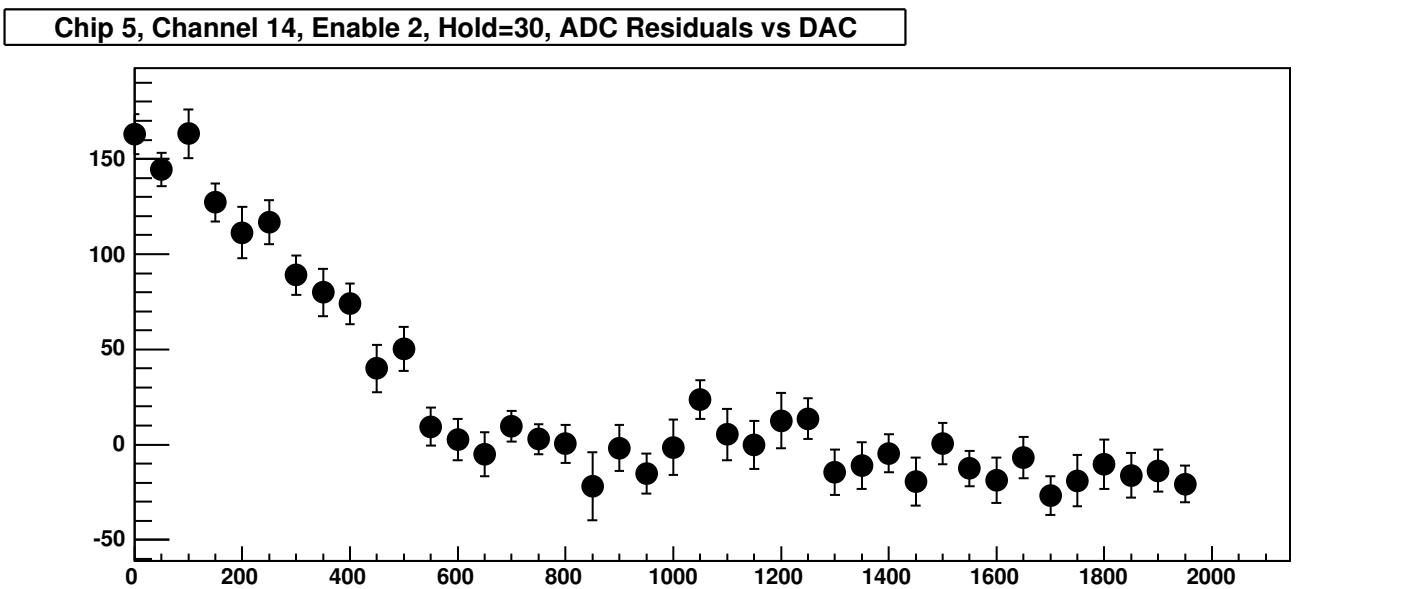

Chip 5, Channel 14, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 6.658 / 11
p0 -1211 ± 17.16
p1 0.2462 ± 0.01539
-750
-800
-850
-900
-950
-1000
-1050
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

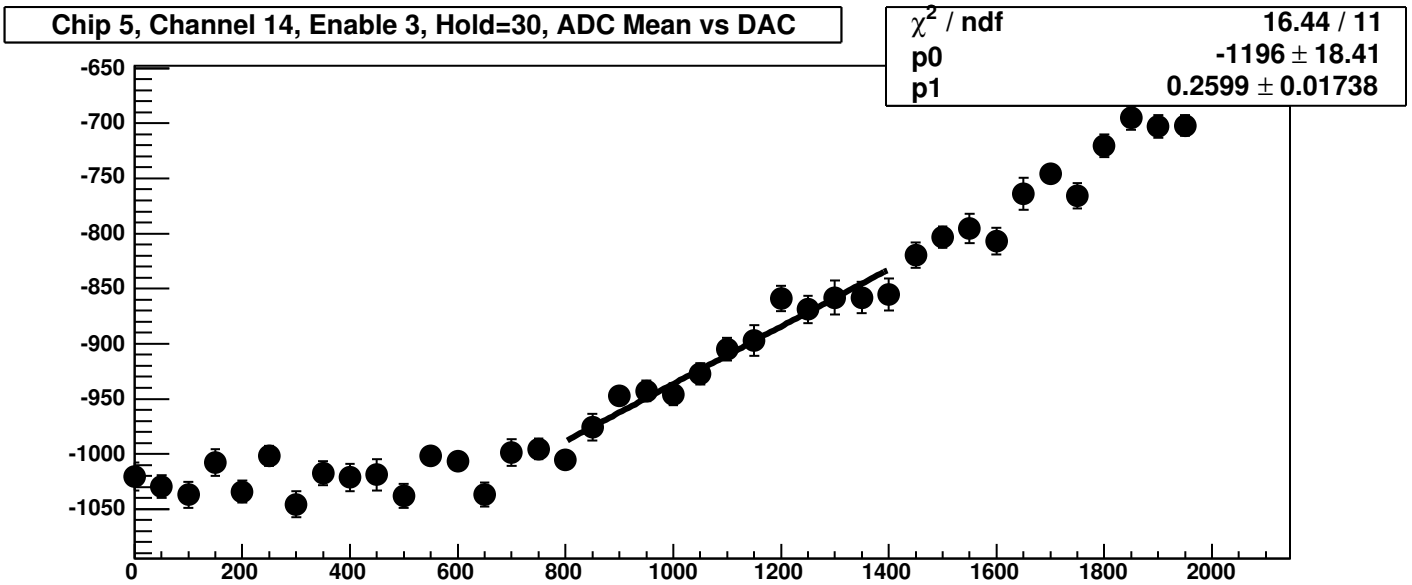

Chip 5, Channel 14, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

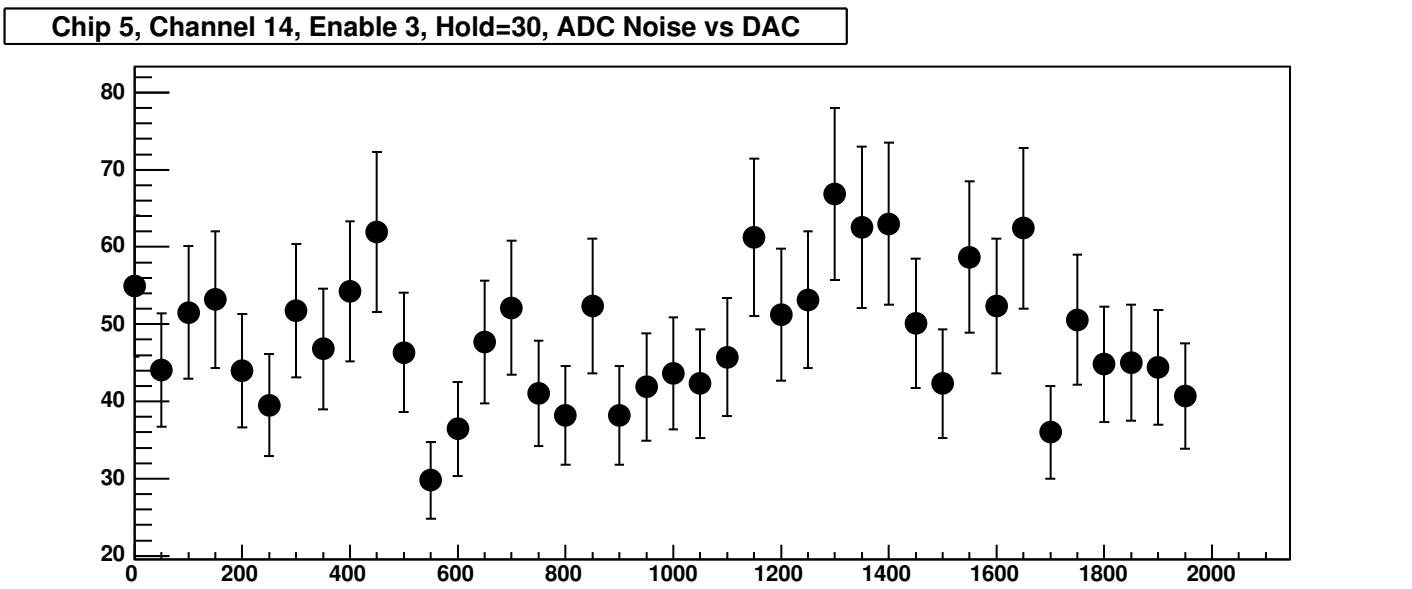

Chip 5, Channel 14, Enable 1, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

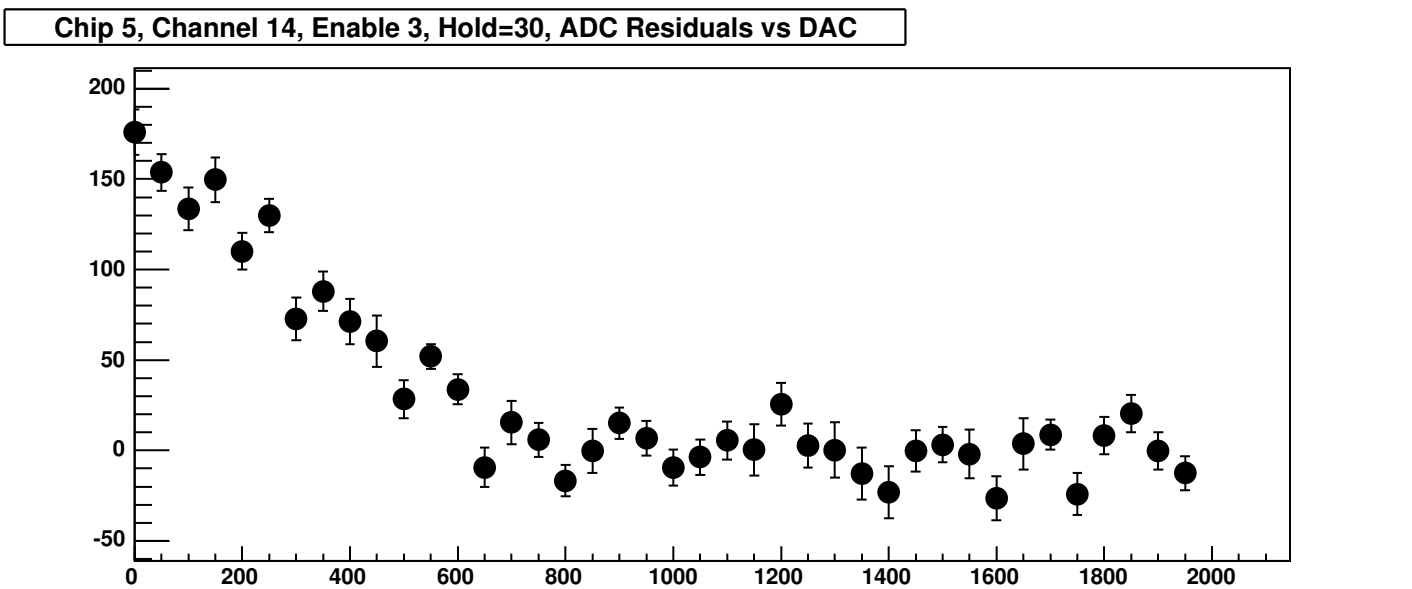

Chip 5, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 14.27 / 11
p0 -1184 ± 18.9
p1 0.2513 ± 0.01682
-700
-750
-800
-850
-900
-950
-1000
-1050
0
200
400
600
800
1000
1200
1400
1600
1800
2000

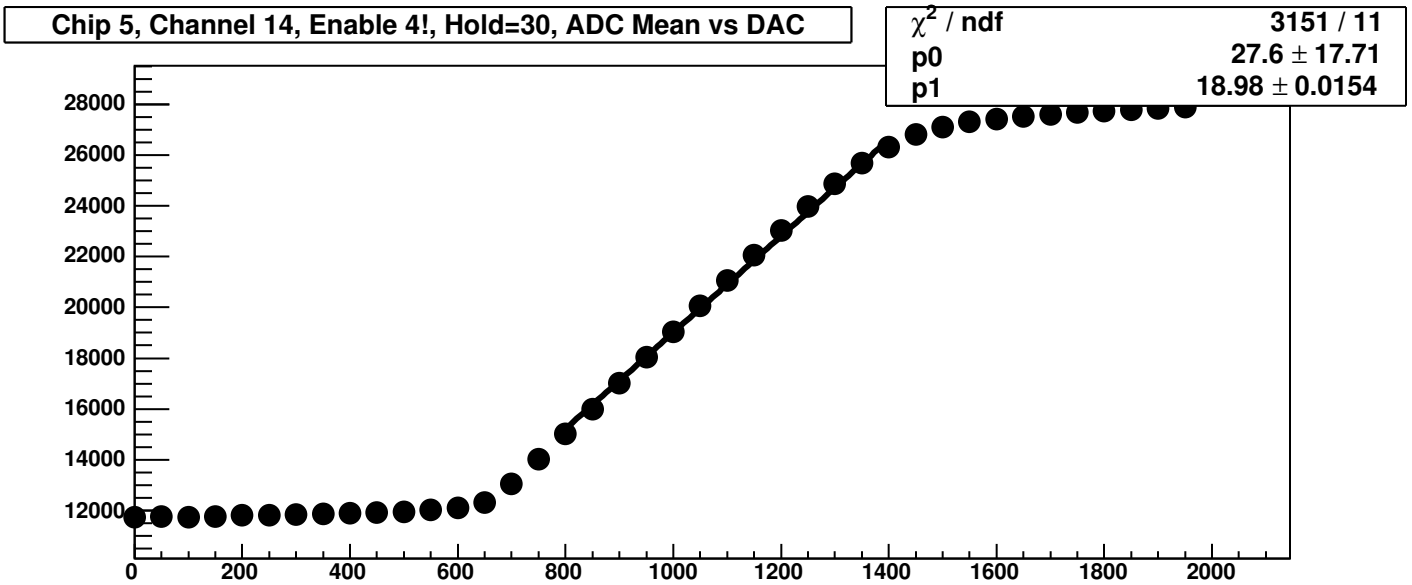

Chip 5, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

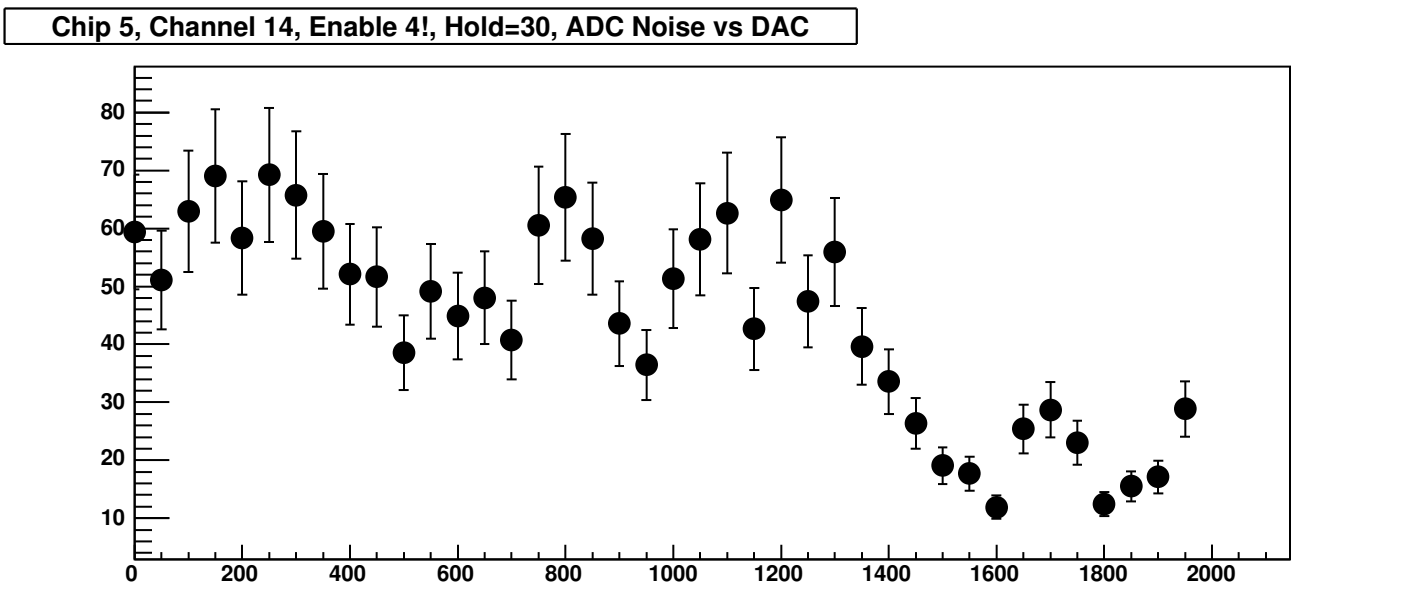

Chip 5, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

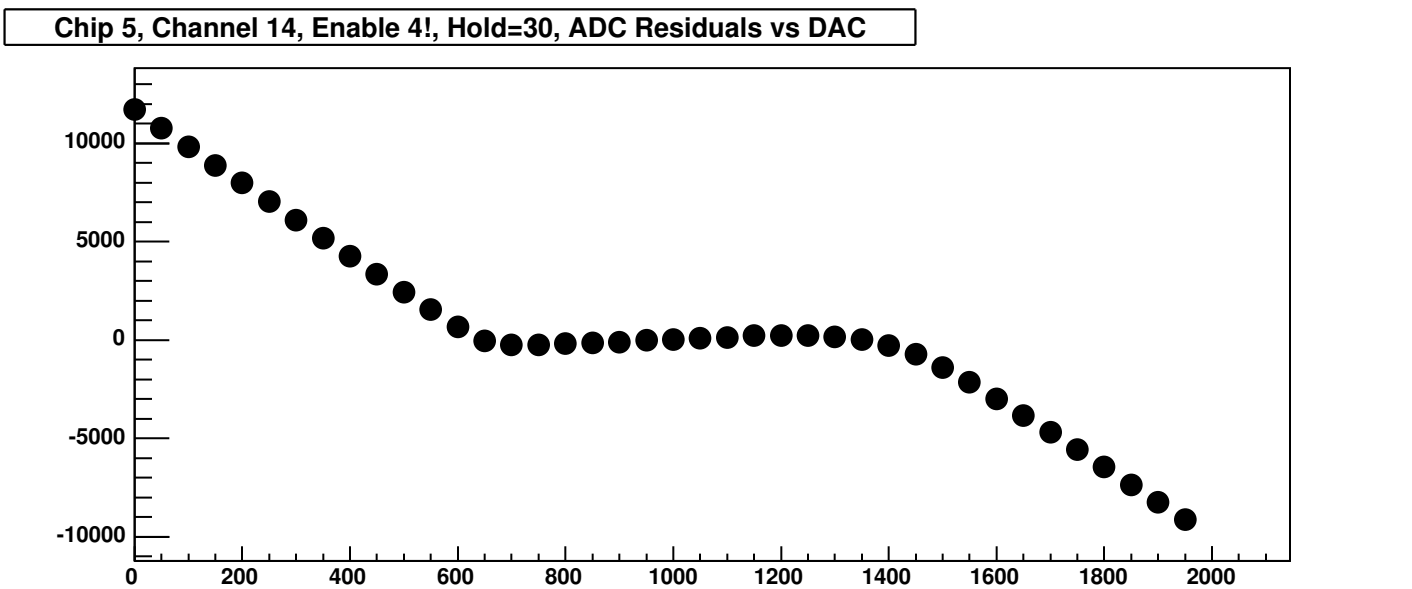

Chip 5, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 16.44 / 11
p0 -1196 ± 18.41
p1 0.2599 ± 0.01738
-650
-700
-750
-800
-850
-900
-950
-1000
-1050
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

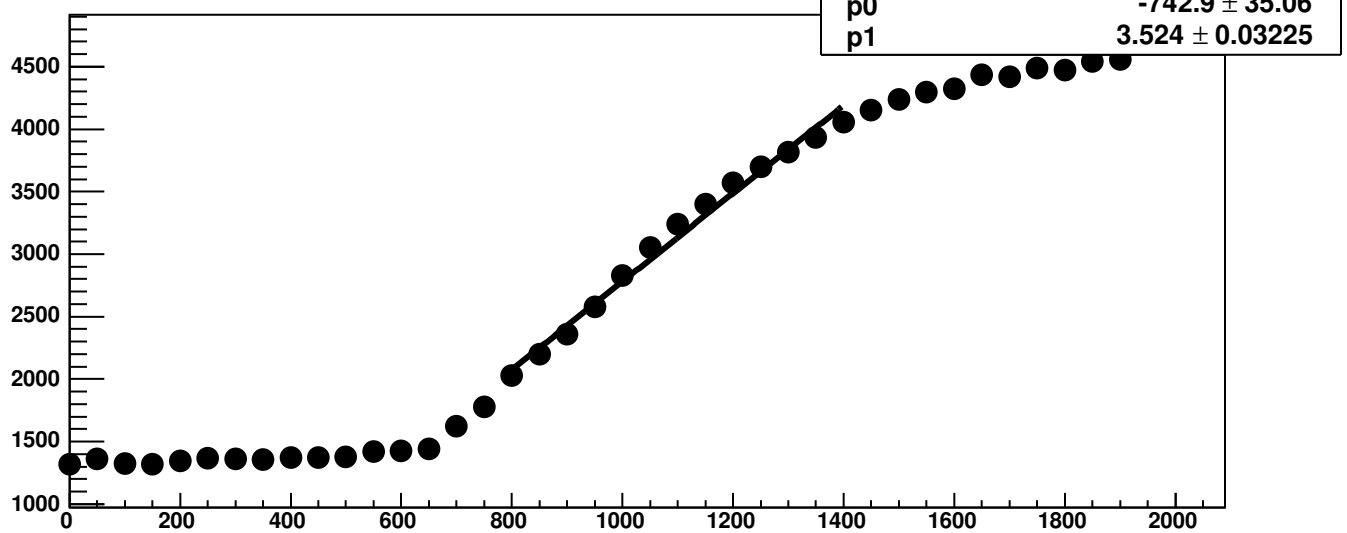

Chip 5, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 14, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 3151 / 11
p0 27.6 ± 17.71
p1 18.98 ± 0.0154
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

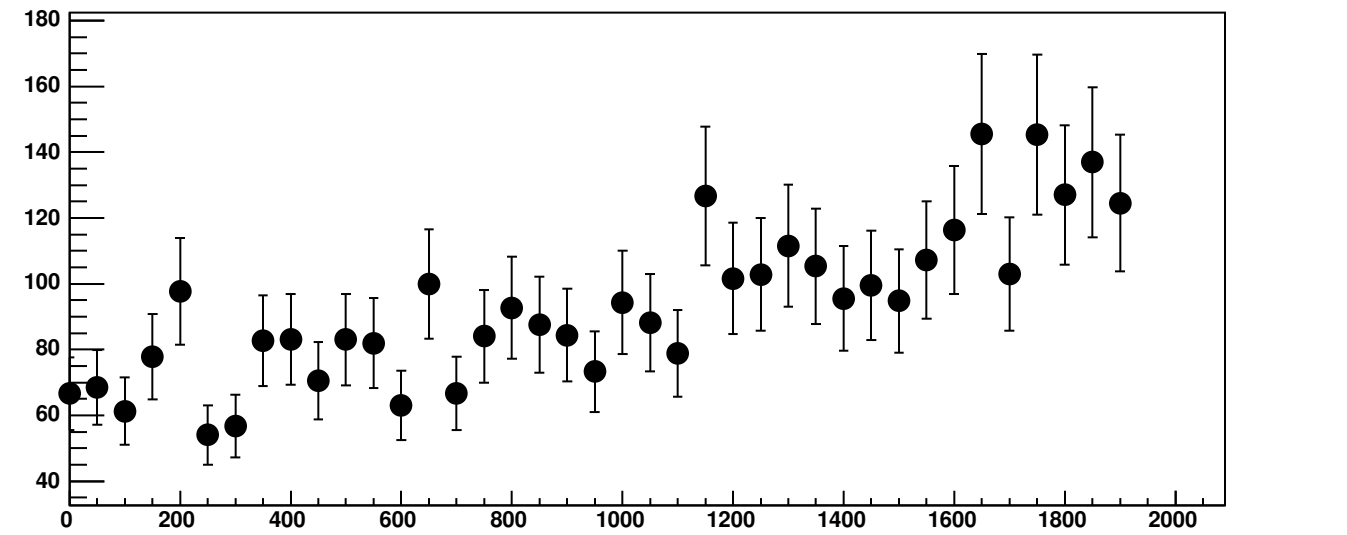

Chip 5, Channel 14, Enable 4!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 14, Enable 4!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

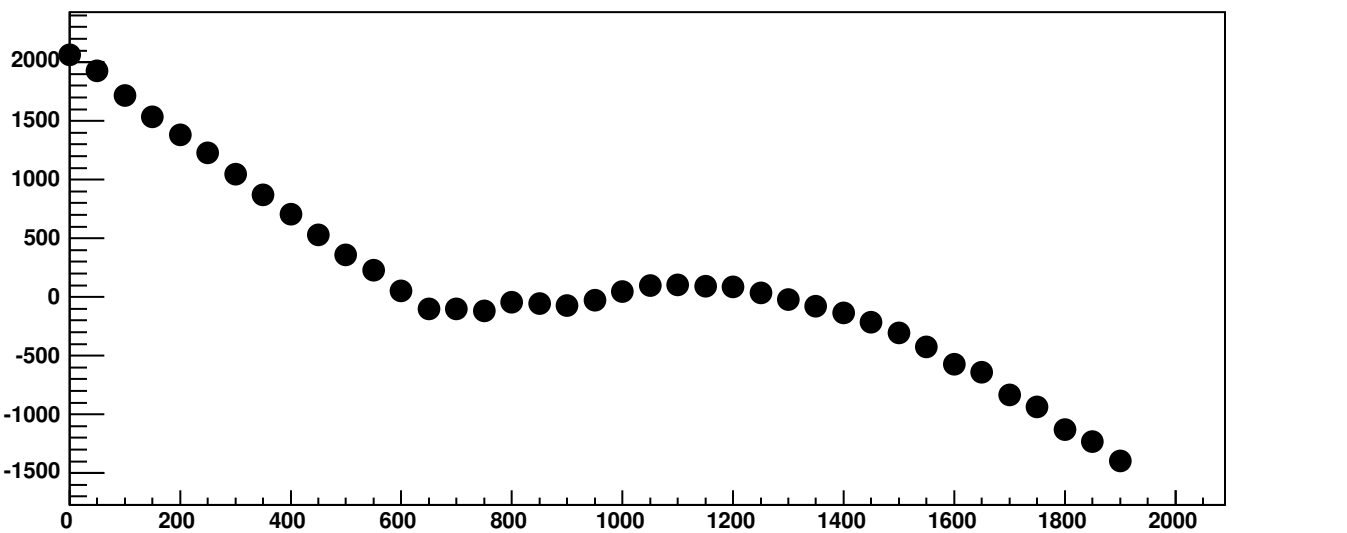
Chip 5, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC

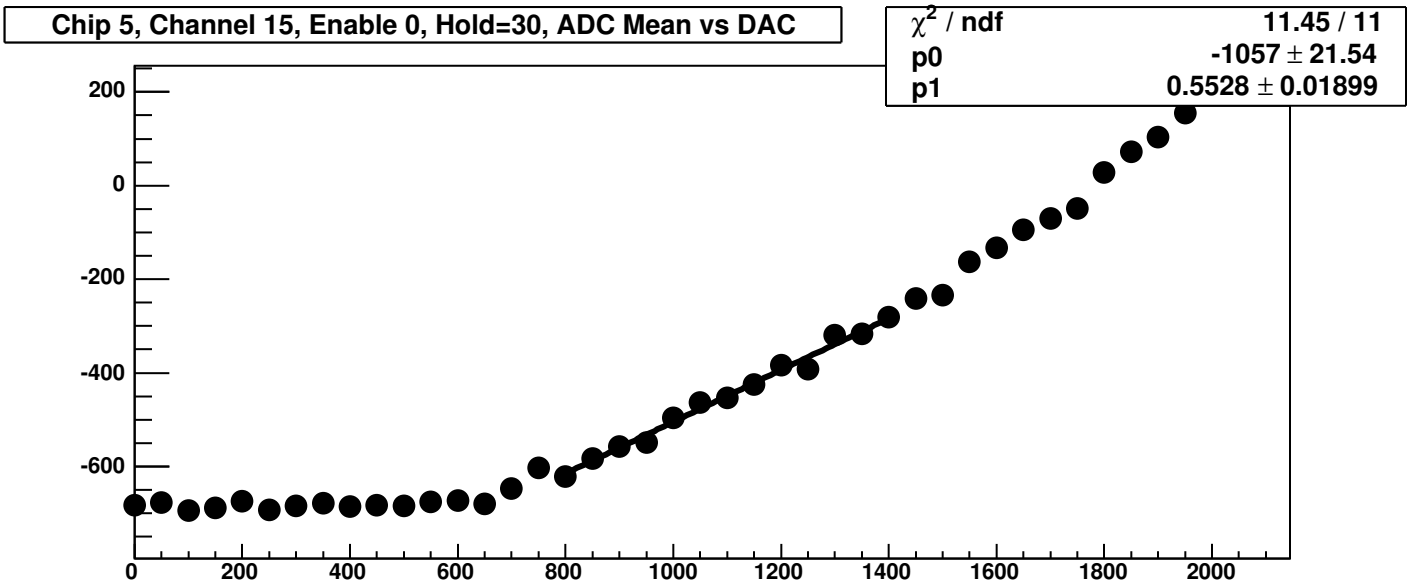


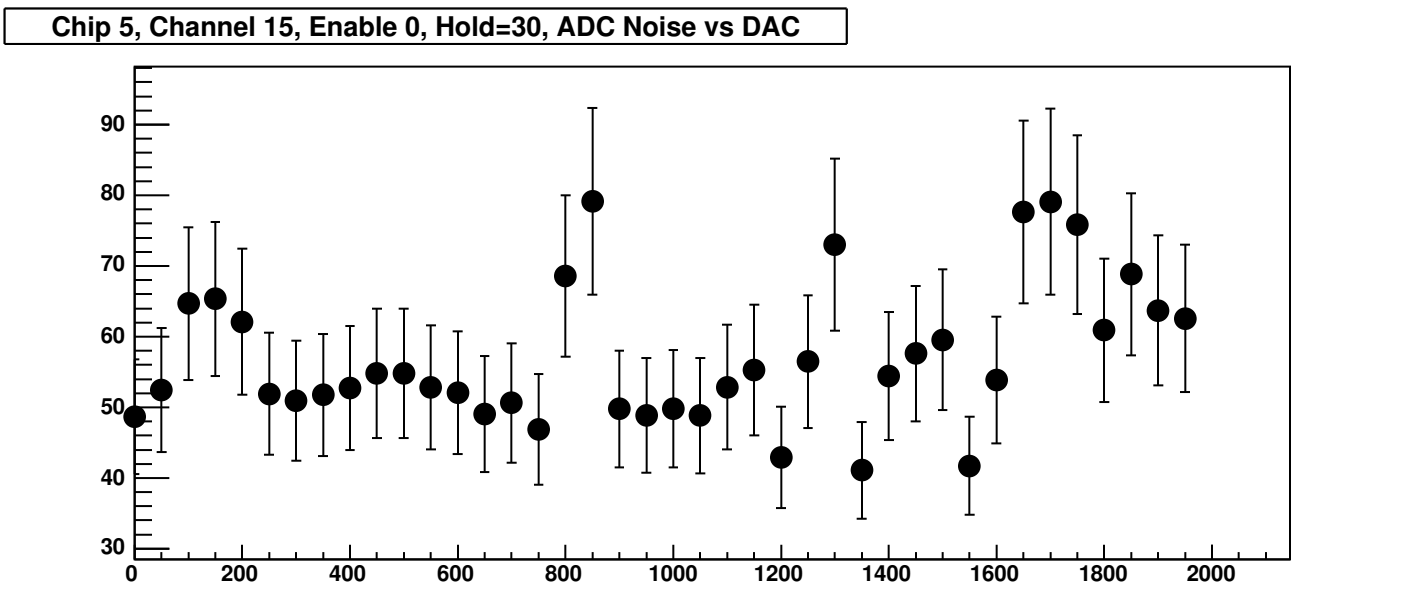
Chip 5, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC

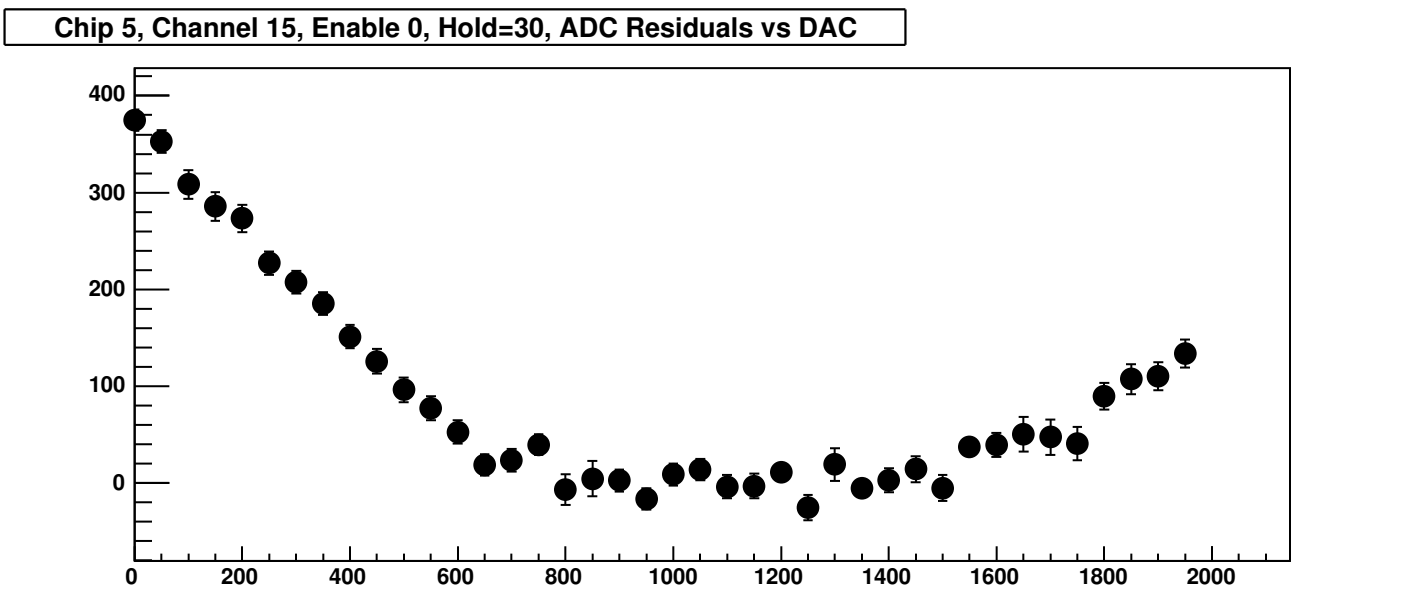


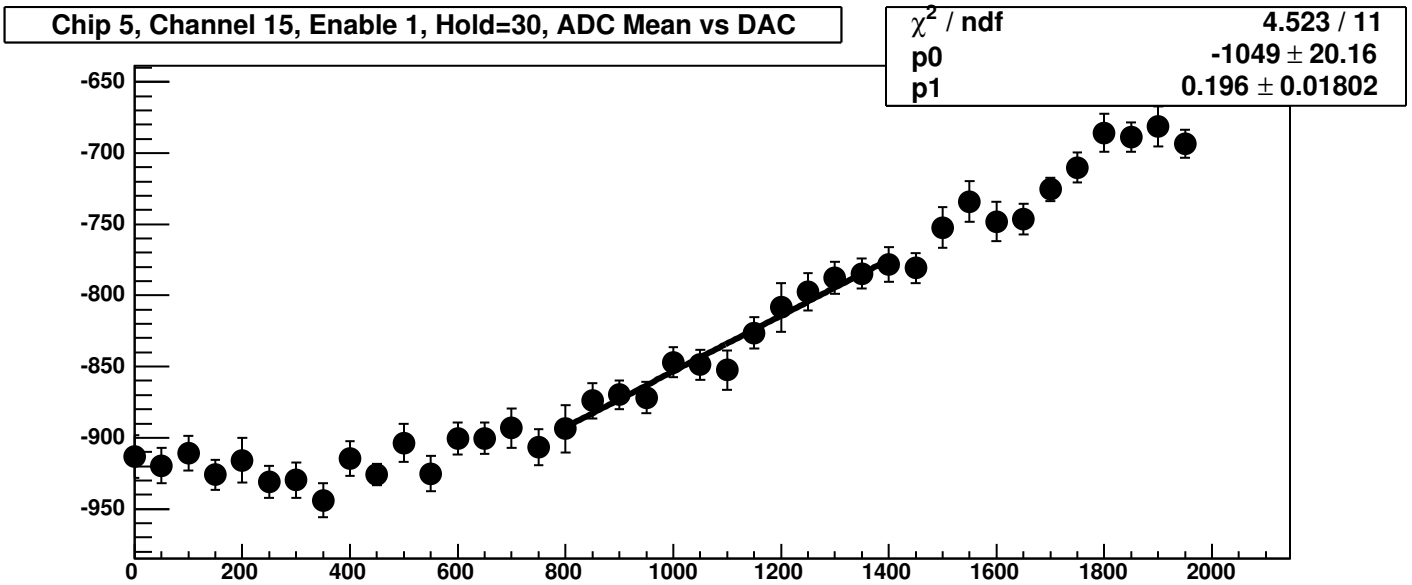
Chip 5, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC

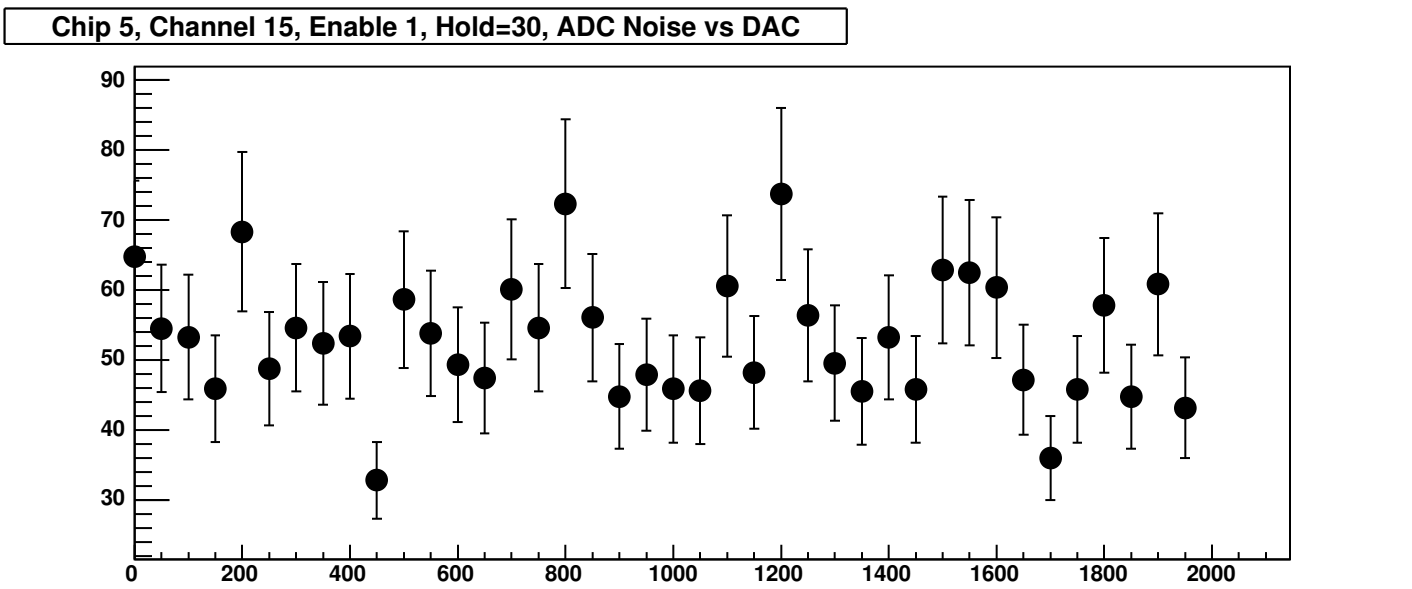


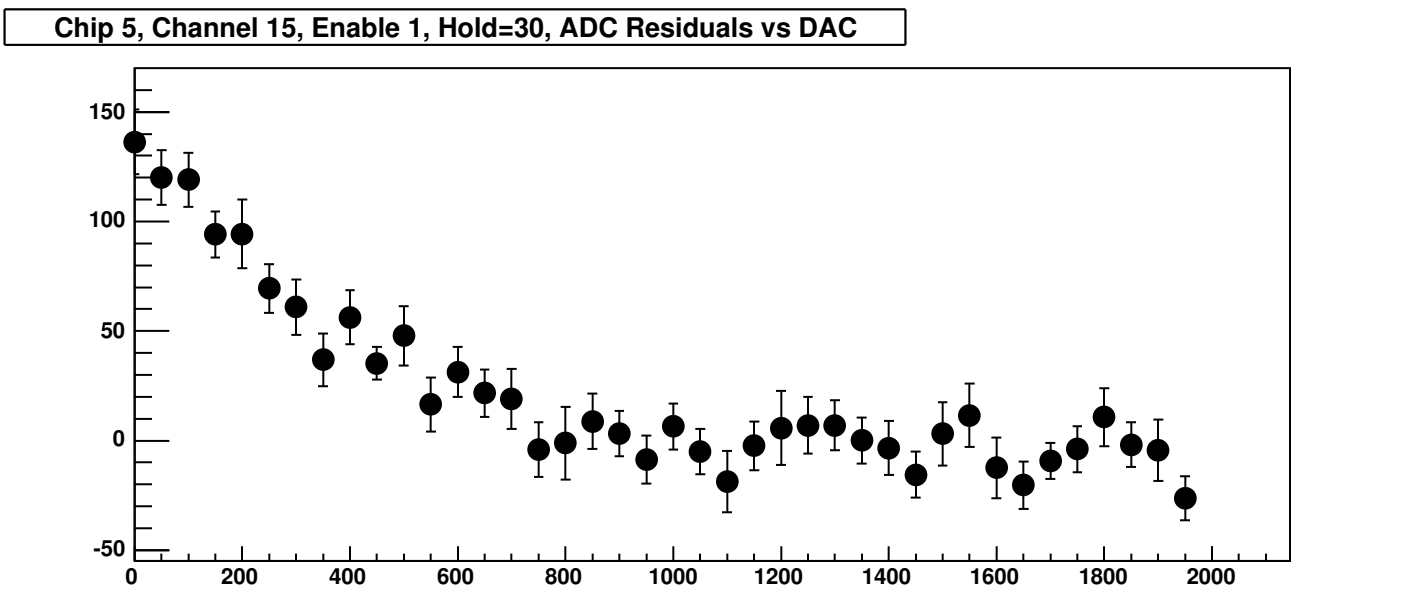

Chip 5, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 11.45 / 11
p0 -1057 ± 21.54
p1 0.5528 ± 0.01899
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

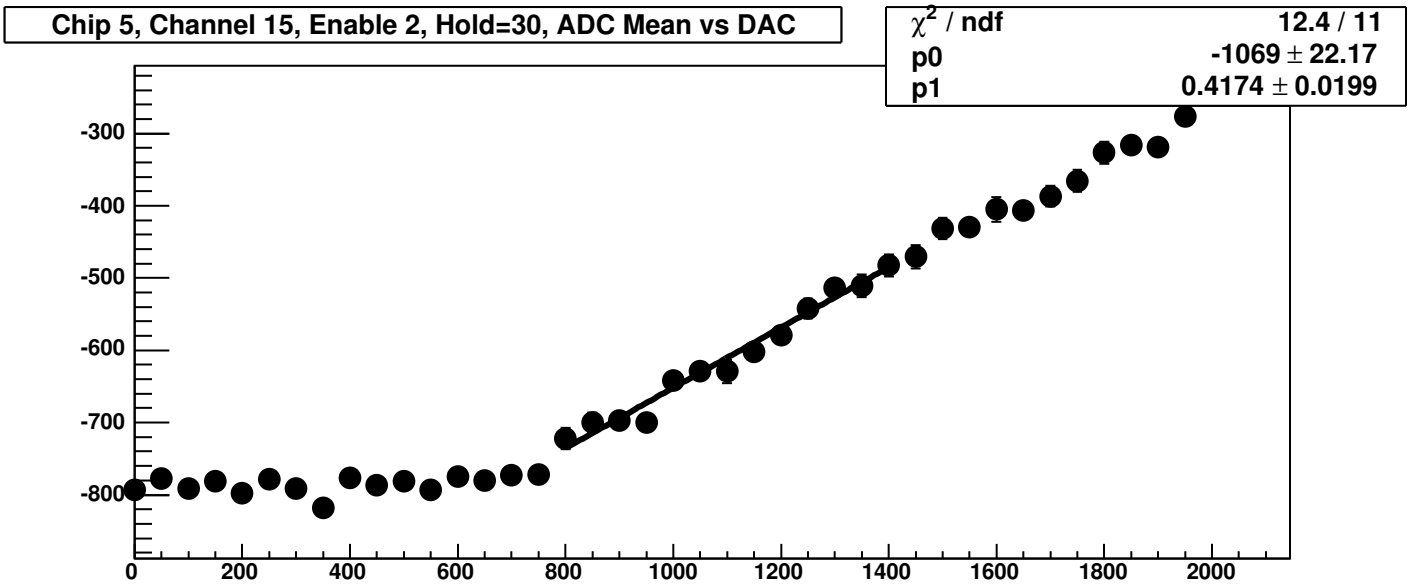

Chip 5, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

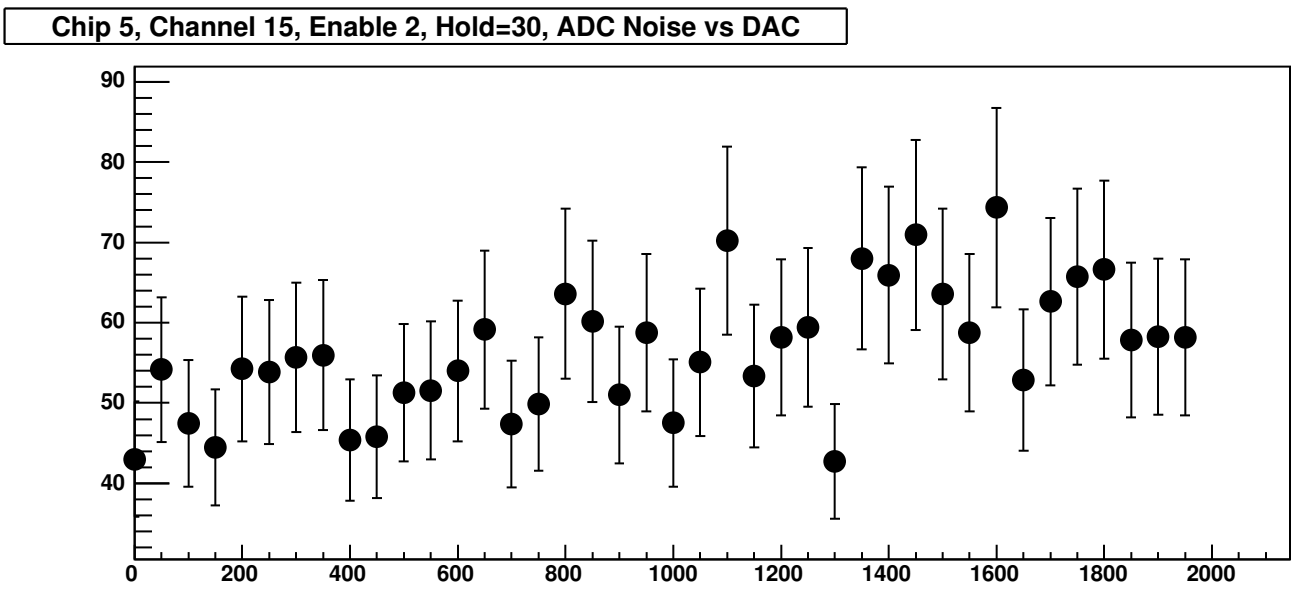

Chip 5, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
400
300
200
100
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

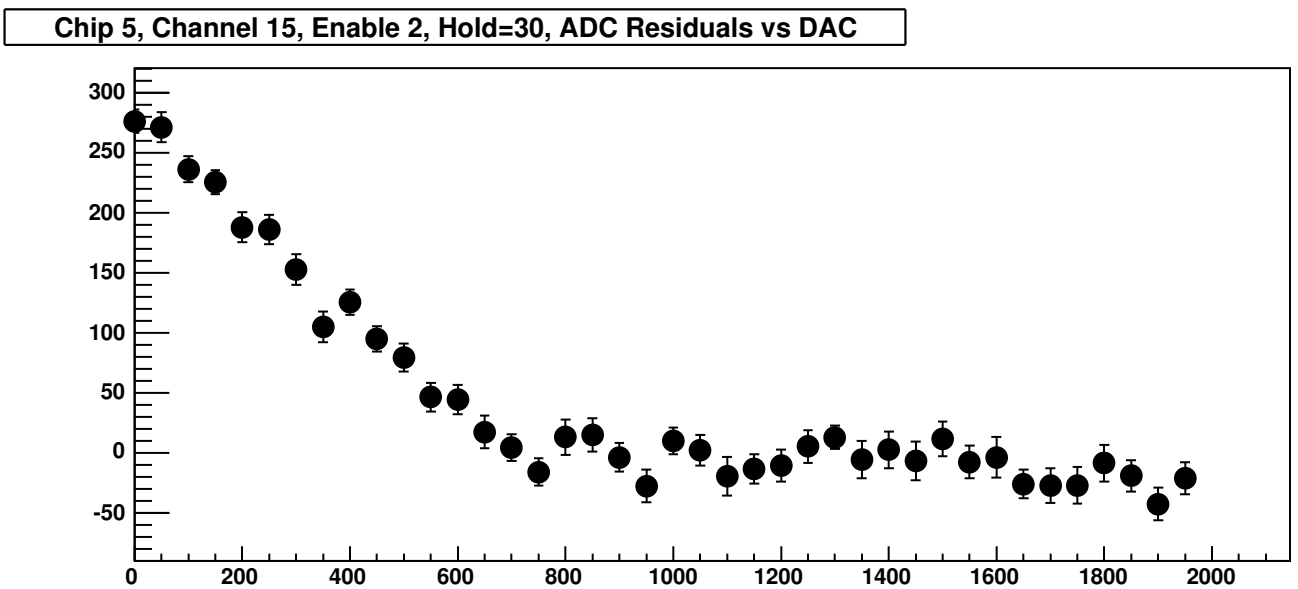

Chip 5, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4.523 / 11
p0 -1049 ± 20.16
p1 0.196 ± 0.01802
-650
-700
-750
-800
-850
-900
-950
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

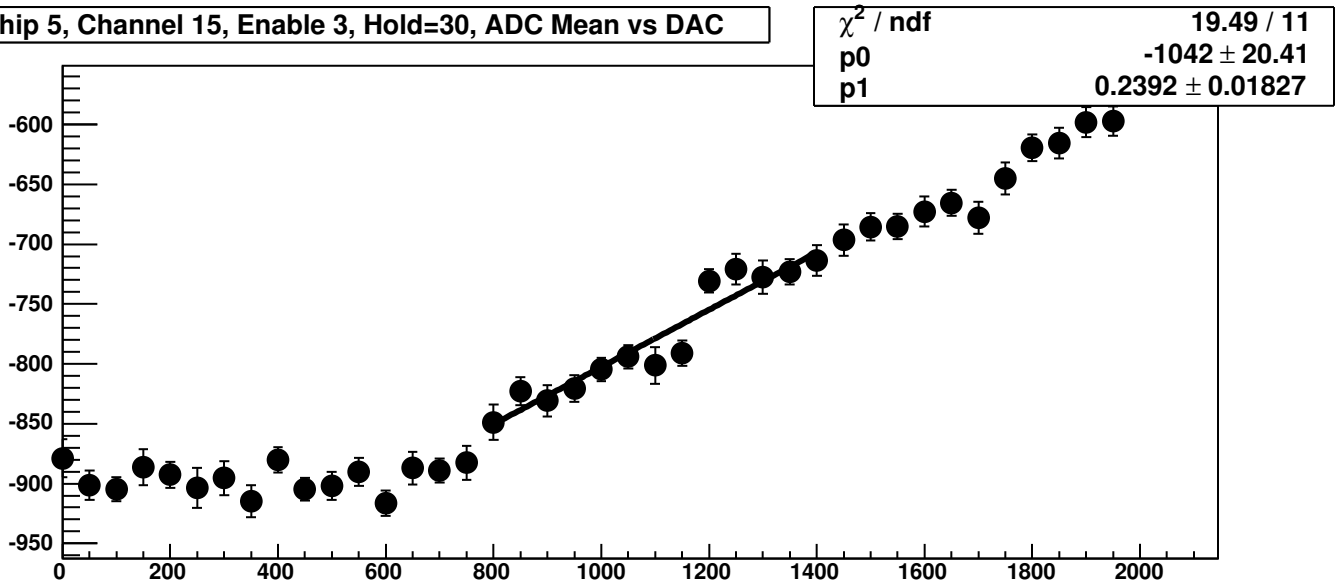

Chip 5, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 15, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 12.4 / 11
p0 -1069 ± 22.17
p1 0.4174 ± 0.0199
-300
-400
-500
-600
-700
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

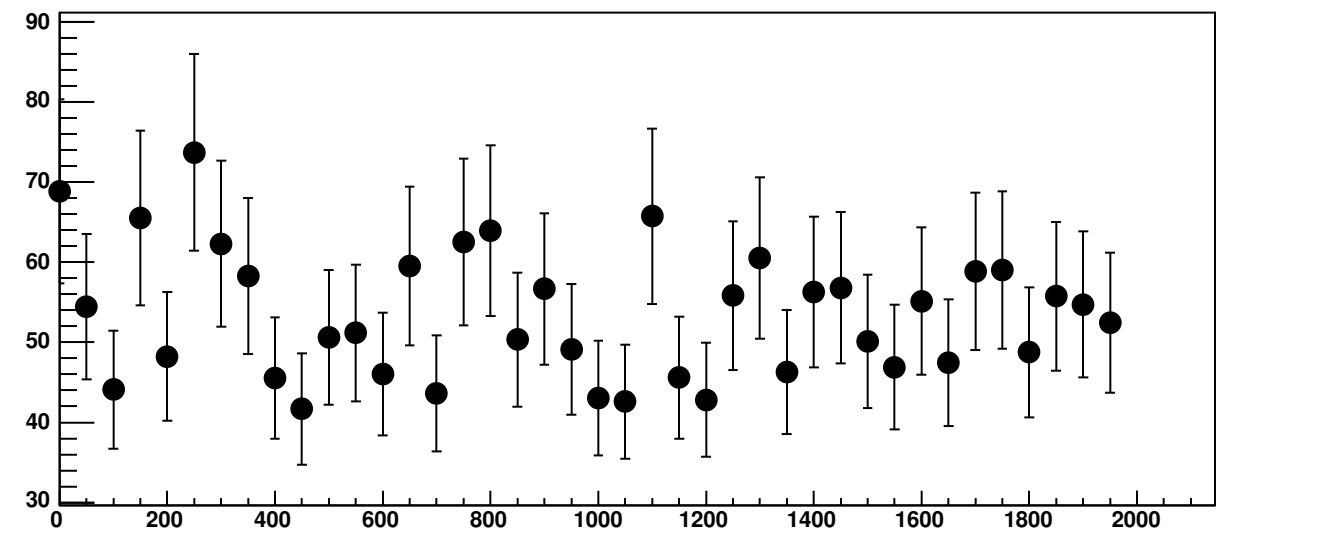

Chip 5, Channel 15, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 15, Enable 2, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

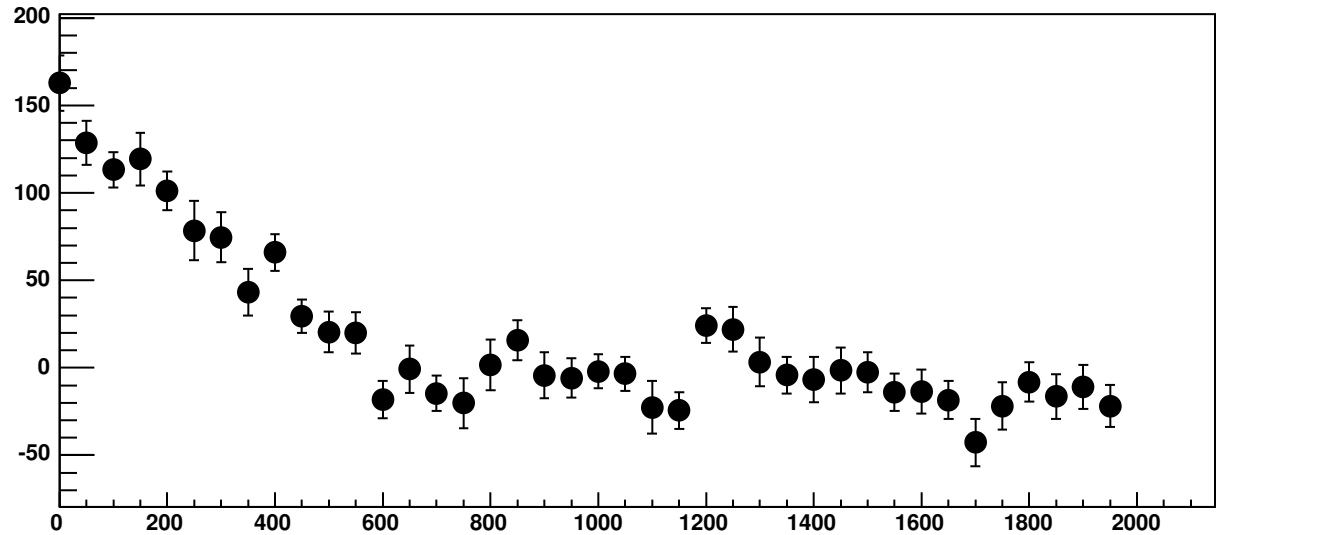
Chip 5, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC

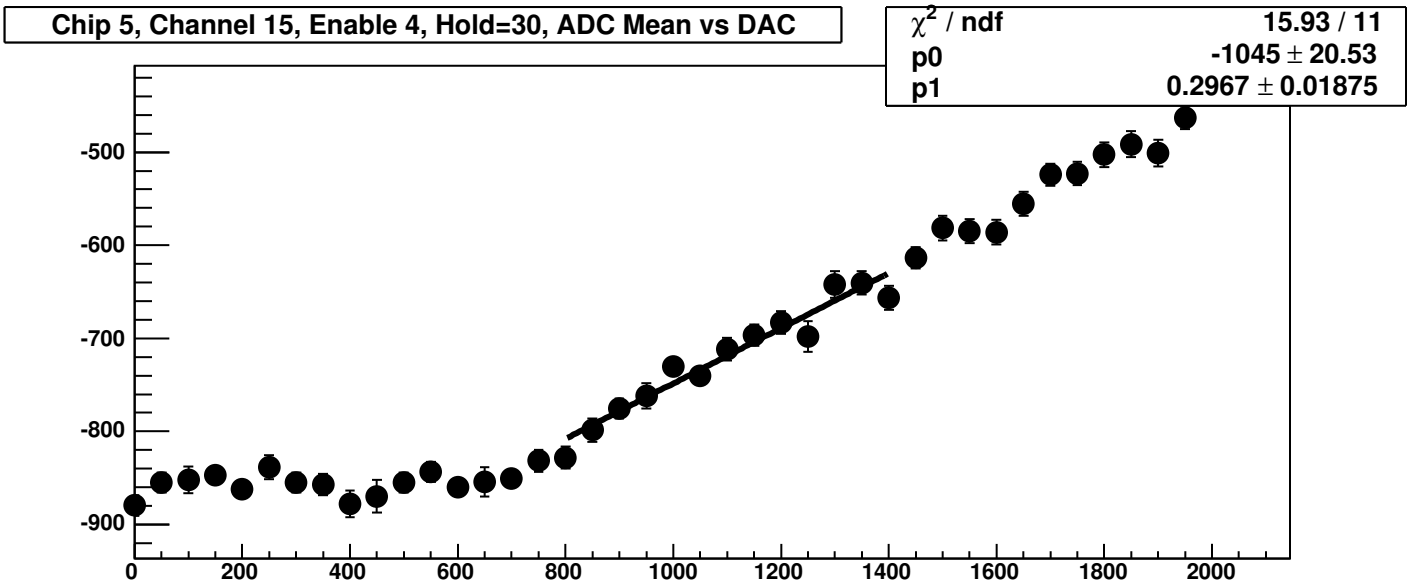


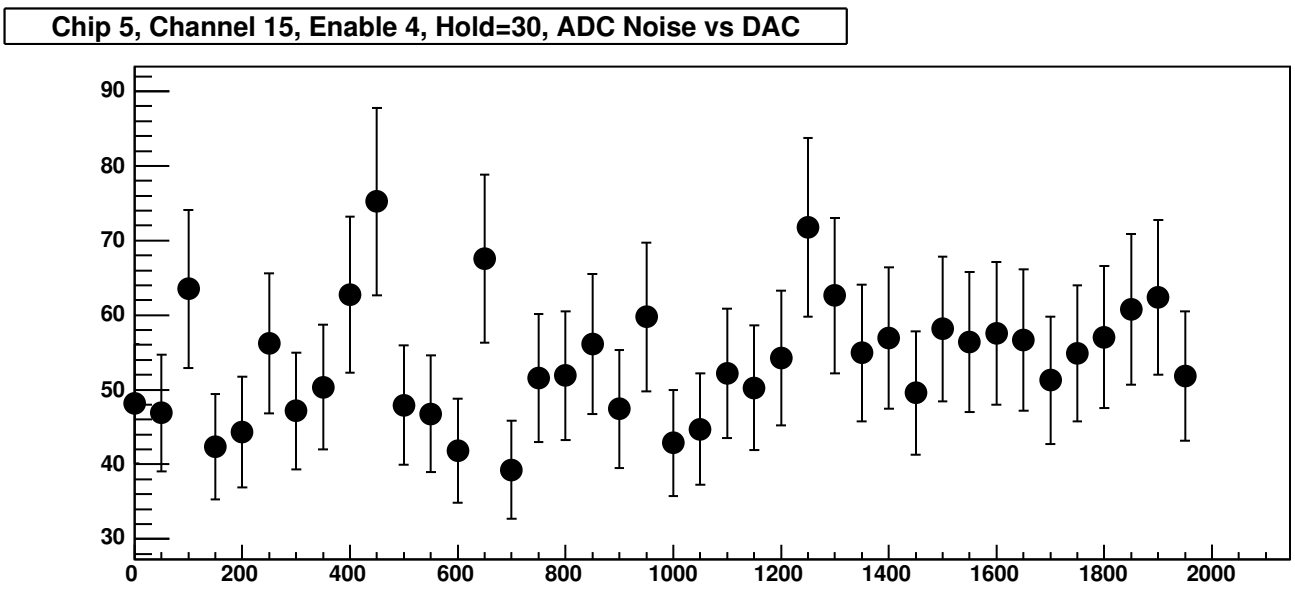
Chip 5, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC

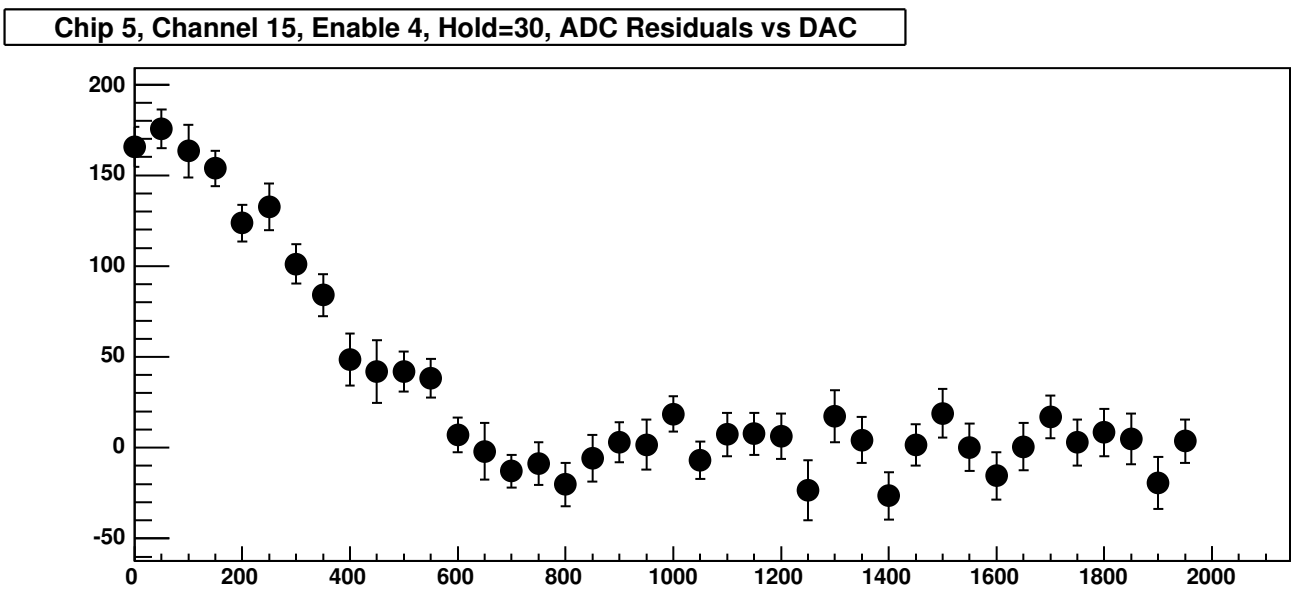


Chip 5, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC

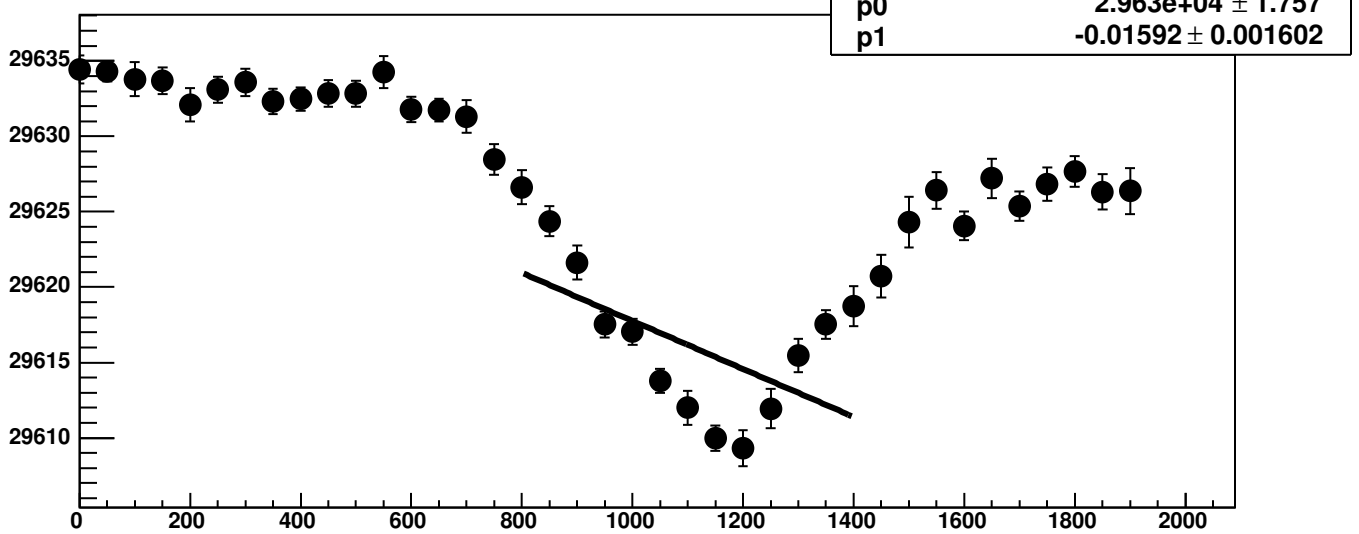



Chip 5, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 15.93 / 11
p0 -1045 ± 20.53
p1 0.2967 ± 0.01875
-500
-600
-700
-800
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

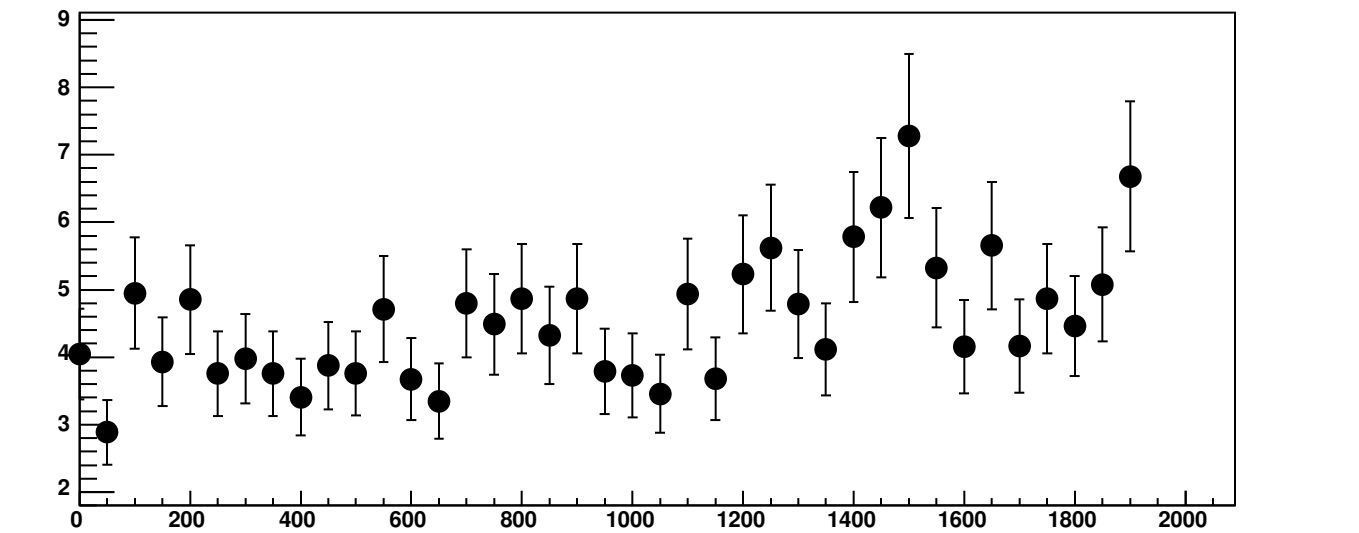

Chip 5, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

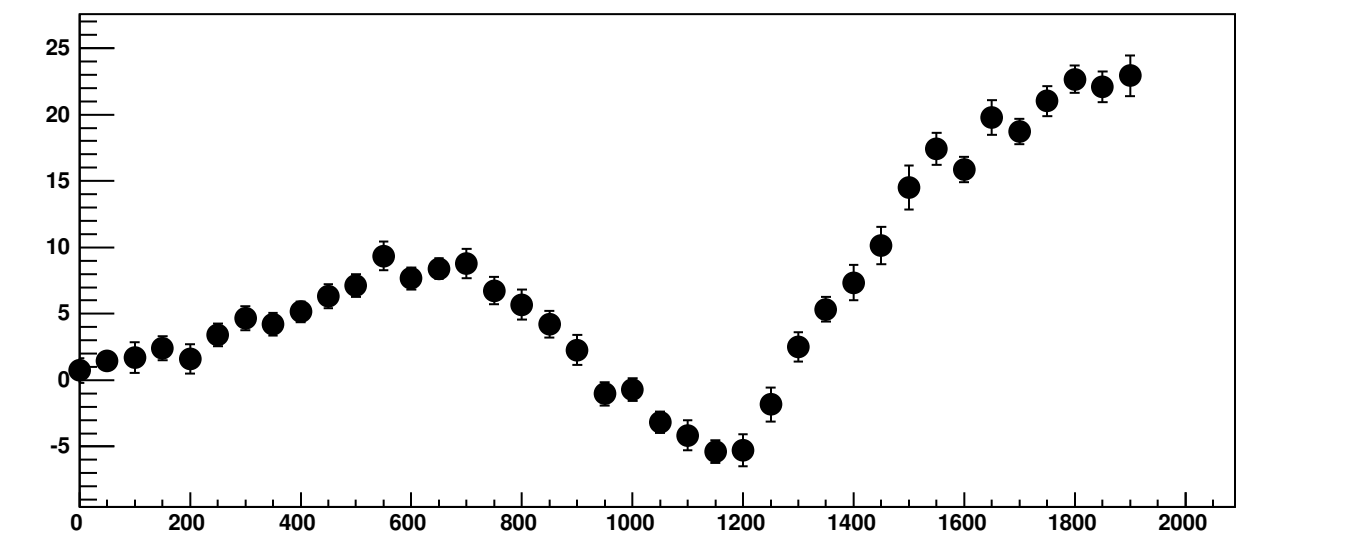
Chip 5, Channel 15, Enable 5!, Hold=30, ADC Mean vs DAC

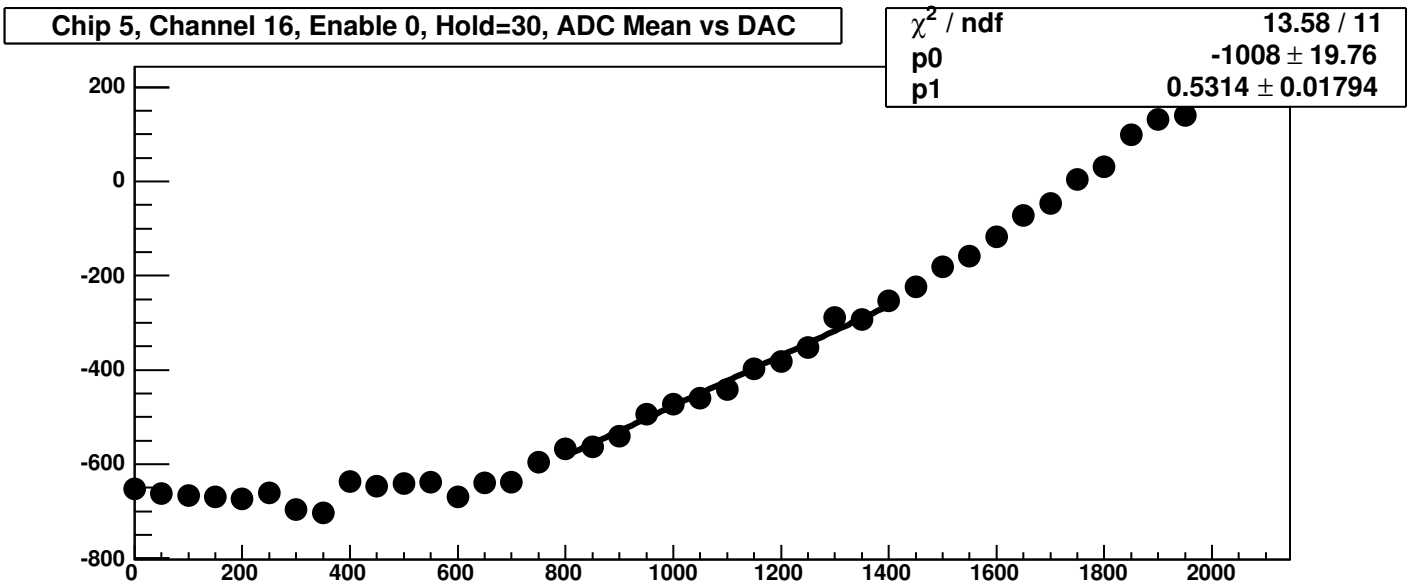


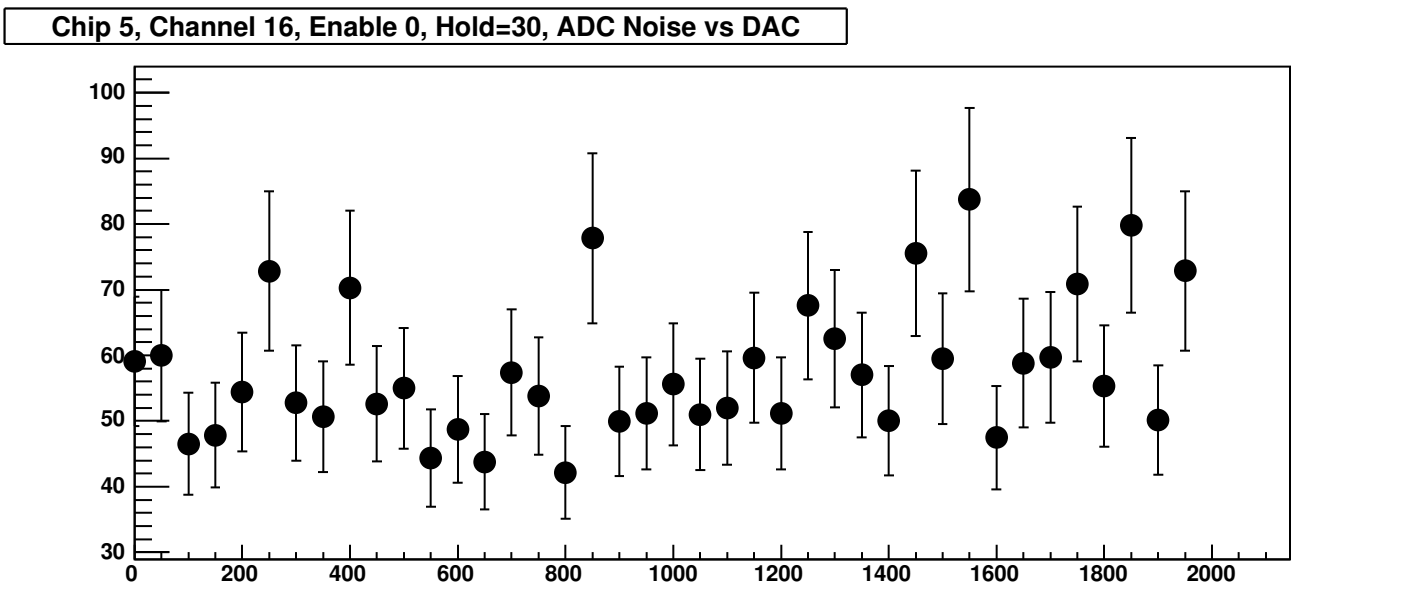
Chip 5, Channel 15, Enable 5!, Hold=30, ADC Noise vs DAC

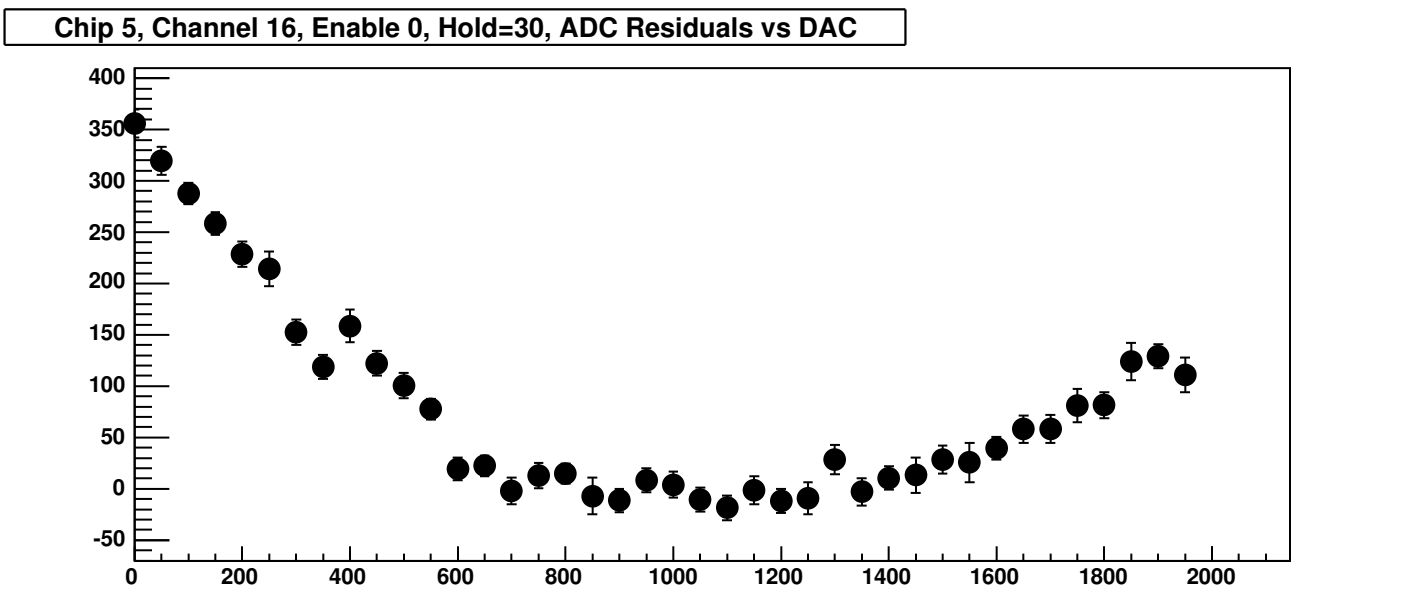


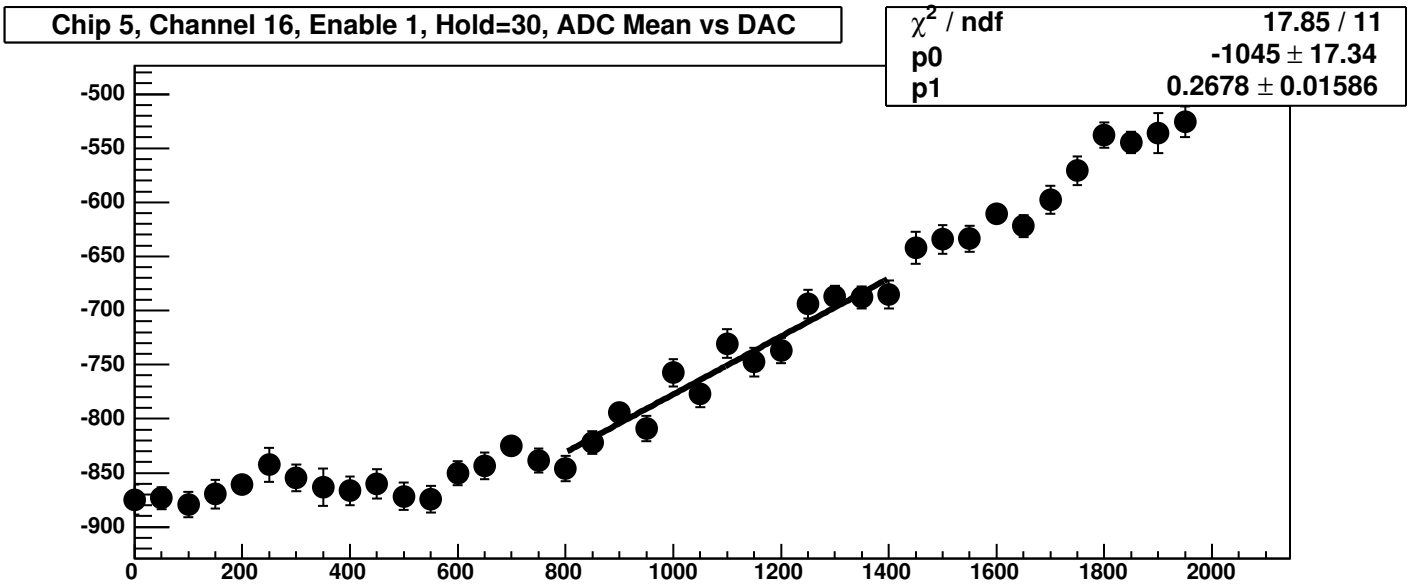
Chip 5, Channel 15, Enable 5!, Hold=30, ADC Residuals vs DAC

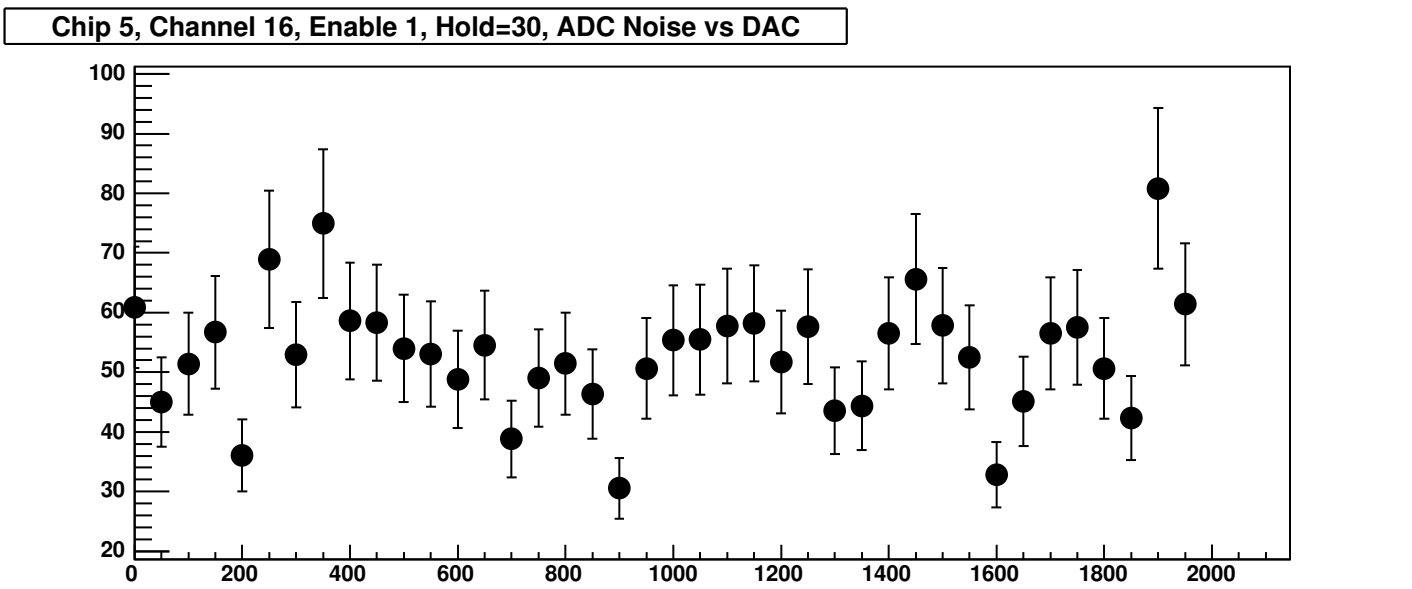


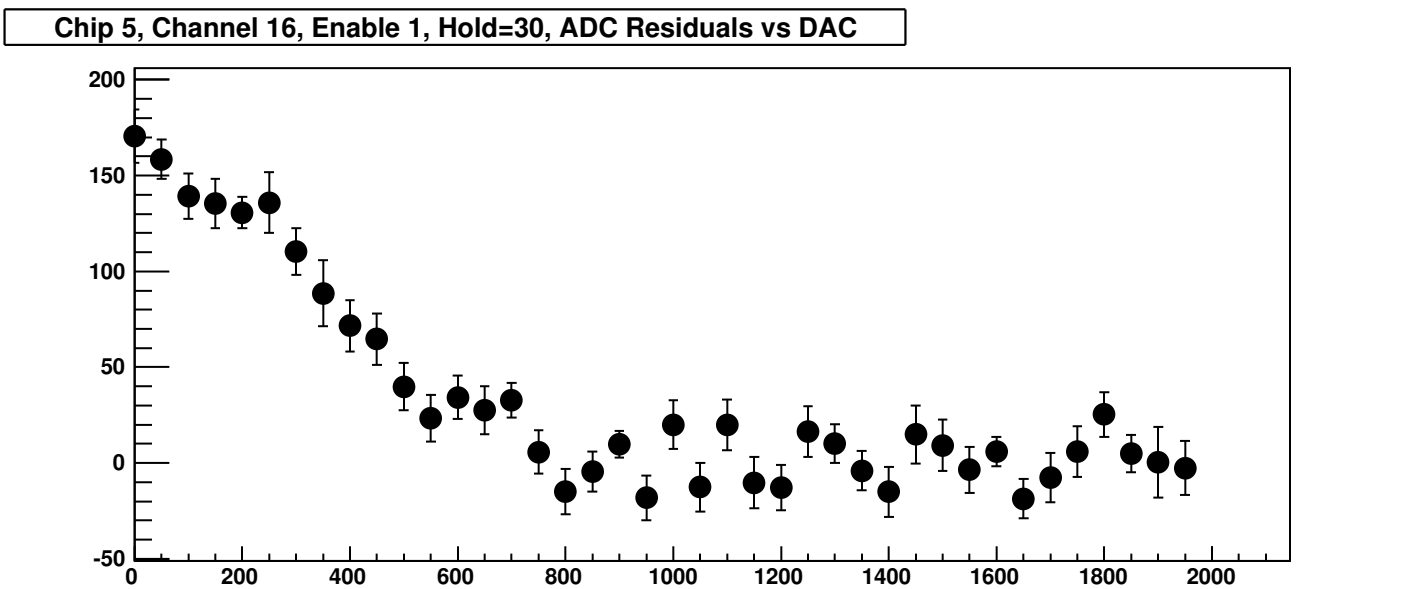

Chip 5, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 13.58 / 11
p0 -1008 ± 19.76
p1 0.5314 ± 0.01794
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

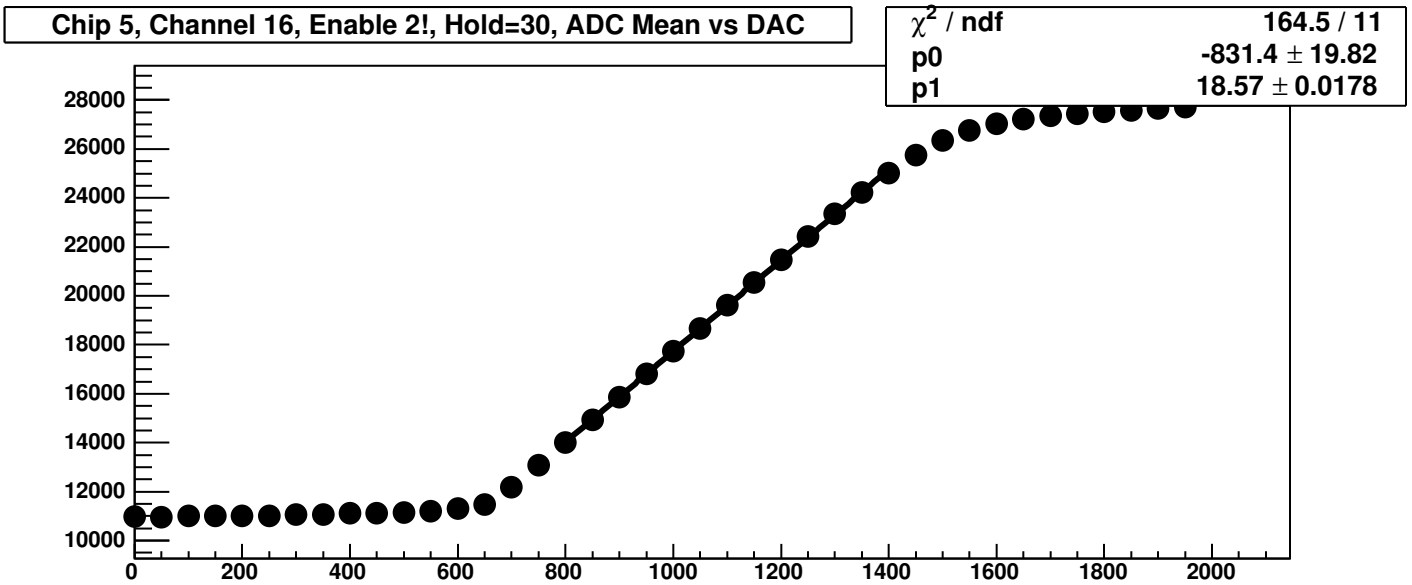

Chip 5, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

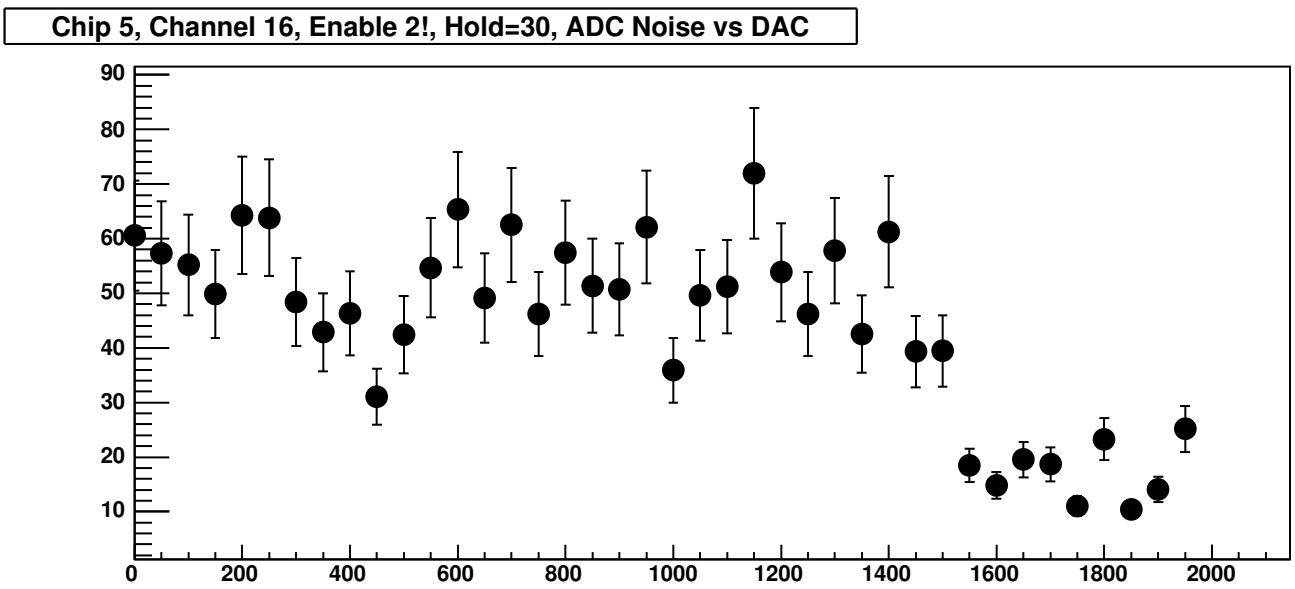

Chip 5, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
400
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

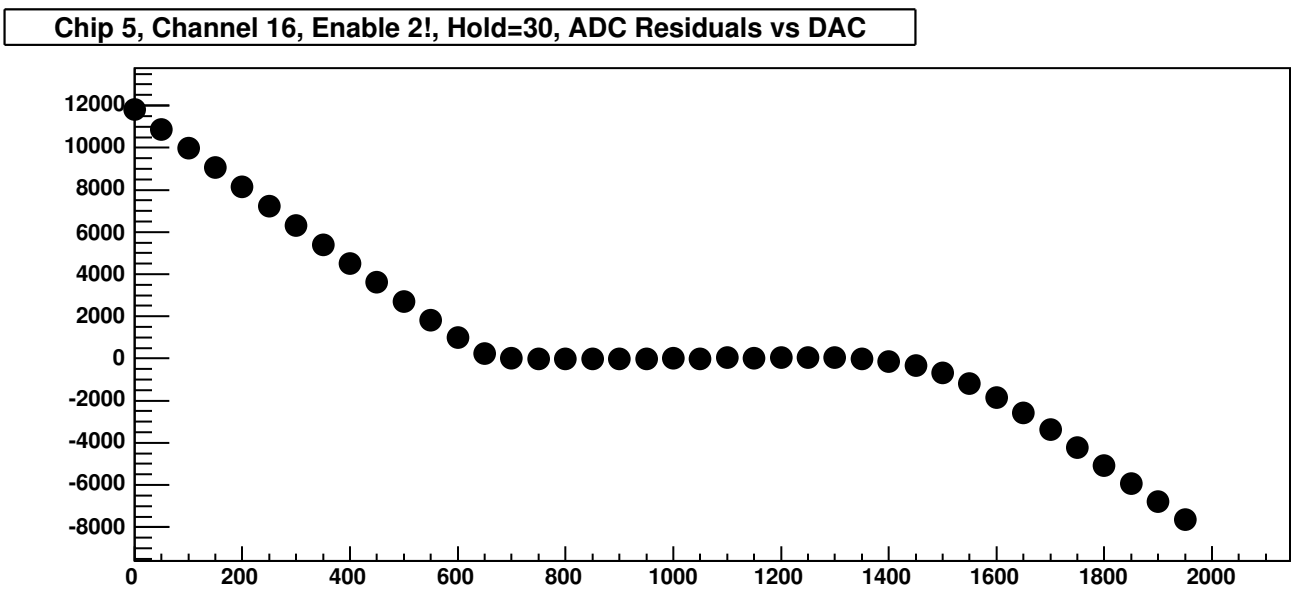

Chip 5, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 17.85 / 11
p0 -1045 ± 17.34
p1 0.2678 ± 0.01586
-500
-550
-600
-650
-700
-750
-800
-850
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

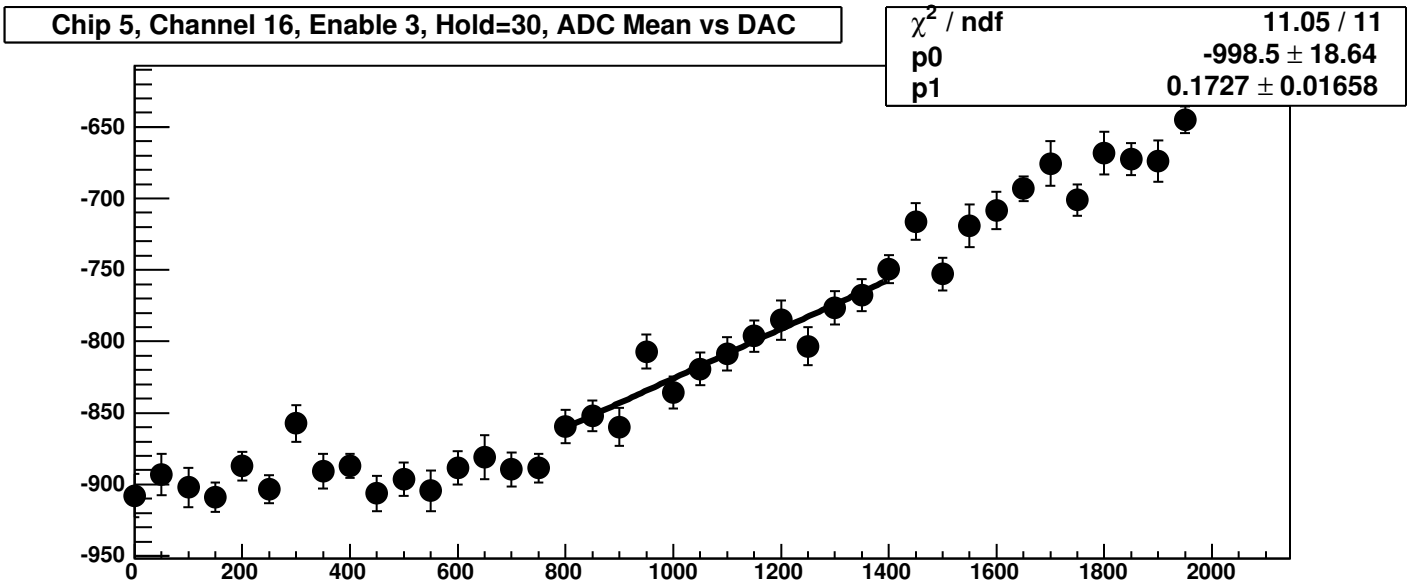

Chip 5, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

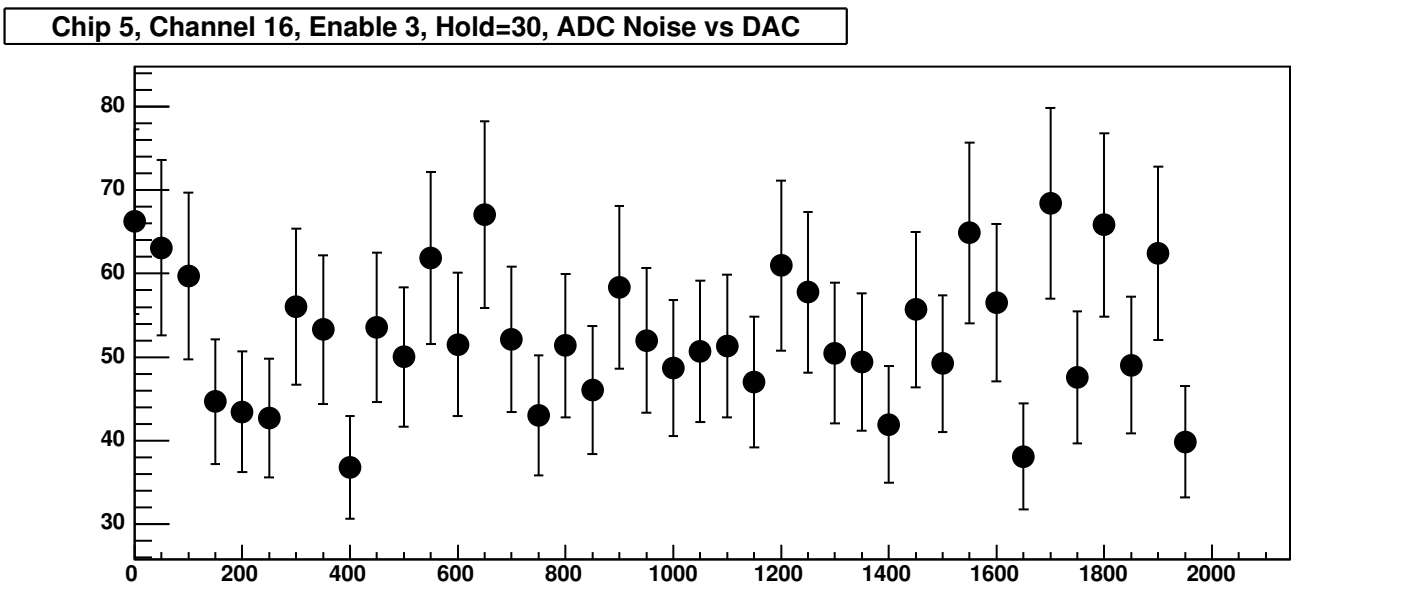

Chip 5, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

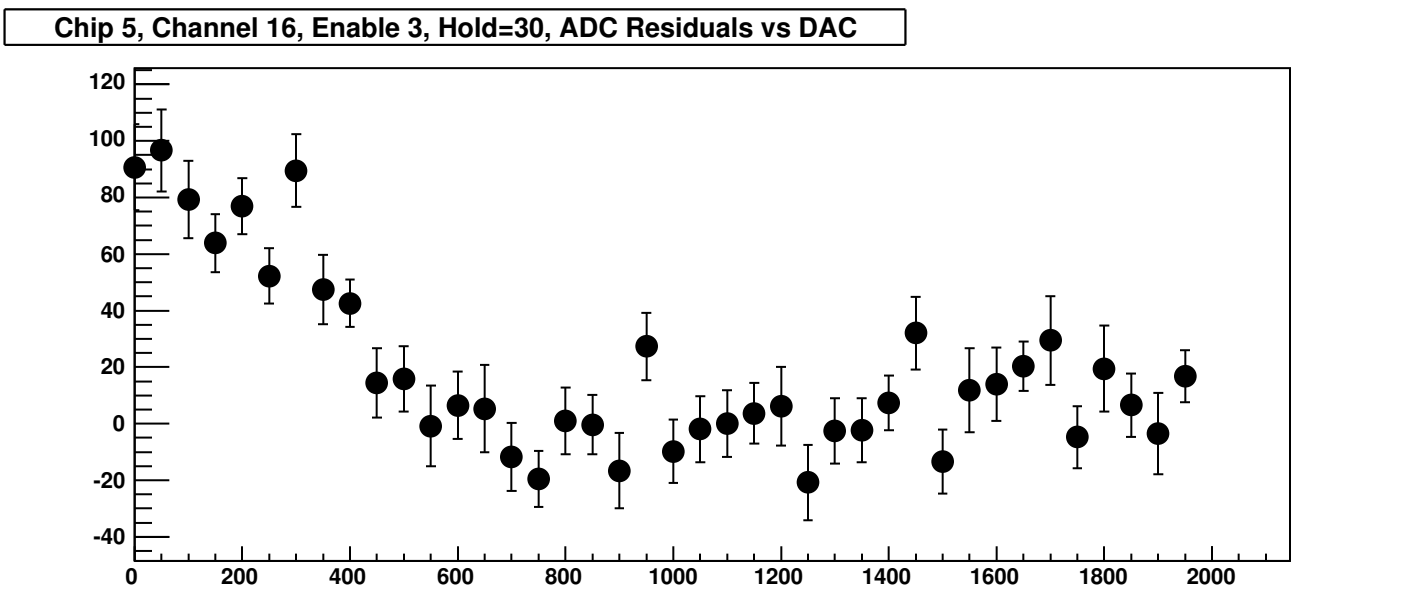

Chip 5, Channel 16, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 164.5 / 11
p0 -831.4 ± 19.82
p1 18.57 ± 0.0178
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

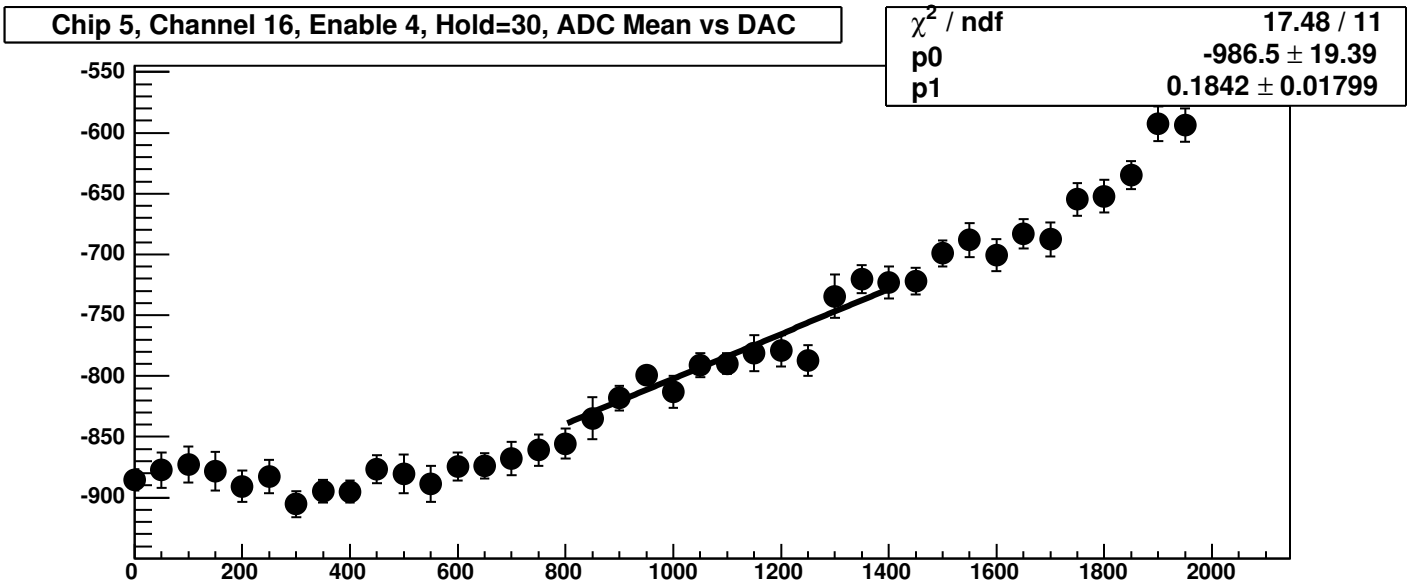

Chip 5, Channel 16, Enable 2!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

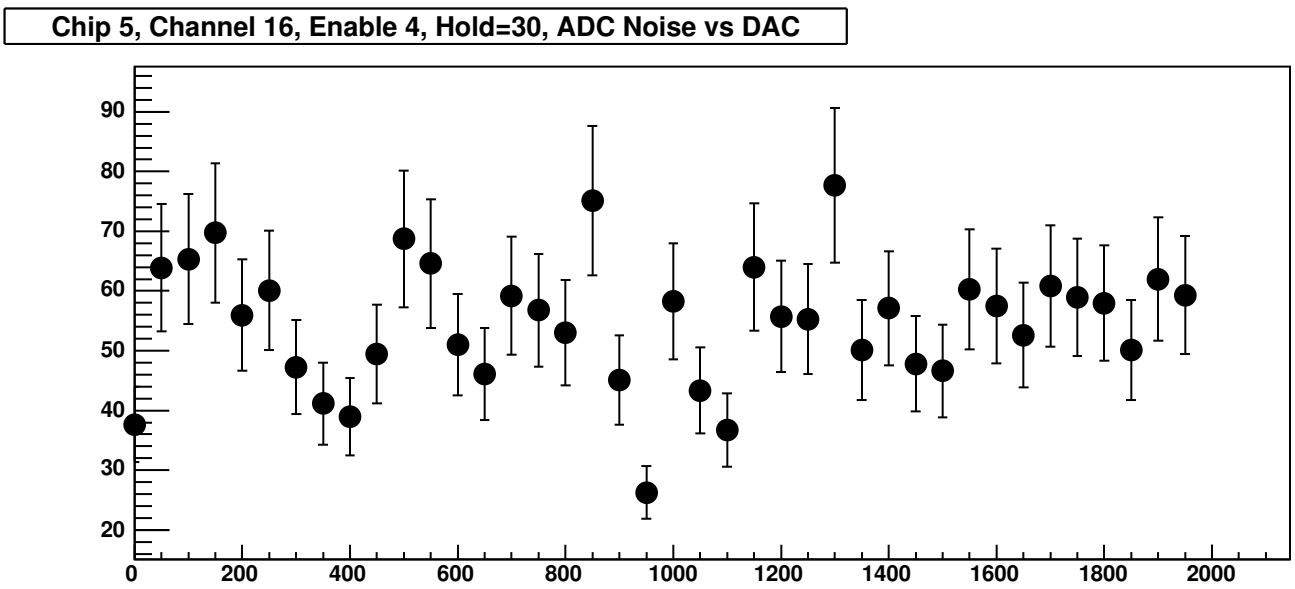

Chip 5, Channel 16, Enable 2!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

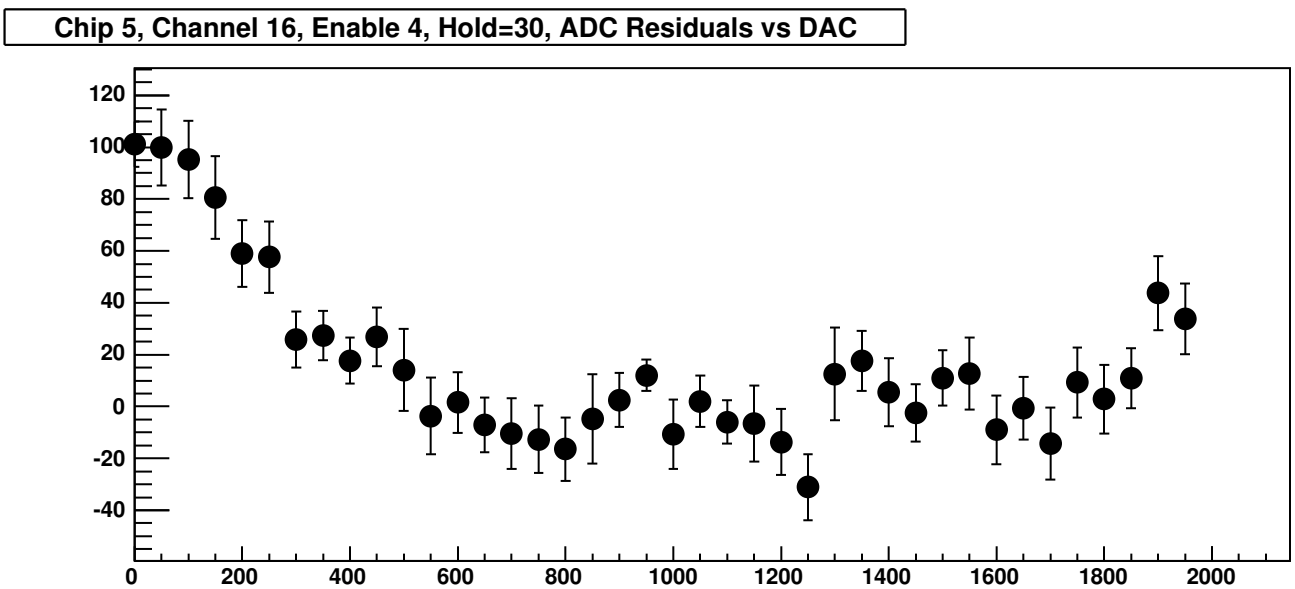

Chip 5, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 11.05 / 11
p0 -998.5 ± 18.64
p1 0.1727 ± 0.01658
-650
-700
-750
-800
-850
-900
-950
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

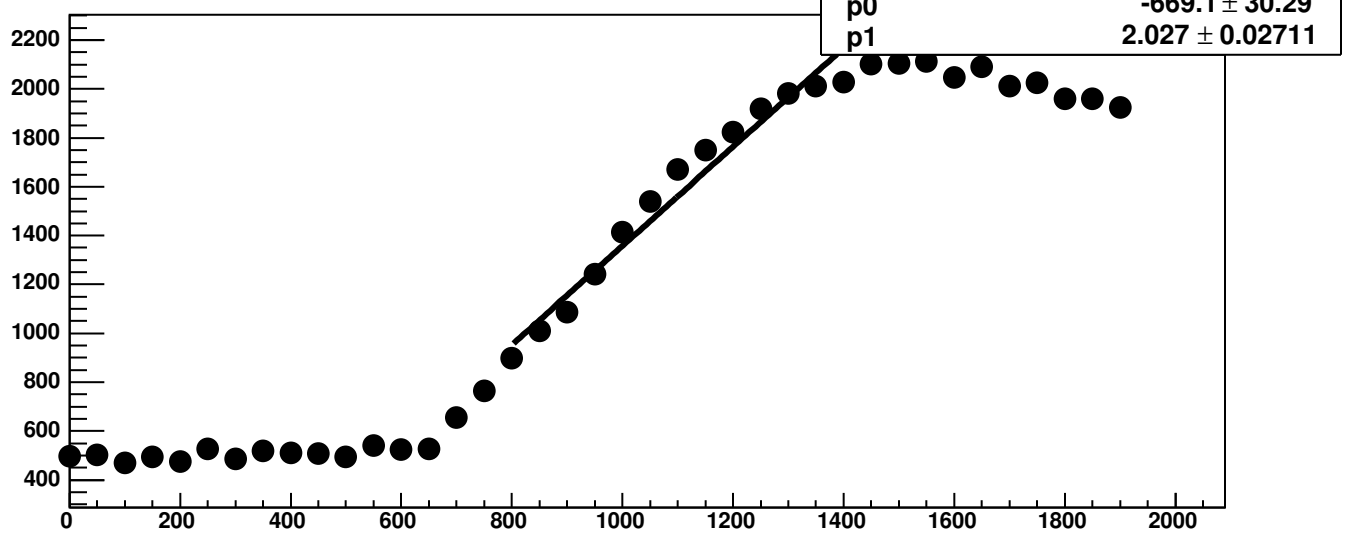

Chip 5, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 17.48 / 11
p0 -986.5 ± 19.39
p1 0.1842 ± 0.01799
-550
-600
-650
-700
-750
-800
-850
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

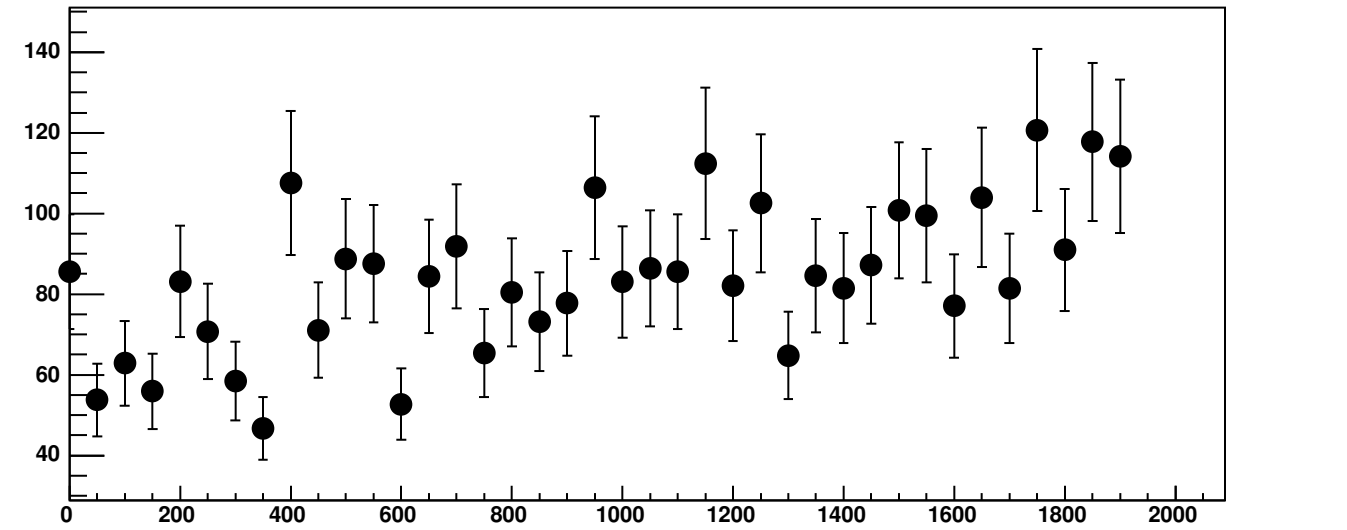

Chip 5, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

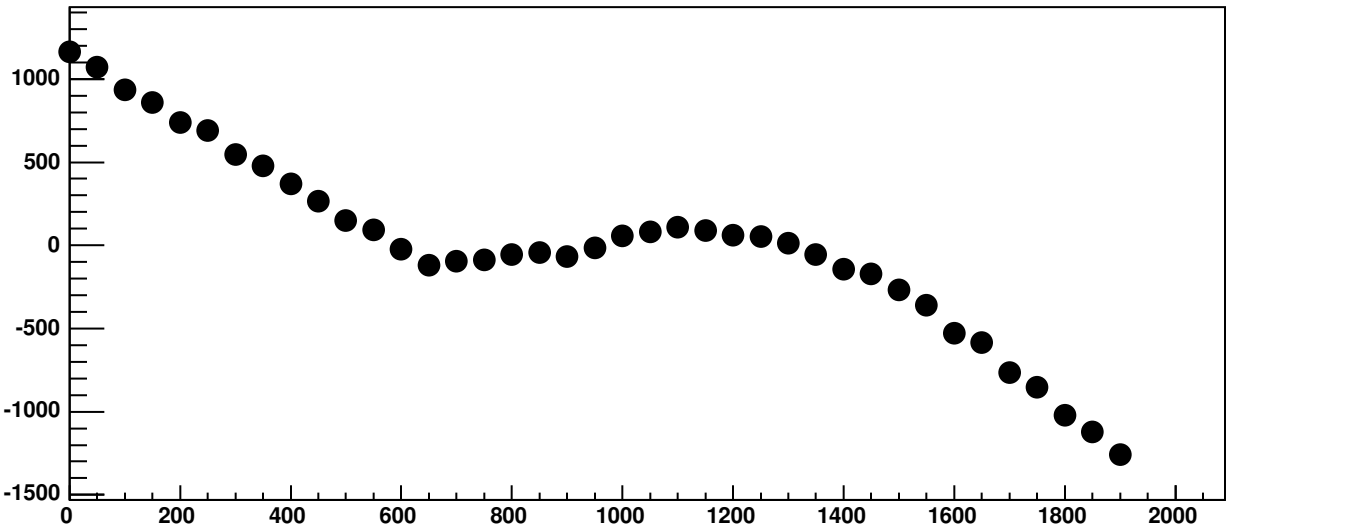
Chip 5, Channel 16, Enable 5, Hold=30, ADC Mean vs DAC

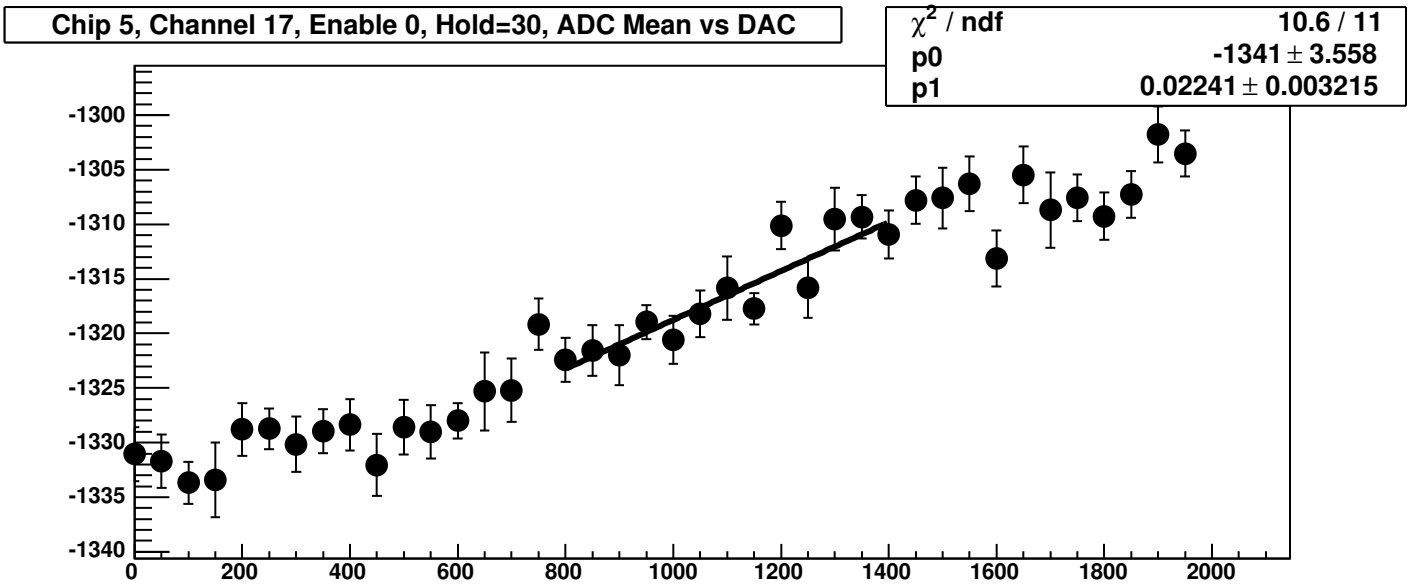


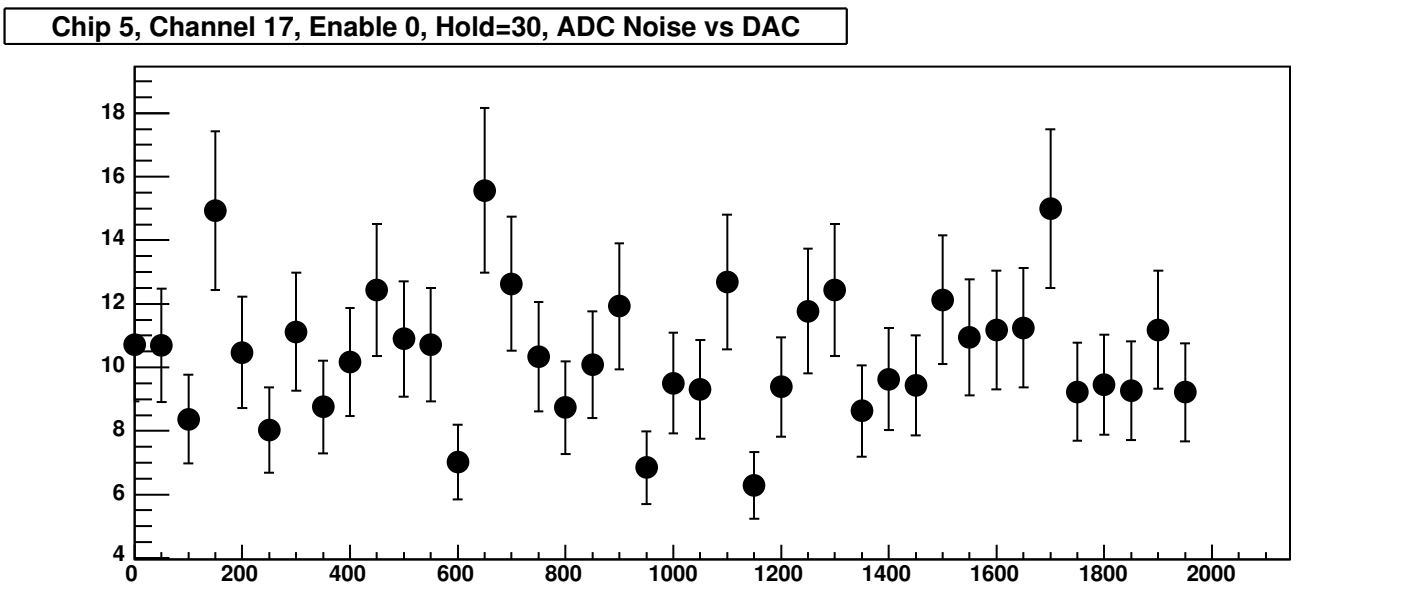
Chip 5, Channel 16, Enable 5, Hold=30, ADC Noise vs DAC

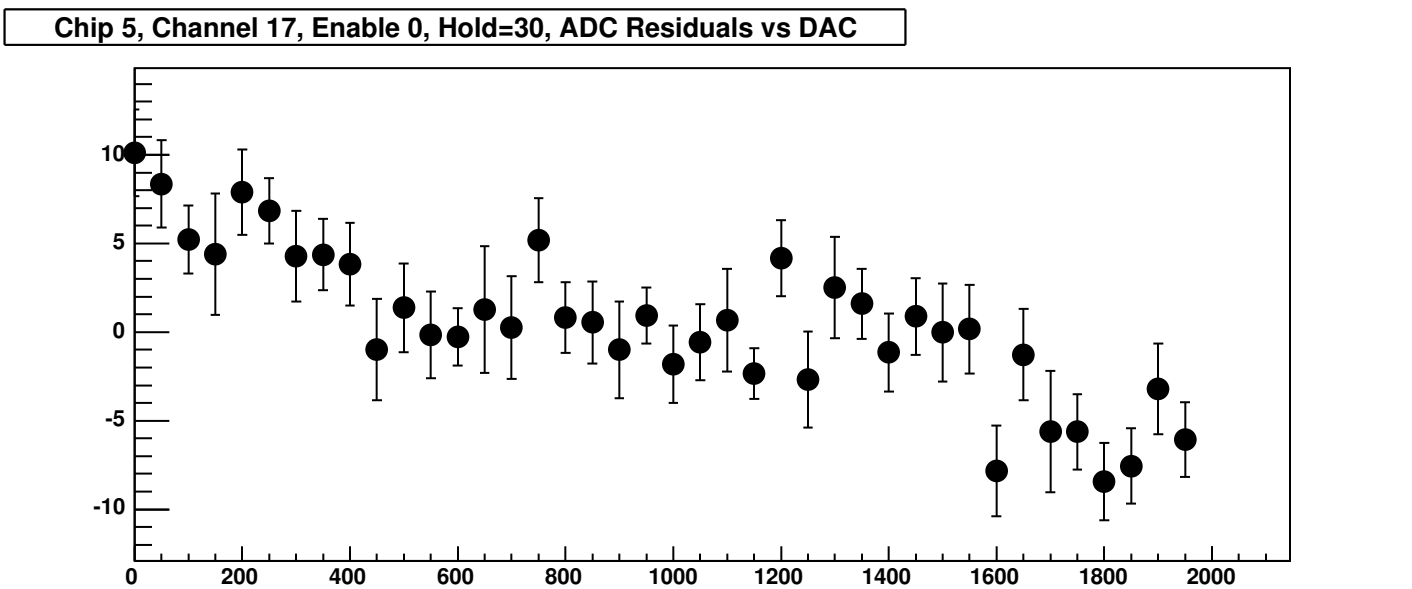


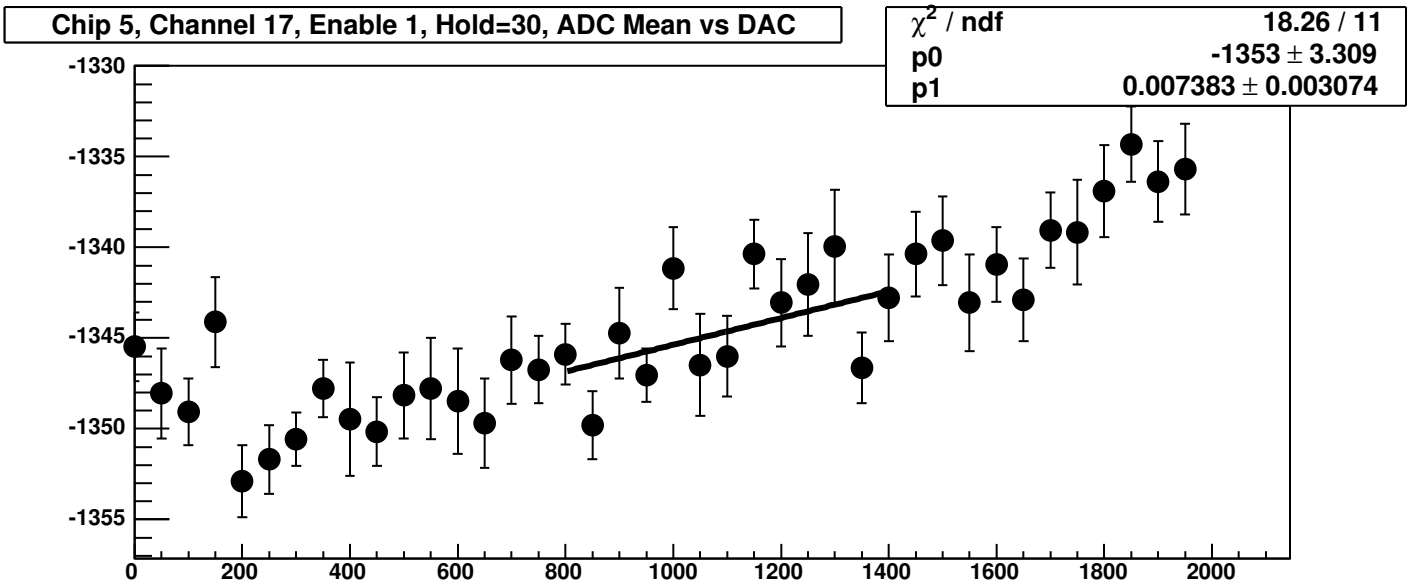
Chip 5, Channel 16, Enable 5, Hold=30, ADC Residuals vs DAC

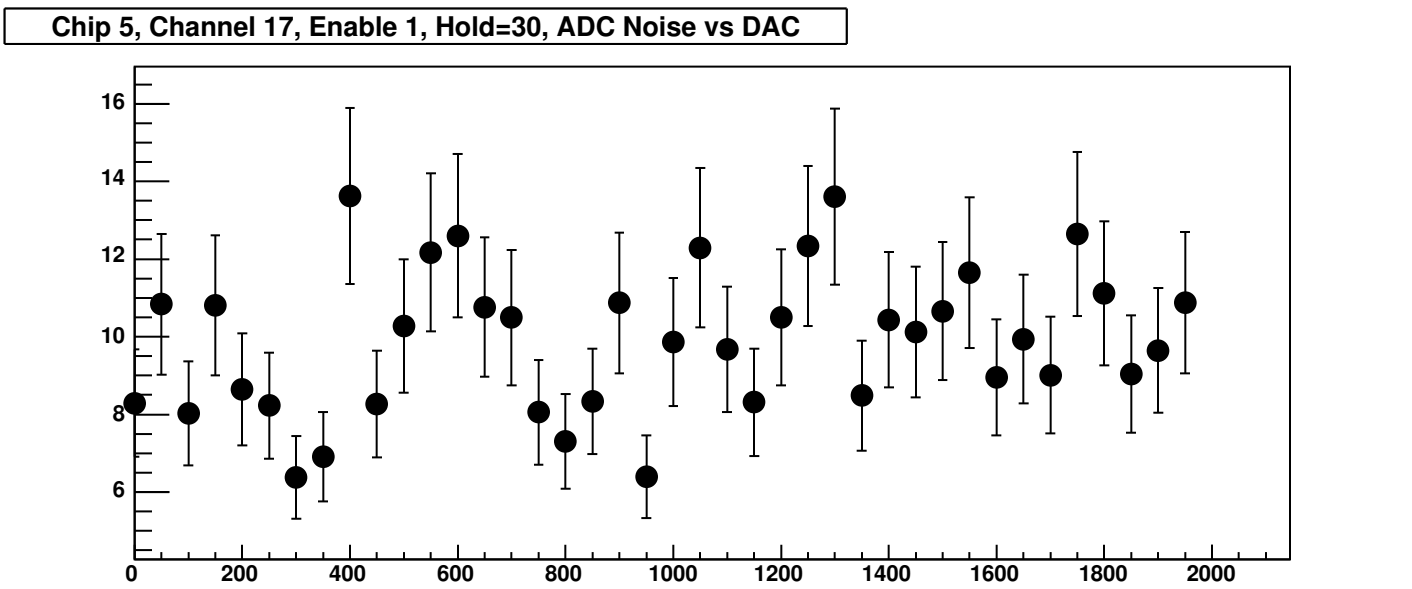


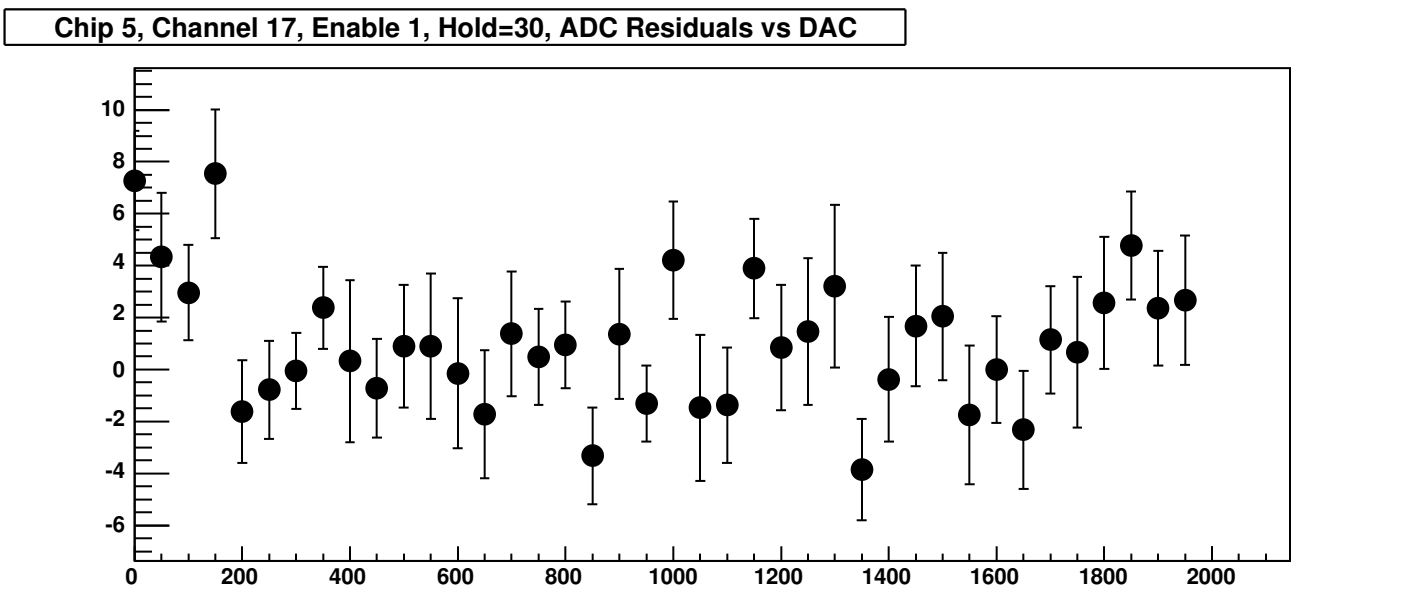

Chip 5, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 10.6 / 11
p0 -1341 ± 3.558
p1 0.02241 ± 0.003215
-1300
-1305
-1310
-1315
-1320
-1325
-1330
-1335
-1340
0
200
400
600
800
1000
1200
1400
1600
1800
2000

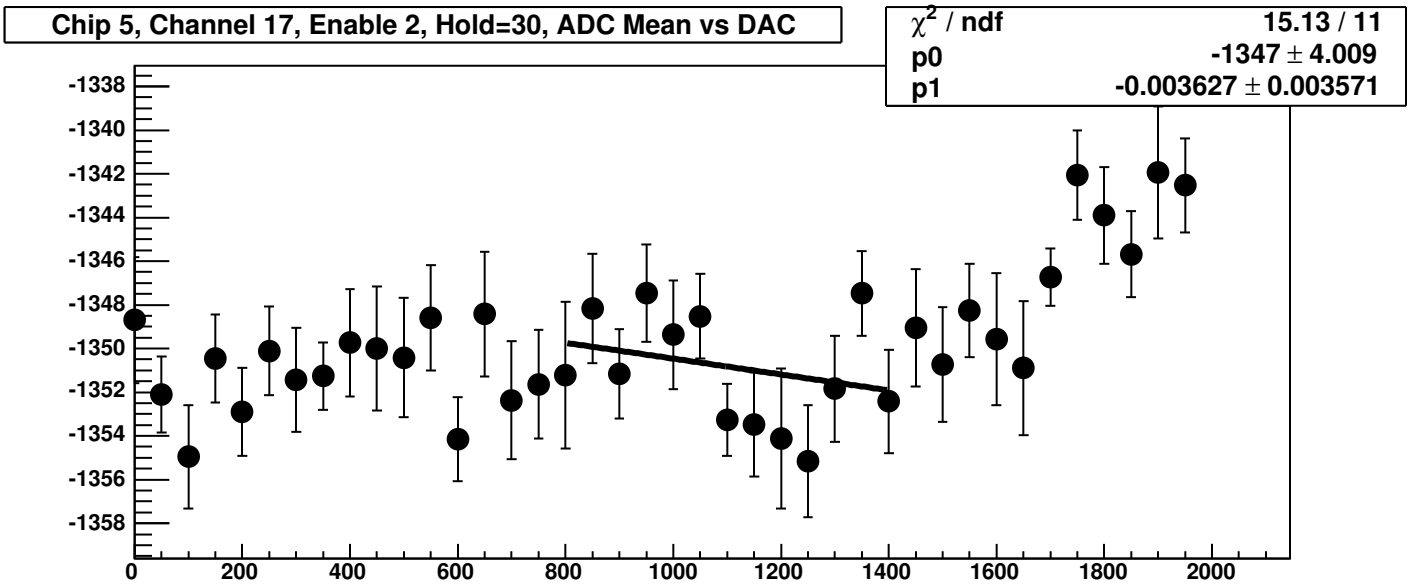

Chip 5, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
200
400
600
800
1000
1200
1400
1600
1800
2000

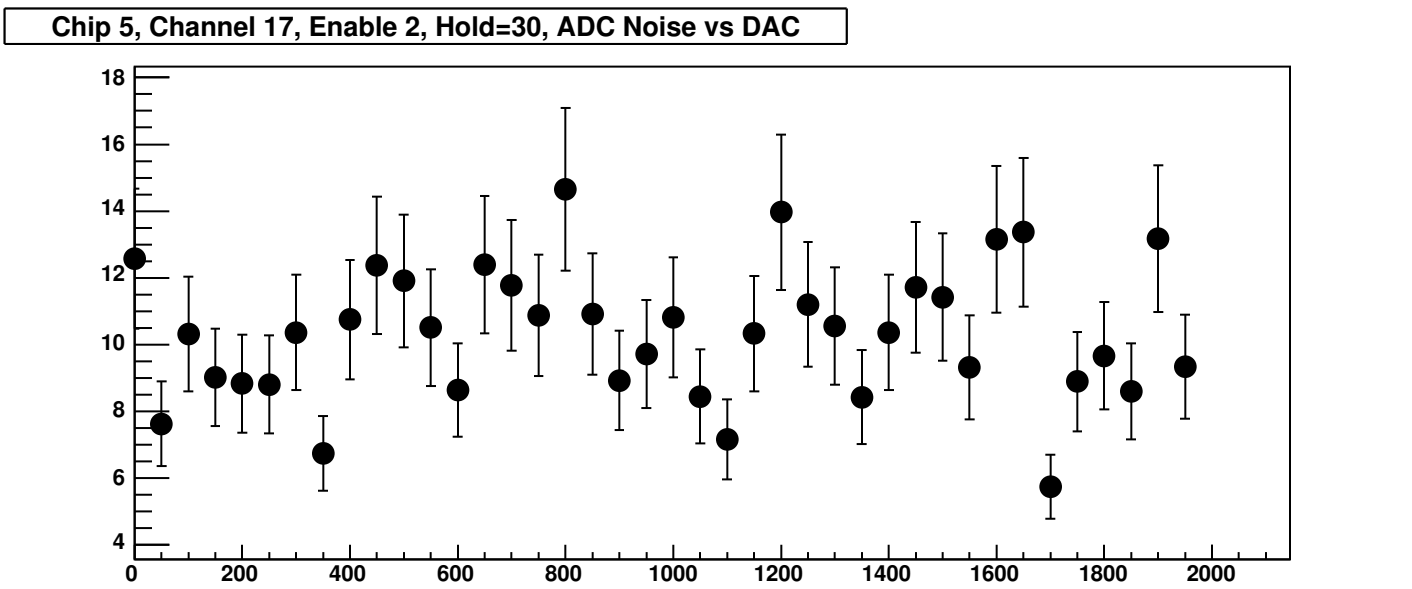

Chip 5, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

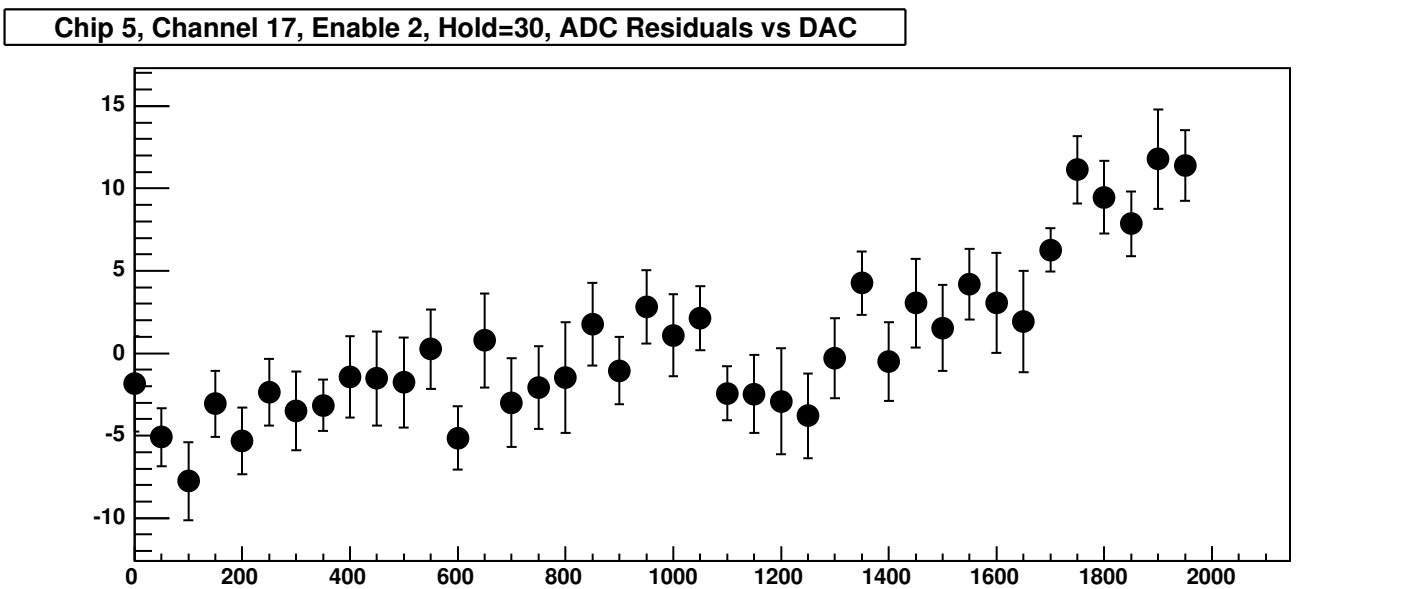

Chip 5, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 18.26 / 11
p0 -1353 ± 3.309
p1 0.007383 ± 0.003074
-1330
-1335
-1340
-1345
-1350
-1355
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

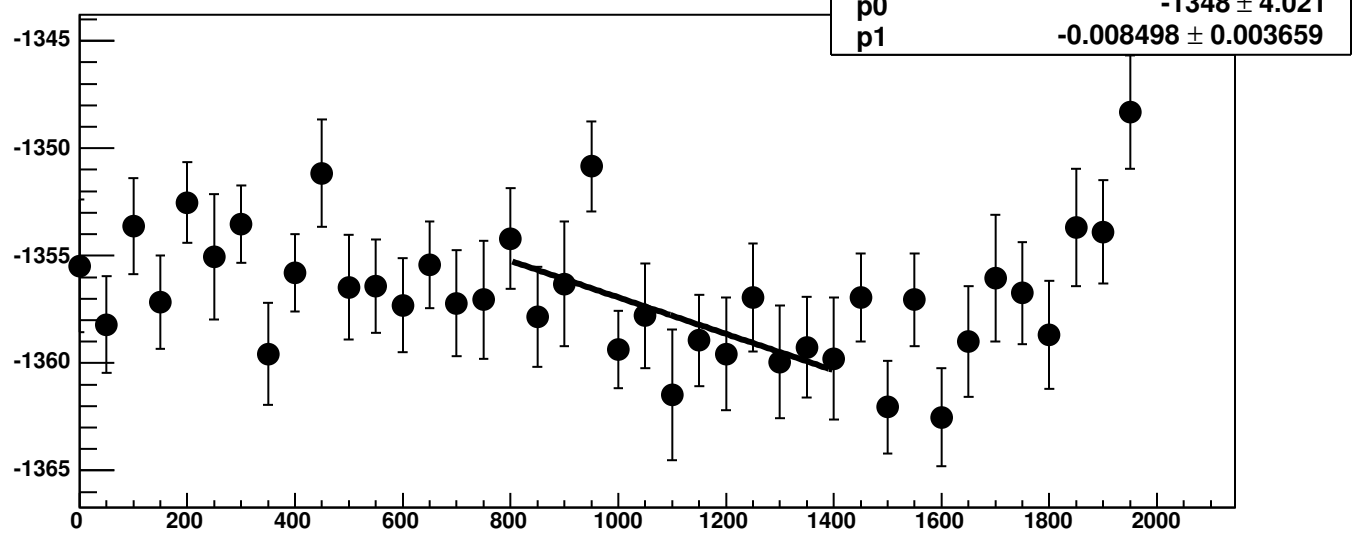

Chip 5, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 17, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 15.13 / 11
p0 -1347 ± 4.009
p1 -0.003627 ± 0.003571
-1338
-1340
-1342
-1344
-1346
-1348
-1350
-1352
-1354
-1356
-1358
0
200
400
600
800
1000
1200
1400
1600
1800
2000

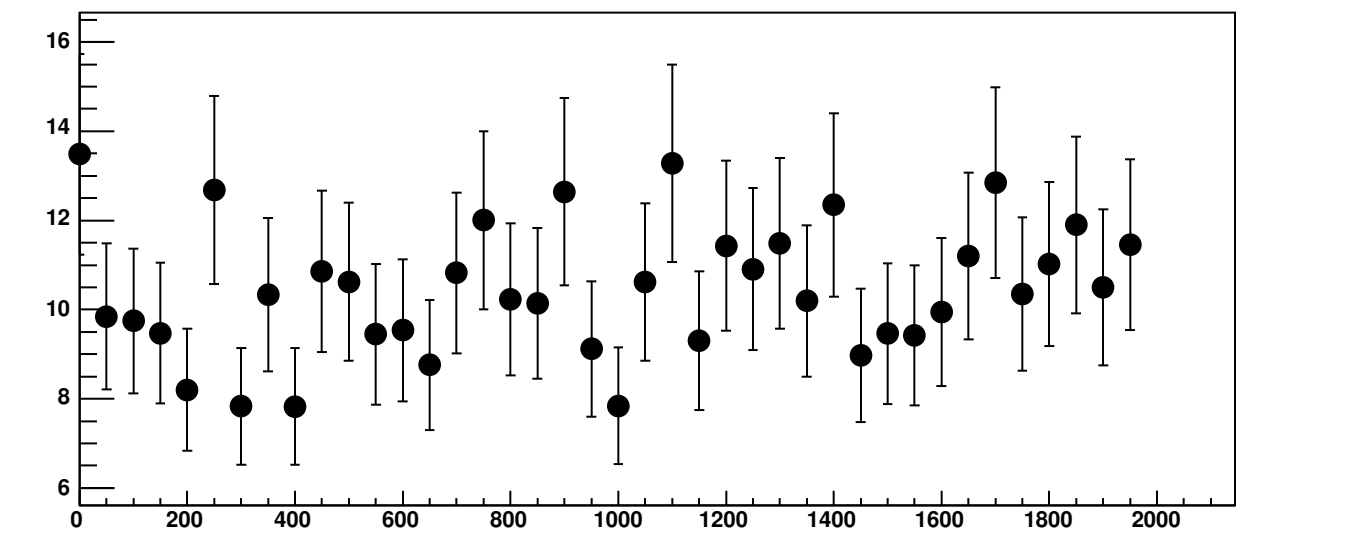

Chip 5, Channel 17, Enable 2, Hold=30, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 17, Enable 2, Hold=30, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

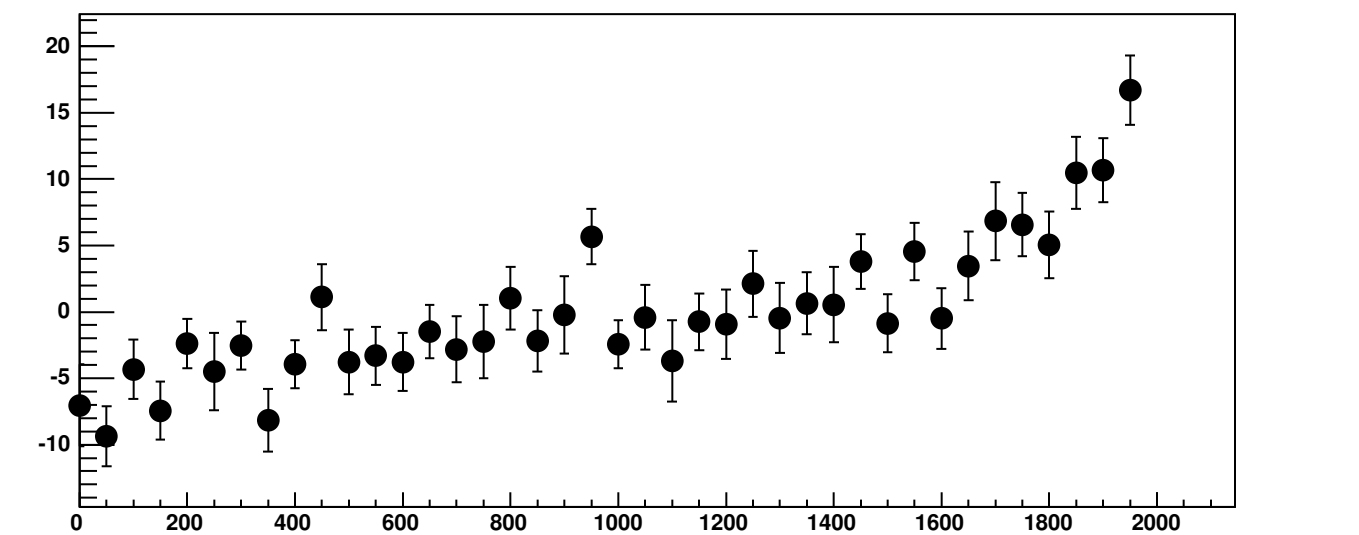
Chip 5, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC



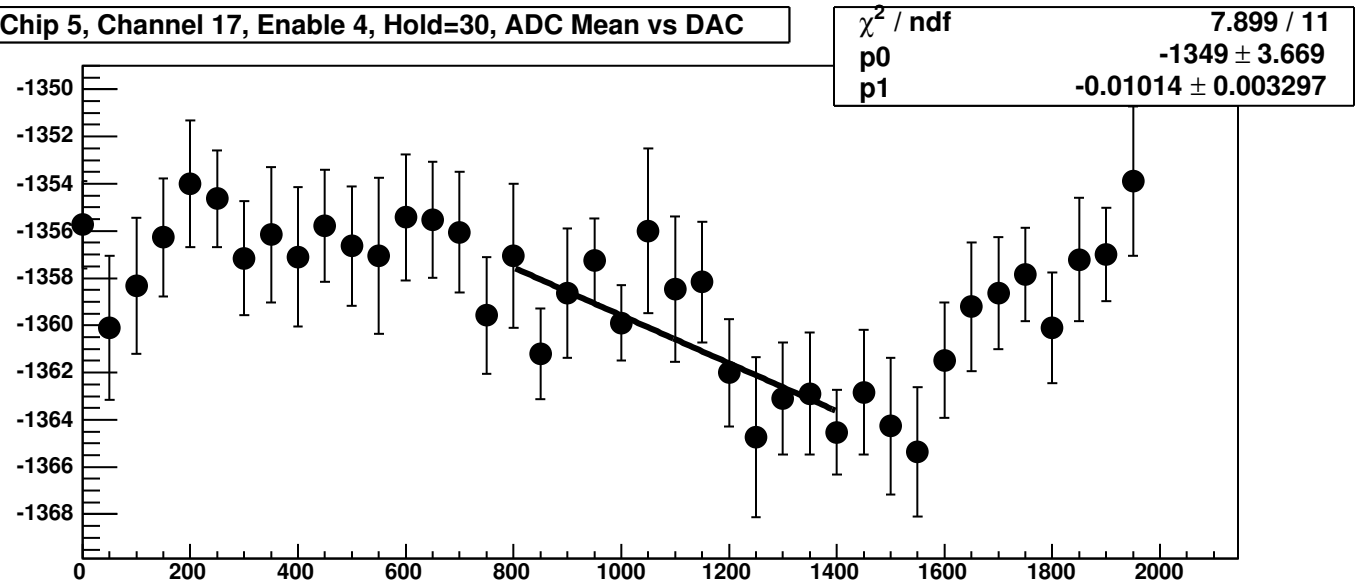
Chip 5, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC



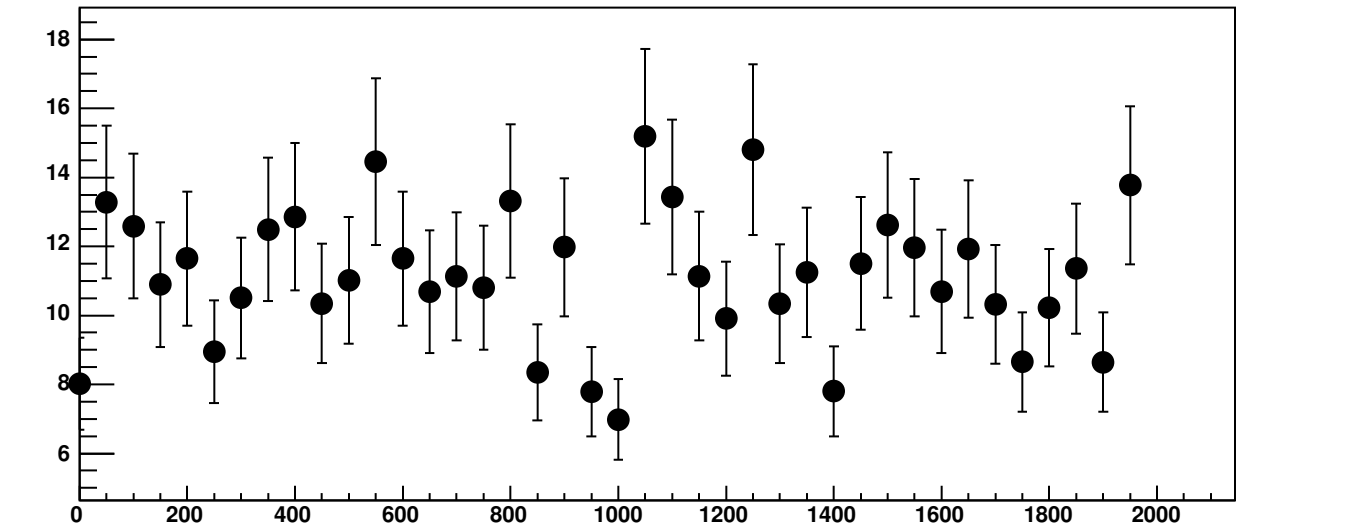
Chip 5, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC



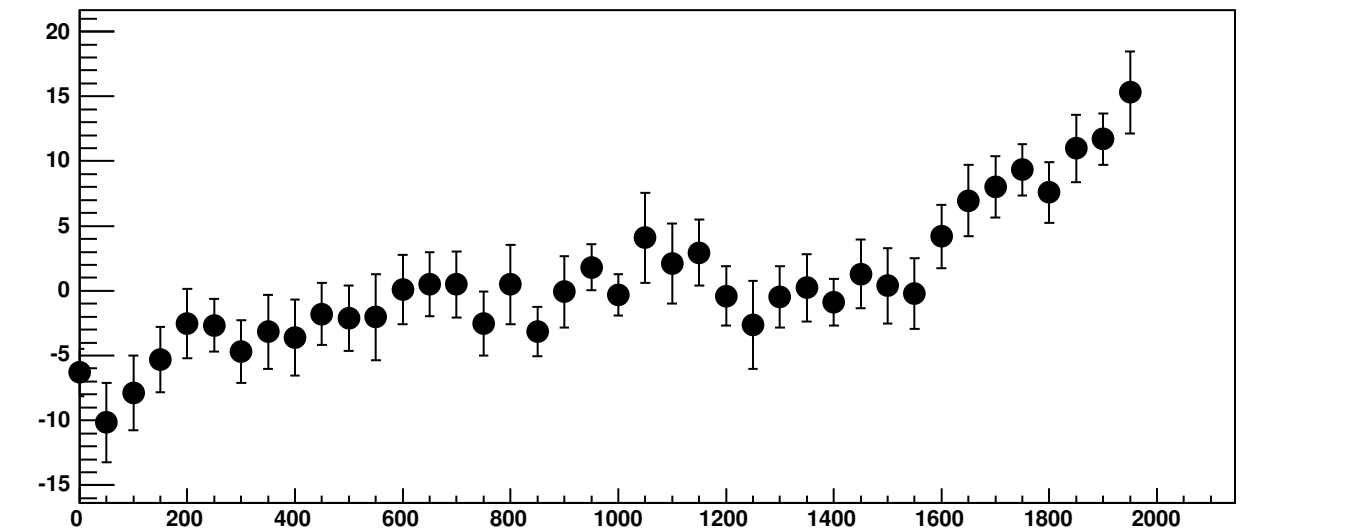
Chip 5, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC

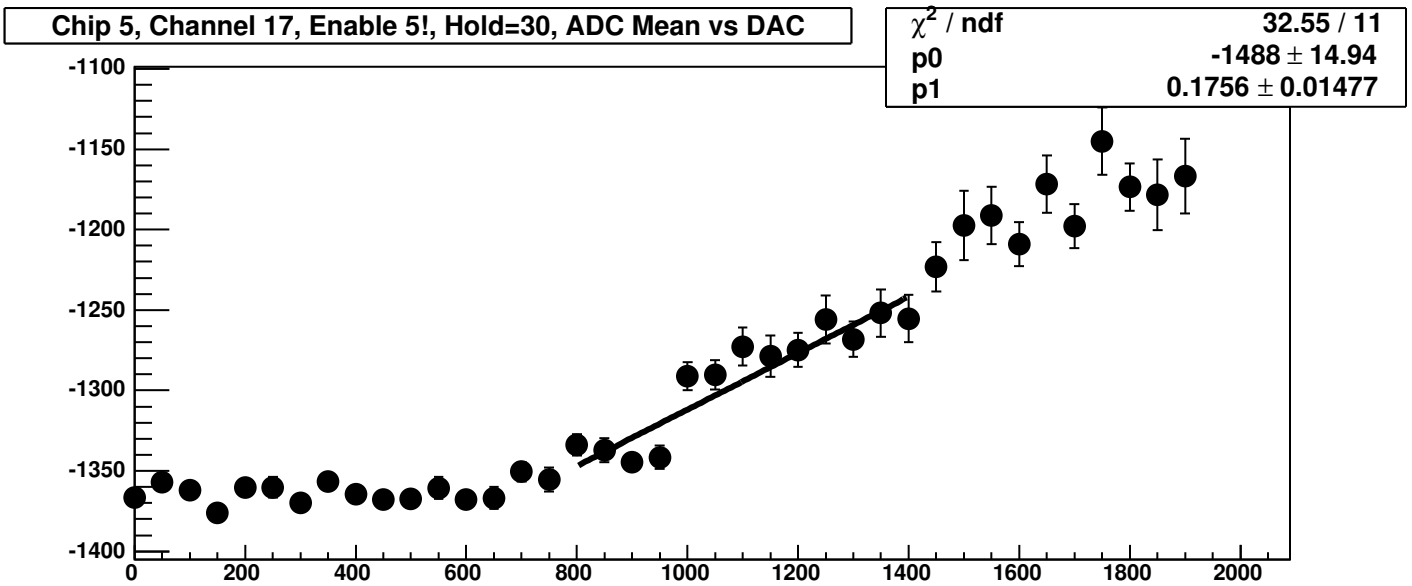


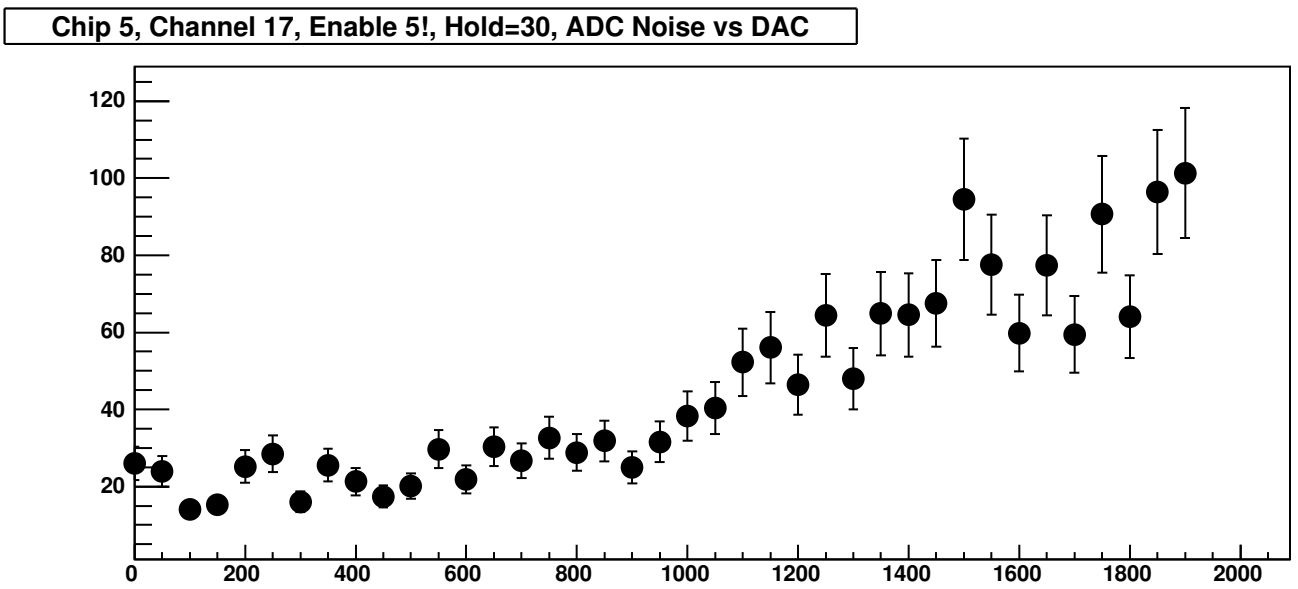
Chip 5, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC

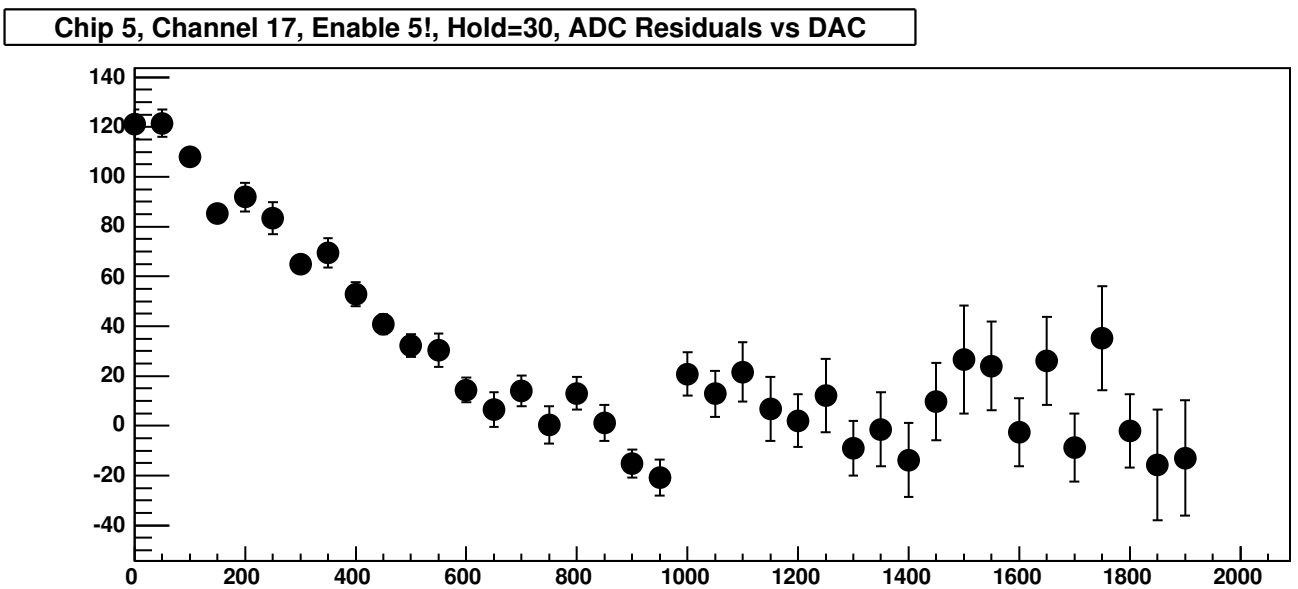


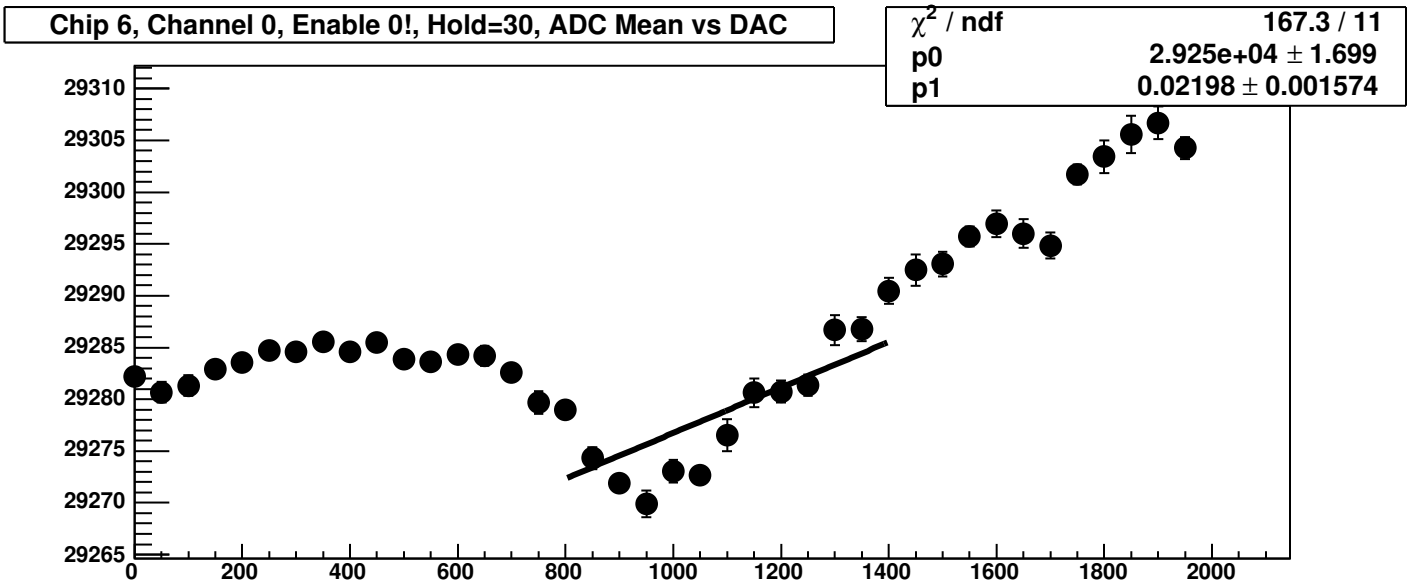
Chip 5, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC

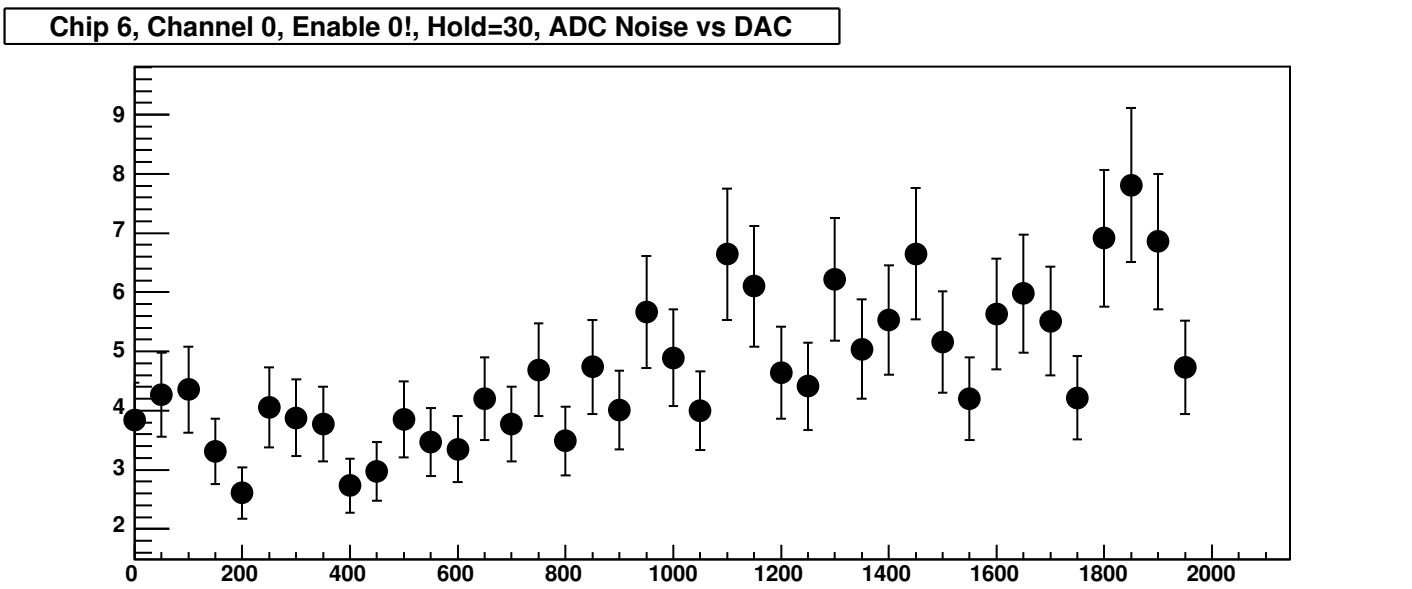


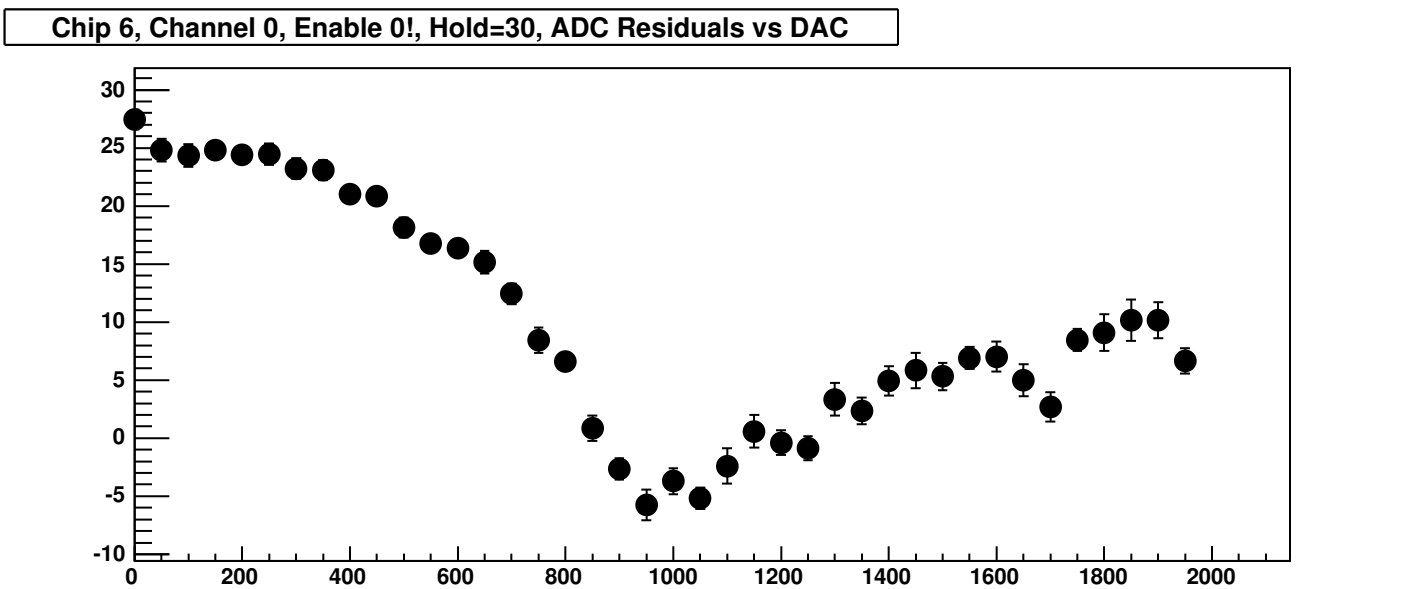

Chip 5, Channel 17, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 32.55 / 11
p0 -1488 ± 14.94
p1 0.1756 ± 0.01477
-1100
-1150
-1200
-1250
-1300
-1350
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 5, Channel 17, Enable 5!, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

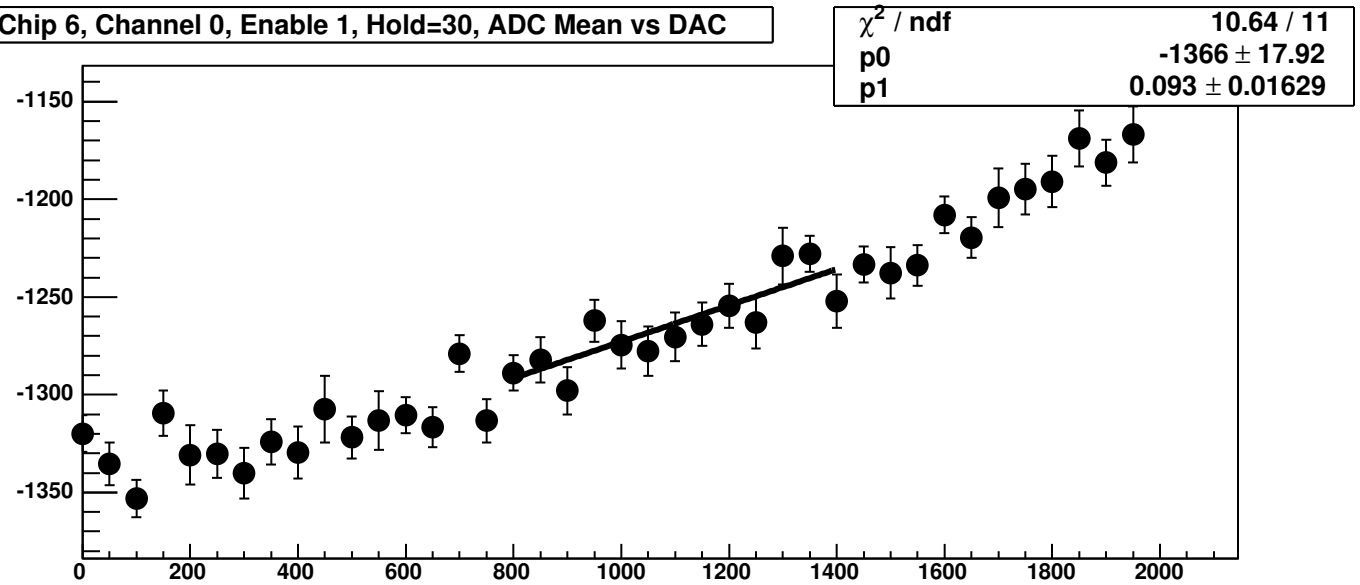

Chip 5, Channel 17, Enable 5!, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 0, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 167.3 / 11
p0 2.925e+04 ± 1.699
p1 0.02198 ± 0.001574
29310
29305
29300
29295
29290
29285
29280
29275
29270
29265
0 200 400 600 800 1000 1200 1400 1600 1800 2000

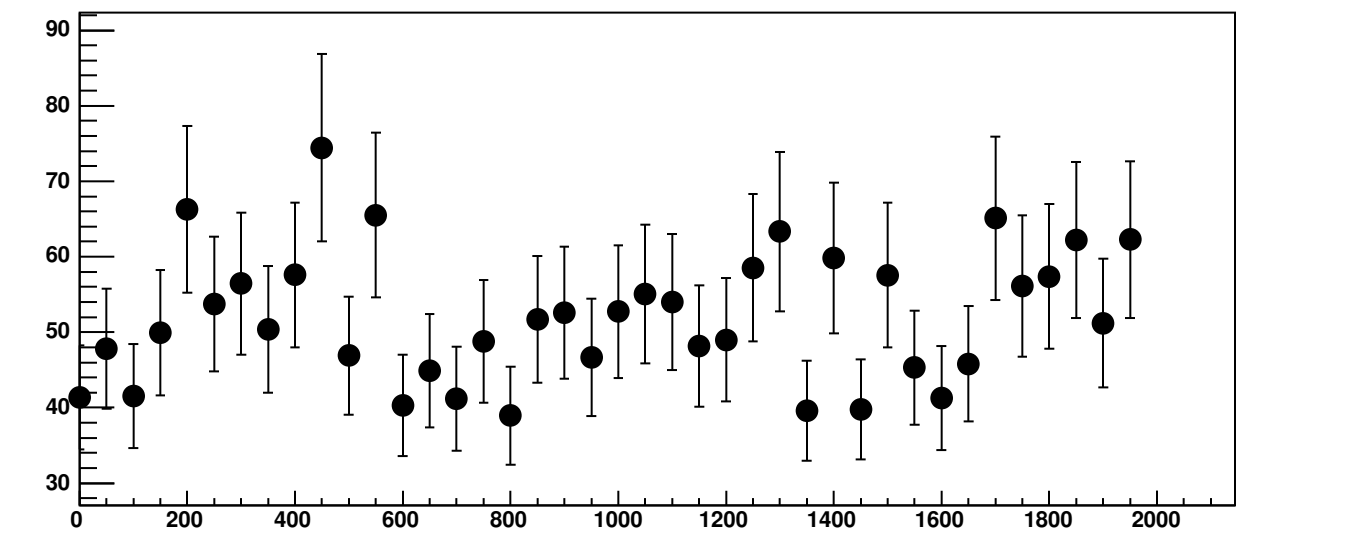

Chip 6, Channel 0, Enable 0!, Hold=30, ADC Noise vs DAC
9
8
7
6
5
4
3
2
0 200 400 600 800 1000 1200 1400 1600 1800 2000


Chip 6, Channel 0, Enable 0!, Hold=30, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0 200 400 600 800 1000 1200 1400 1600 1800 2000

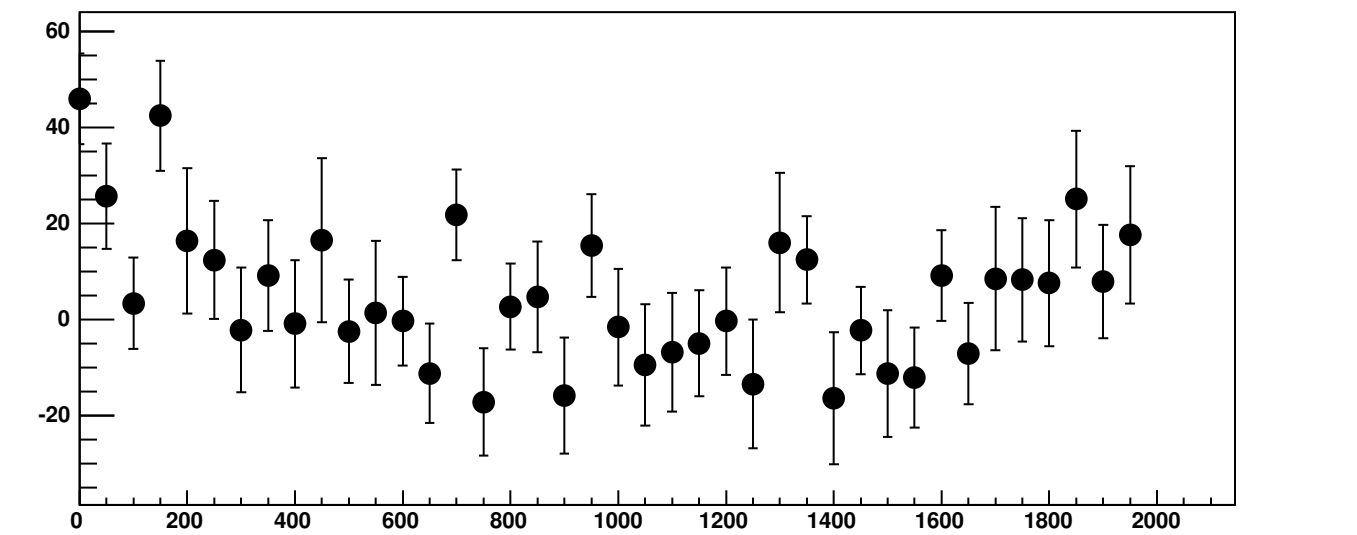
Chip 6, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC

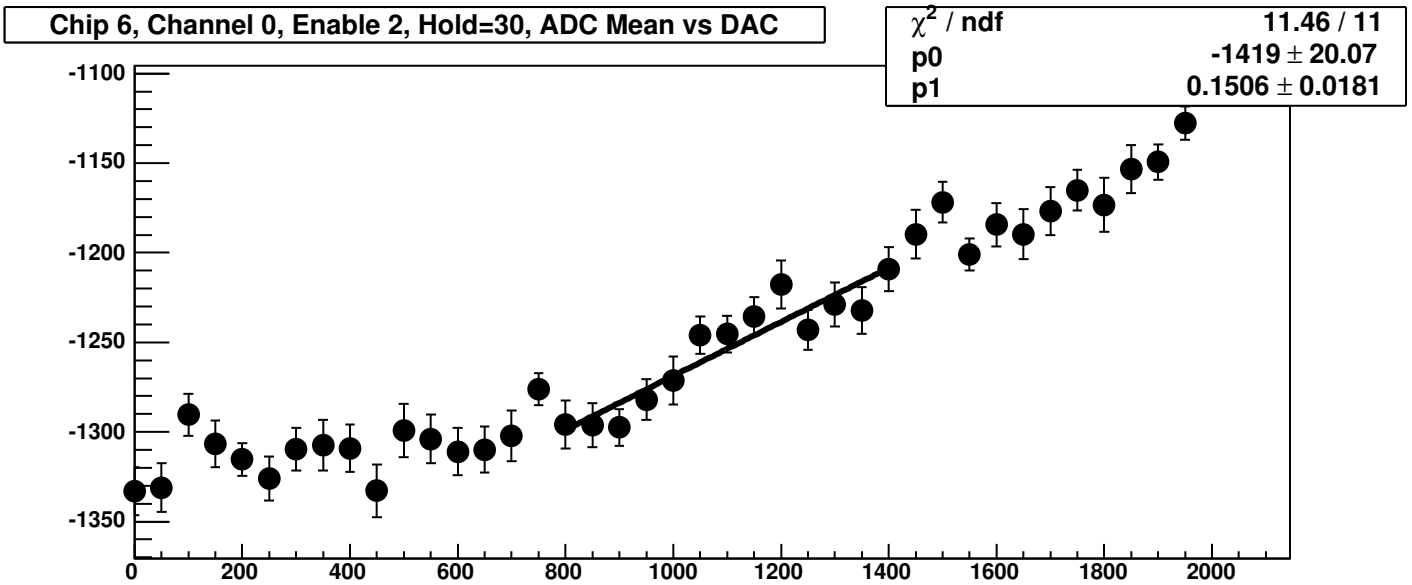


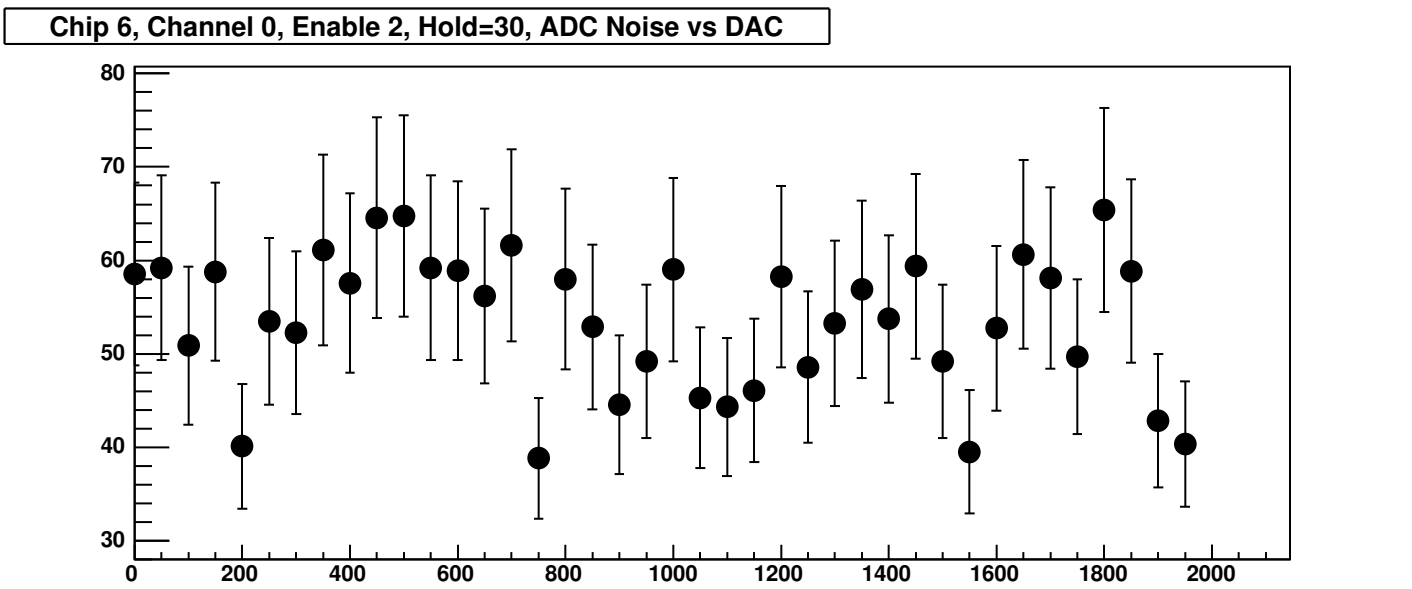
Chip 6, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC

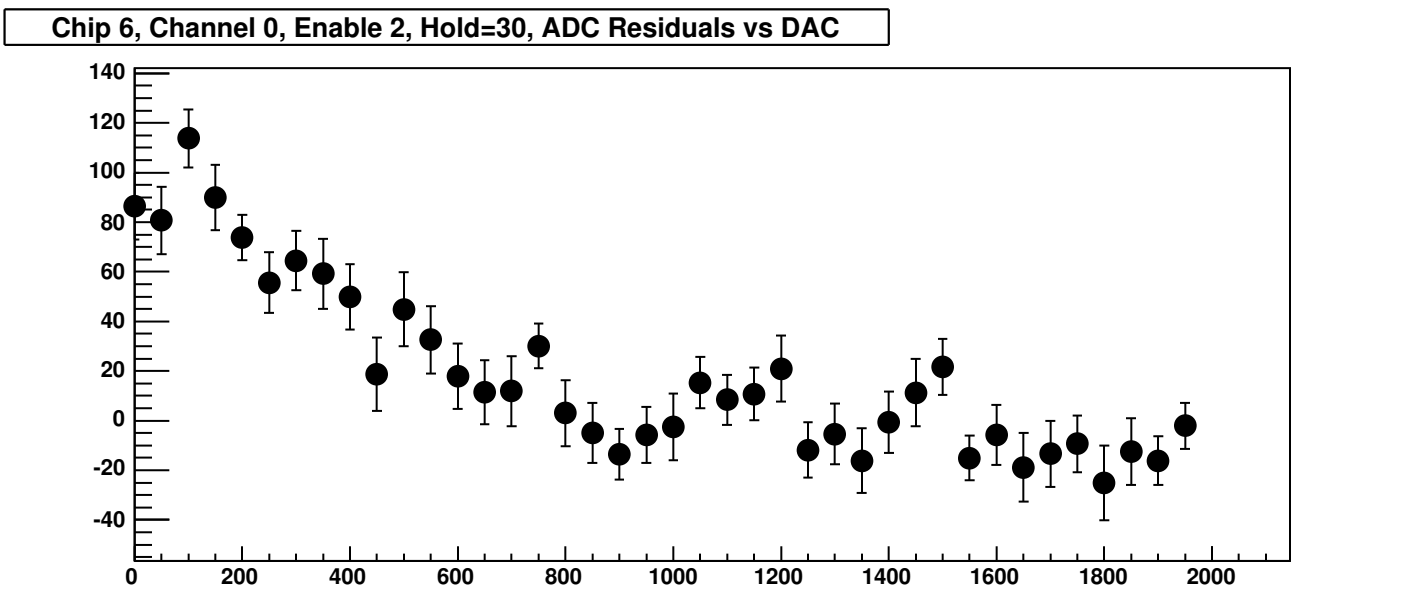


Chip 6, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC

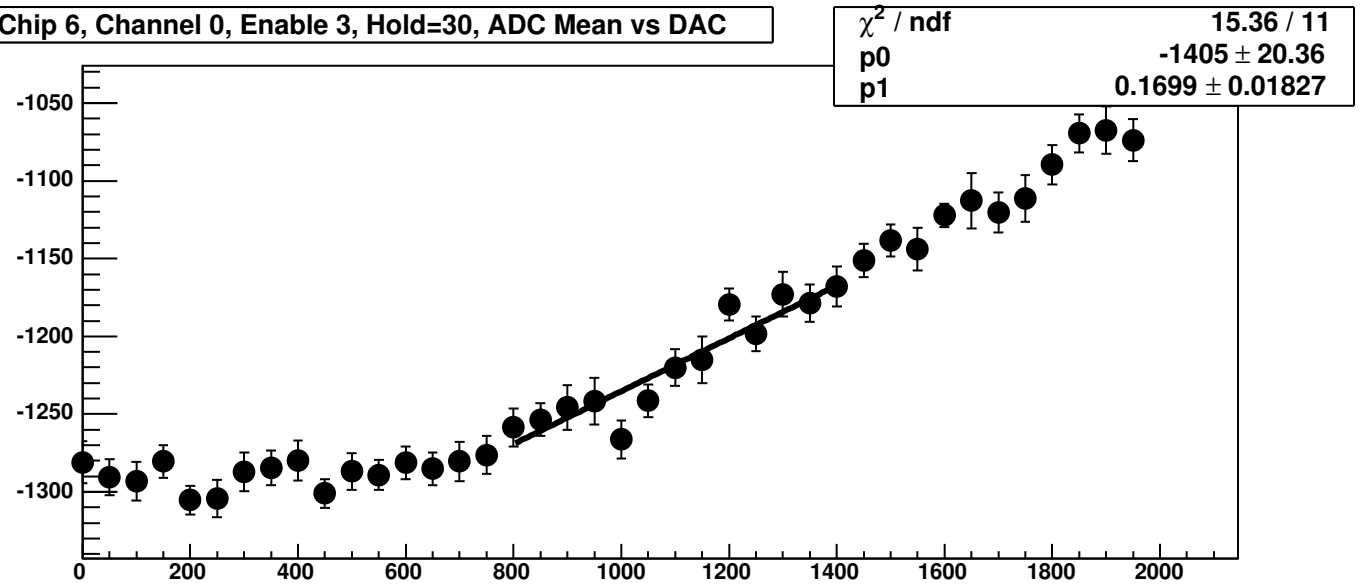



Chip 6, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 11.46 / 11
p0 -1419 ± 20.07
p1 0.1506 ± 0.0181
-1100
-1150
-1200
-1250
-1300
-1350
0
200
400
600
800
1000
1200
1400
1600
1800
2000

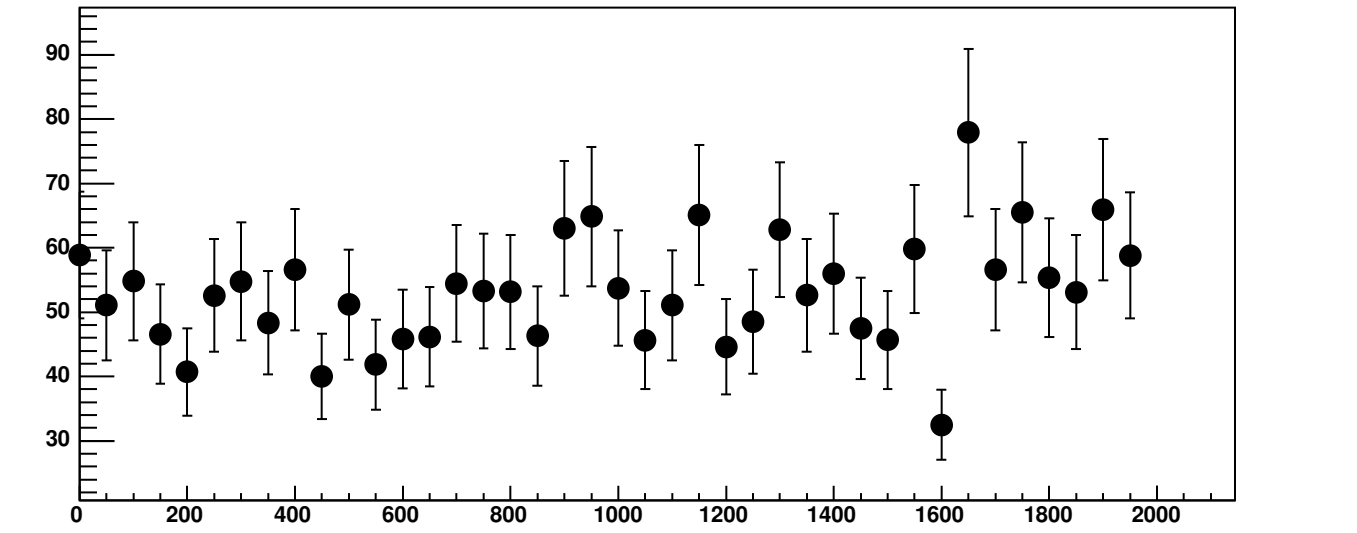

Chip 6, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

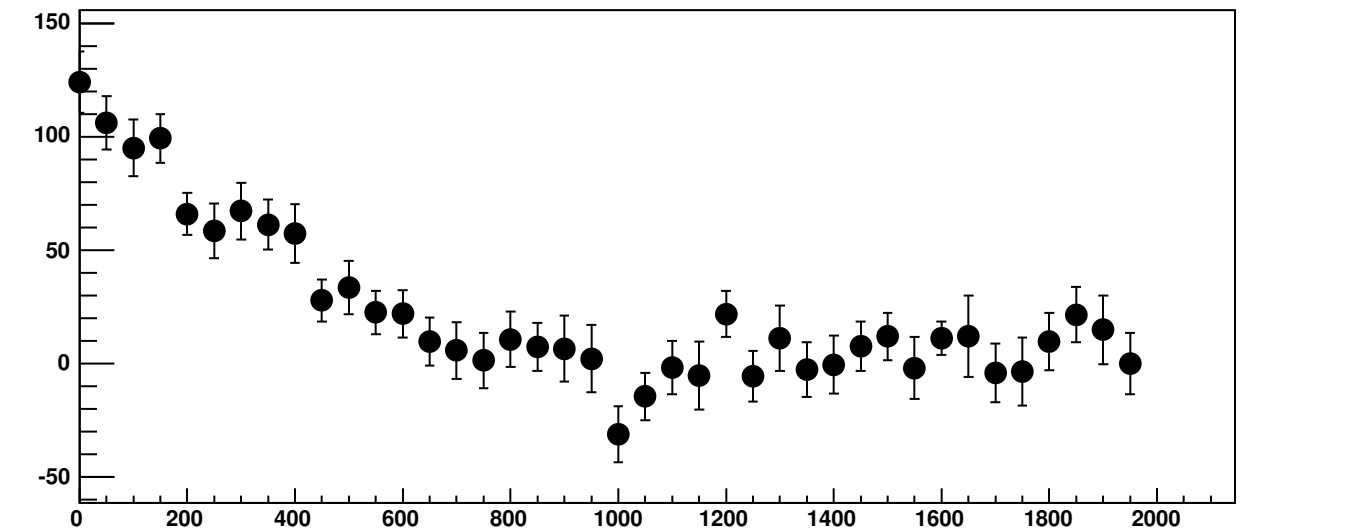
Chip 6, Channel 0, Enable 3, Hold=30, ADC Mean vs DAC

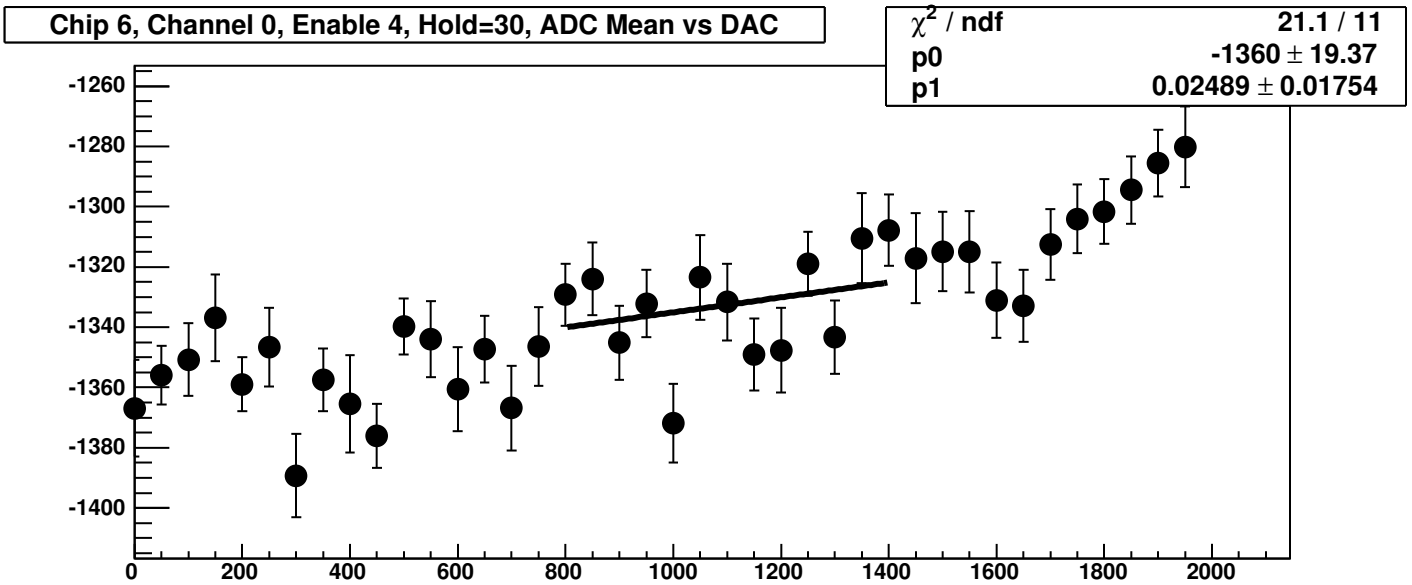


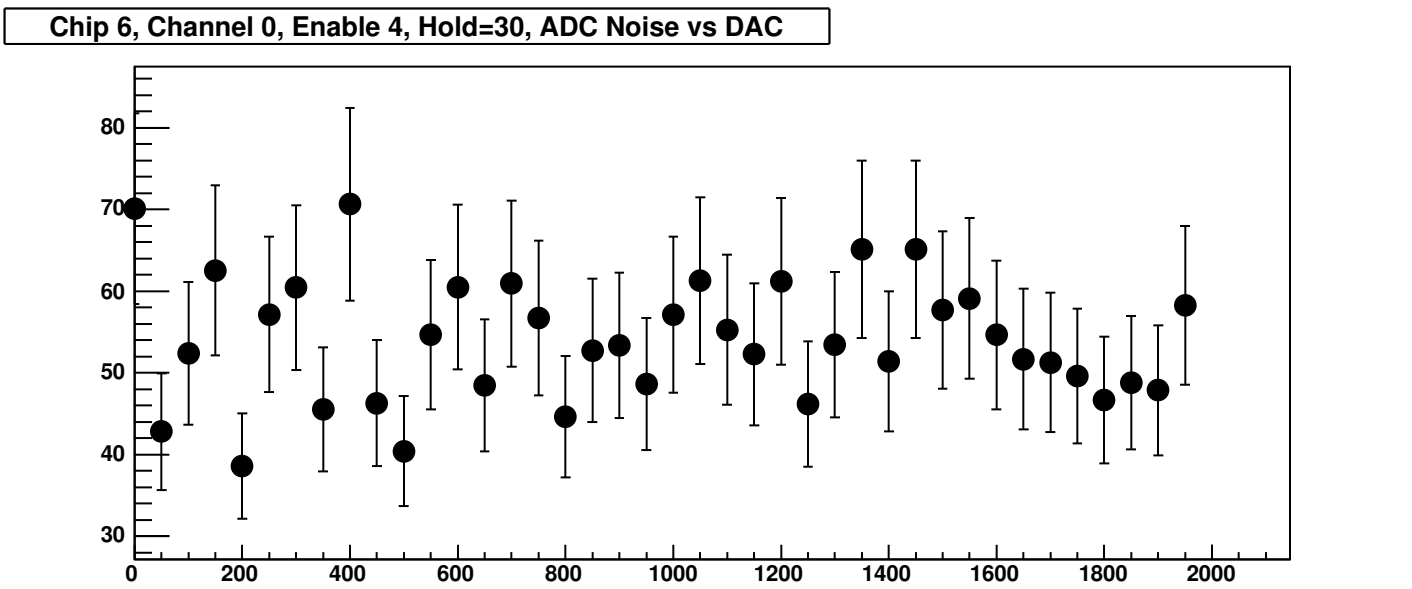
Chip 6, Channel 0, Enable 3, Hold=30, ADC Noise vs DAC

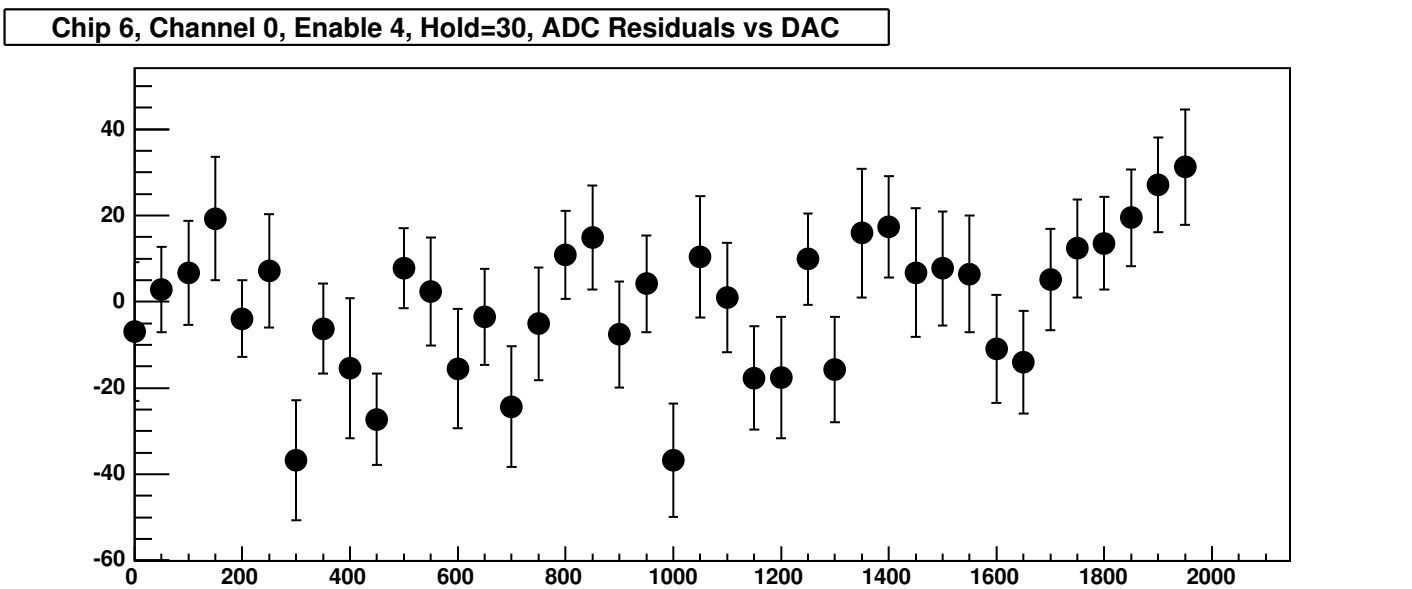


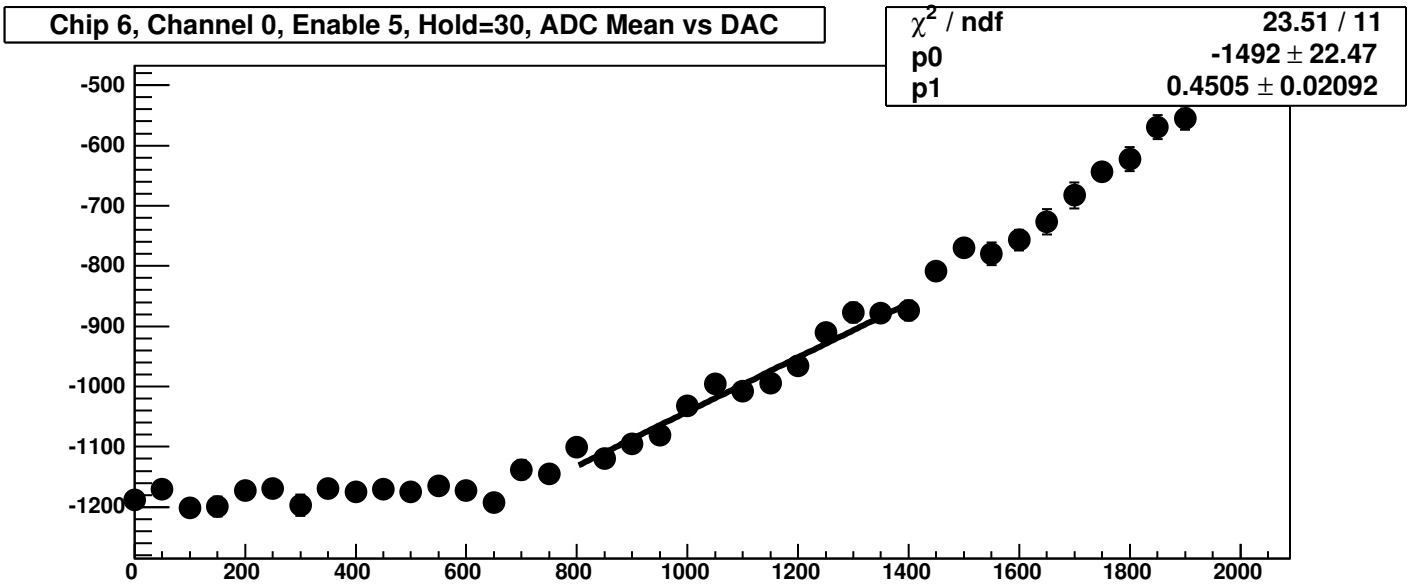
Chip 6, Channel 0, Enable 3, Hold=30, ADC Residuals vs DAC

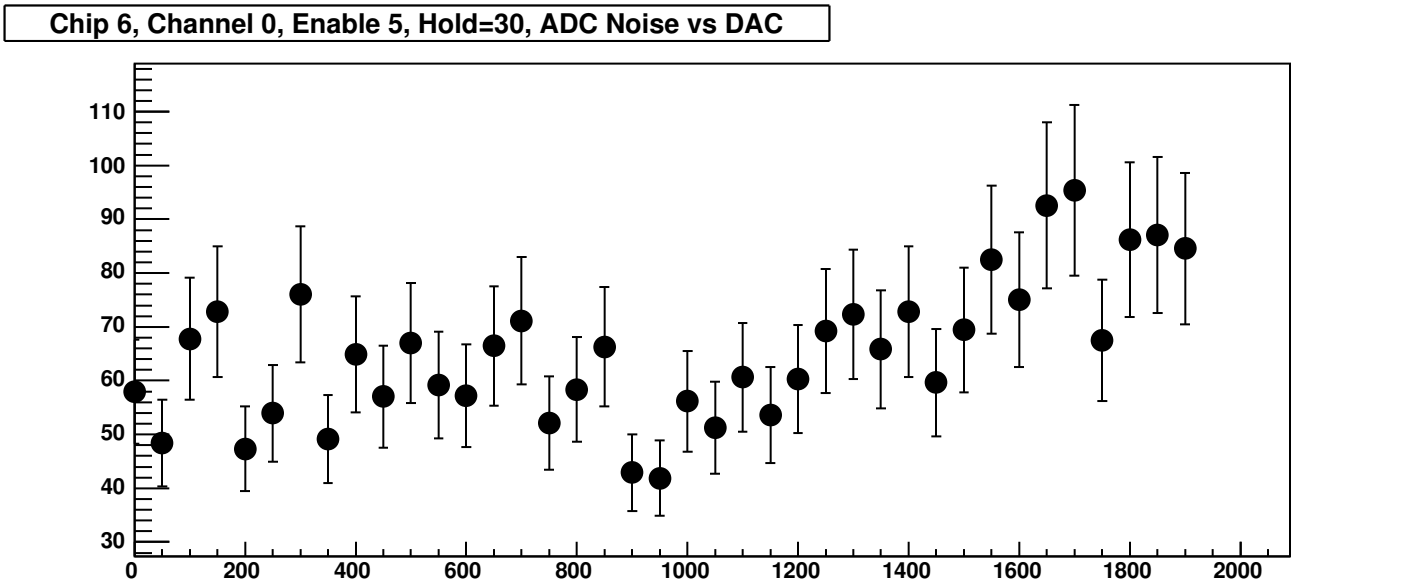


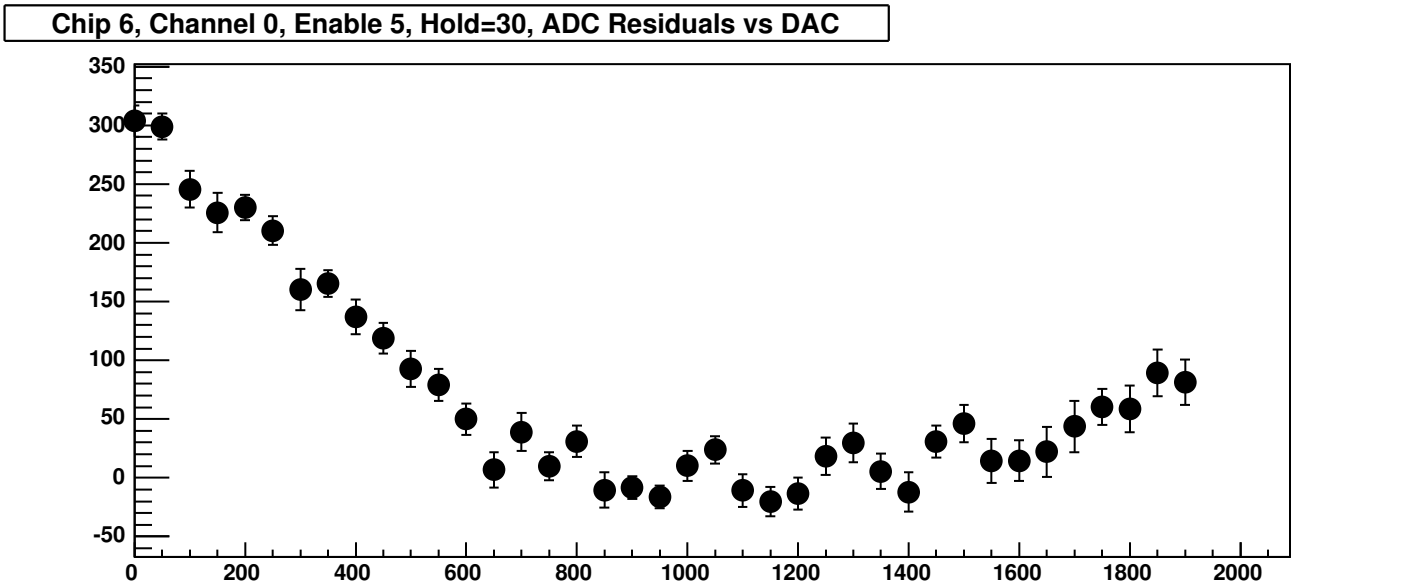

Chip 6, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 21.1 / 11
p0 -1360 ± 19.37
p1 0.02489 ± 0.01754
-1260
-1280
-1300
-1320
-1340
-1360
-1380
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

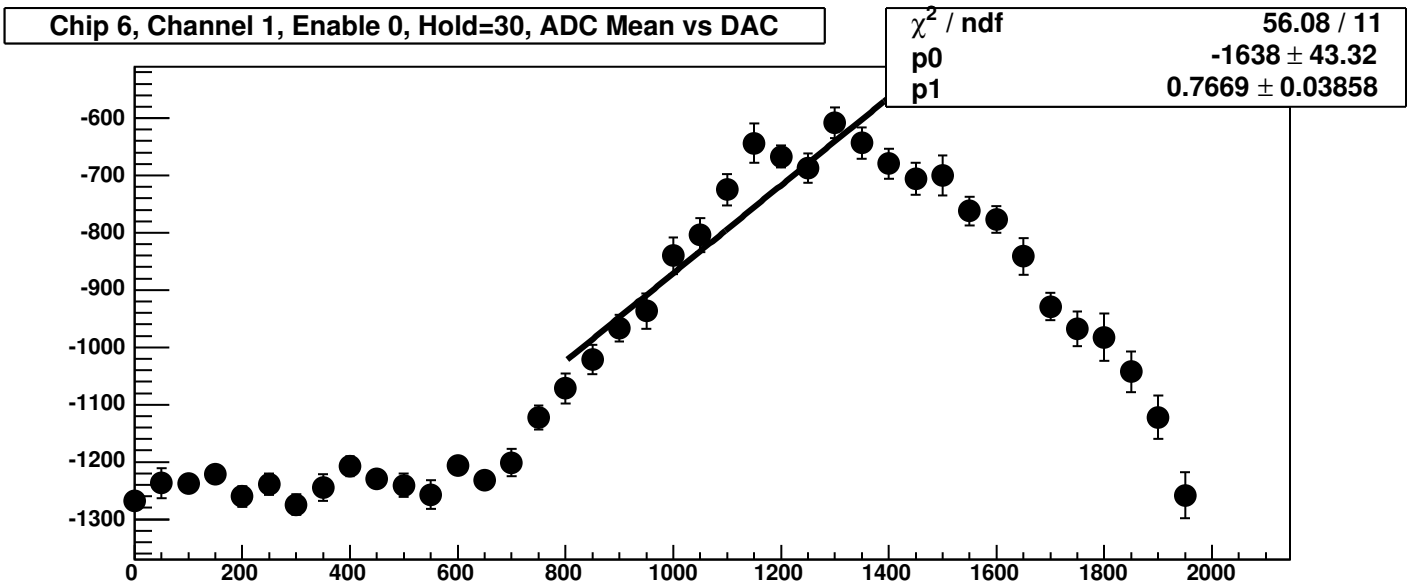

Chip 6, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

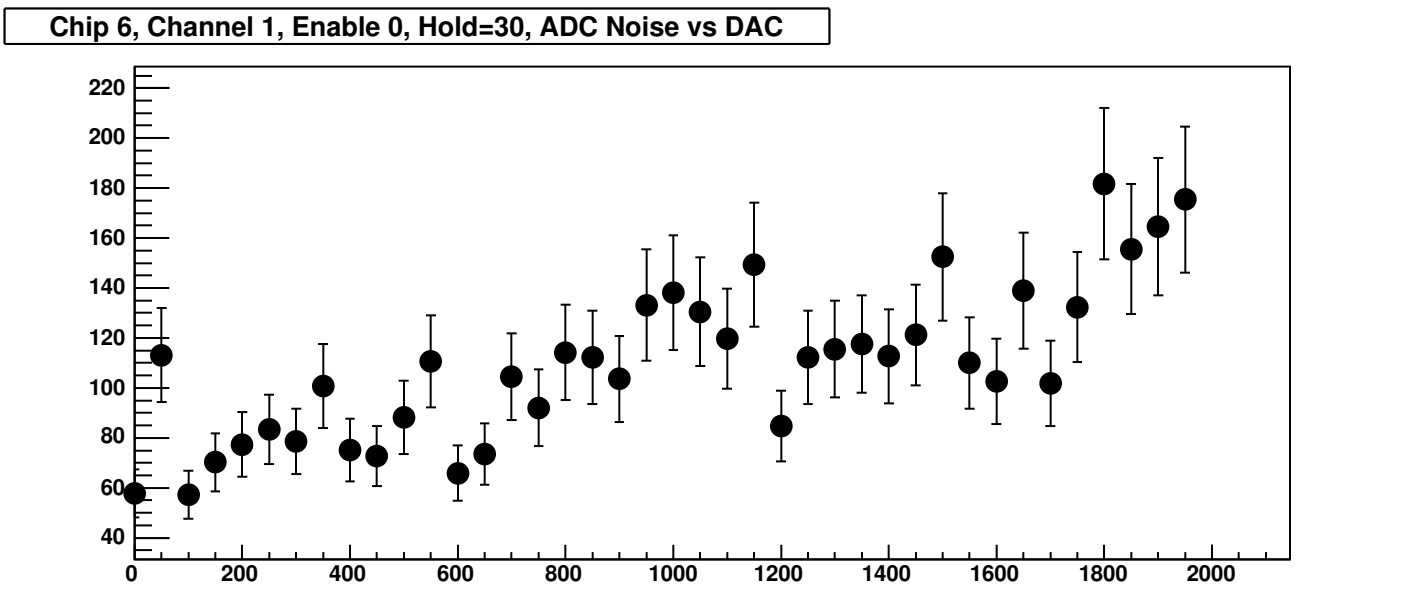

Chip 6, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

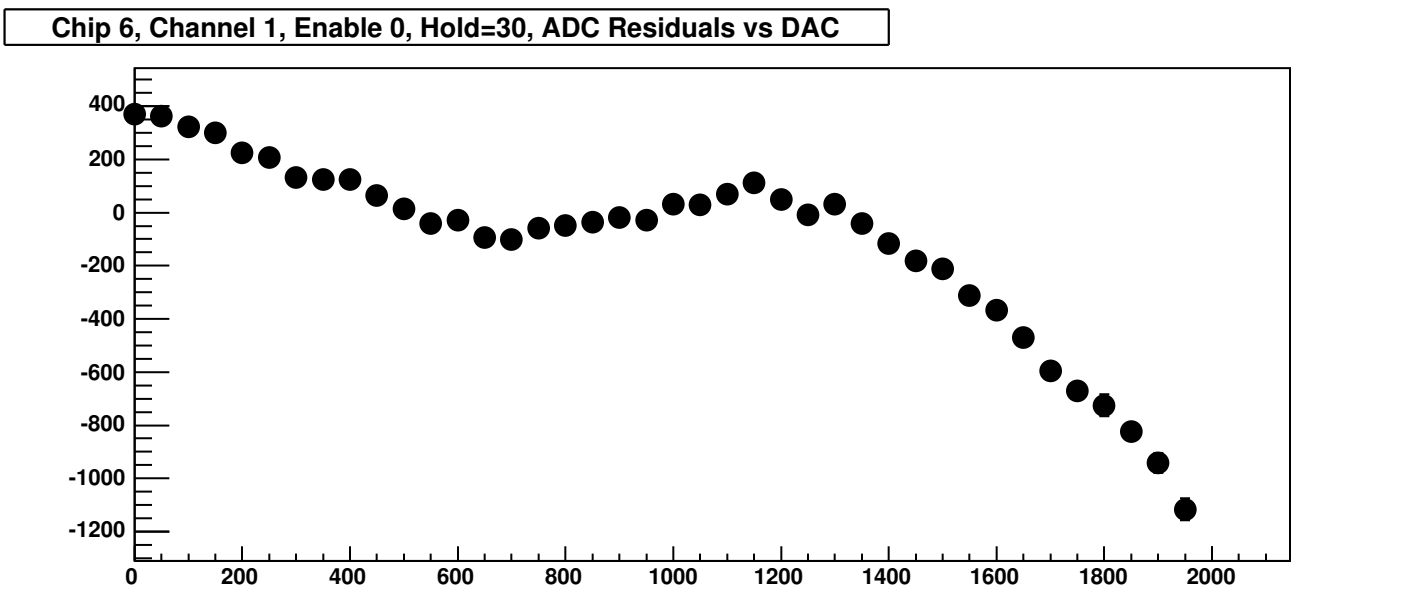

Chip 6, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 23.51 / 11
p0 -1492 ± 22.47
p1 0.4505 ± 0.02092
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

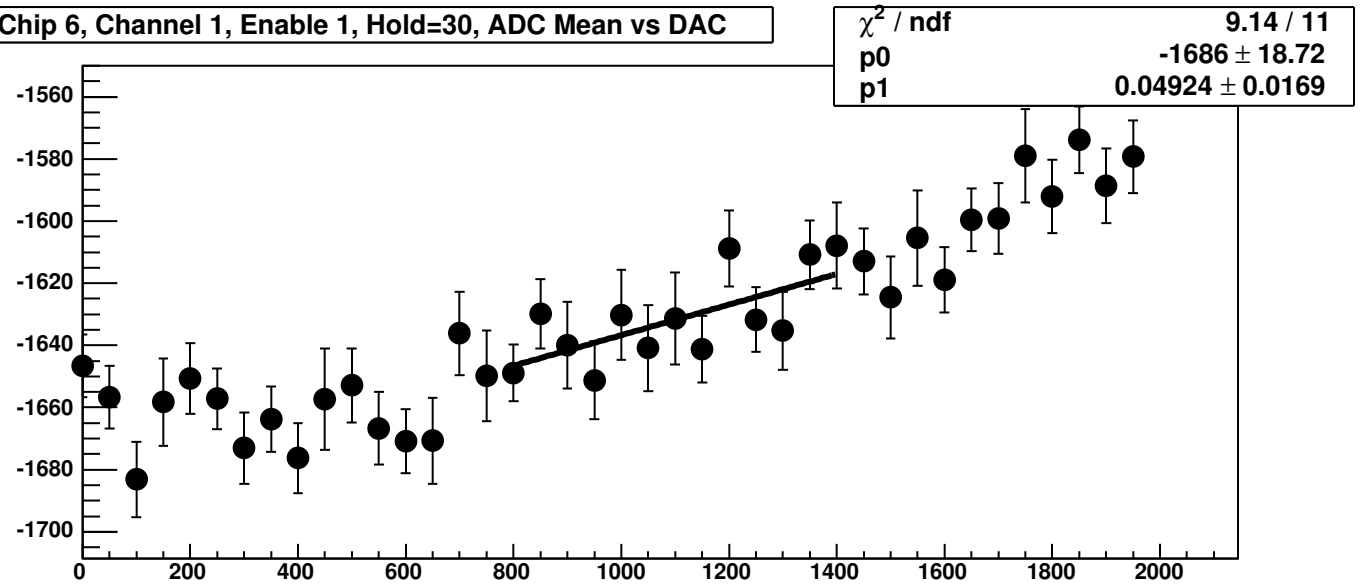

Chip 6, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 1, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 56.08 / 11
p0 -1638 ± 43.32
p1 0.7669 ± 0.03858
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

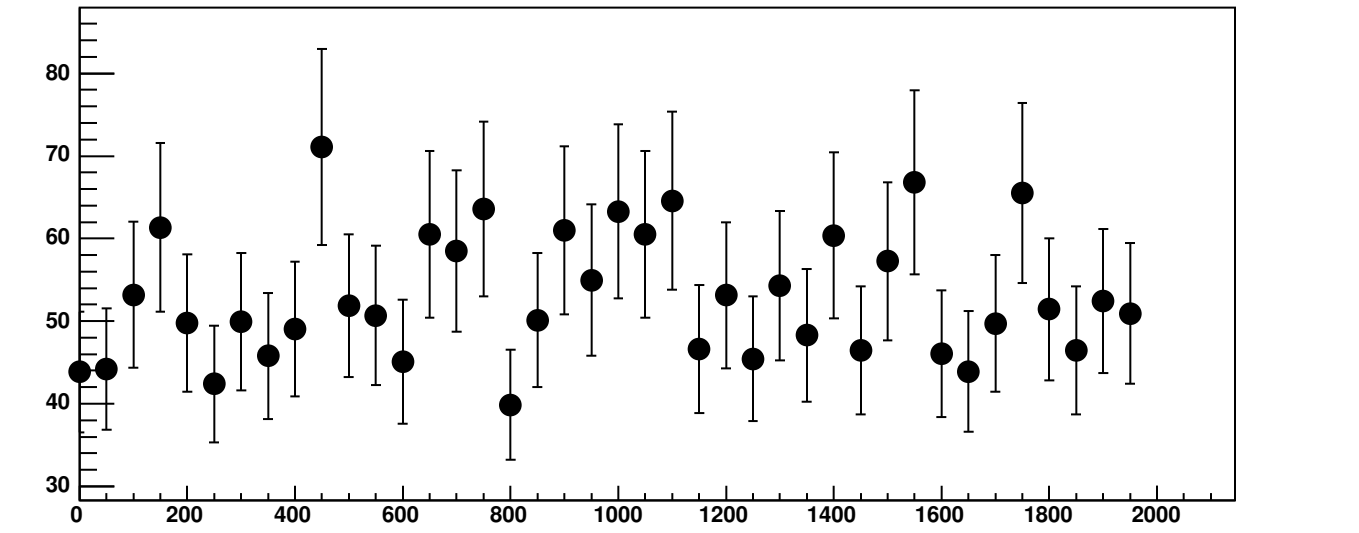

Chip 6, Channel 1, Enable 0, Hold=30, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 1, Enable 0, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

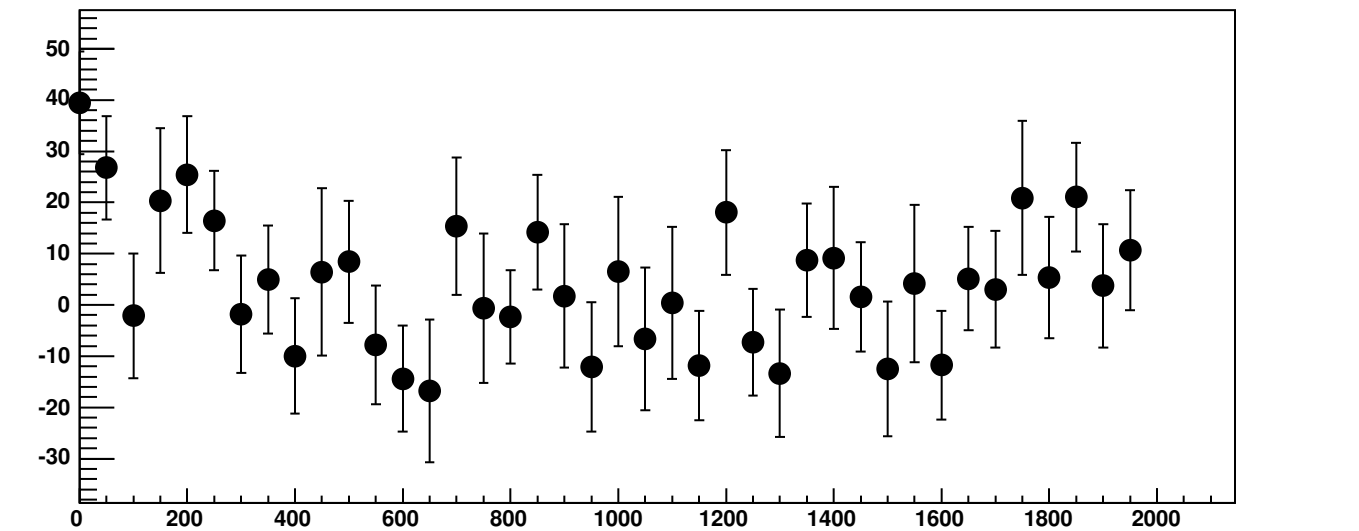
Chip 6, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC

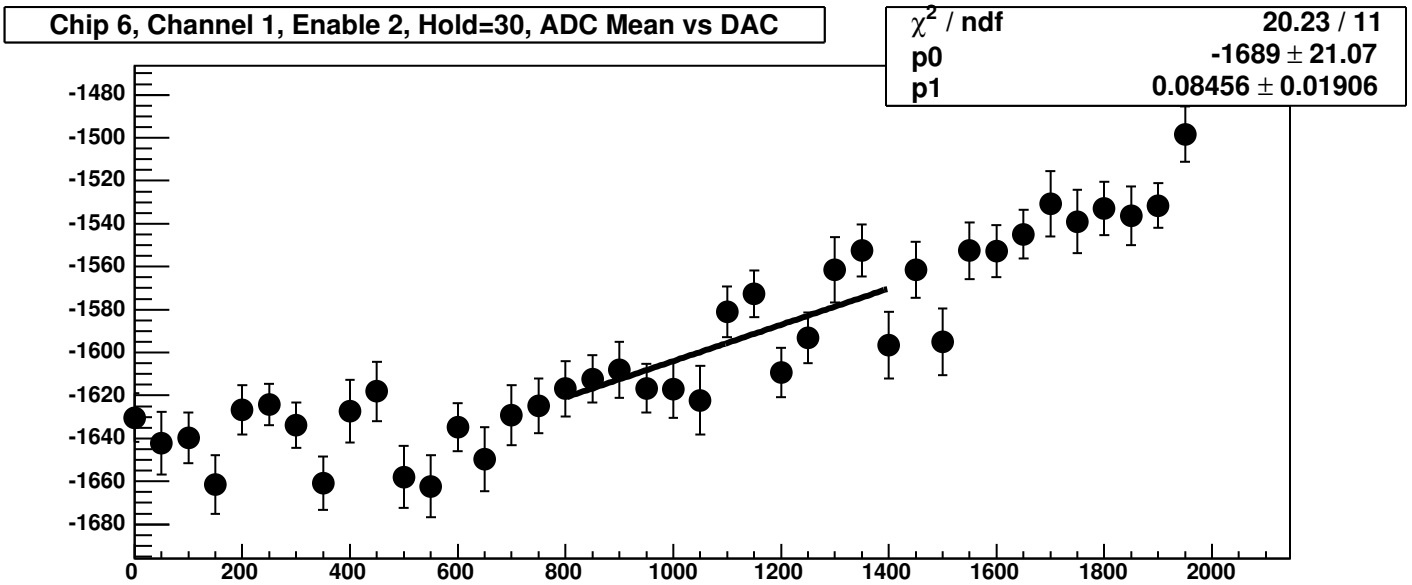


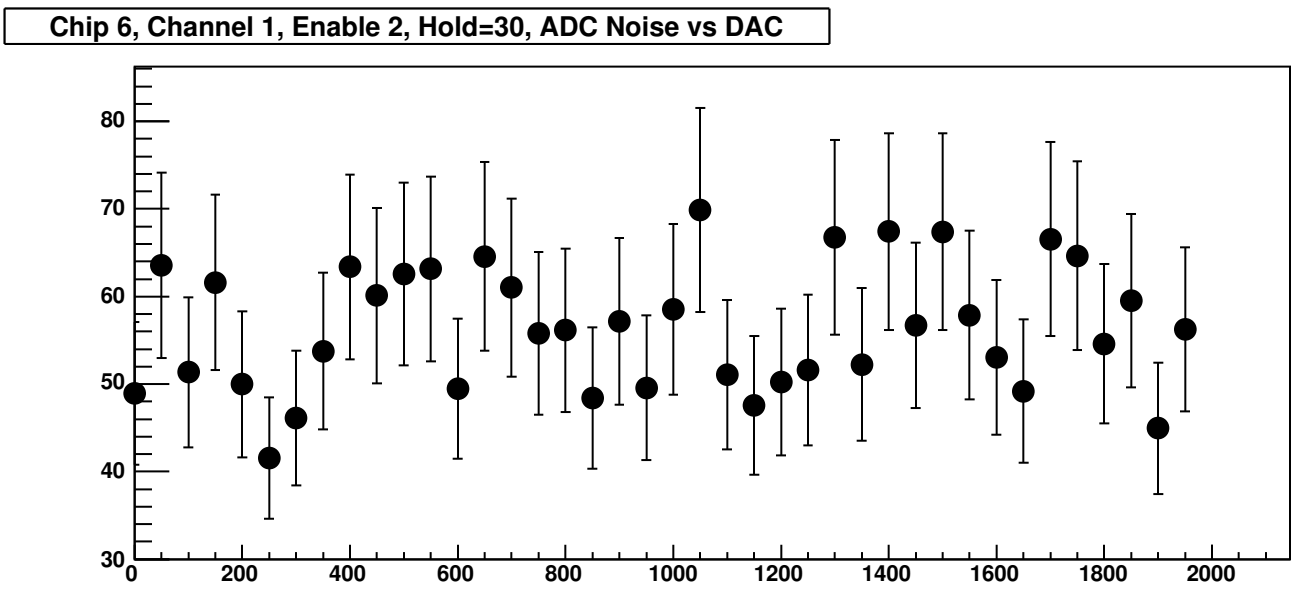
Chip 6, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC

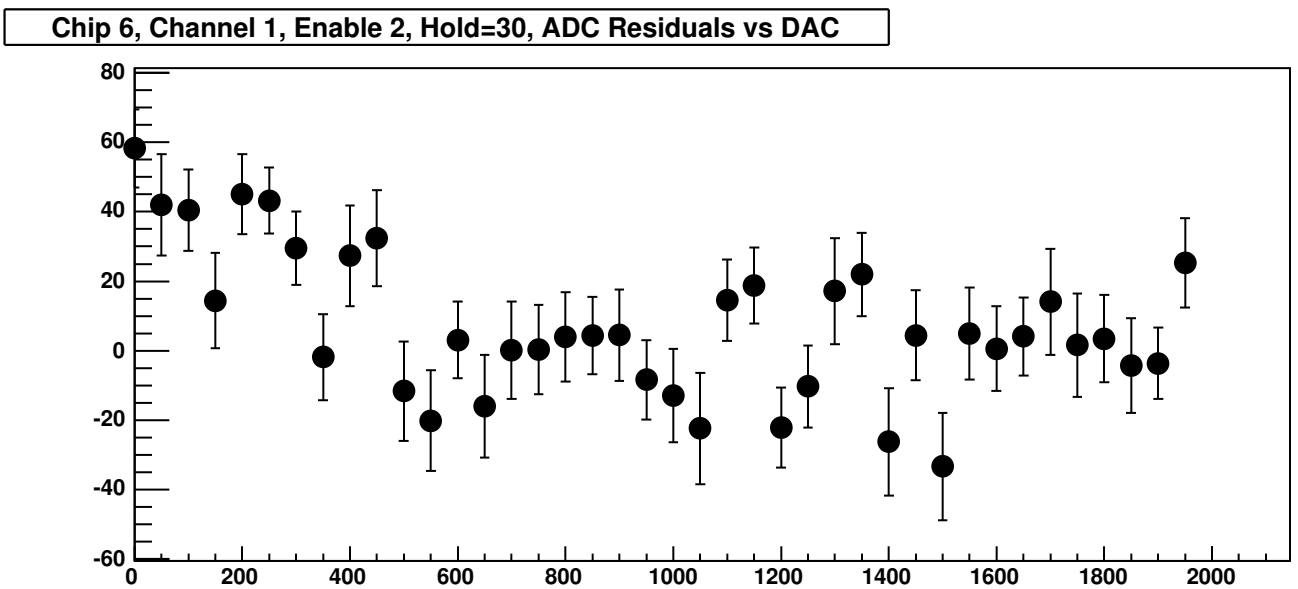


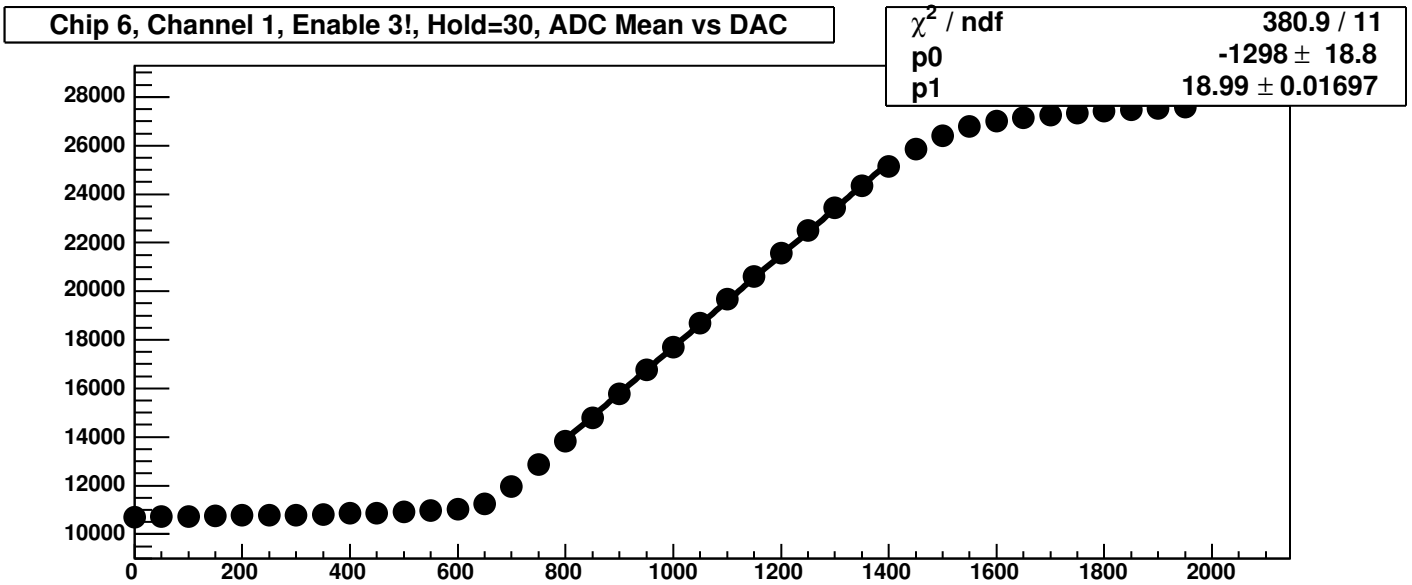
Chip 6, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC

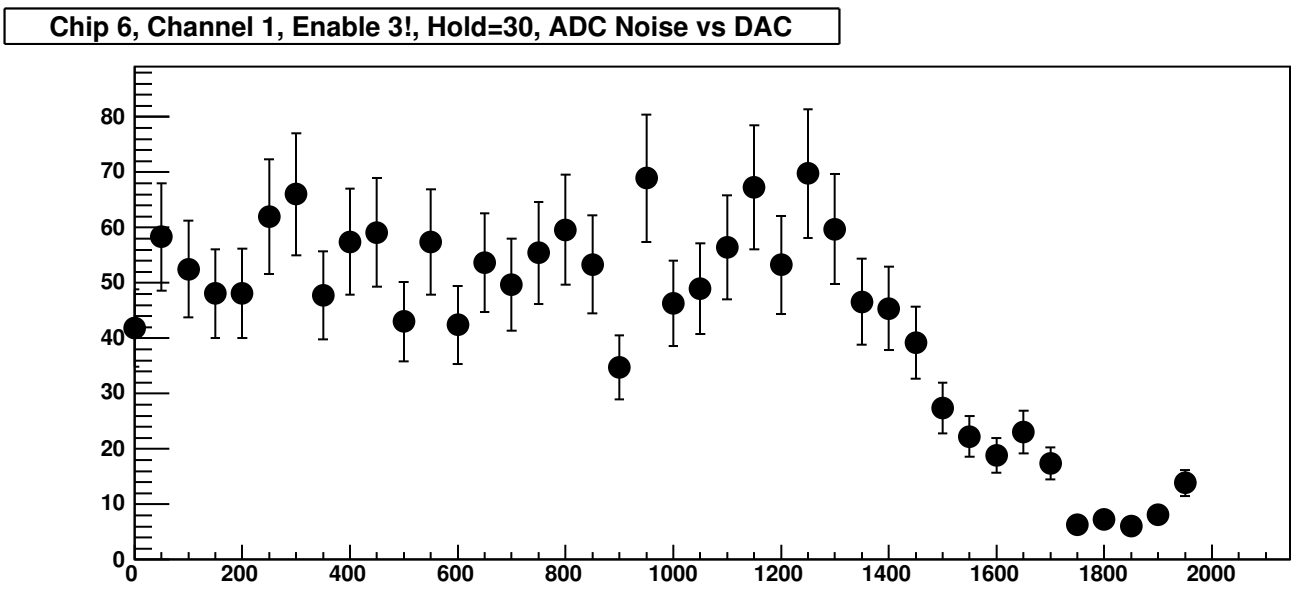


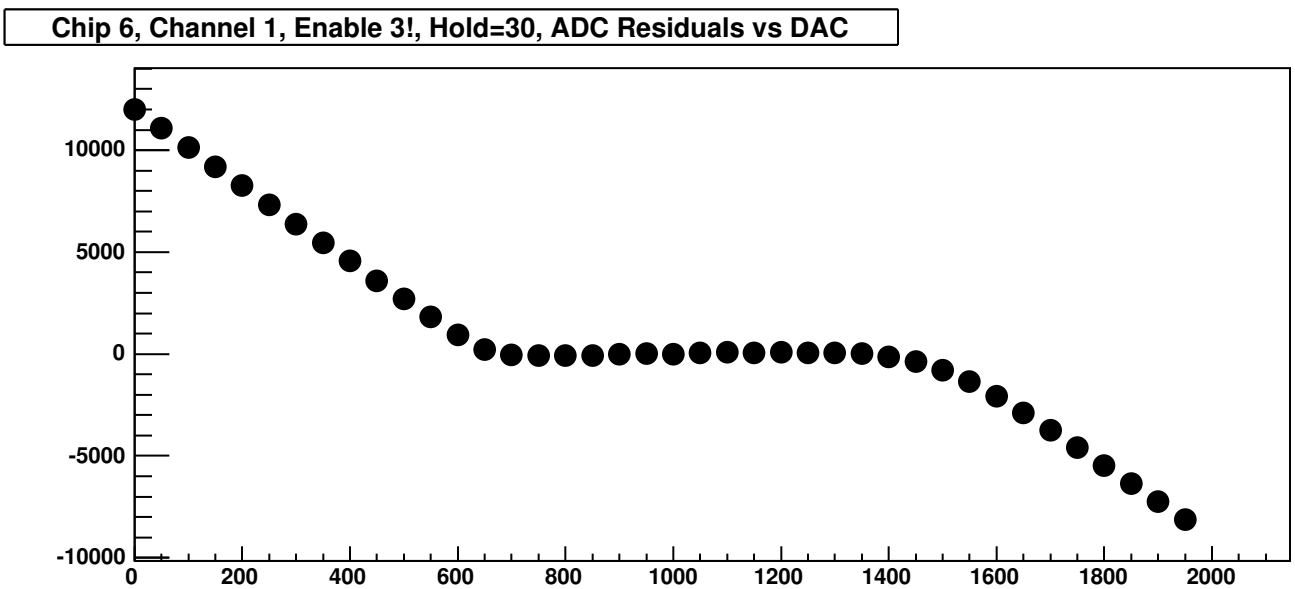

Chip 6, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 20.23 / 11
p0 -1689 ± 21.07
p1 0.08456 ± 0.01906
-1480
-1500
-1520
-1540
-1560
-1580
-1600
-1620
-1640
-1660
-1680
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

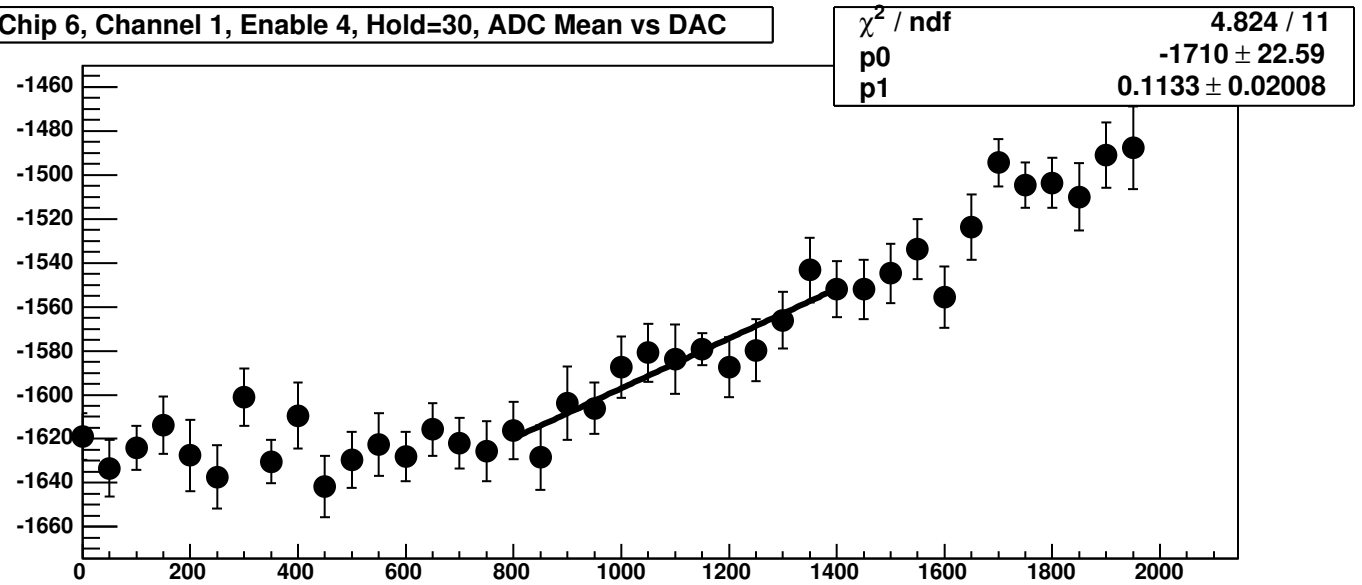

Chip 6, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 1, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 380.9 / 11
p0 -1298 ± 18.8
p1 18.99 ± 0.01697
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

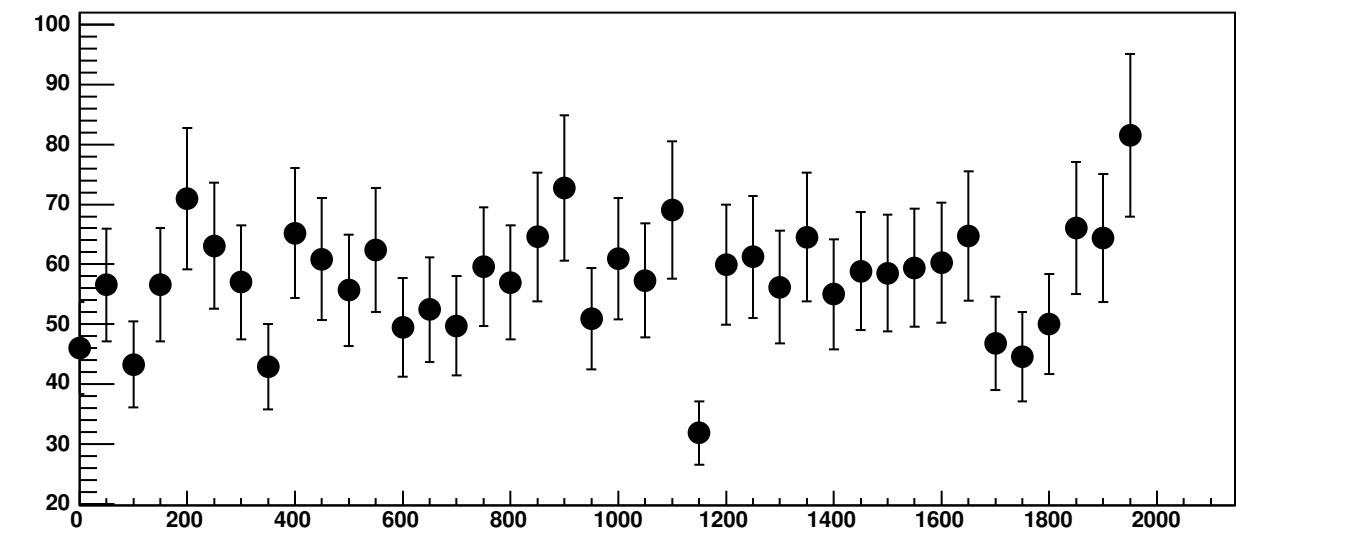

Chip 6, Channel 1, Enable 3!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 1, Enable 3!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

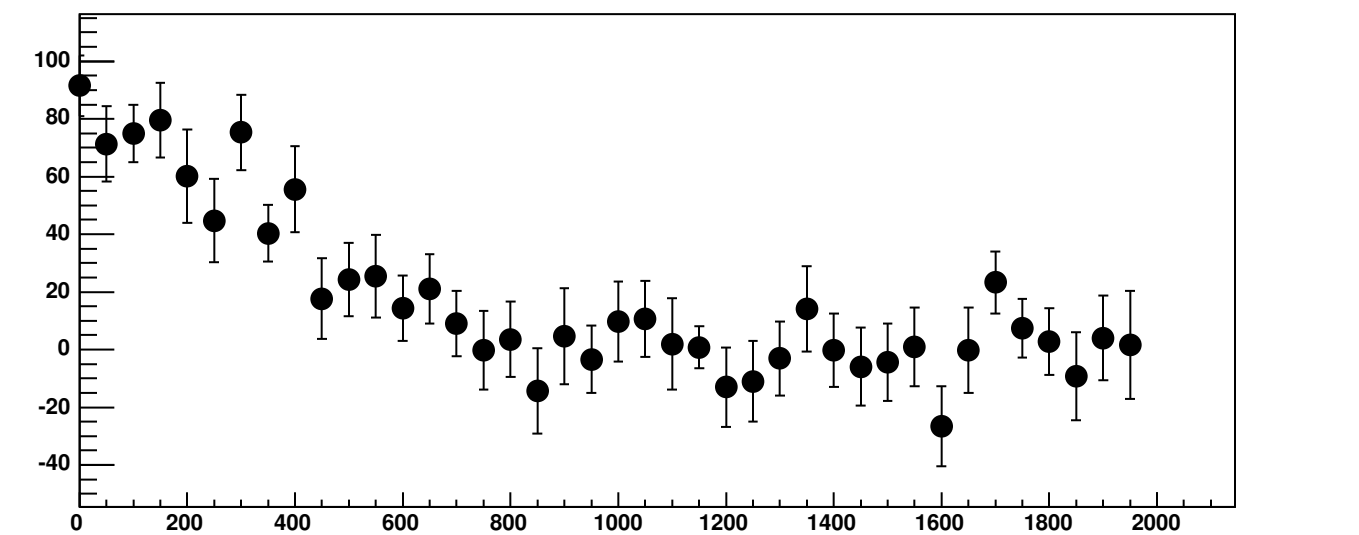
Chip 6, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC

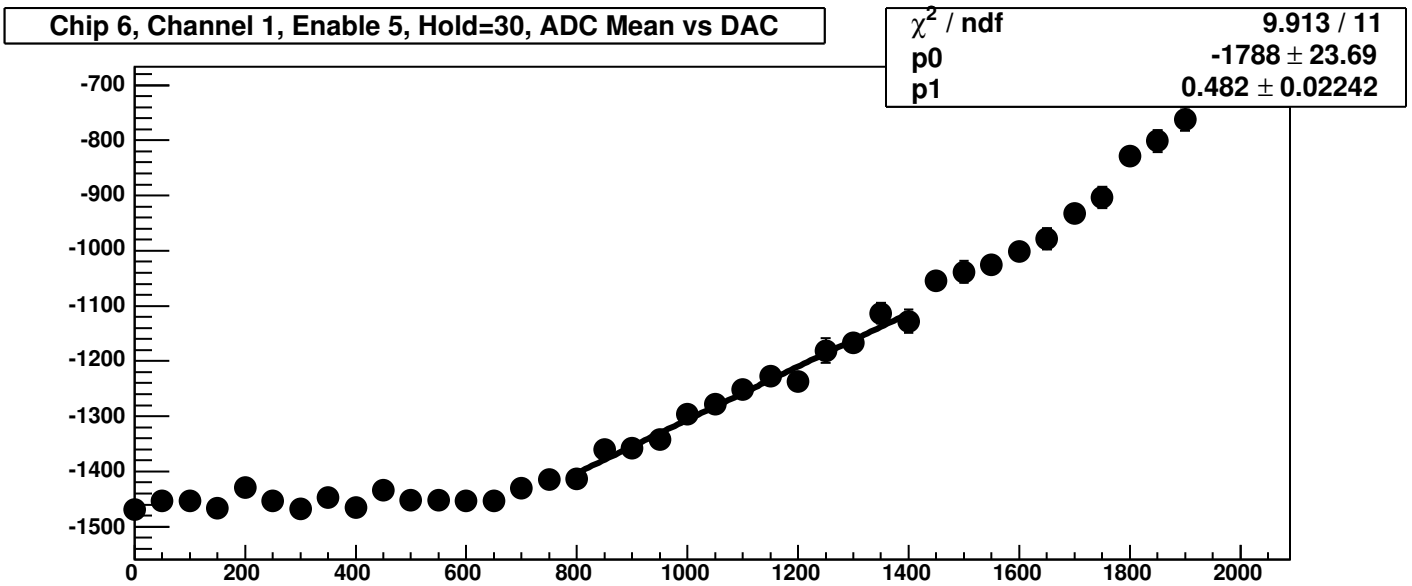


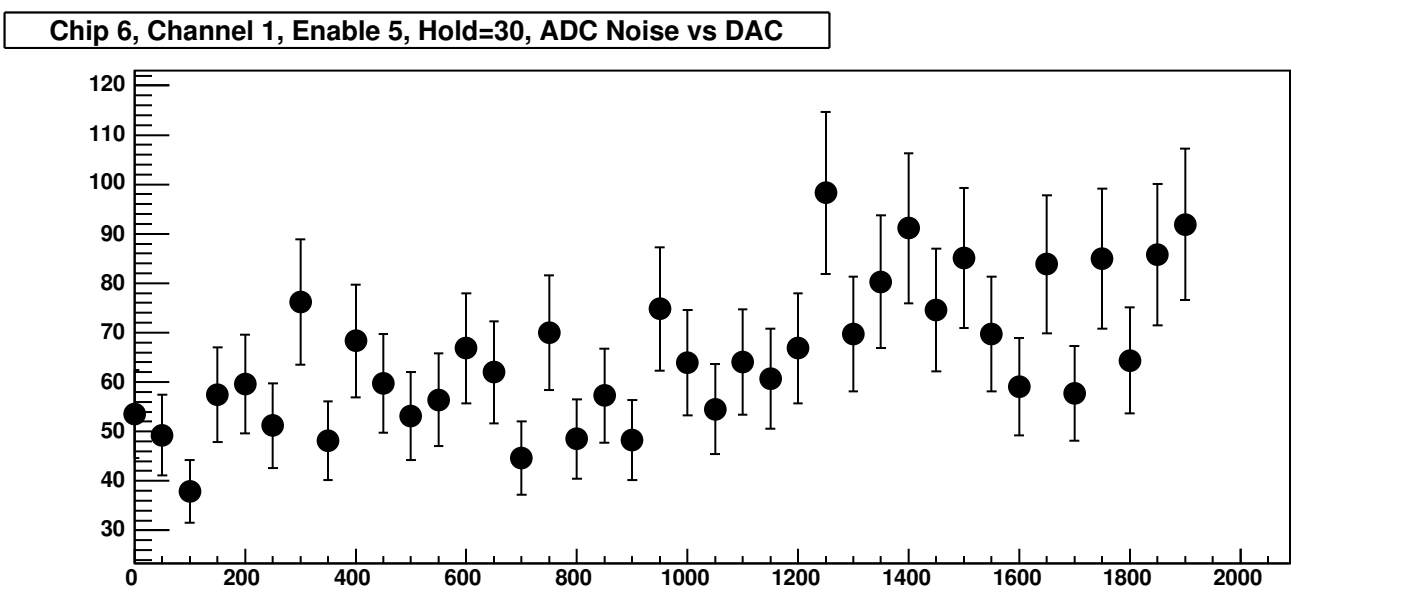
Chip 6, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC

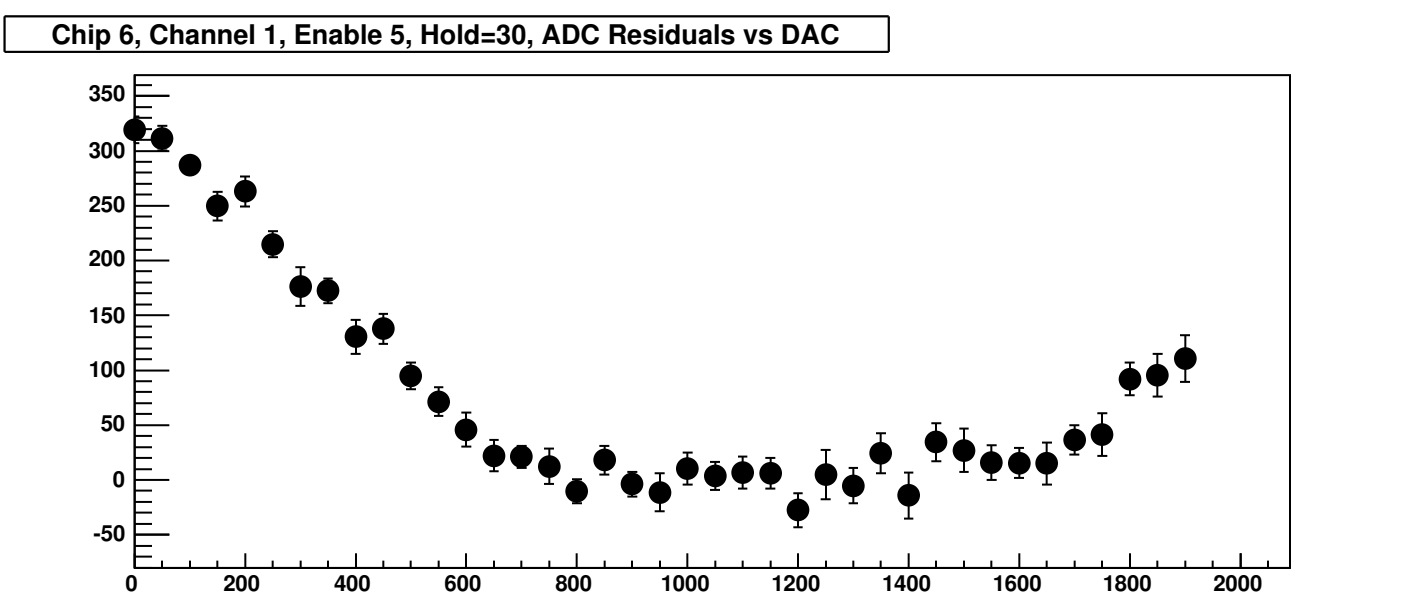


Chip 6, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC

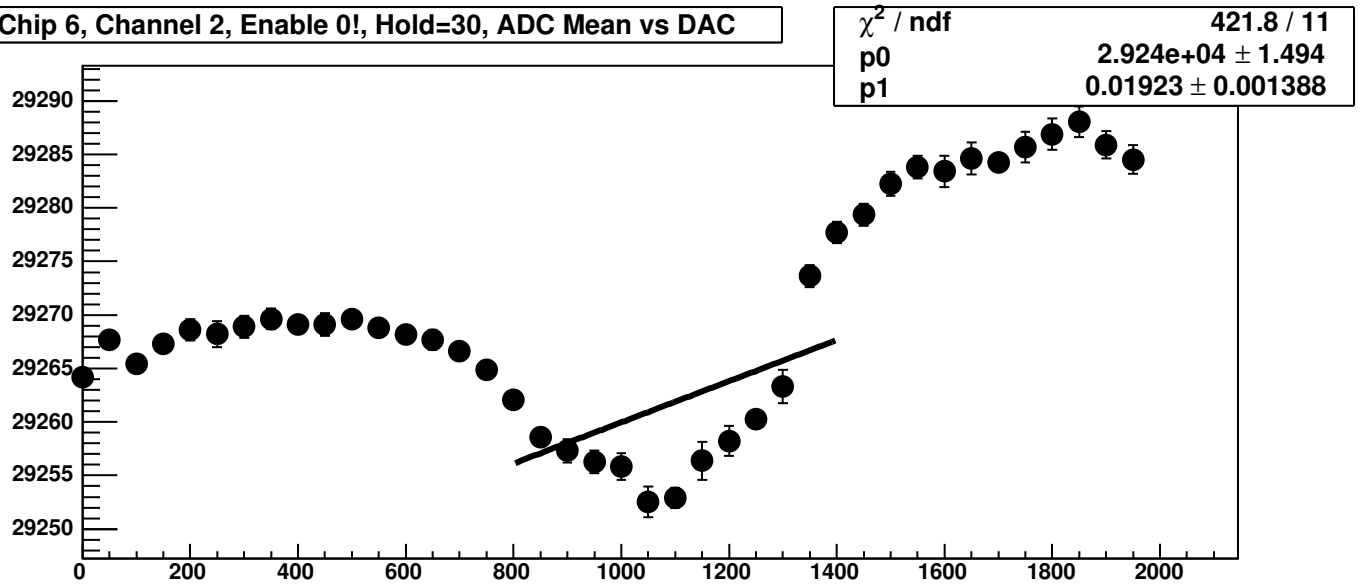



Chip 6, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9.913 / 11
p0 -1788 ± 23.69
p1 0.482 ± 0.02242
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

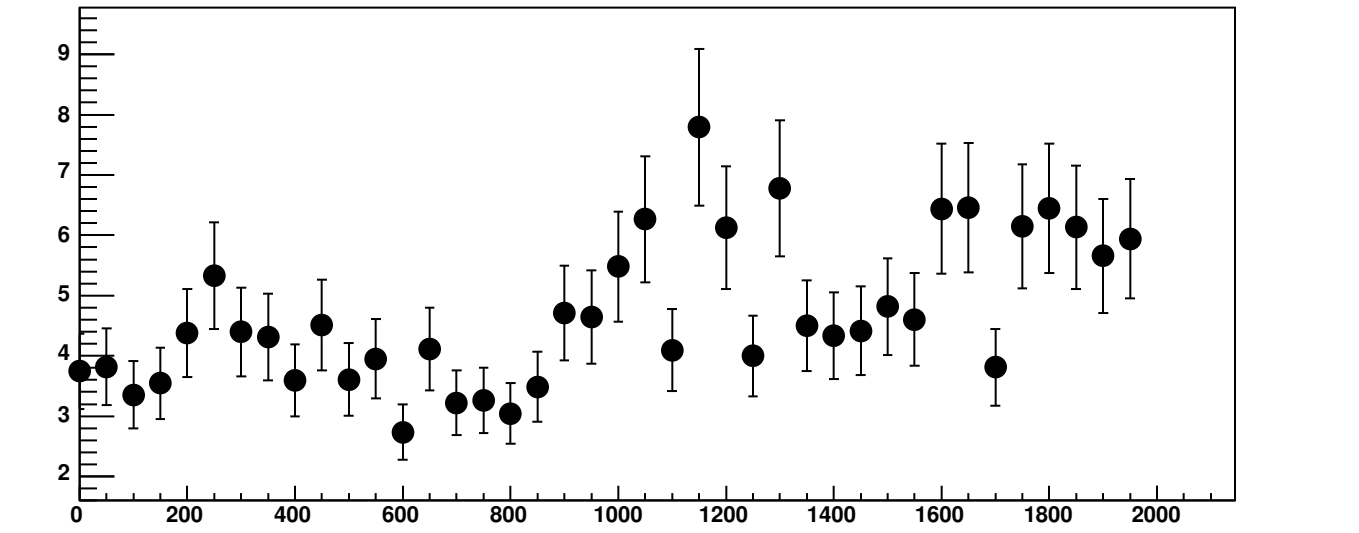

Chip 6, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
120
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

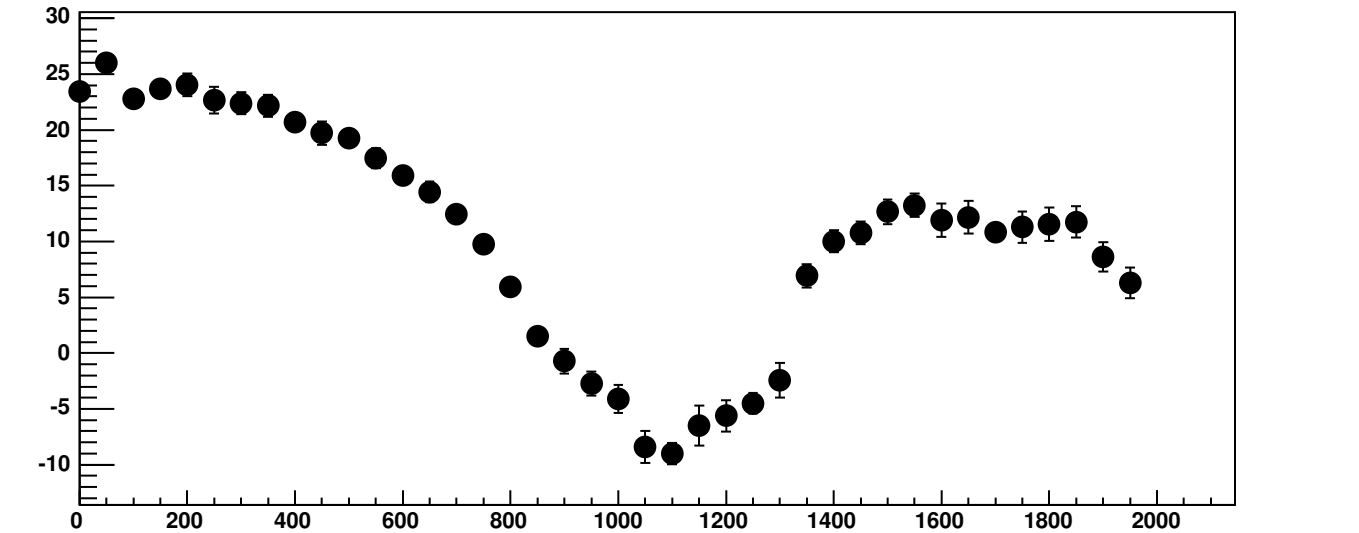
Chip 6, Channel 2, Enable 0!, Hold=30, ADC Mean vs DAC



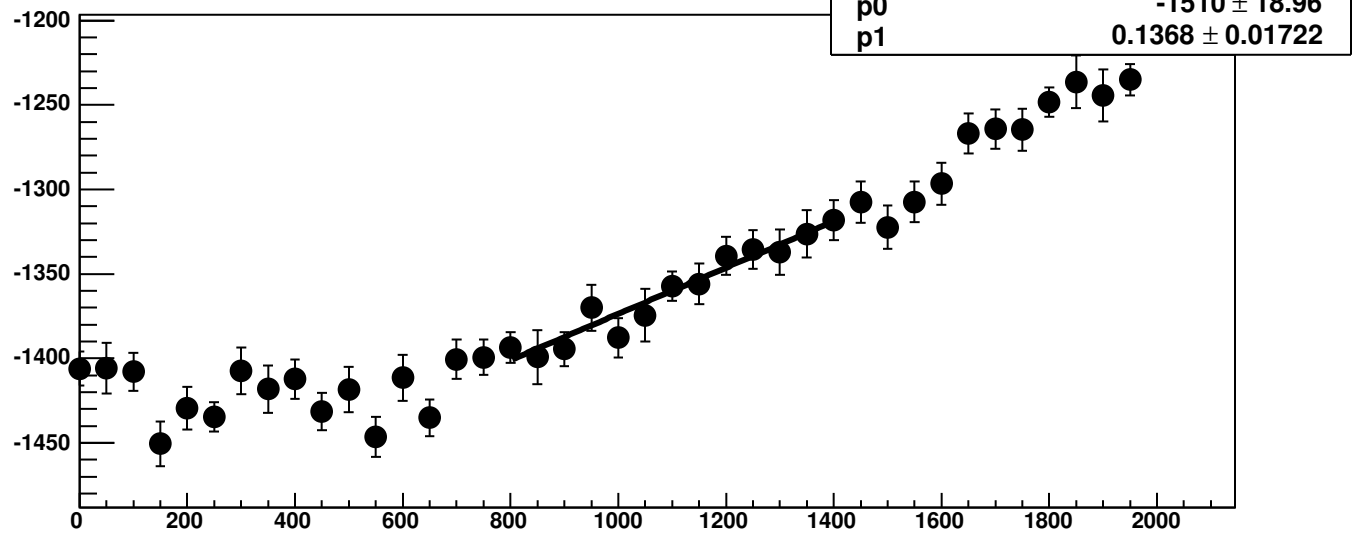
Chip 6, Channel 2, Enable 0!, Hold=30, ADC Noise vs DAC



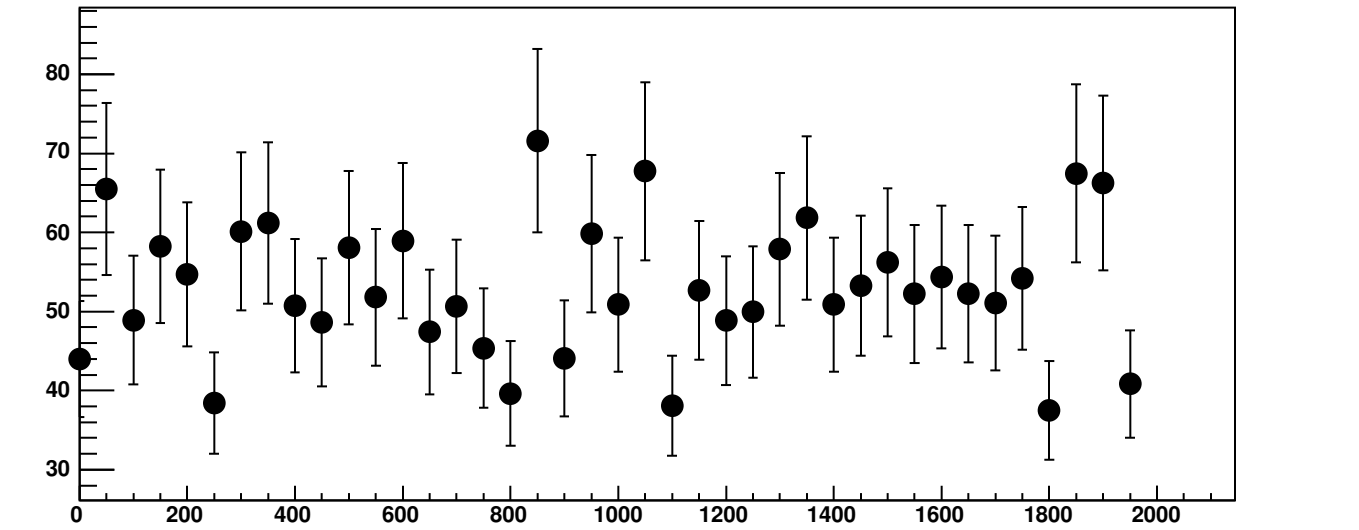
Chip 6, Channel 2, Enable 0!, Hold=30, ADC Residuals vs DAC



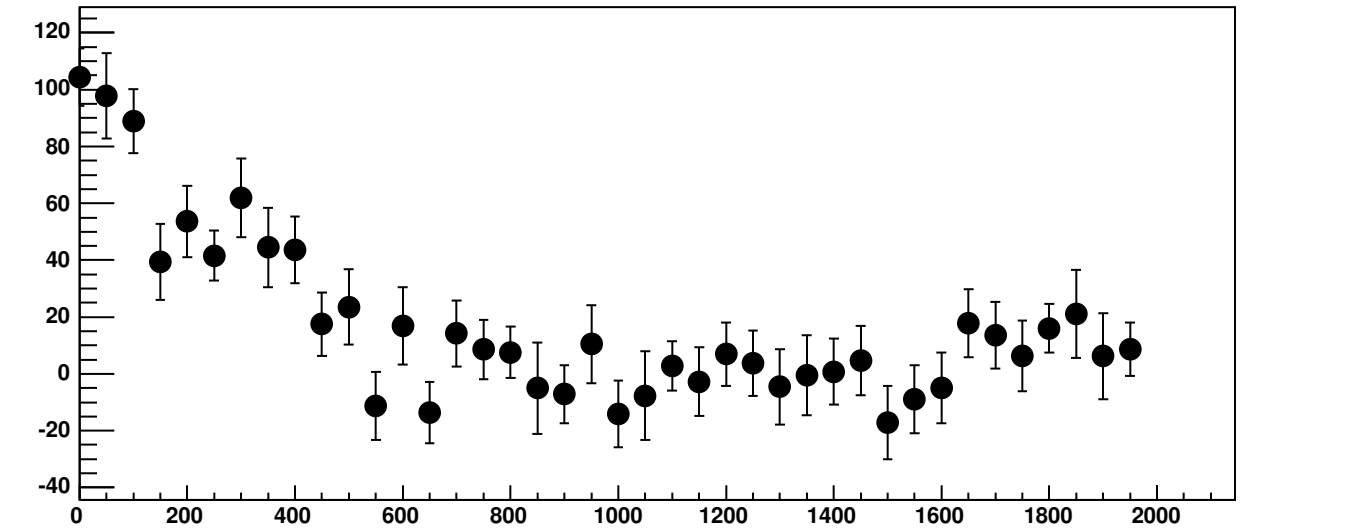
Chip 6, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC

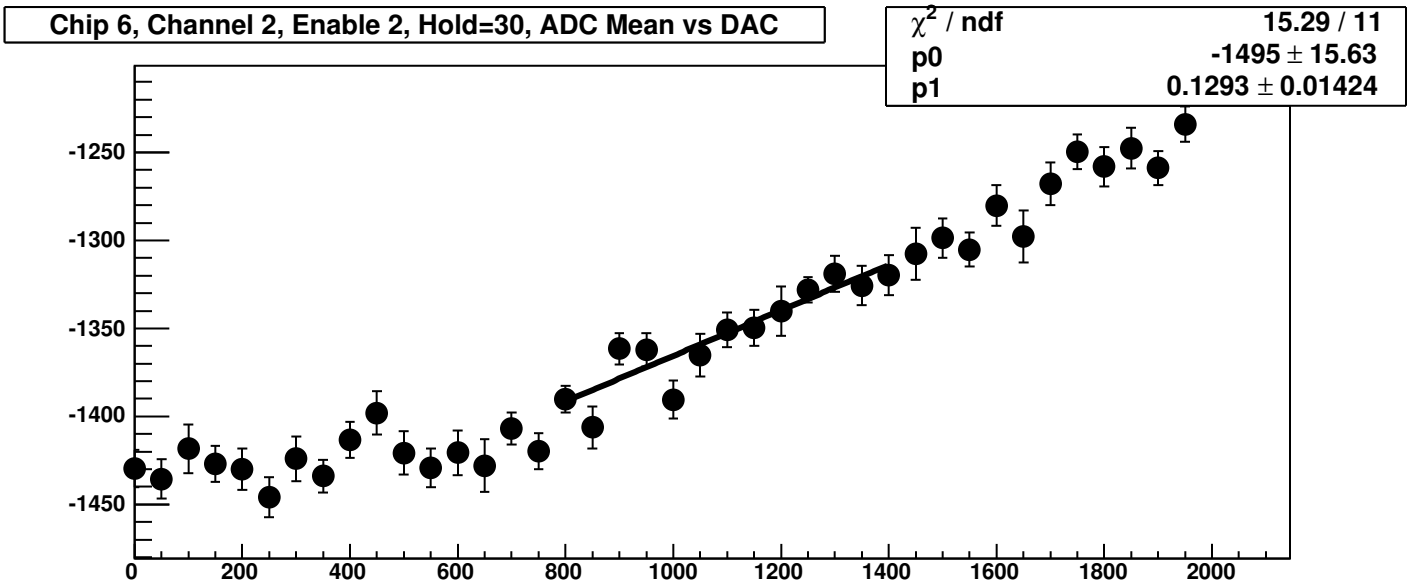


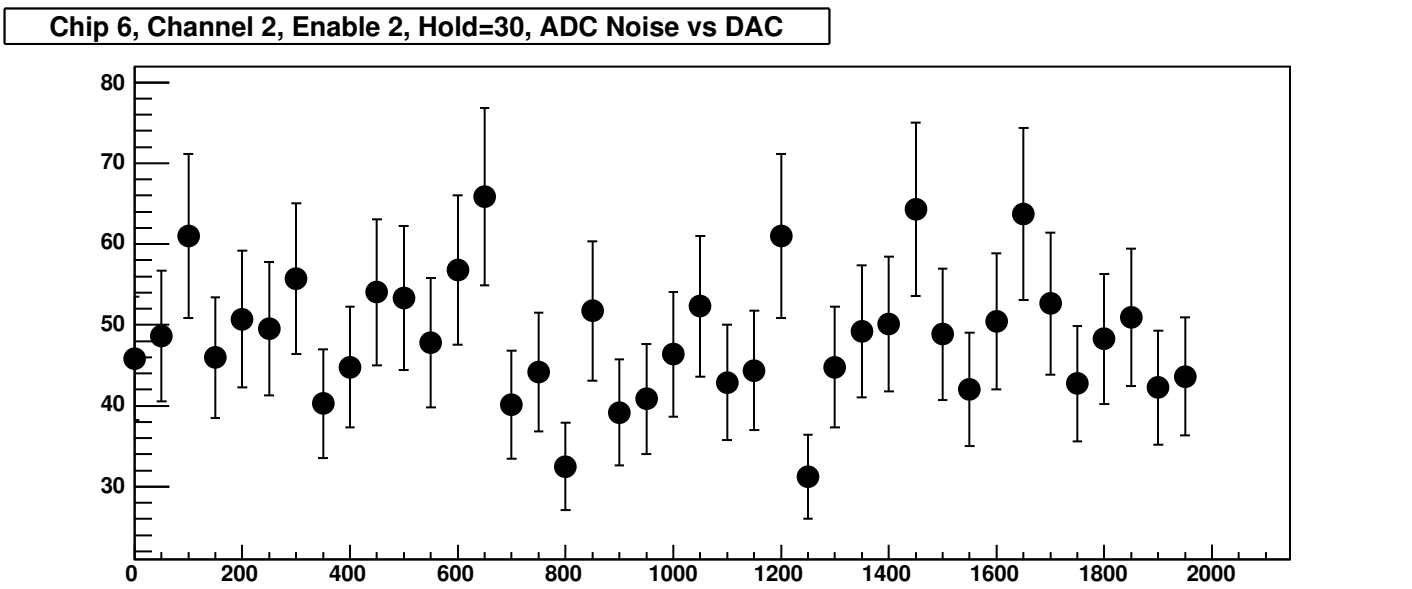
Chip 6, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC

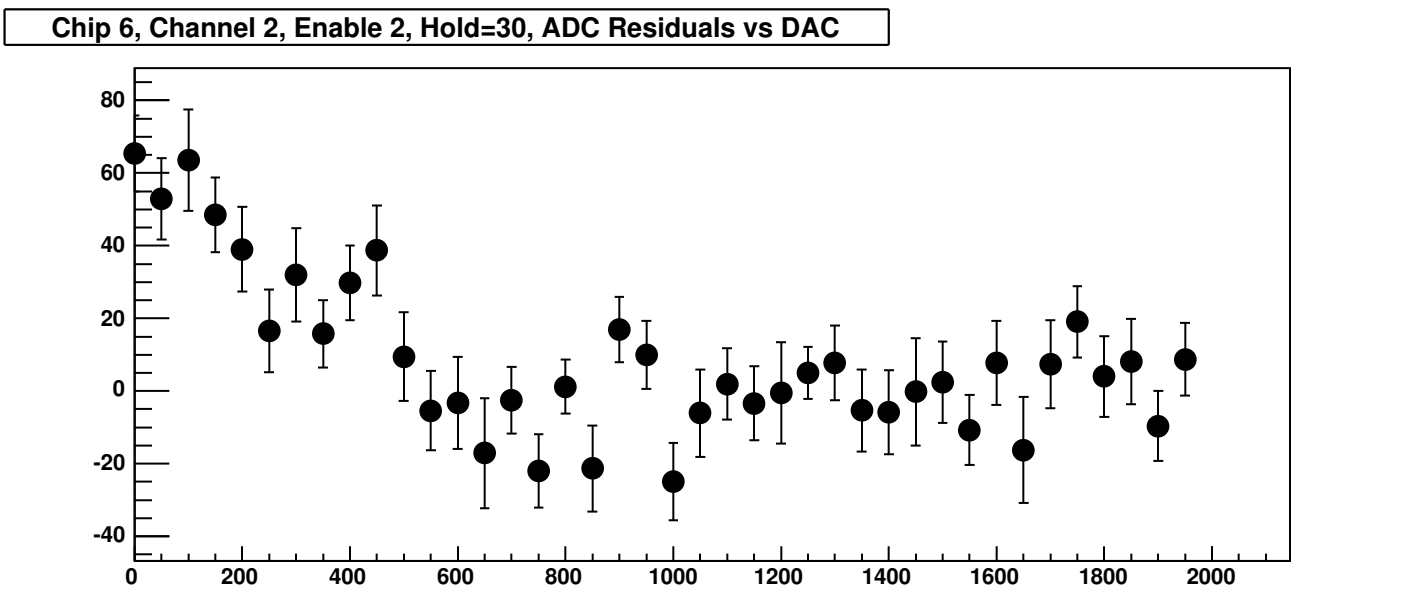


Chip 6, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC

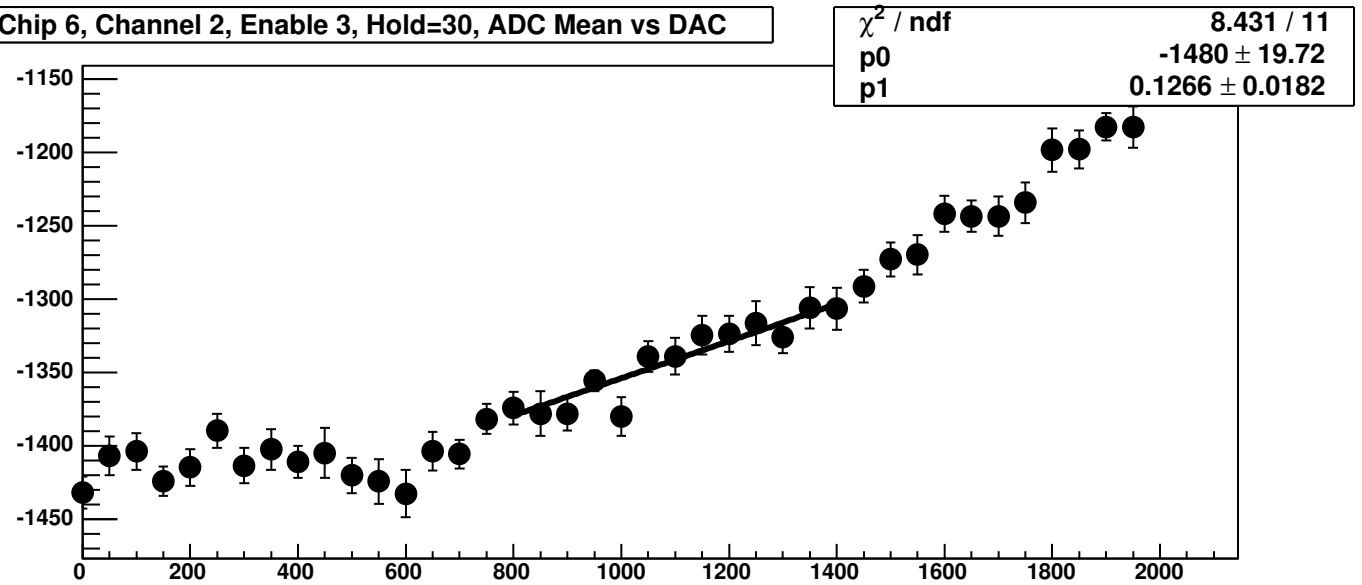



Chip 6, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 15.29 / 11
p0 -1495 ± 15.63
p1 0.1293 ± 0.01424
-1250
-1300
-1350
-1400
-1450
0
200
400
600
800
1000
1200
1400
1600
1800
2000

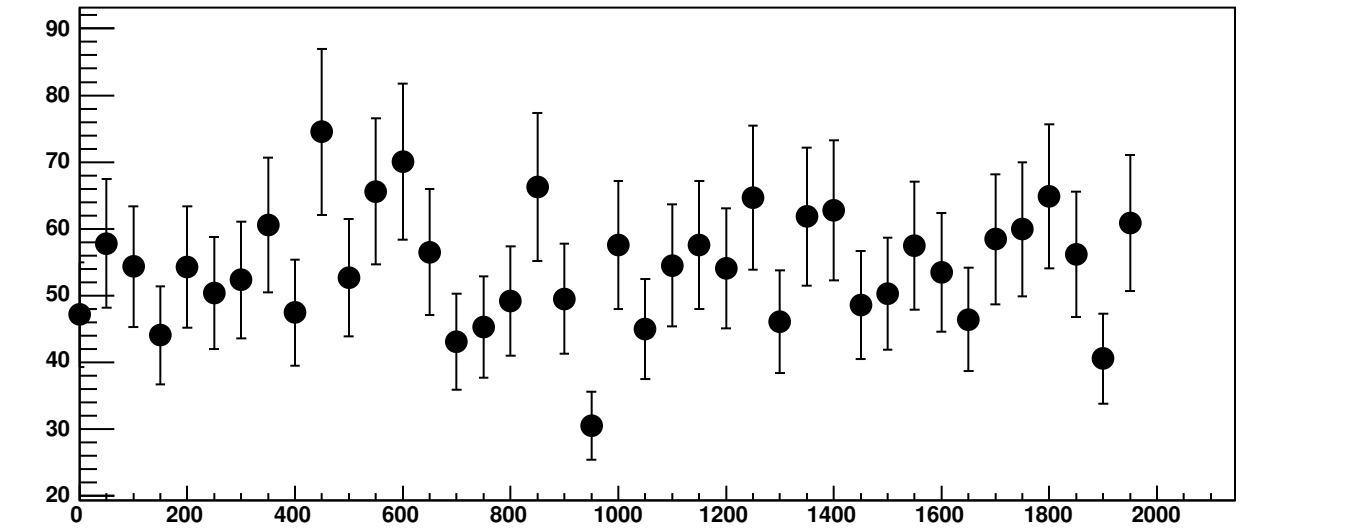

Chip 6, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

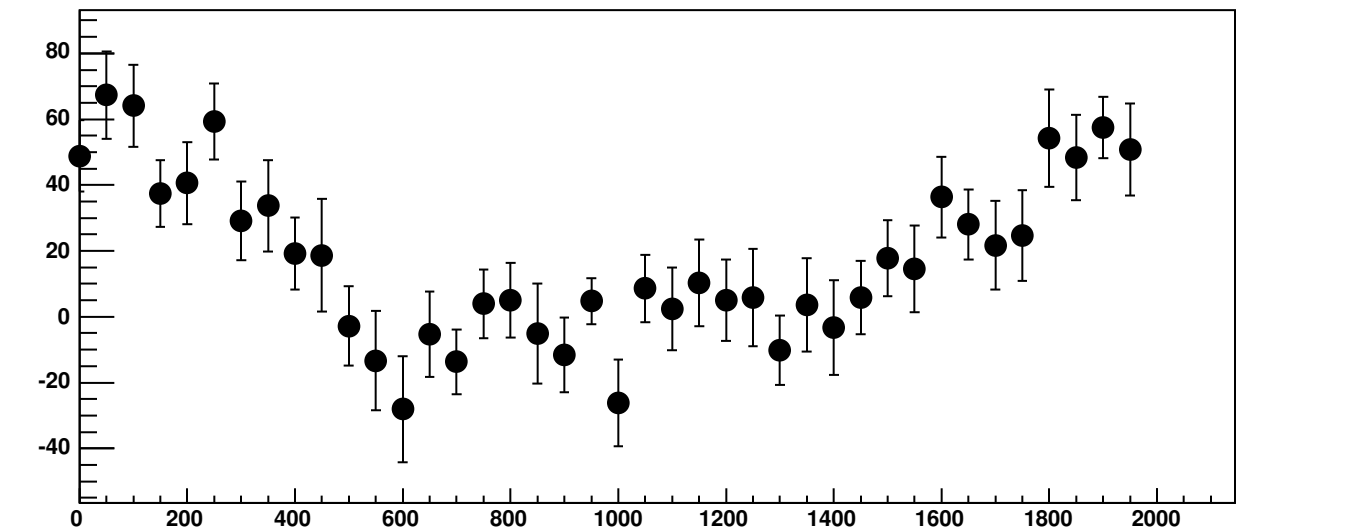
Chip 6, Channel 2, Enable 3, Hold=30, ADC Mean vs DAC

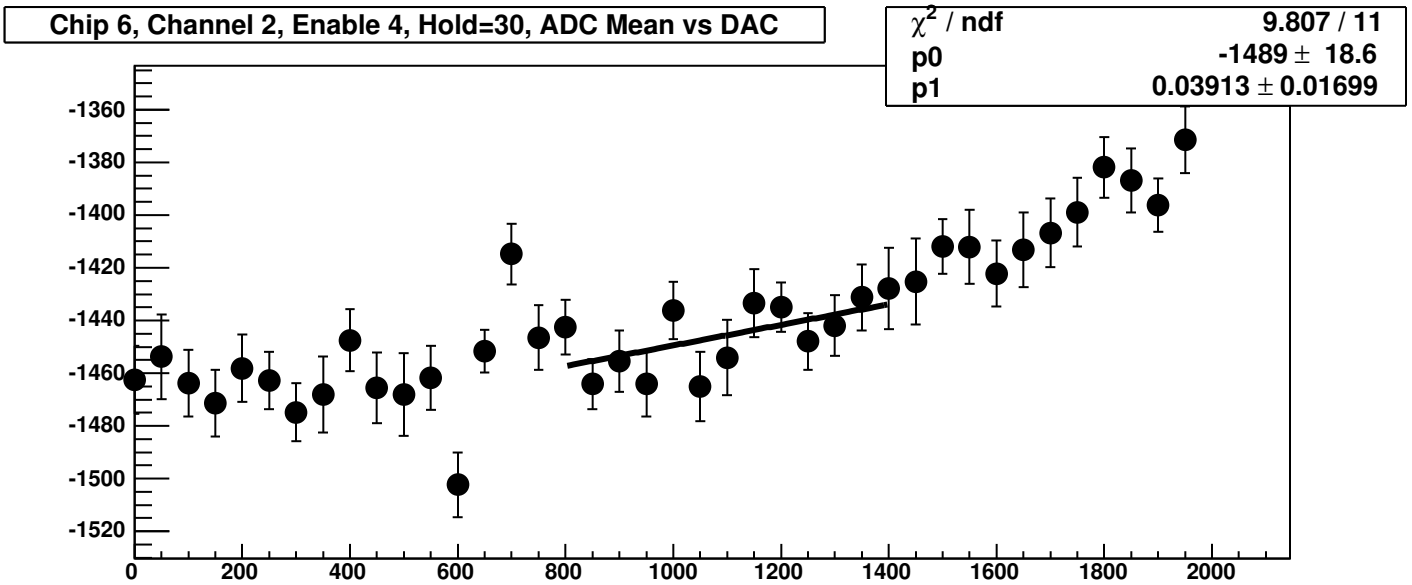


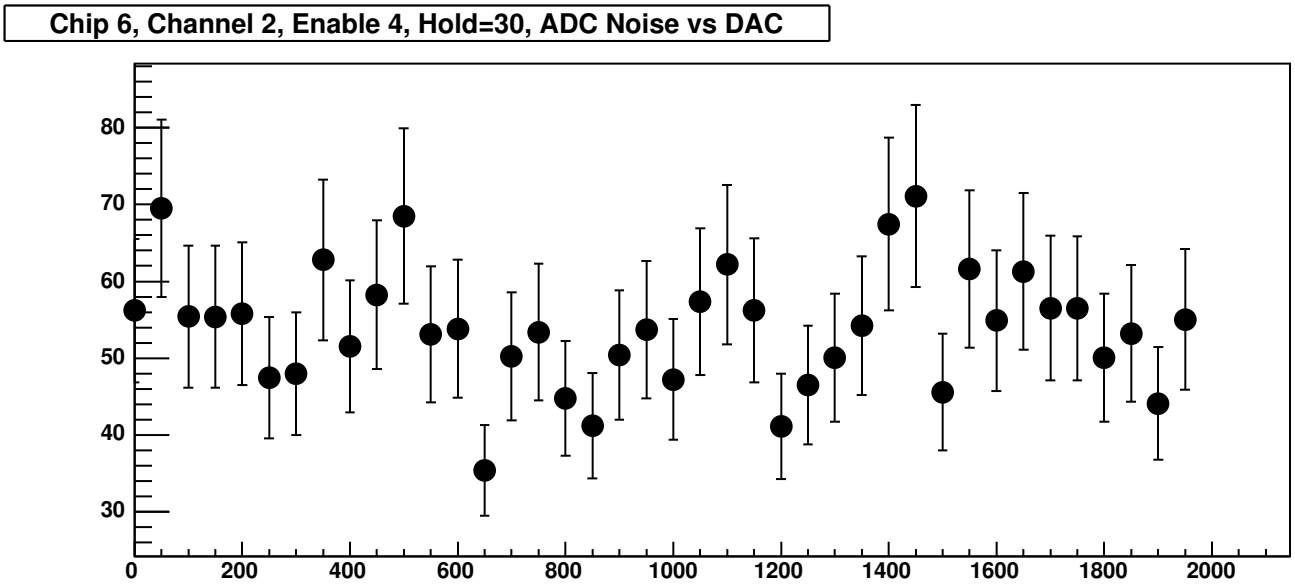
Chip 6, Channel 2, Enable 3, Hold=30, ADC Noise vs DAC

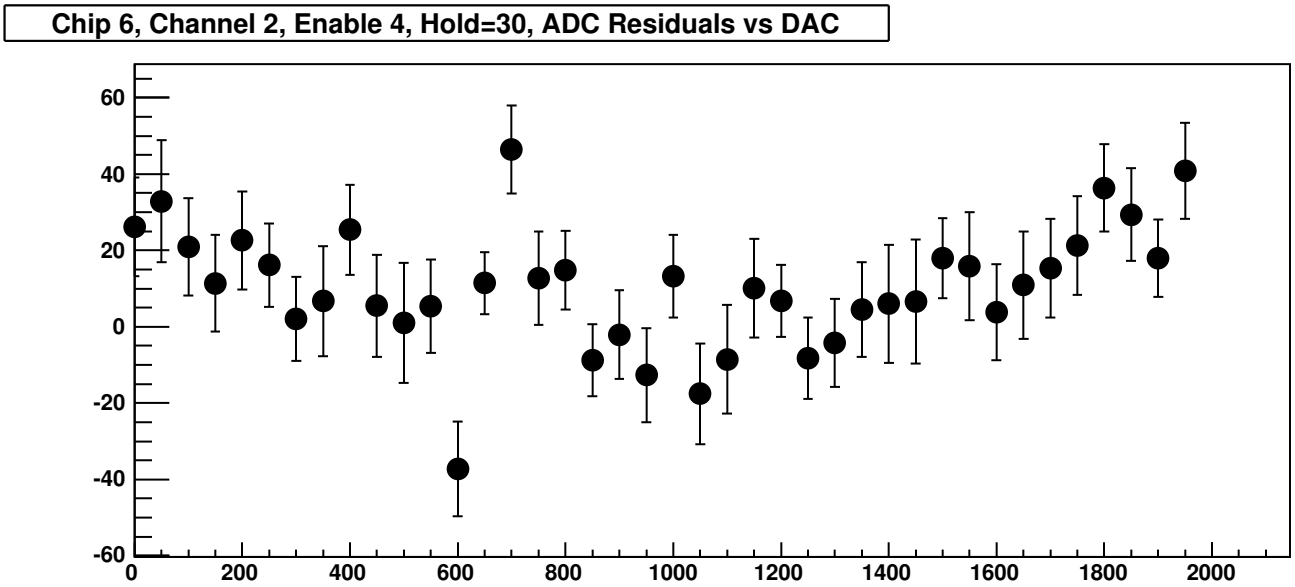


Chip 6, Channel 2, Enable 3, Hold=30, ADC Residuals vs DAC

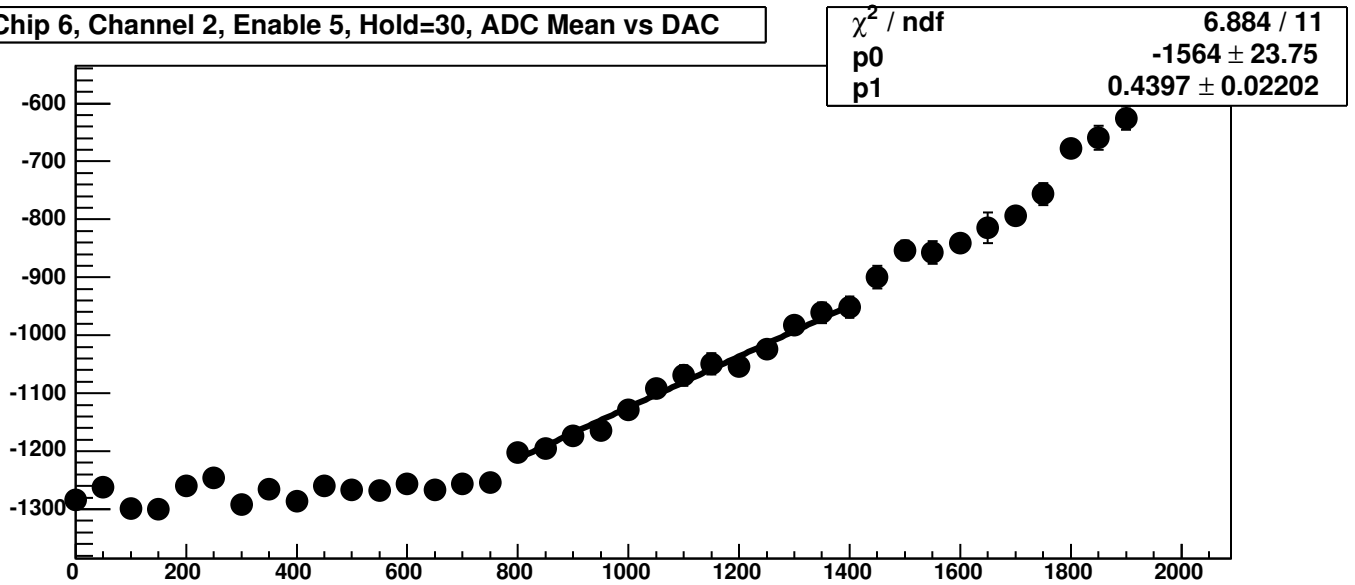



Chip 6, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 9.807 / 11
p0 -1489 ± 18.6
p1 0.03913 ± 0.01699
-1360
-1380
-1400
-1420
-1440
-1460
-1480
-1500
-1520
0
200
400
600
800
1000
1200
1400
1600
1800
2000

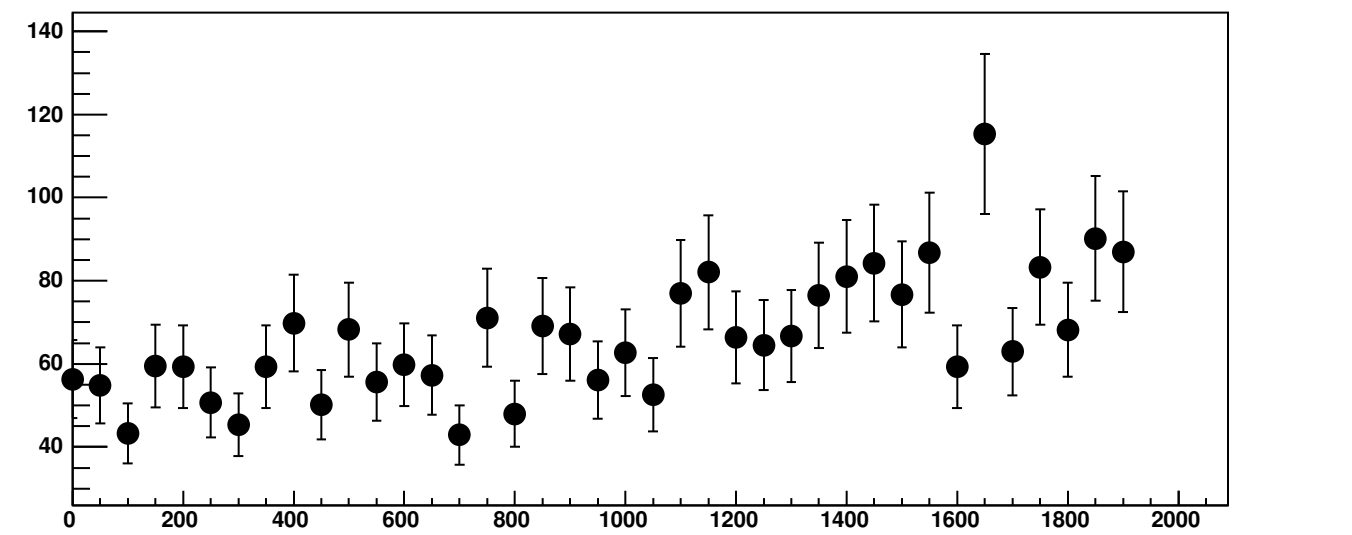

Chip 6, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

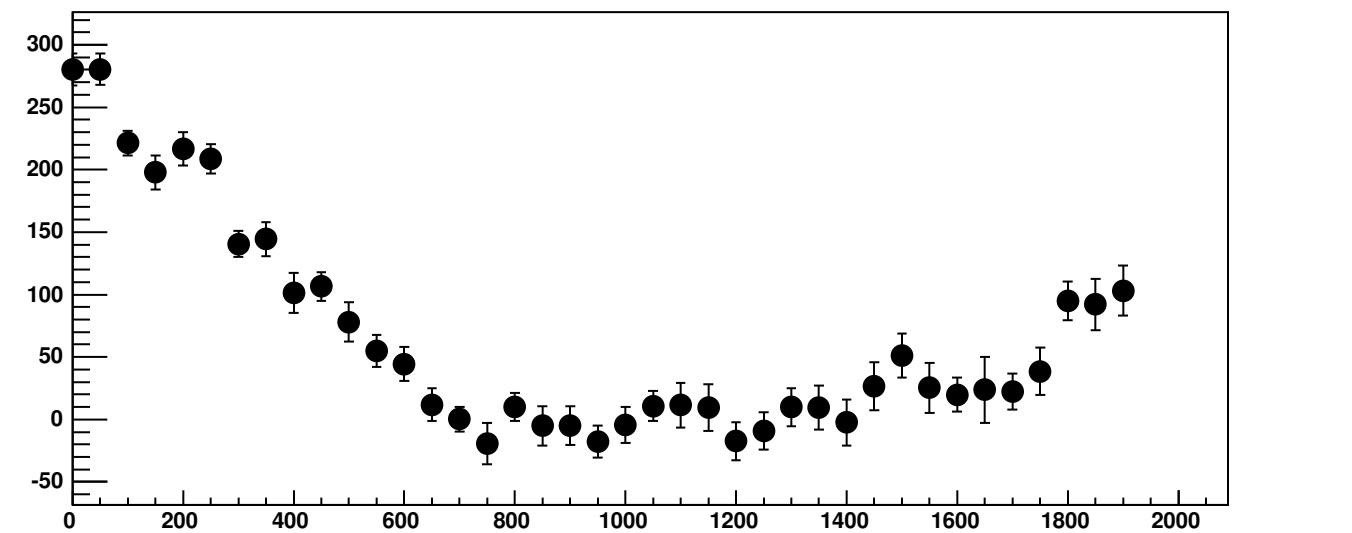
Chip 6, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC

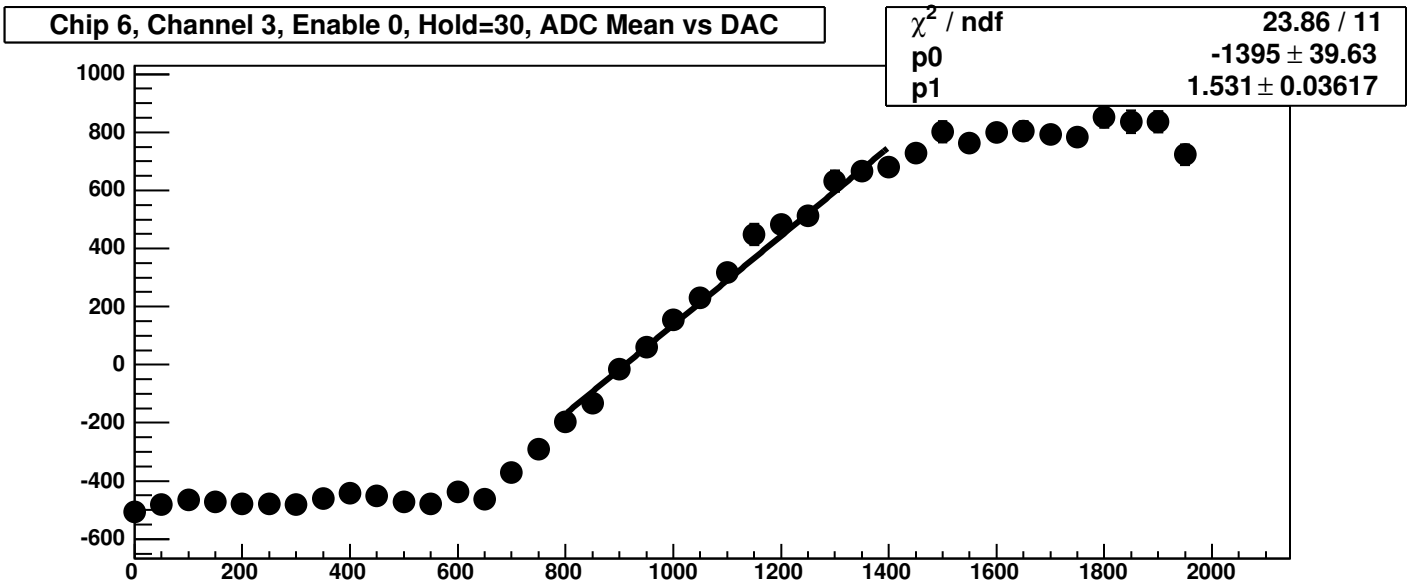


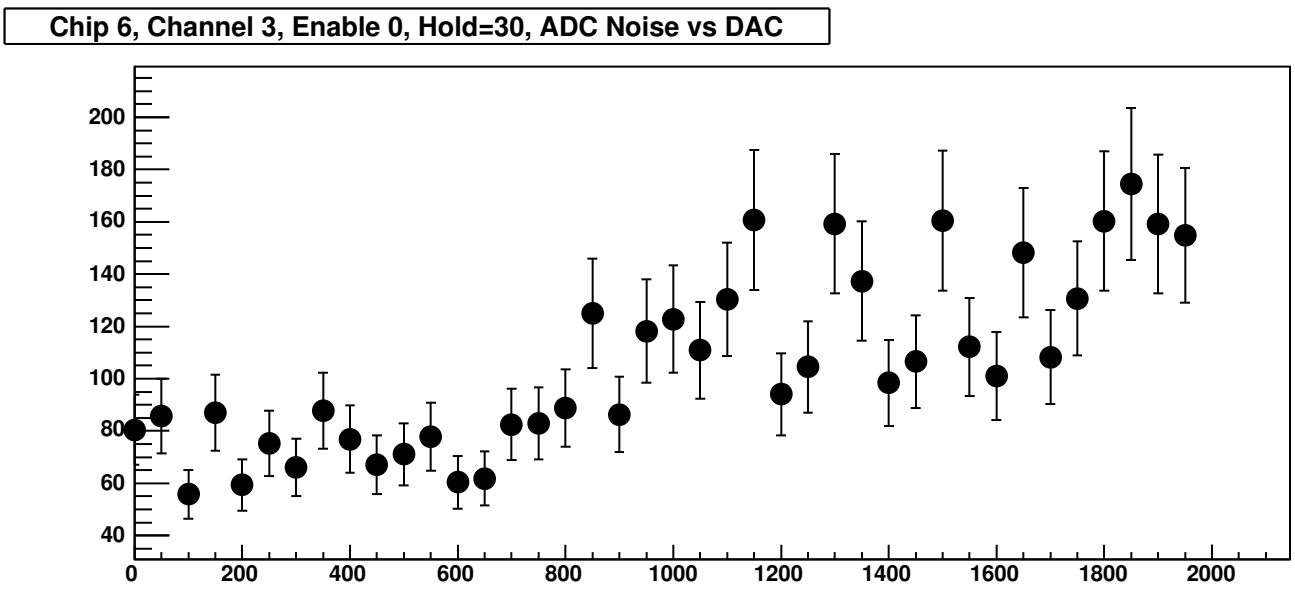
Chip 6, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC

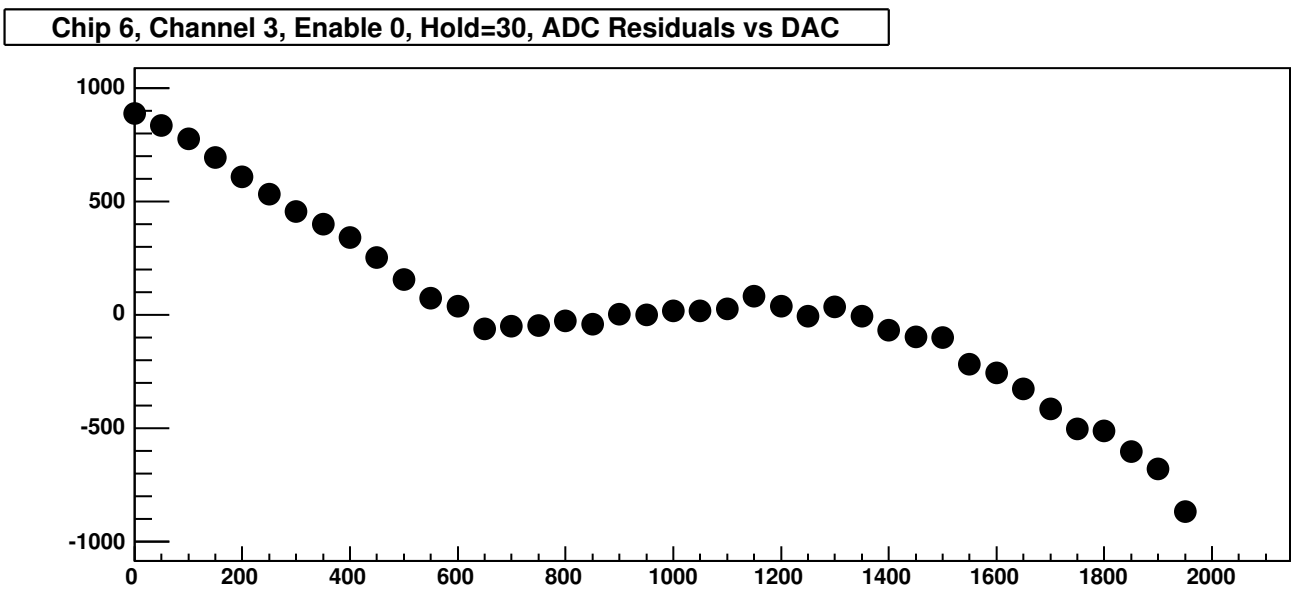


Chip 6, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC

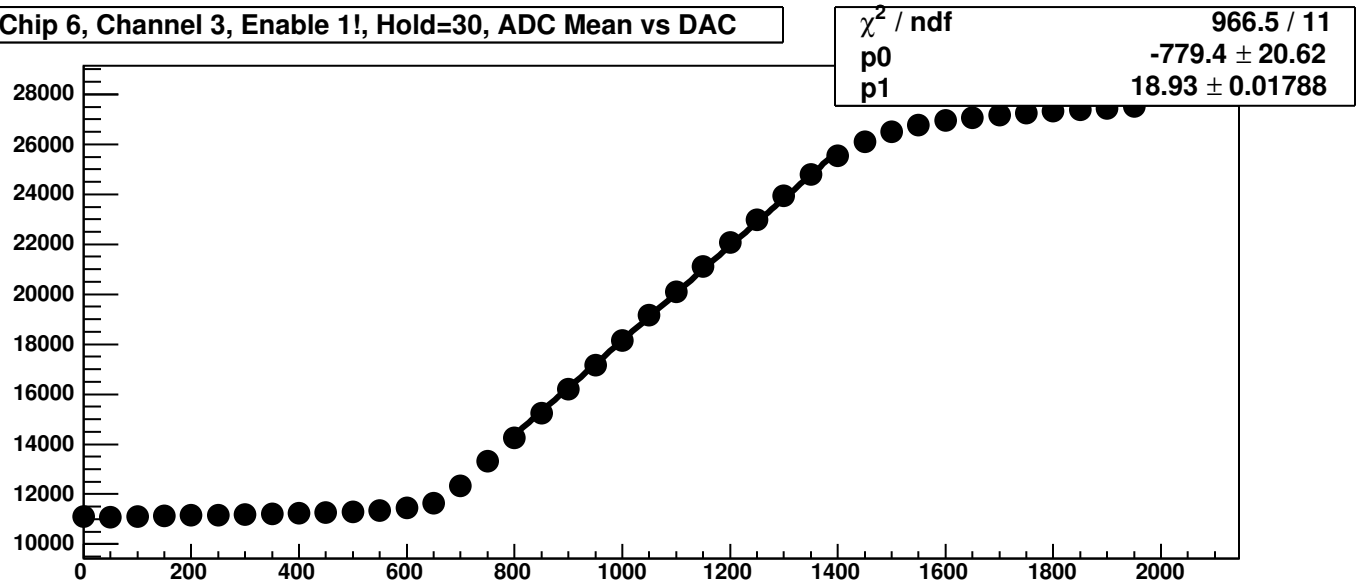



Chip 6, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 23.86 / 11
p0 -1395 ± 39.63
p1 1.531 ± 0.03617
1000
800
600
400
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

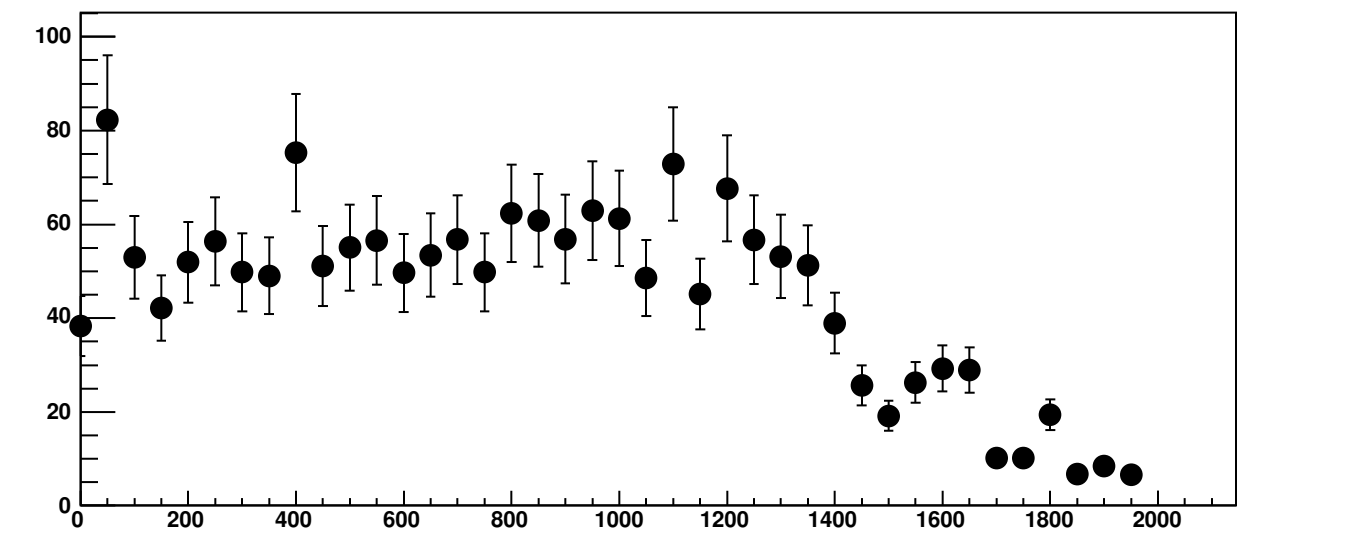

Chip 6, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC
1000
500
0
-500
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

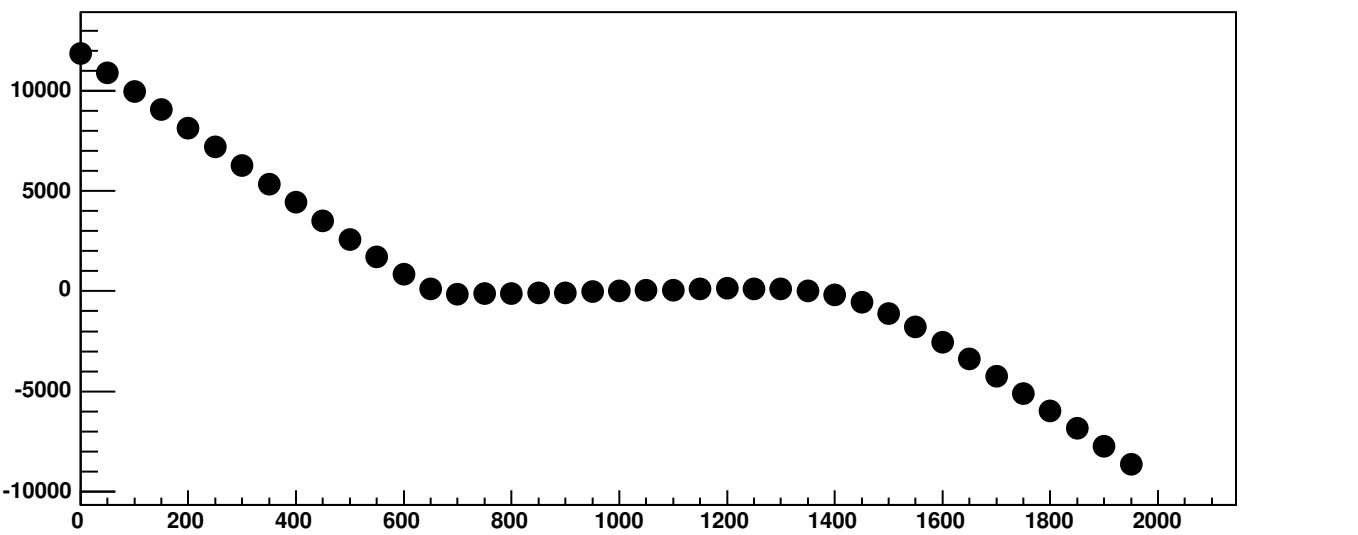
Chip 6, Channel 3, Enable 1!, Hold=30, ADC Mean vs DAC

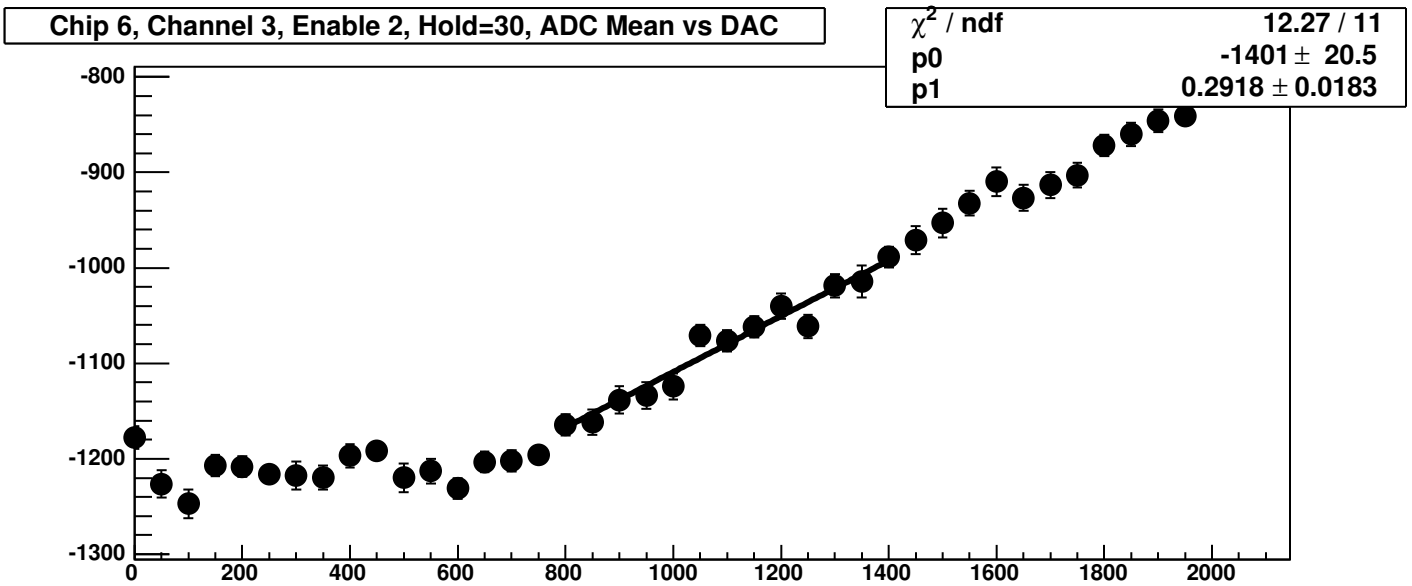


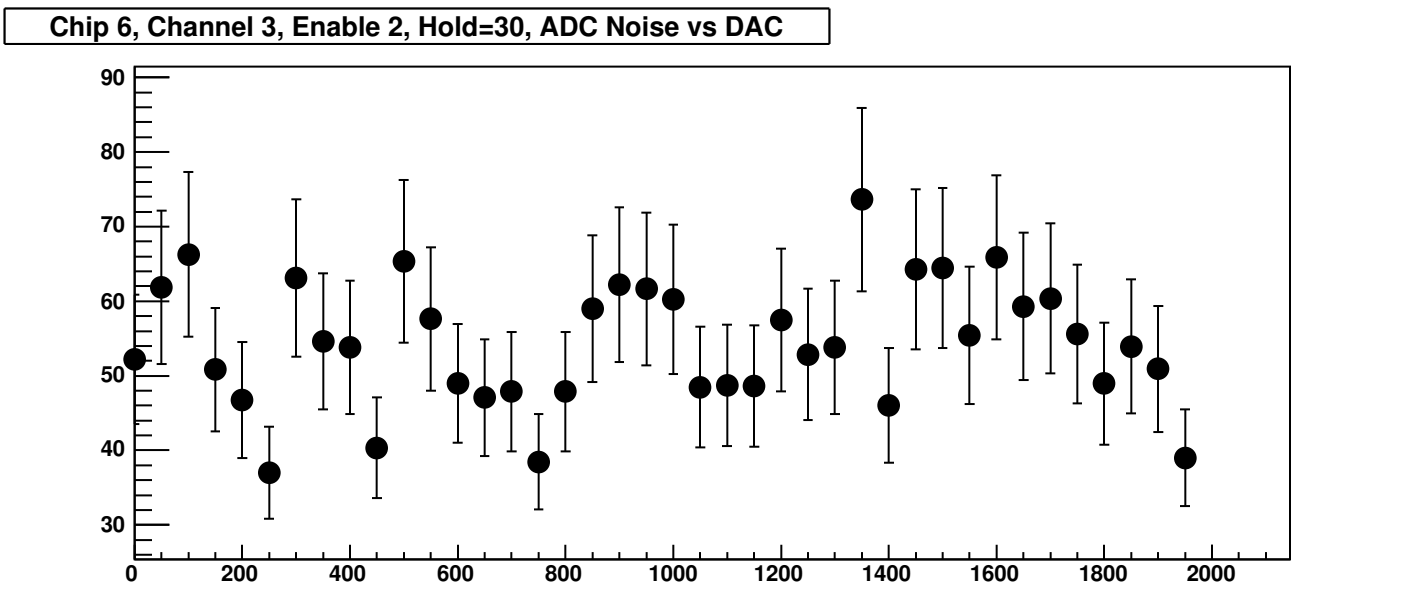
Chip 6, Channel 3, Enable 1!, Hold=30, ADC Noise vs DAC

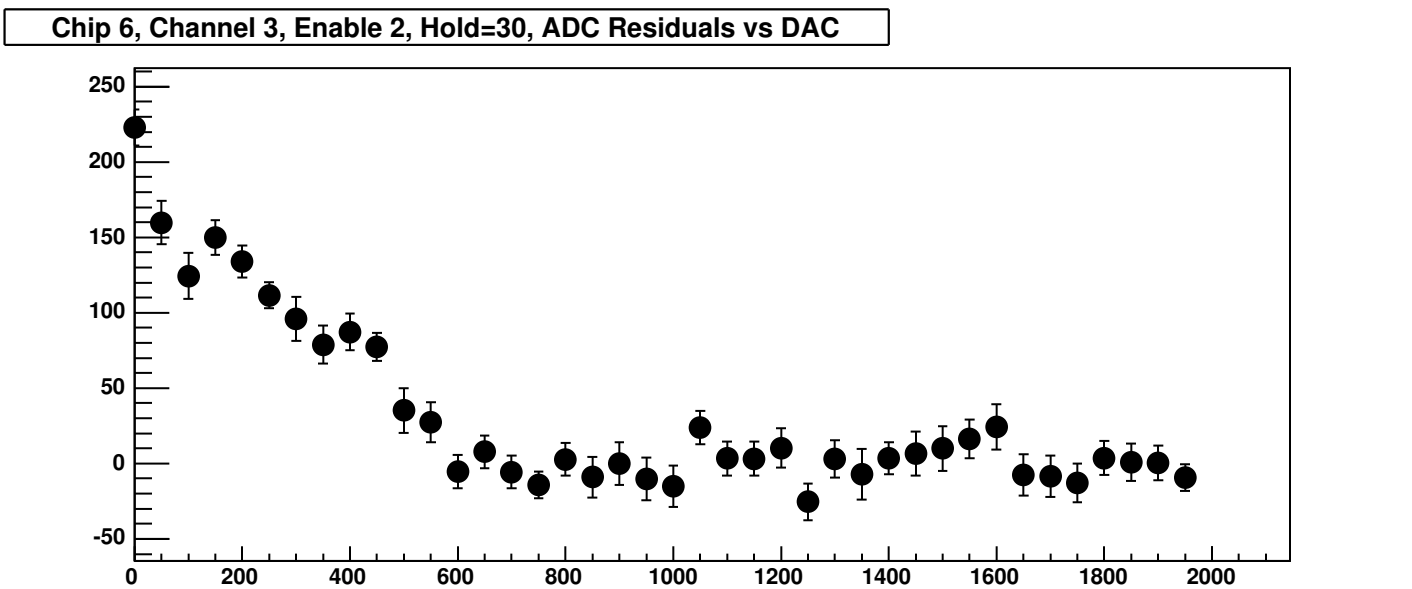


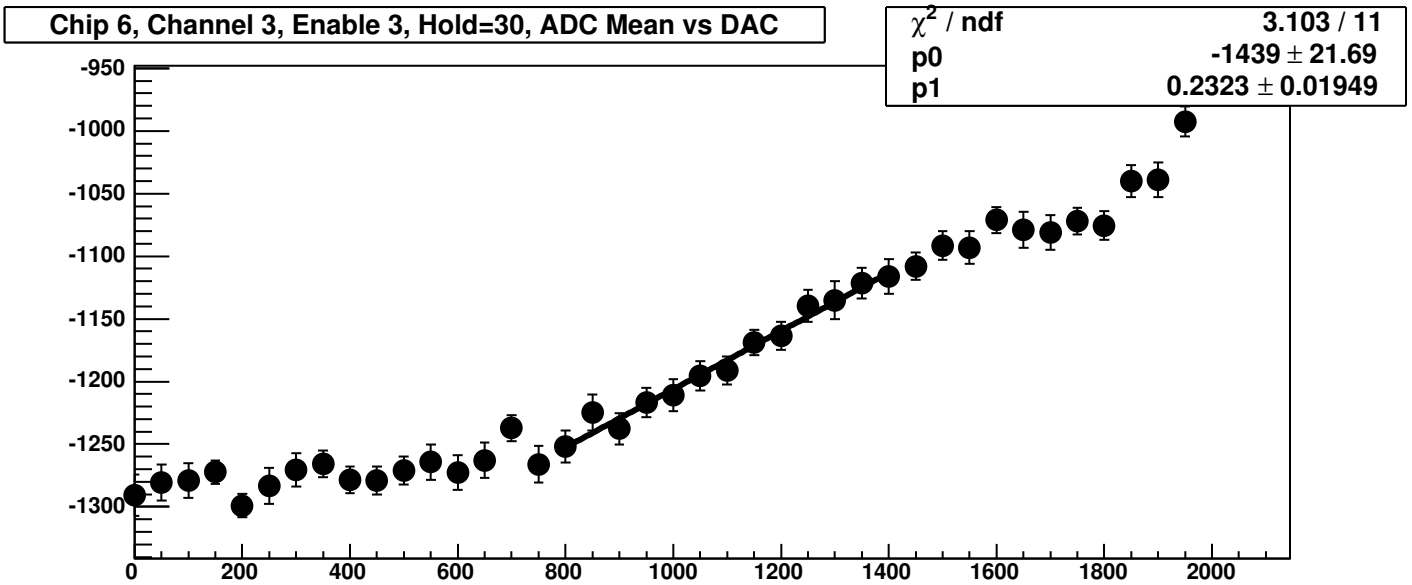
Chip 6, Channel 3, Enable 1!, Hold=30, ADC Residuals vs DAC

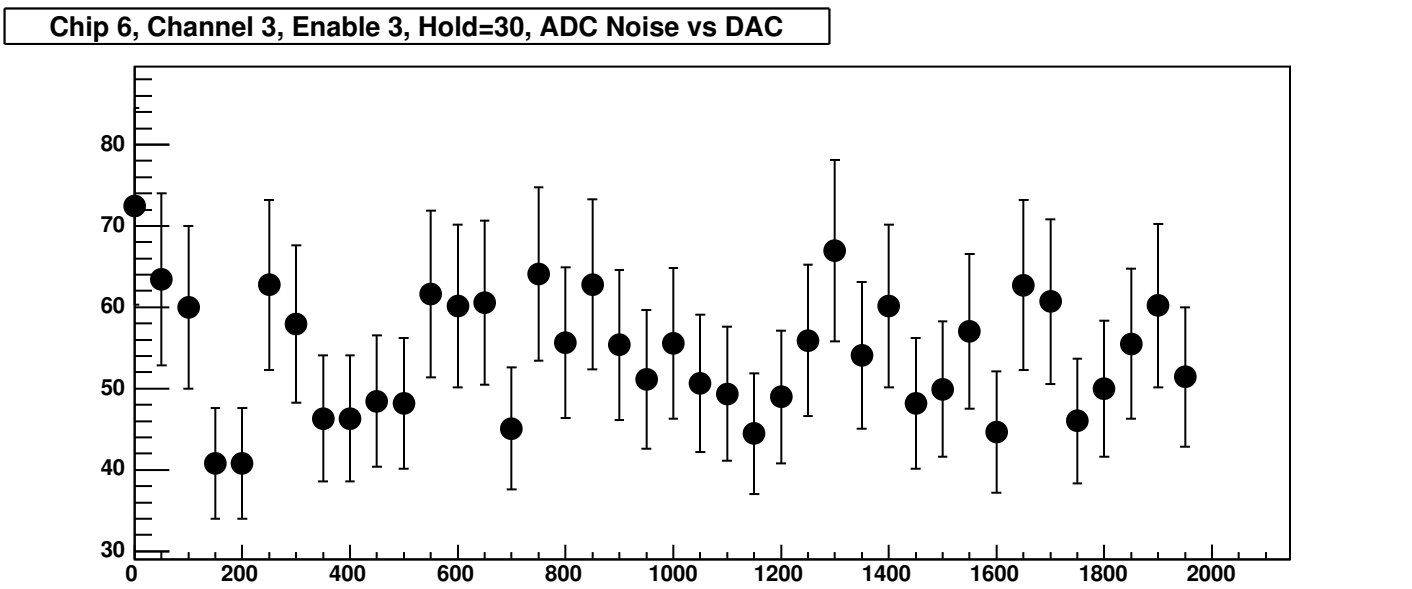


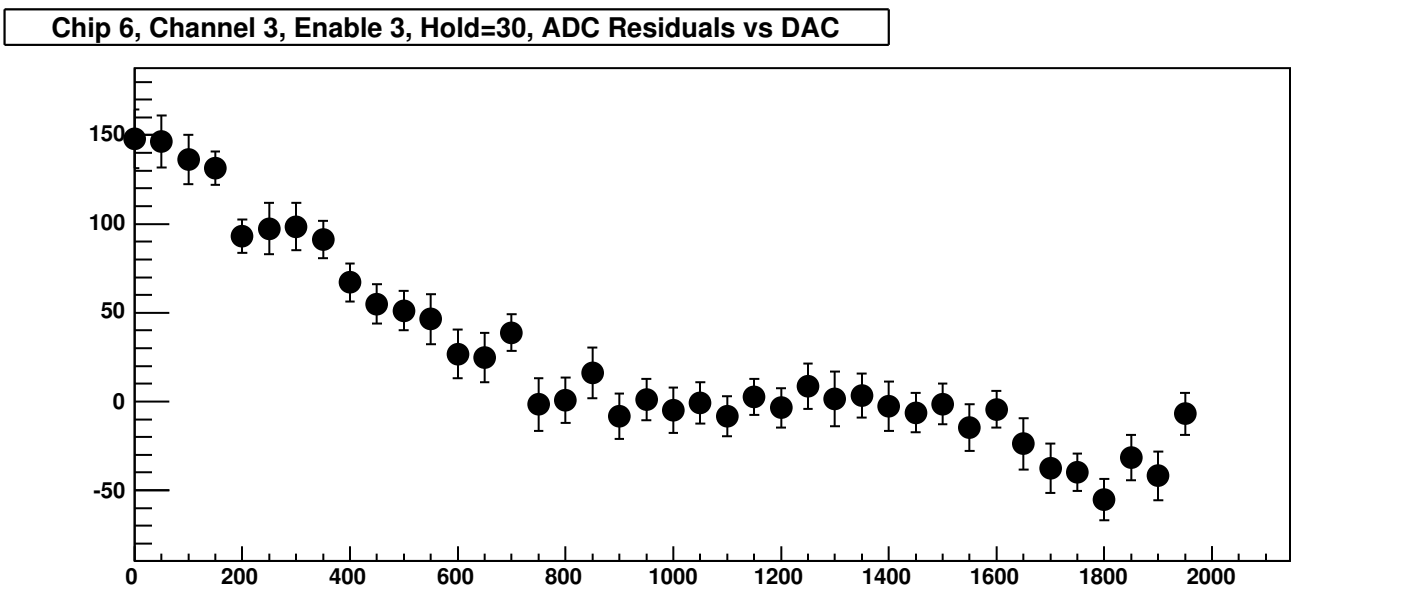

Chip 6, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 12.27 / 11
p0 -1401 ± 20.5
p1 0.2918 ± 0.0183
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

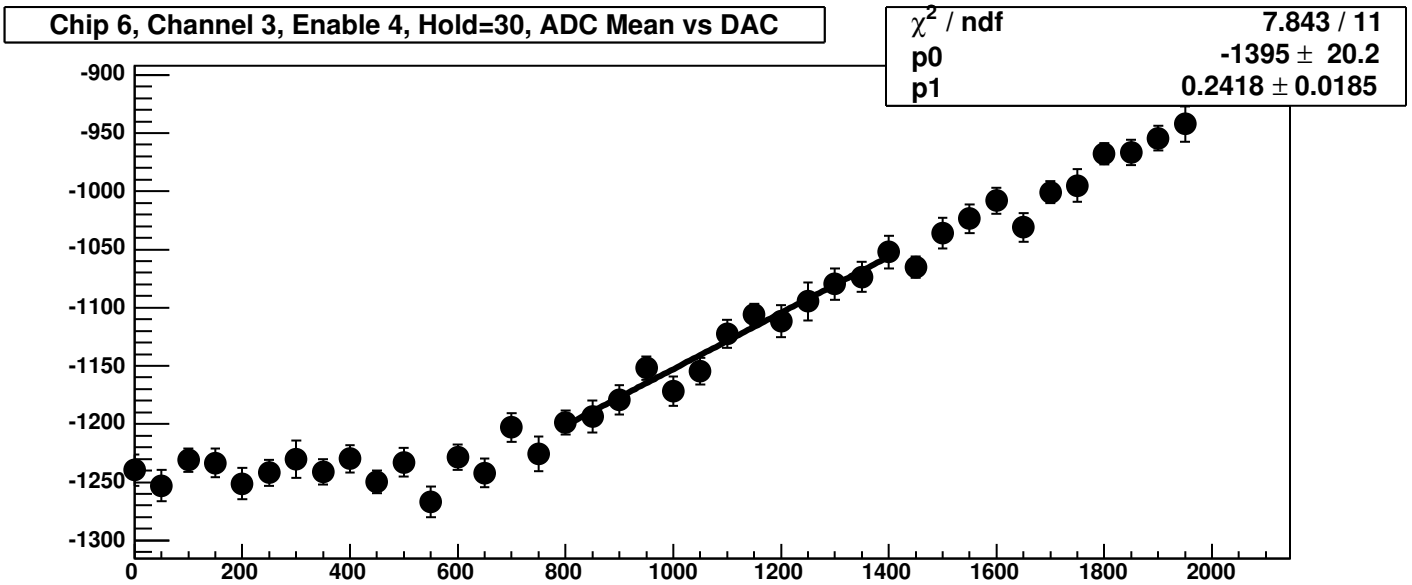

Chip 6, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

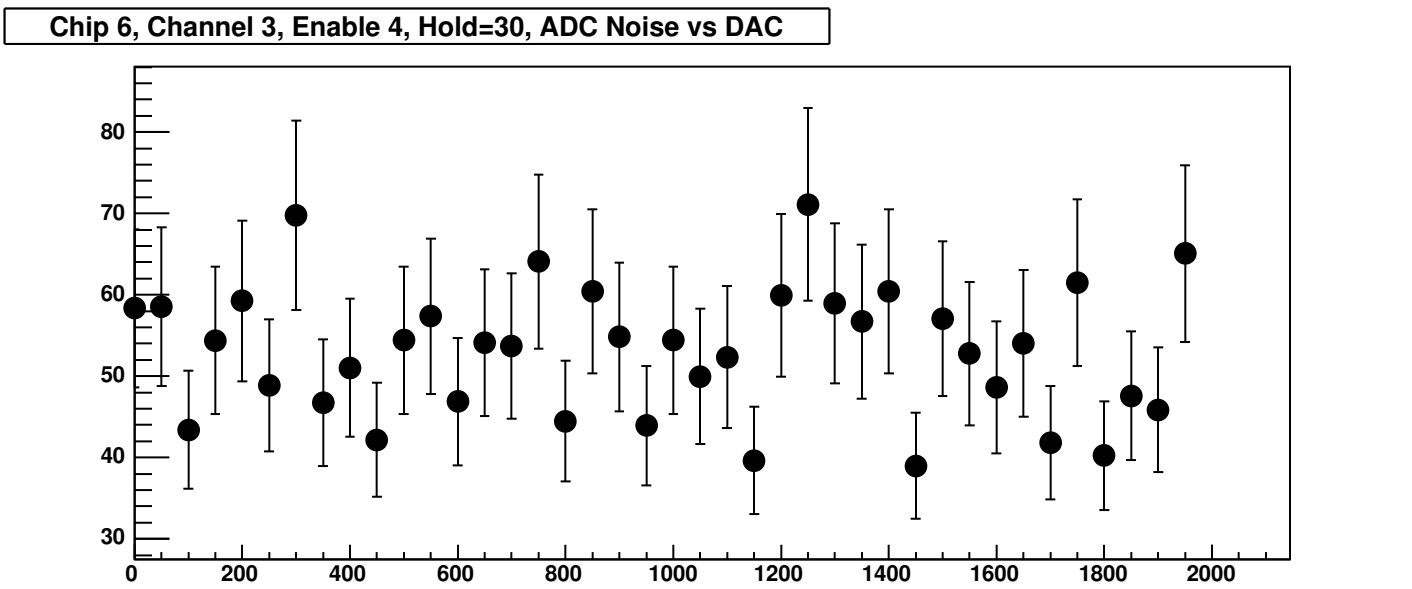

Chip 6, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

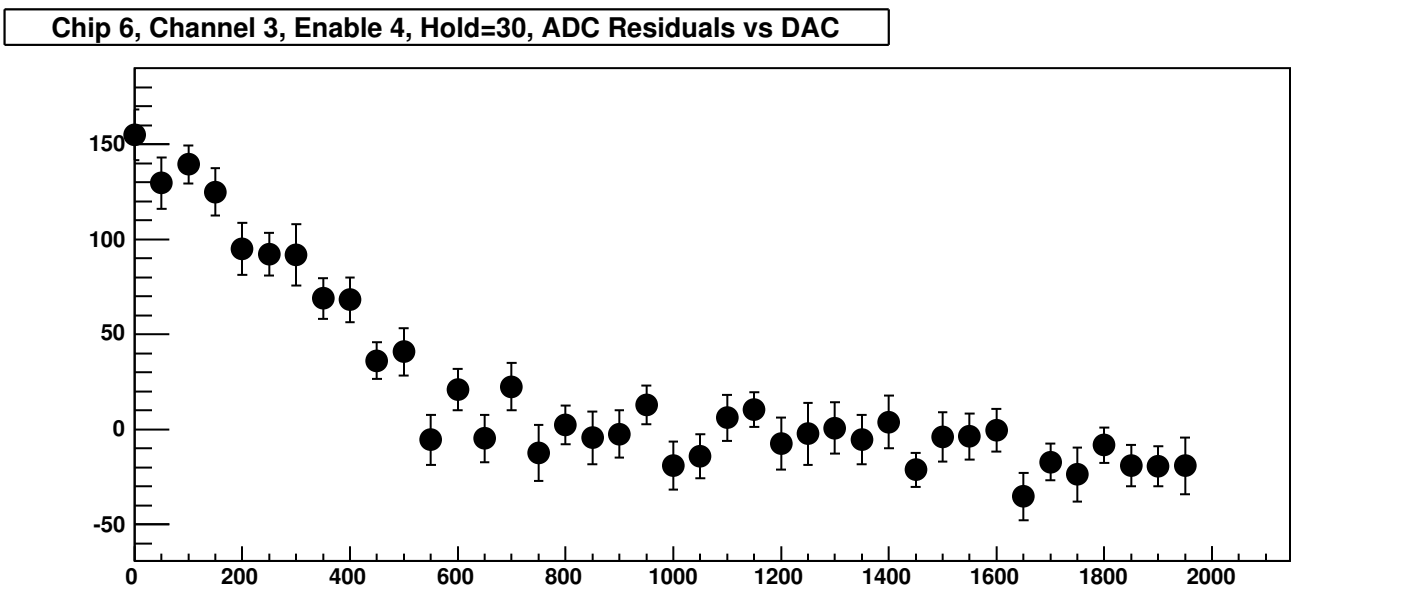

Chip 6, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3.103 / 11
p0 -1439 ± 21.69
p1 0.2323 ± 0.01949
-950
-1000
-1050
-1100
-1150
-1200
-1250
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

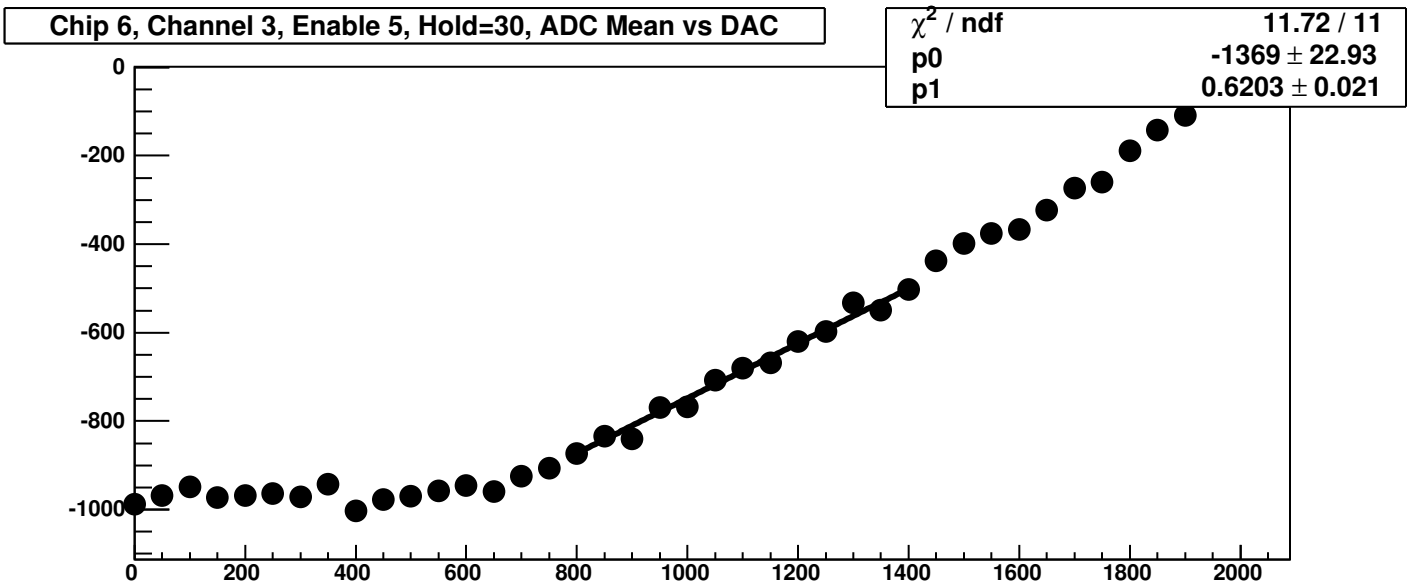

Chip 6, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

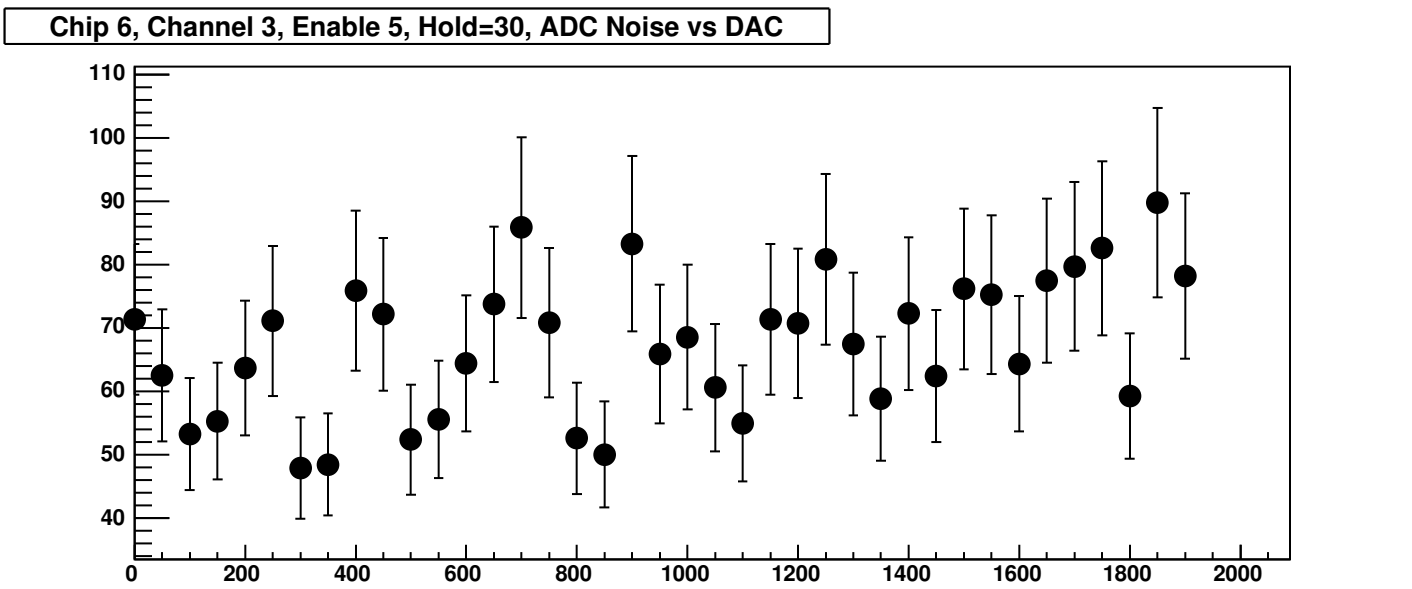

Chip 6, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

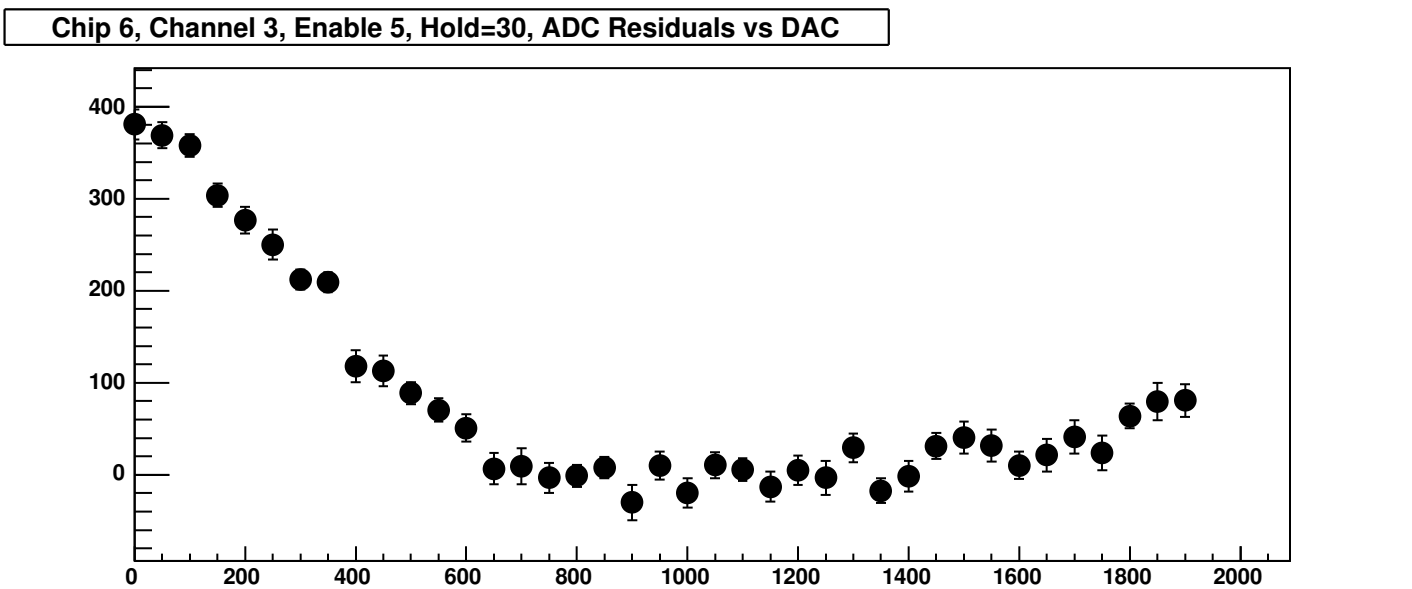

Chip 6, Channel 3, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7.843 / 11
p0 -1395 ± 20.2
p1 0.2418 ± 0.0185
-900
-950
-1000
-1050
-1100
-1150
-1200
-1250
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

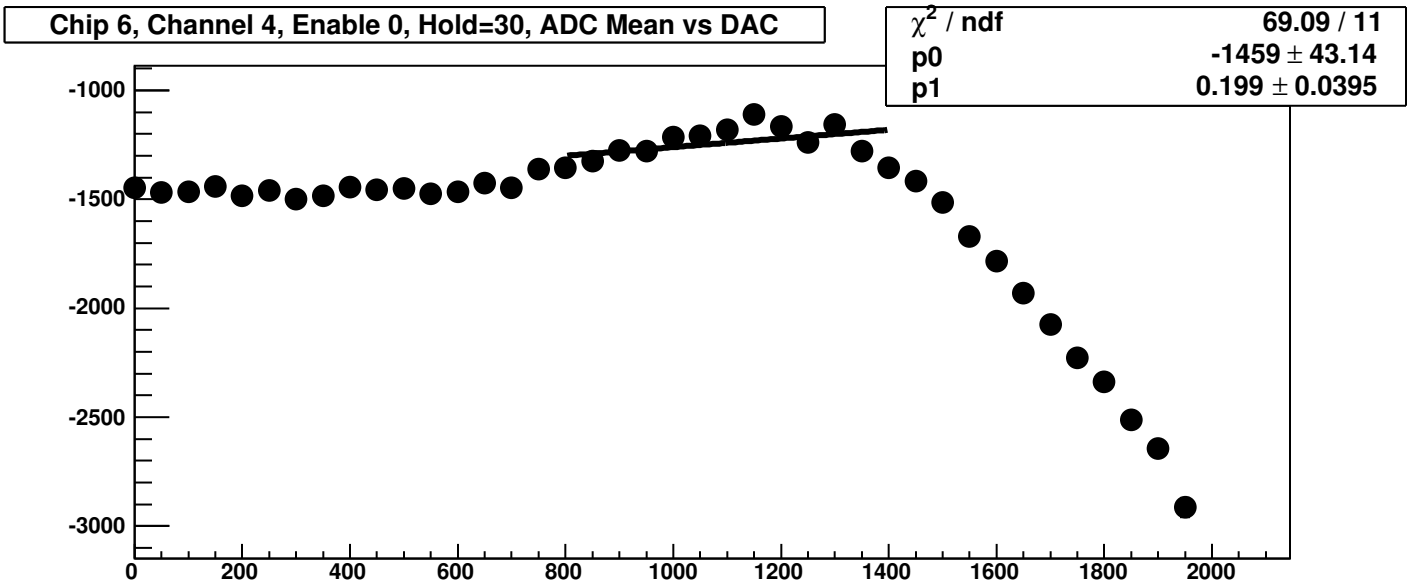

Chip 6, Channel 3, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

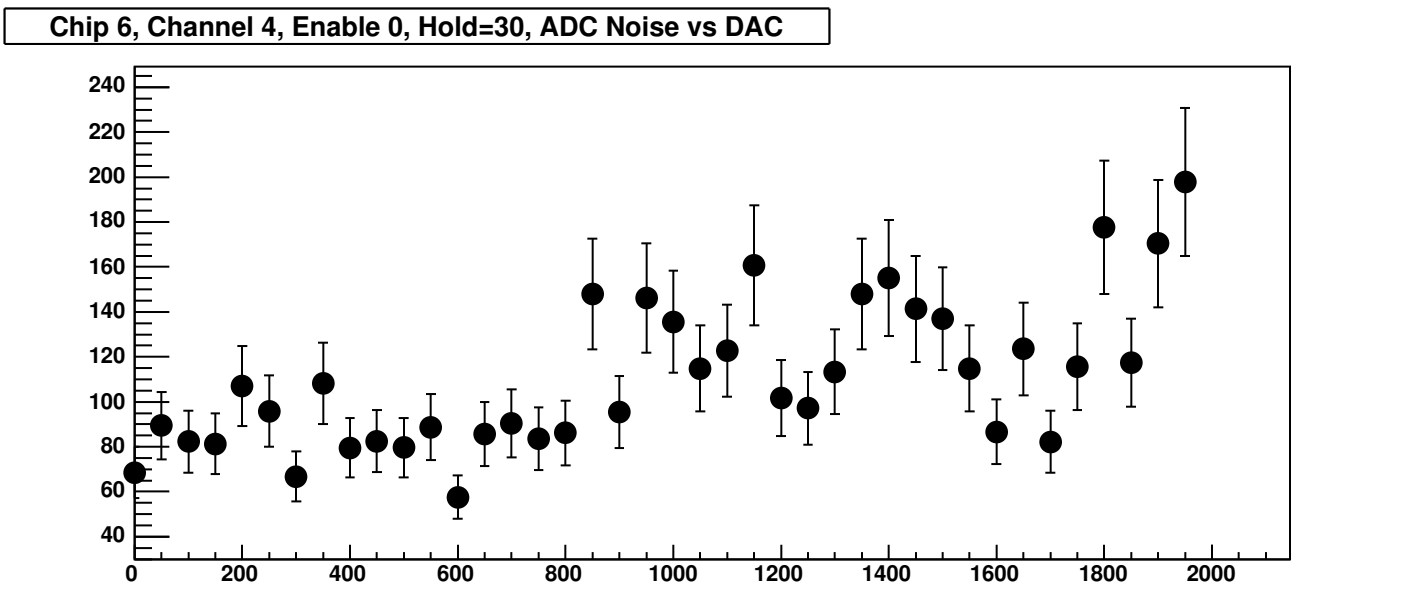

Chip 6, Channel 3, Enable 4, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

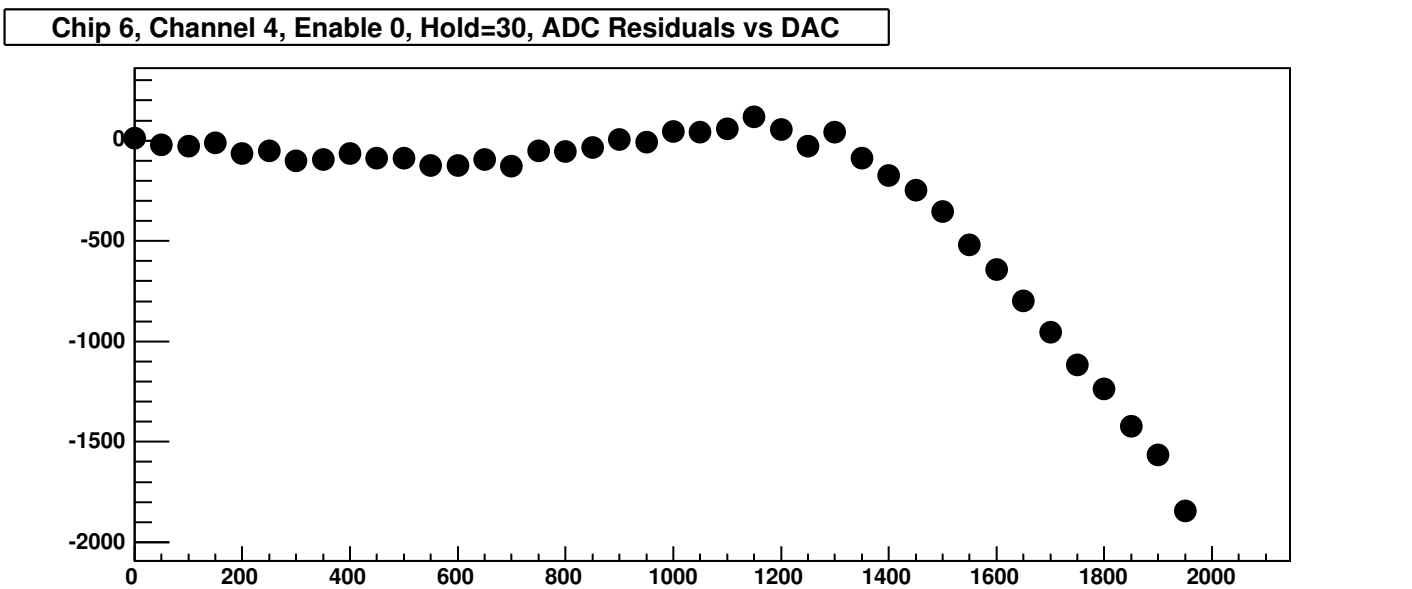

Chip 6, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 11.72 / 11
p0 -1369 ± 22.93
p1 0.6203 ± 0.021
0
-200
-400
-600
-800
-1000
0 200 400 600 800 1000 1200 1400 1600 1800 2000


Chip 6, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
0 200 400 600 800 1000 1200 1400 1600 1800 2000

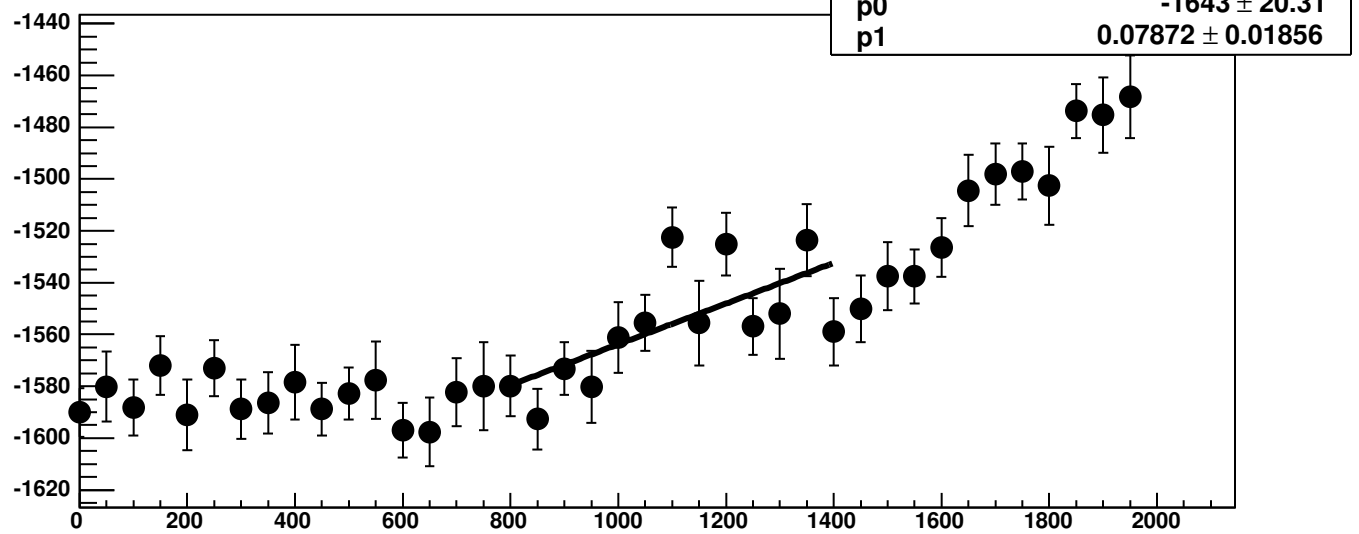

Chip 6, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
400
300
200
100
0
0 200 400 600 800 1000 1200 1400 1600 1800 2000


Chip 6, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 69.09 / 11
p0 -1459 ± 43.14
p1 0.199 ± 0.0395
-1000
-1500
-2000
-2500
-3000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

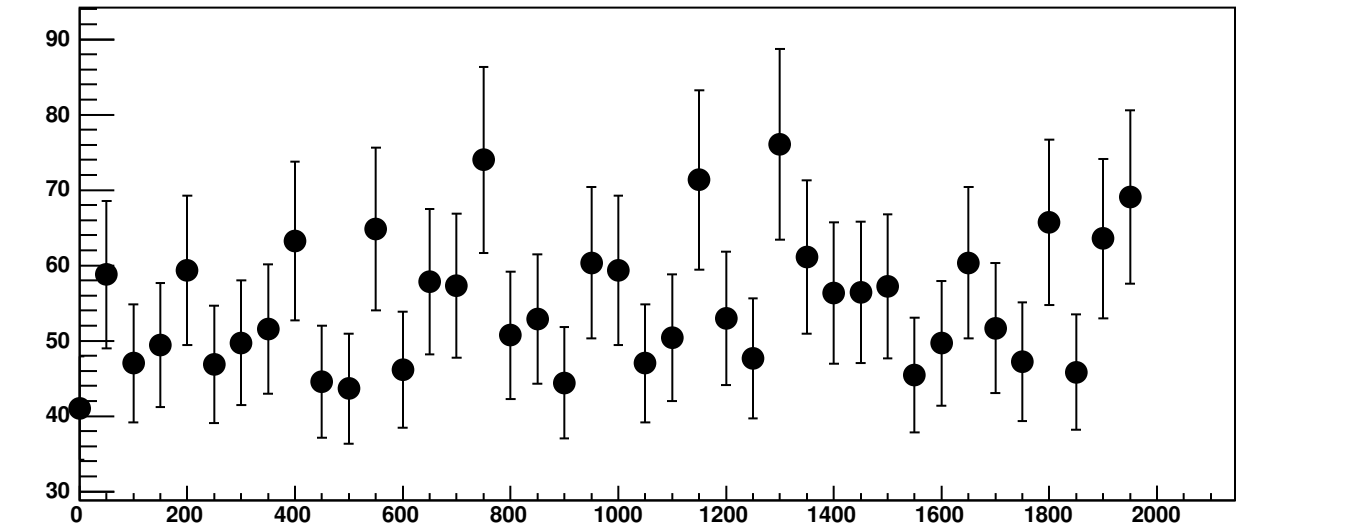

Chip 6, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC
240
220
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

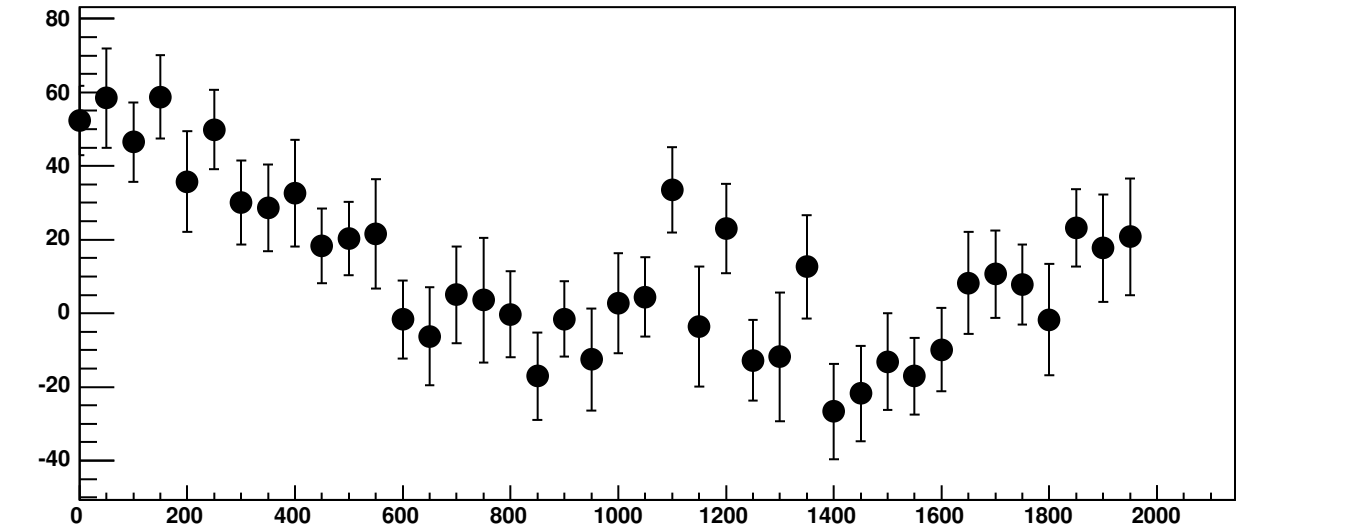
Chip 6, Channel 4, Enable 1, Hold=30, ADC Mean vs DAC



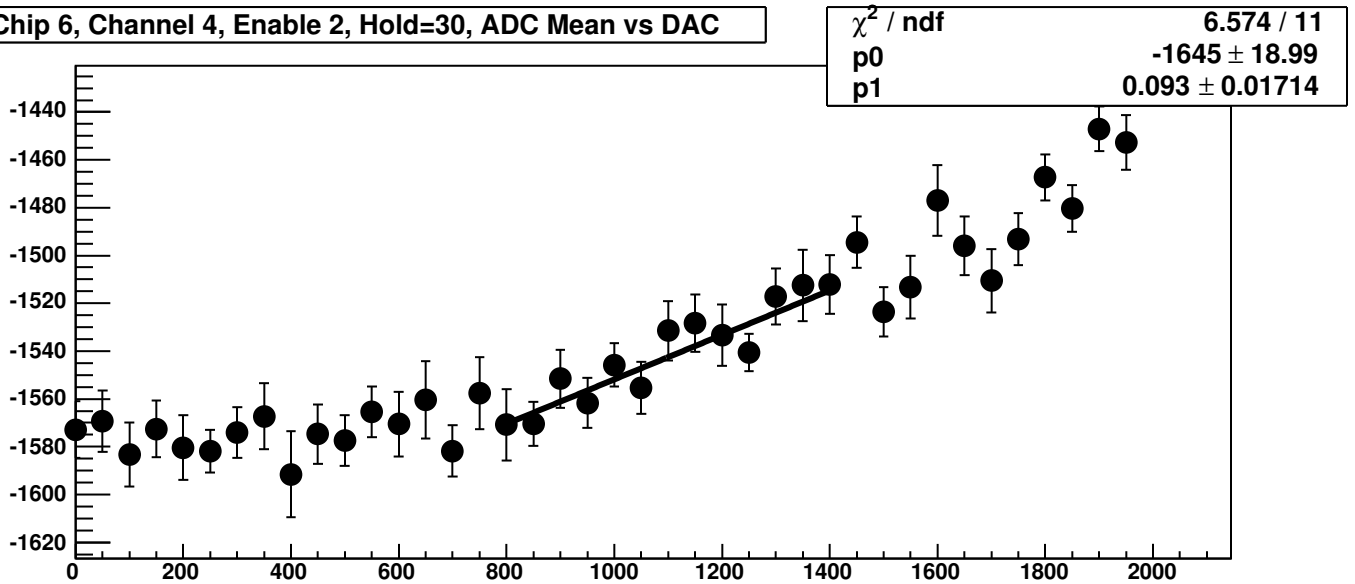
Chip 6, Channel 4, Enable 1, Hold=30, ADC Noise vs DAC



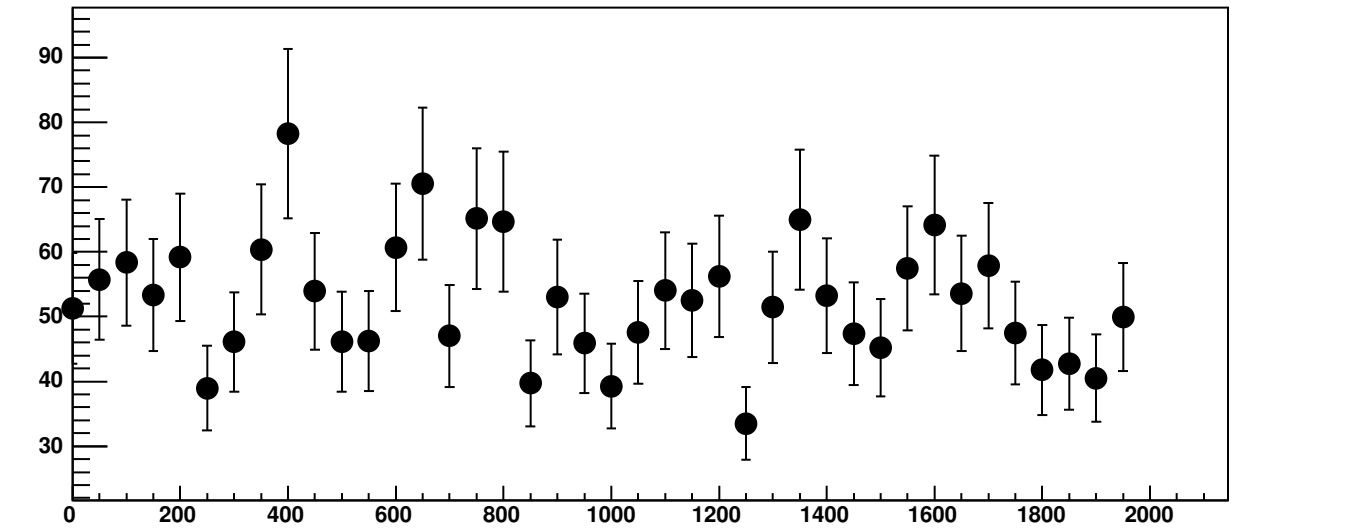
Chip 6, Channel 4, Enable 1, Hold=30, ADC Residuals vs DAC



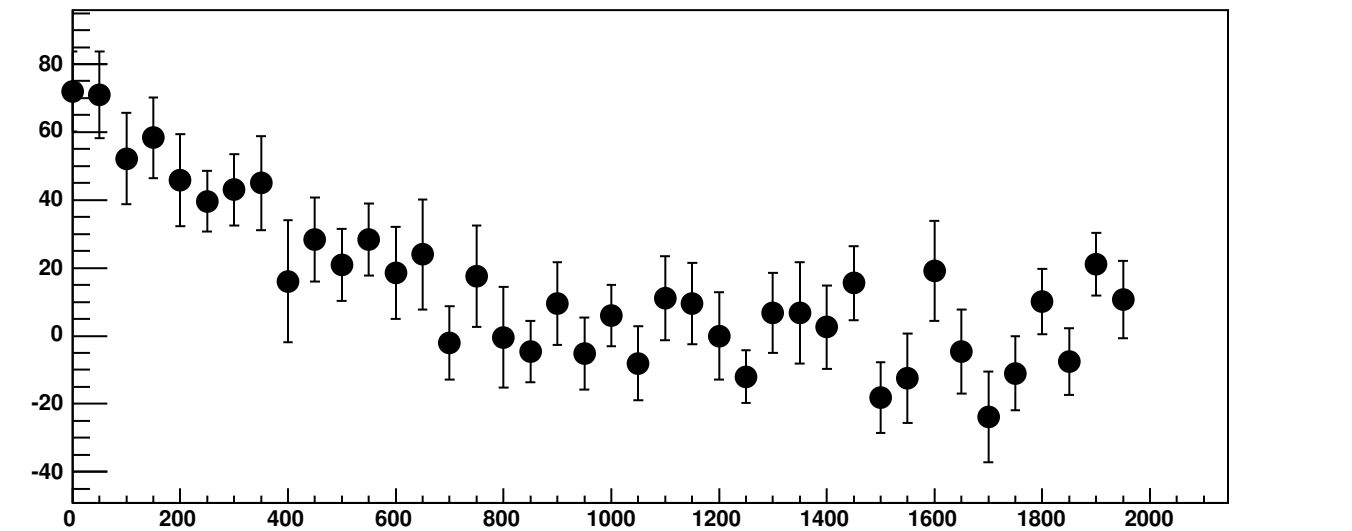
Chip 6, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC

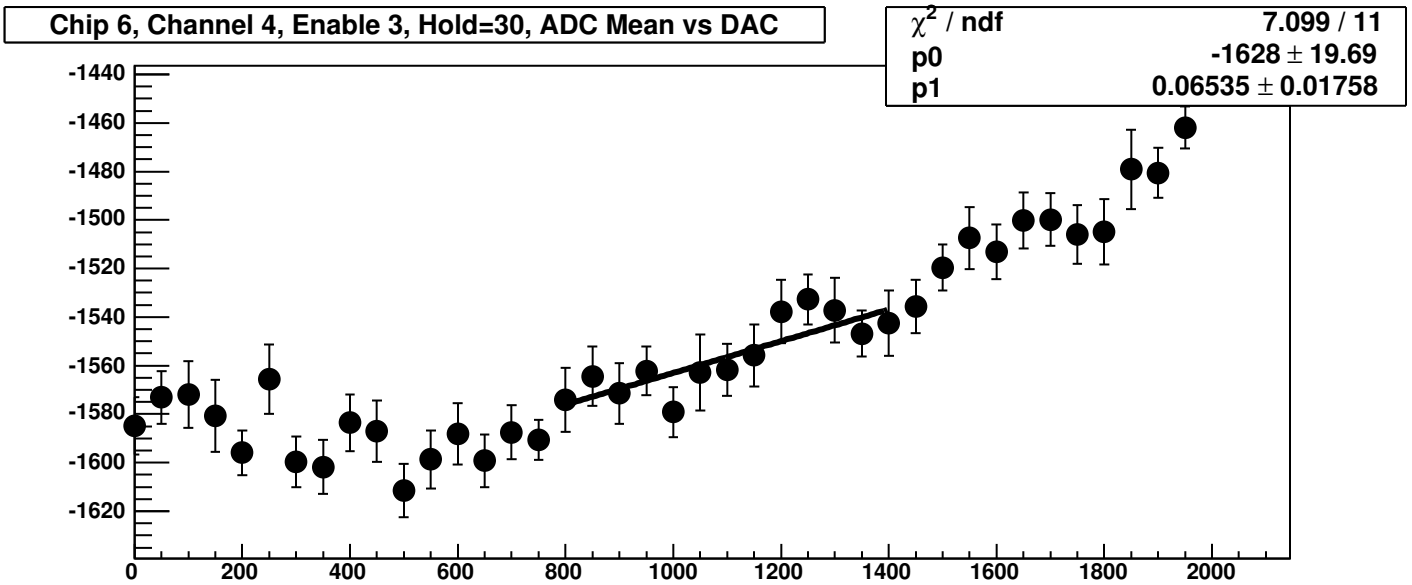


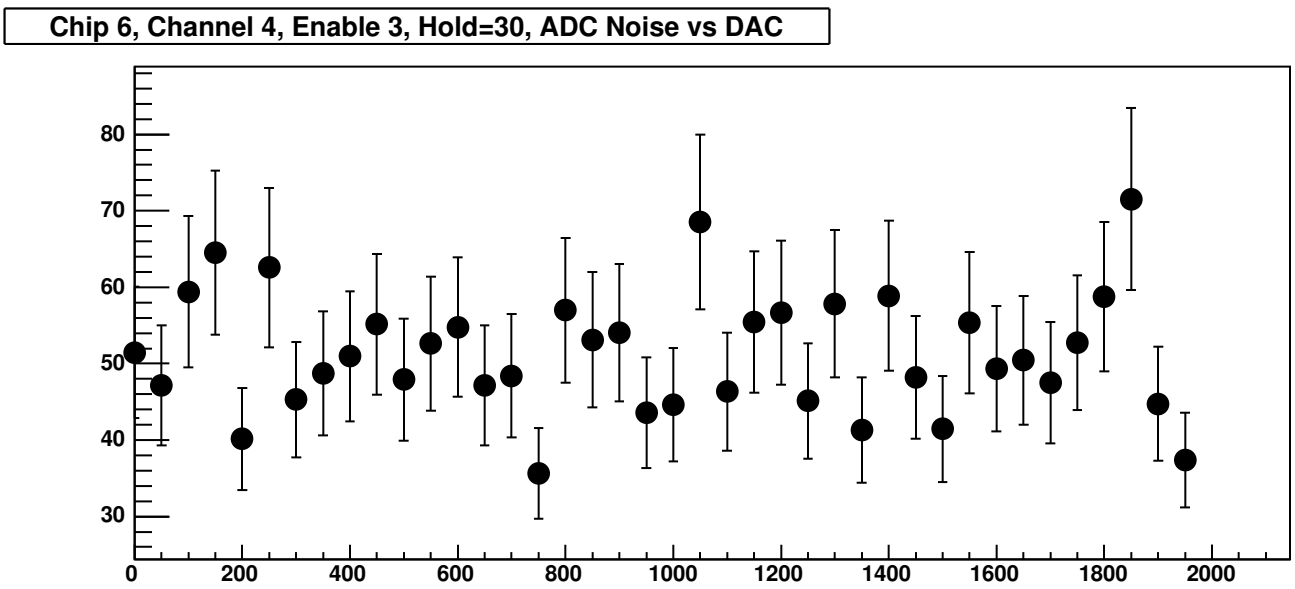
Chip 6, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC

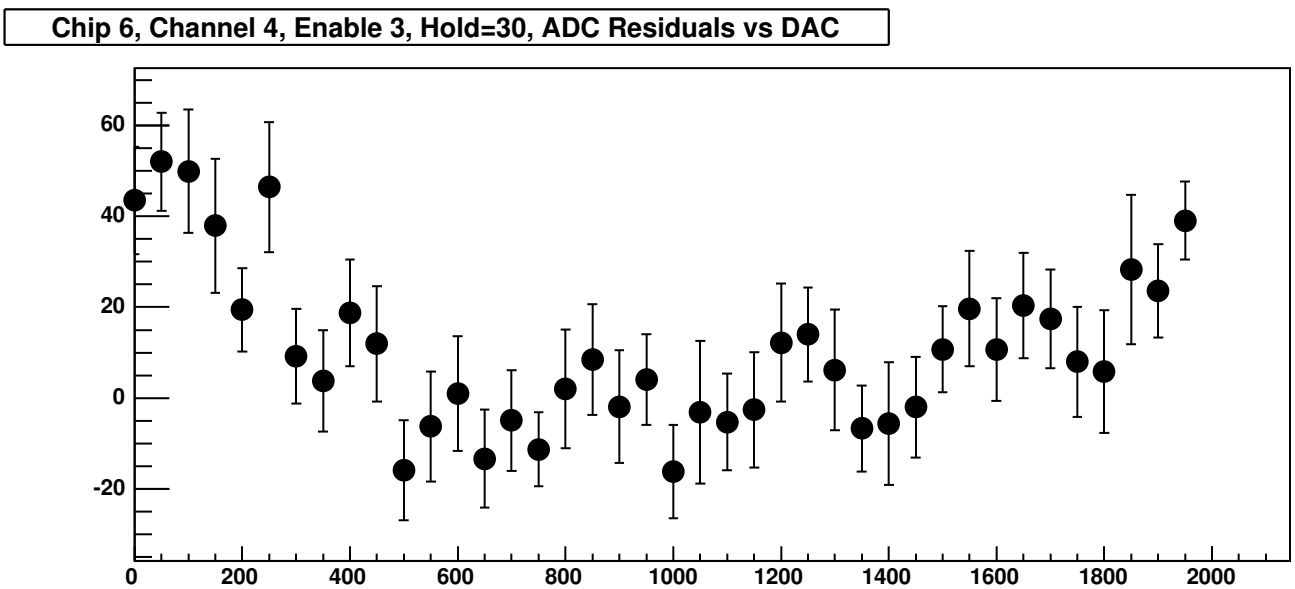


Chip 6, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC

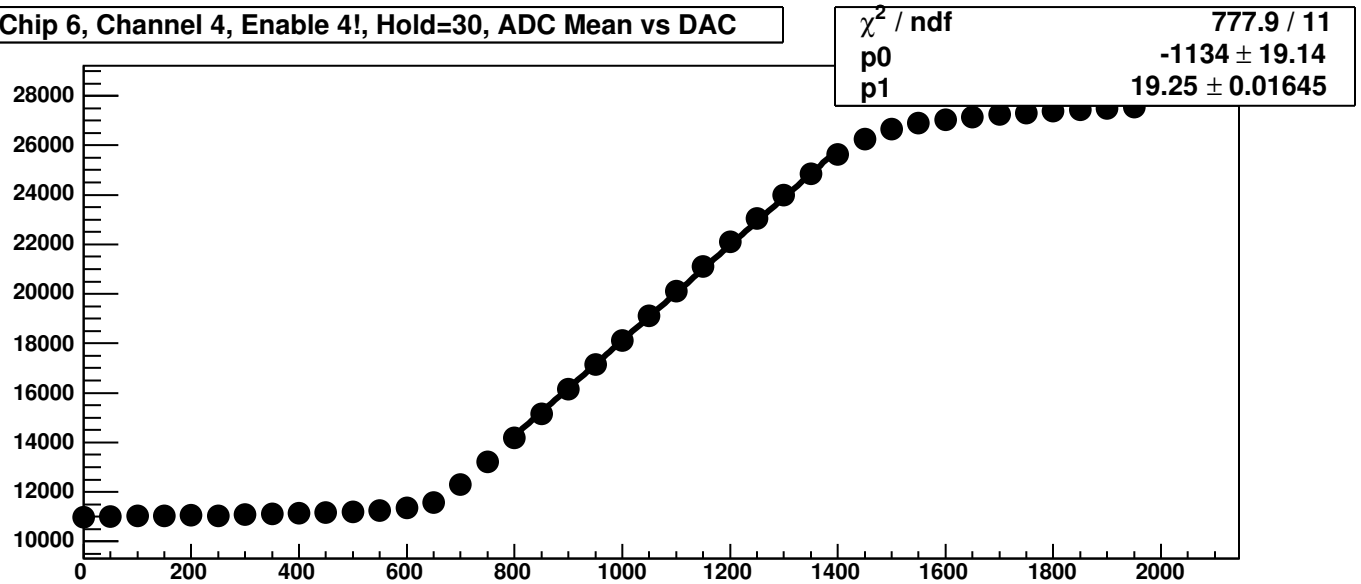



Chip 6, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7.099 / 11
p0 -1628 ± 19.69
p1 0.06535 ± 0.01758
-1440
-1460
-1480
-1500
-1520
-1540
-1560
-1580
-1600
-1620
0
200
400
600
800
1000
1200
1400
1600
1800
2000

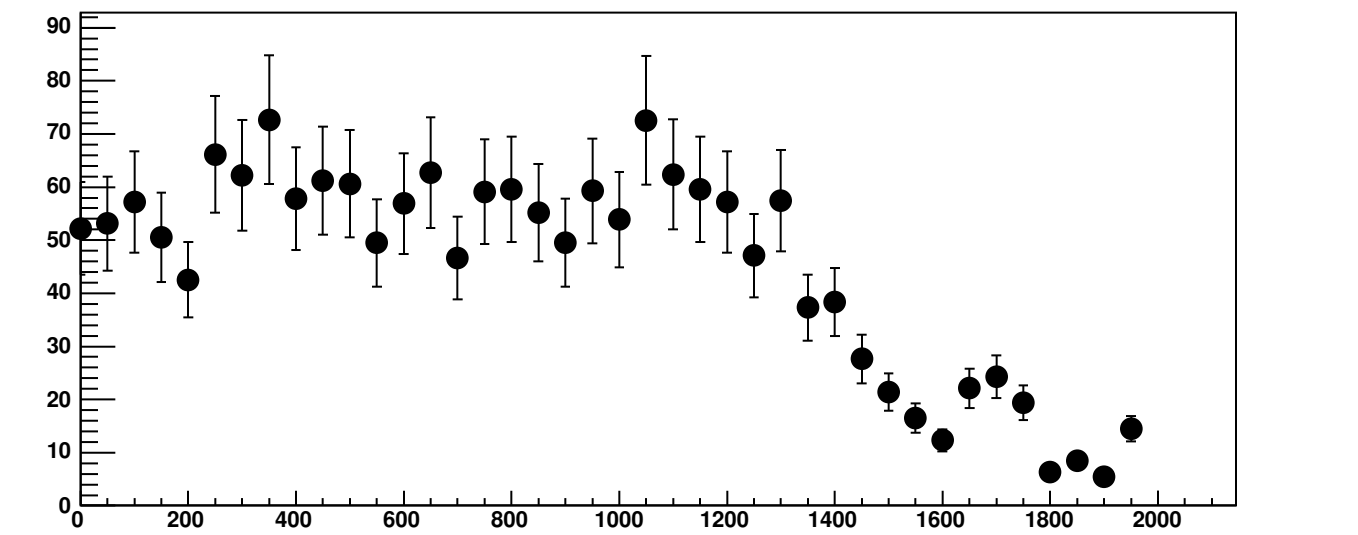

Chip 6, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

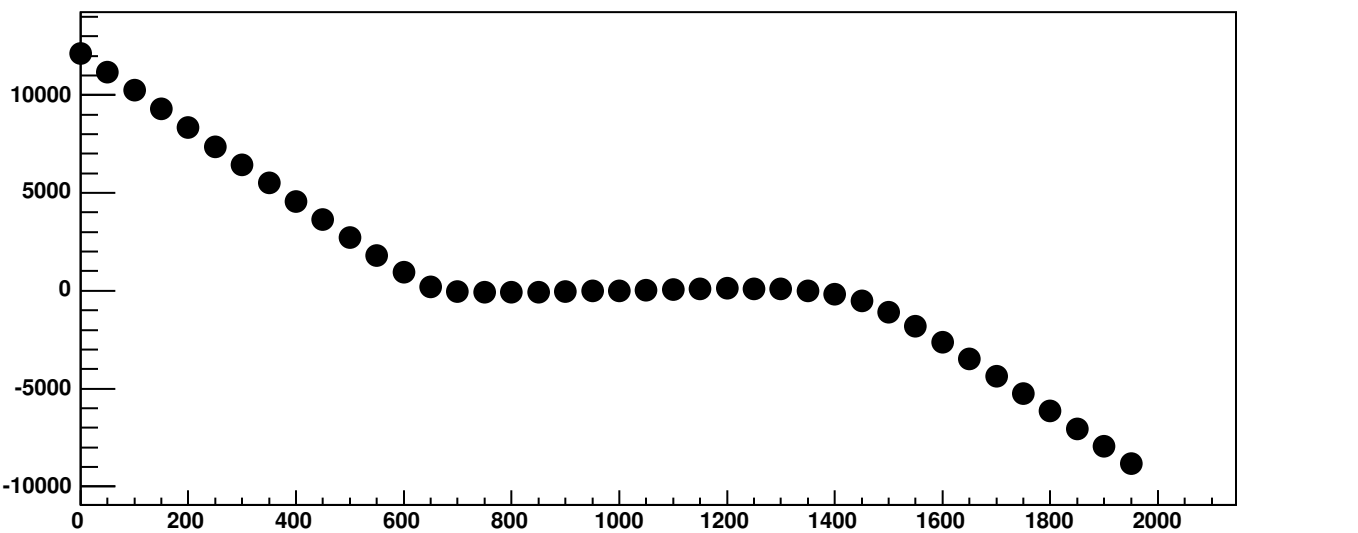
Chip 6, Channel 4, Enable 4!, Hold=30, ADC Mean vs DAC

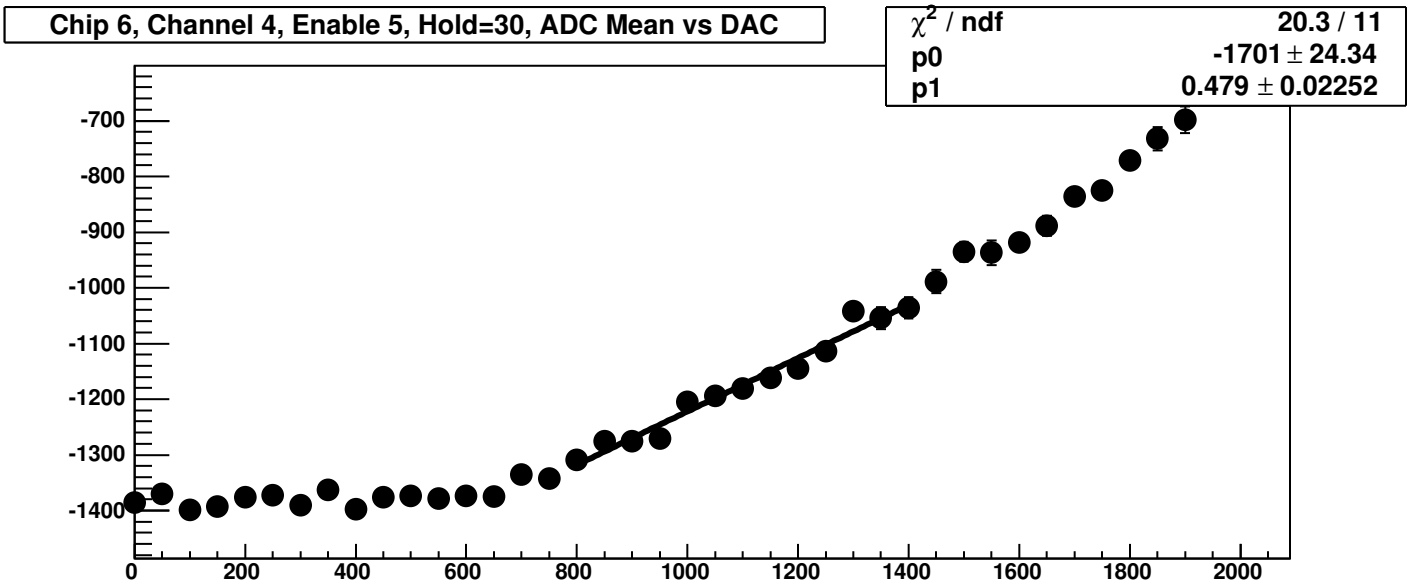


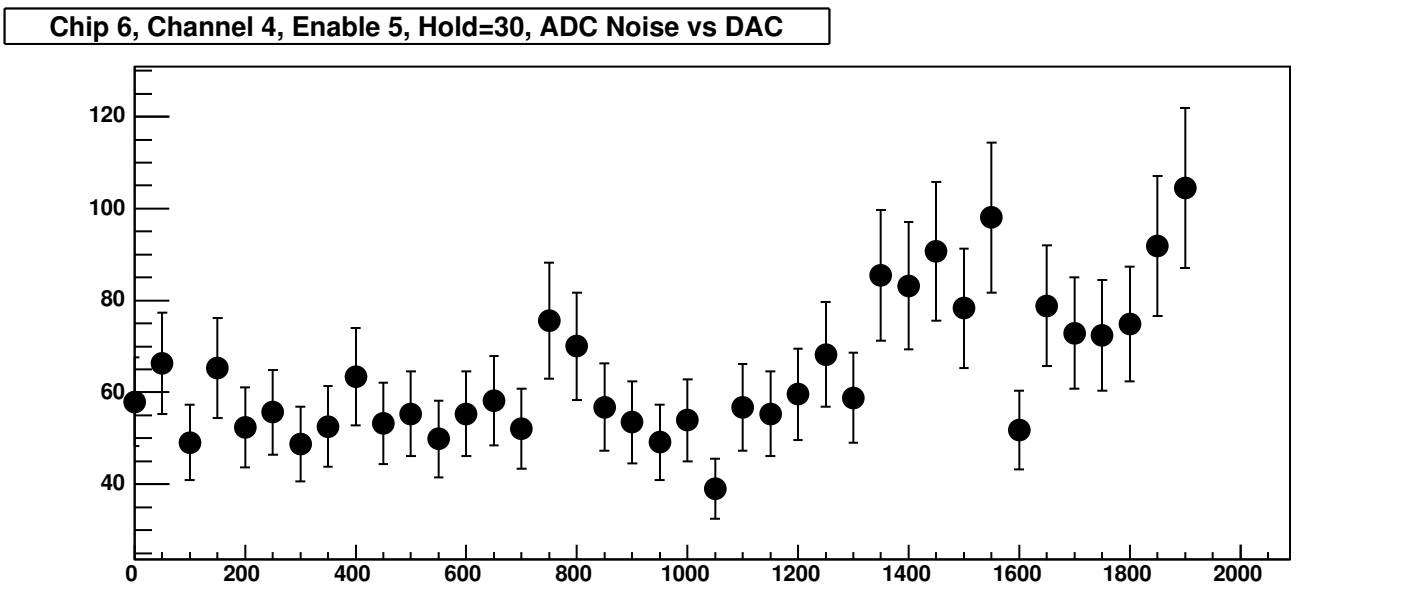
Chip 6, Channel 4, Enable 4!, Hold=30, ADC Noise vs DAC

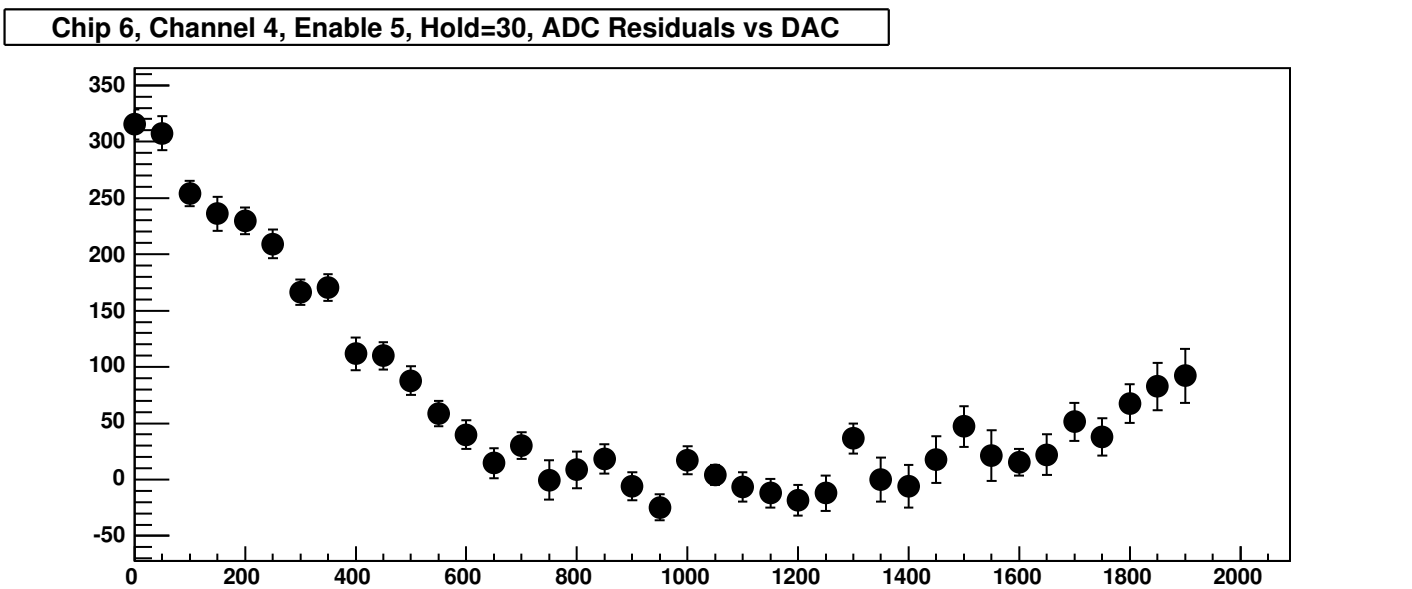


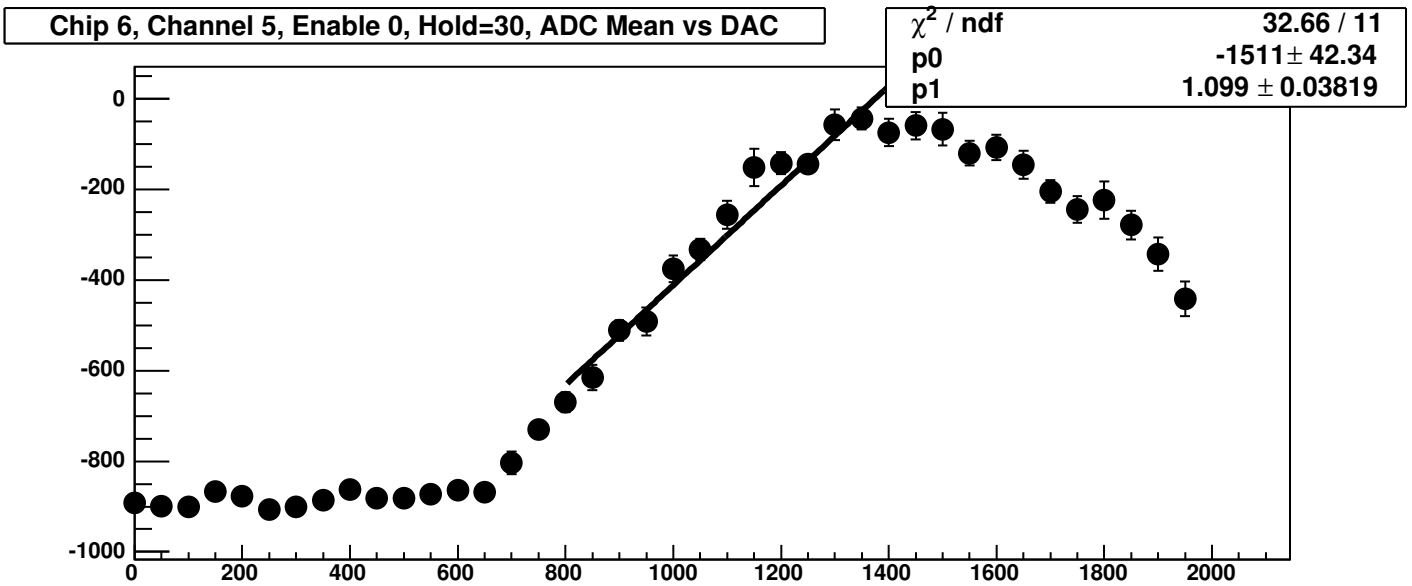
Chip 6, Channel 4, Enable 4!, Hold=30, ADC Residuals vs DAC

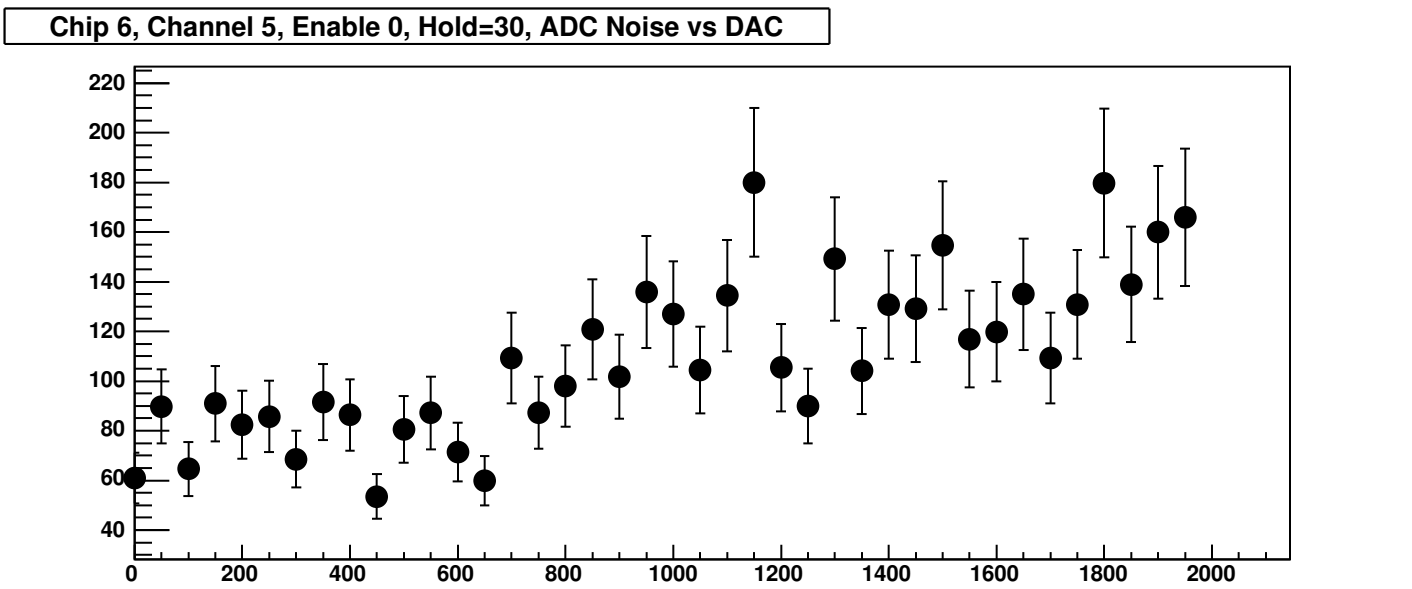


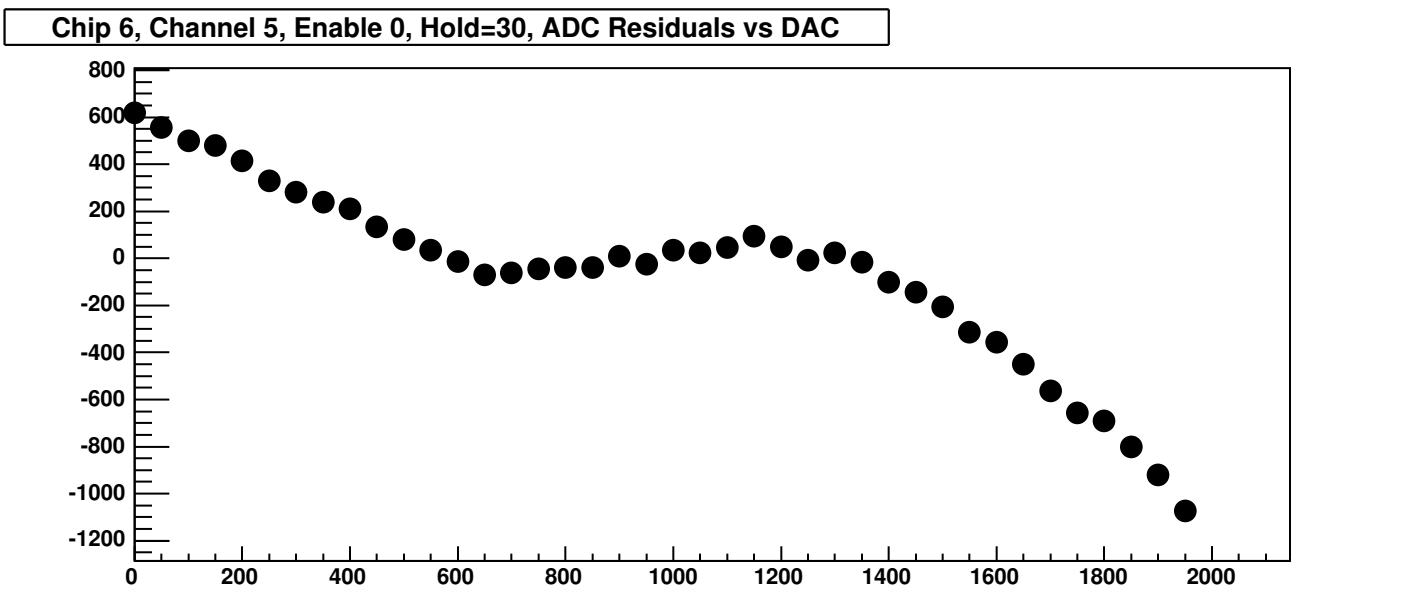

Chip 6, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 20.3 / 11
p0 -1701 ± 24.34
p1 0.479 ± 0.02252
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

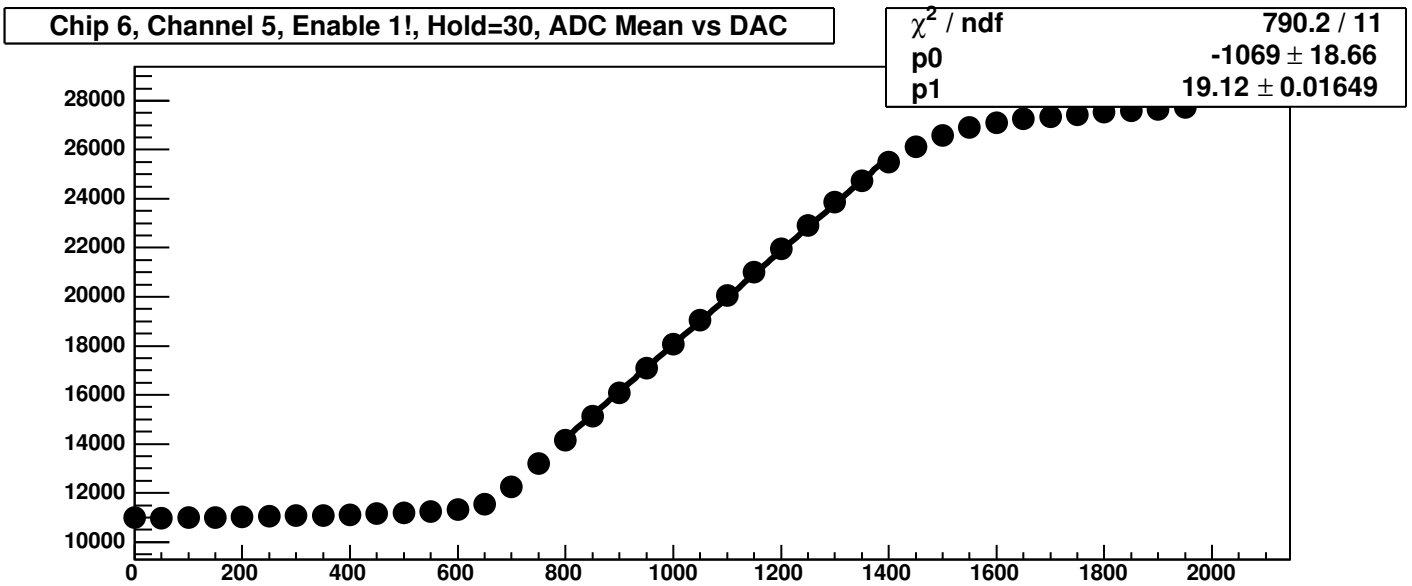

Chip 6, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

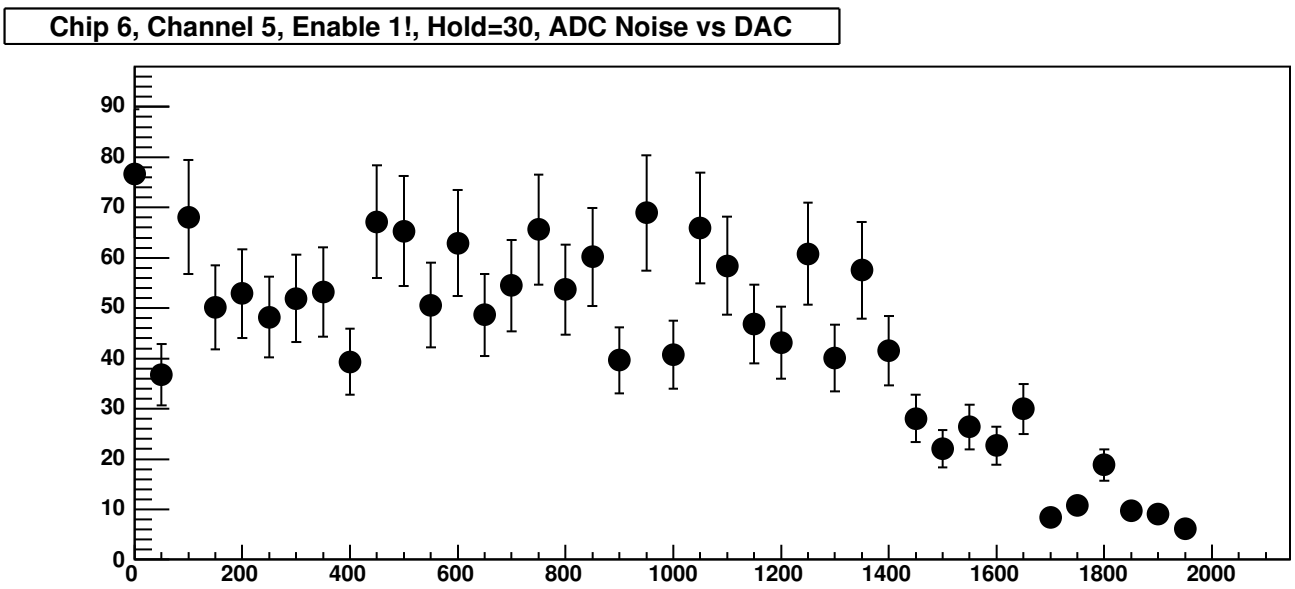

Chip 6, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

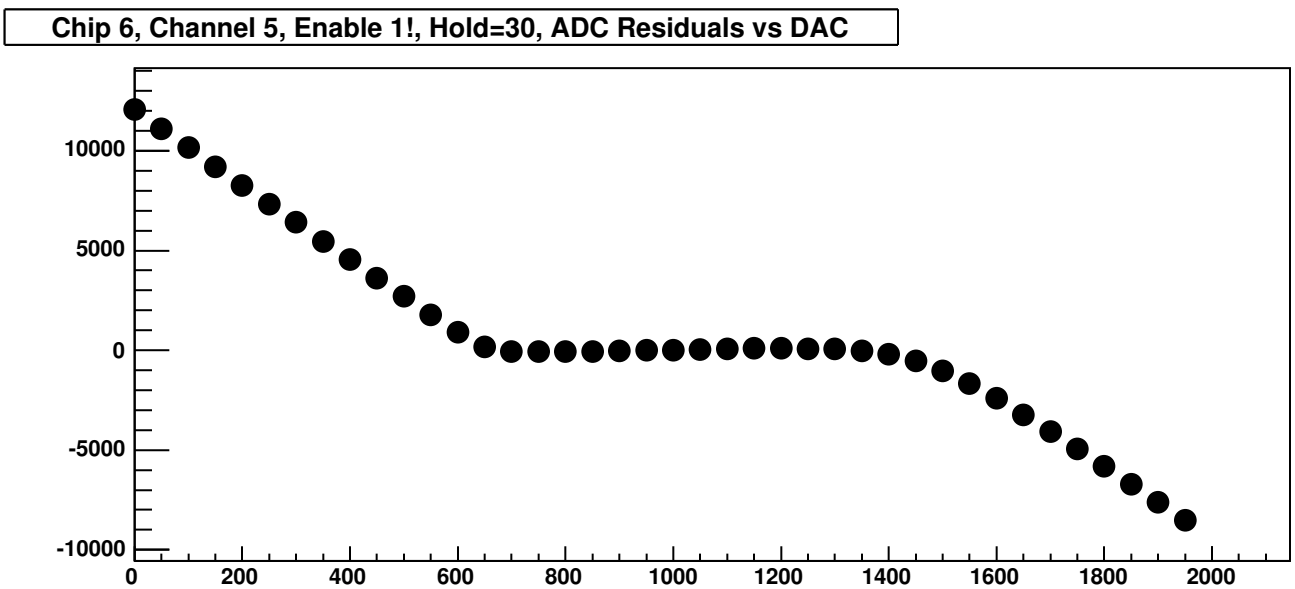

Chip 6, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 32.66 / 11
p0 -1511 ± 42.34
p1 1.099 ± 0.03819
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

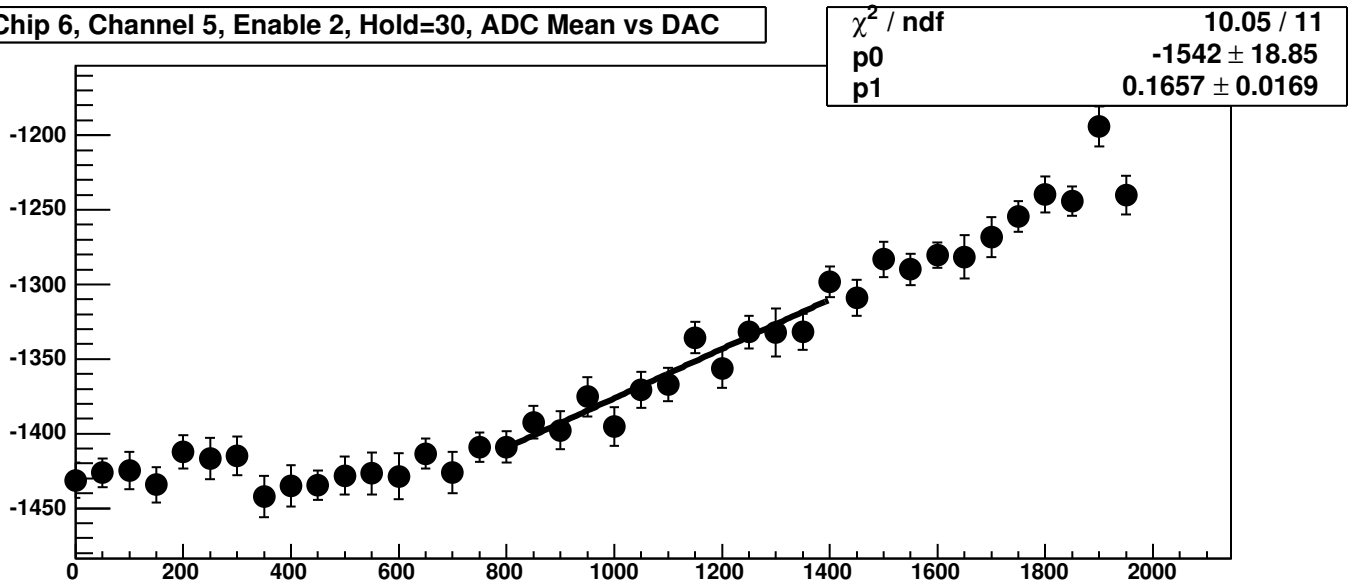

Chip 6, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 5, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 790.2 / 11
p0 -1069 ± 18.66
p1 19.12 ± 0.01649
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

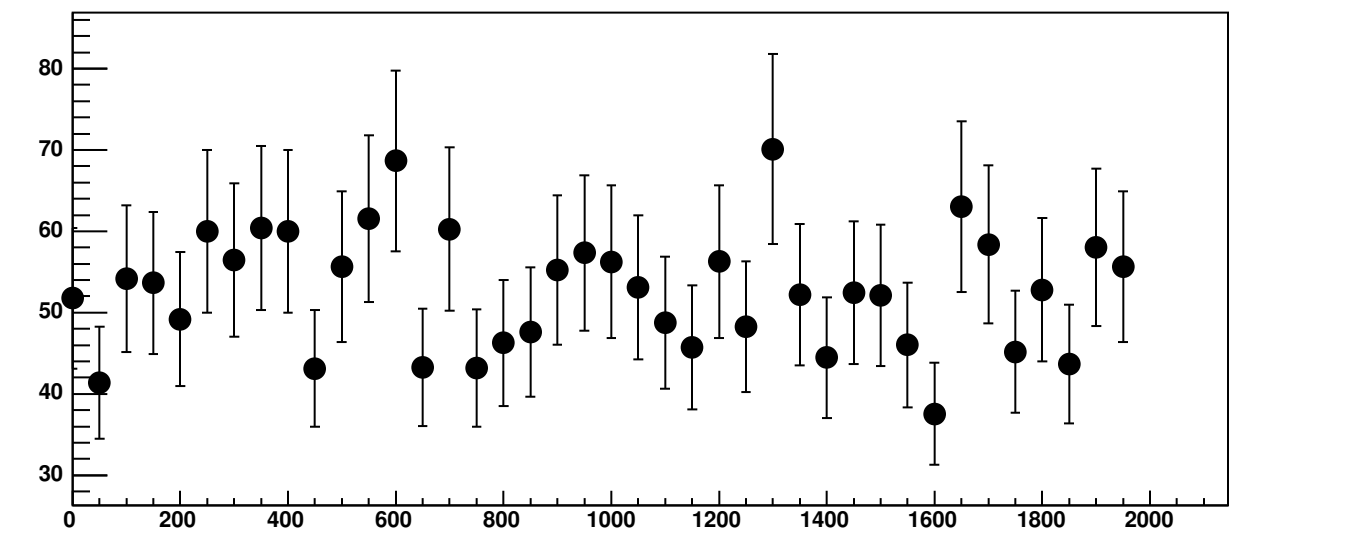

Chip 6, Channel 5, Enable 1!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 5, Enable 1!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

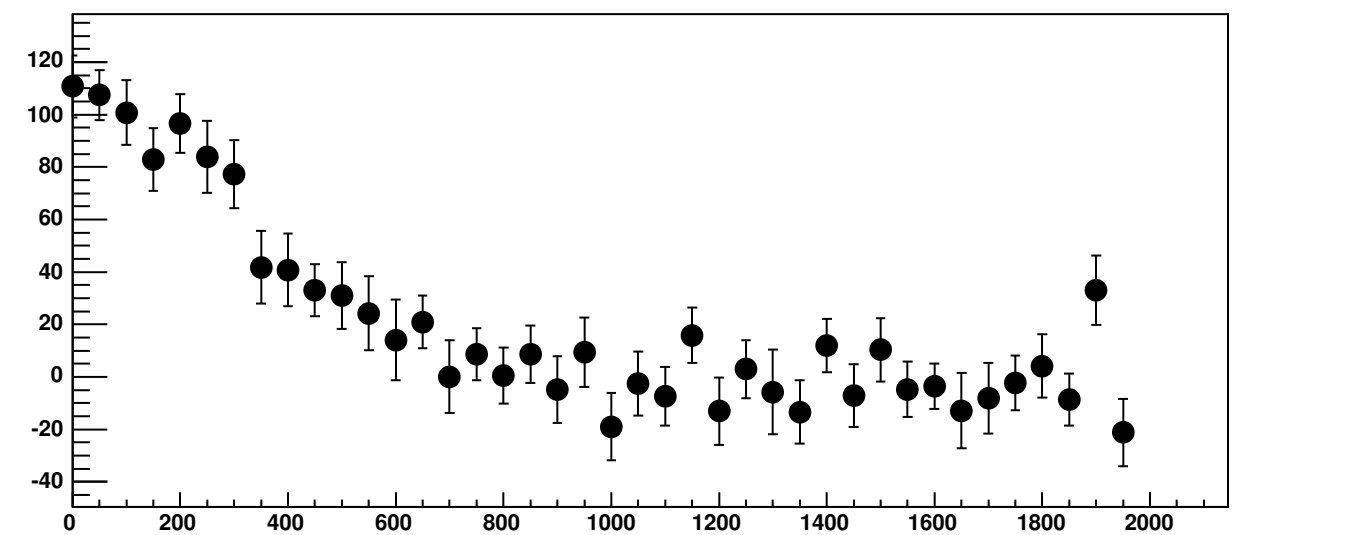
Chip 6, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC

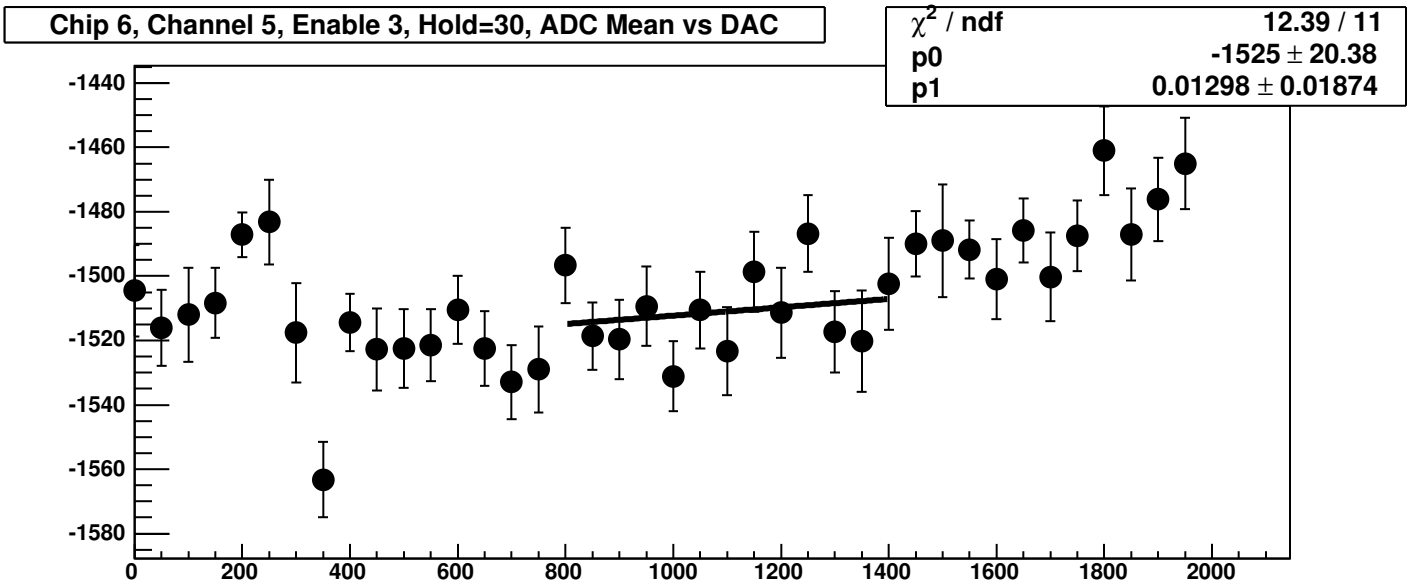


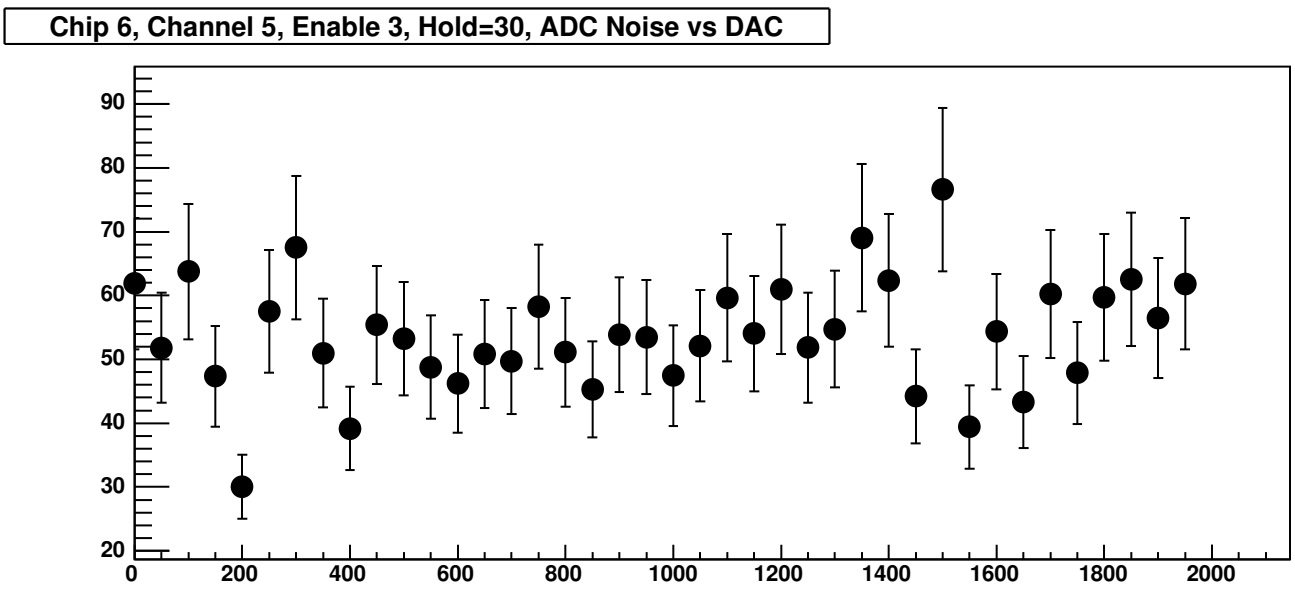
Chip 6, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC

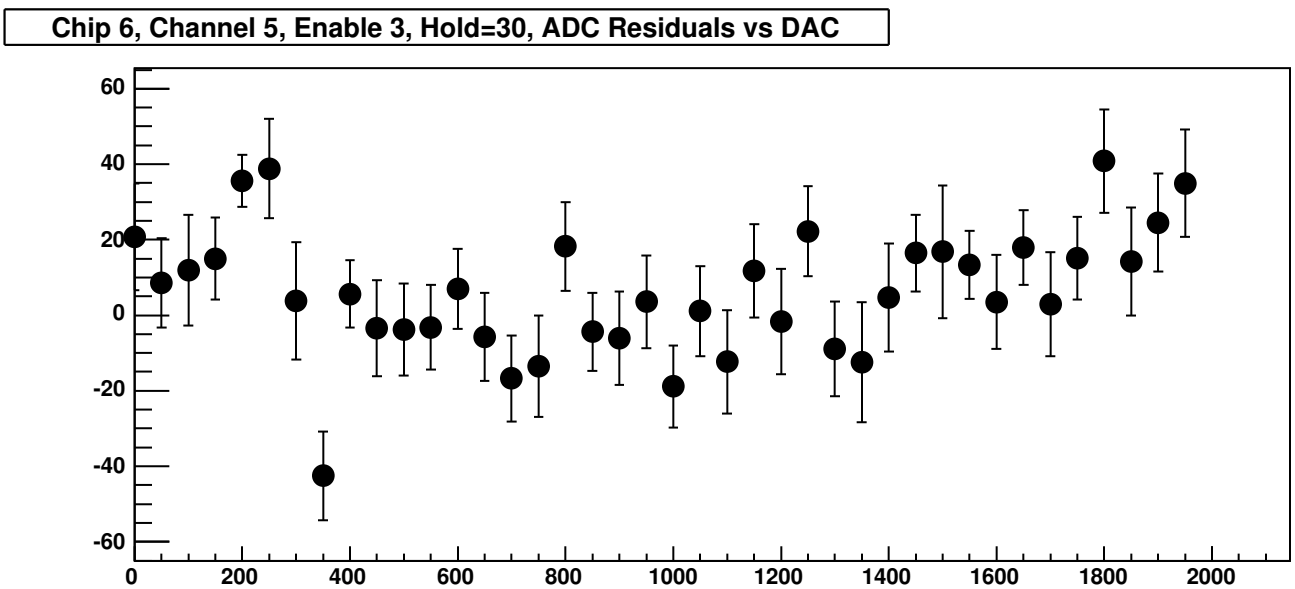


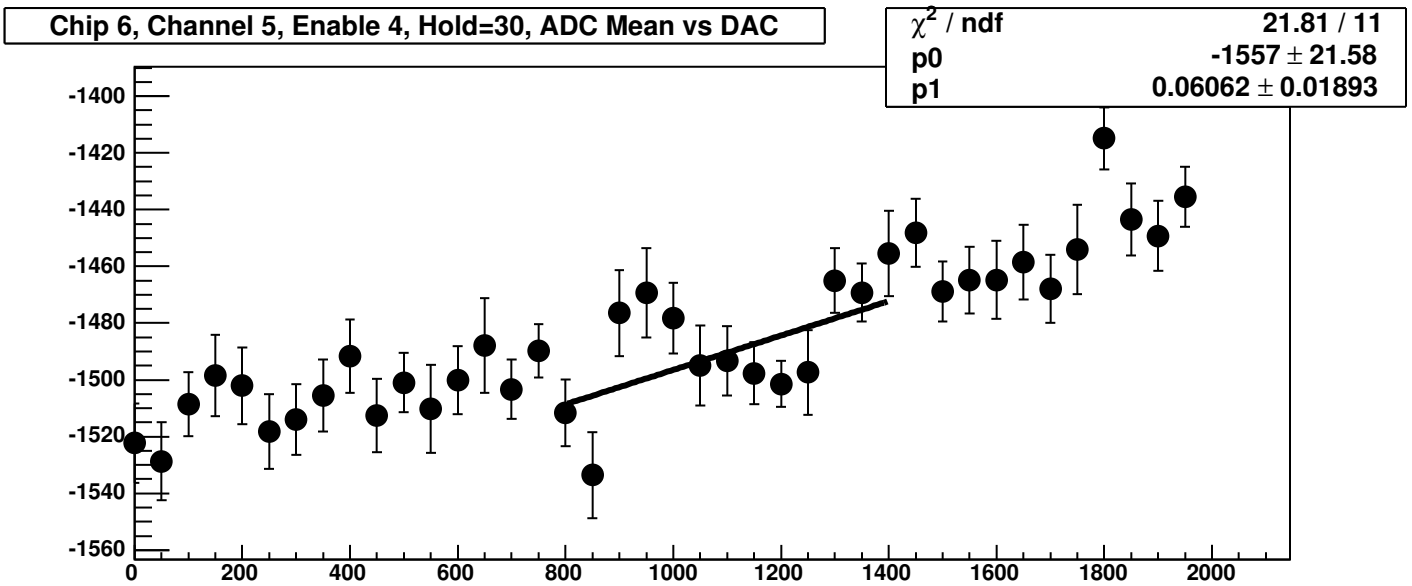
Chip 6, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC

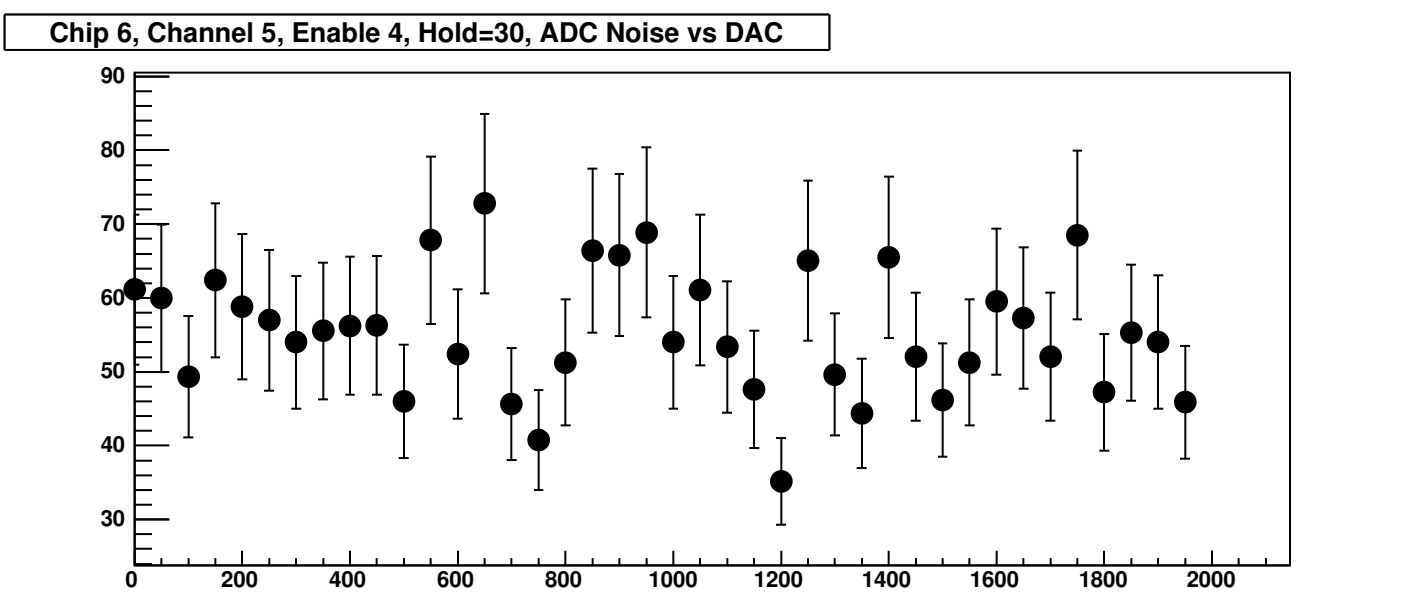


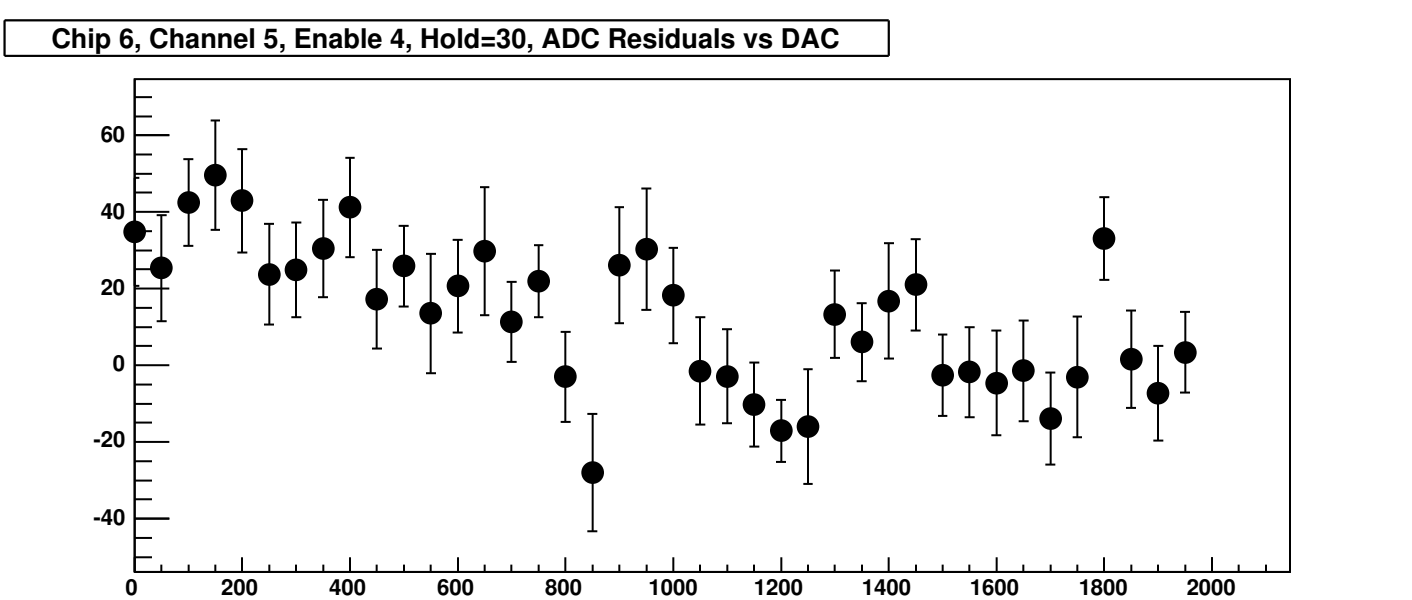

Chip 6, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 12.39 / 11
p0 -1525 ± 20.38
p1 0.01298 ± 0.01874
-1440
-1460
-1480
-1500
-1520
-1540
-1560
-1580
0
200
400
600
800
1000
1200
1400
1600
1800
2000

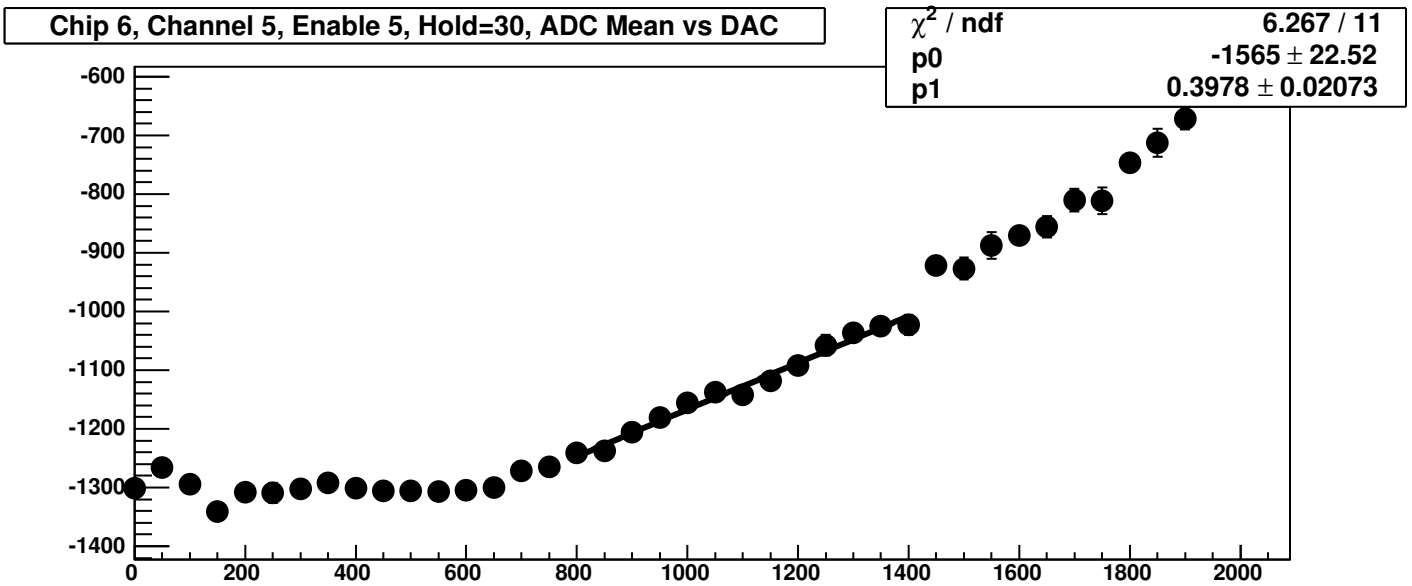

Chip 6, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

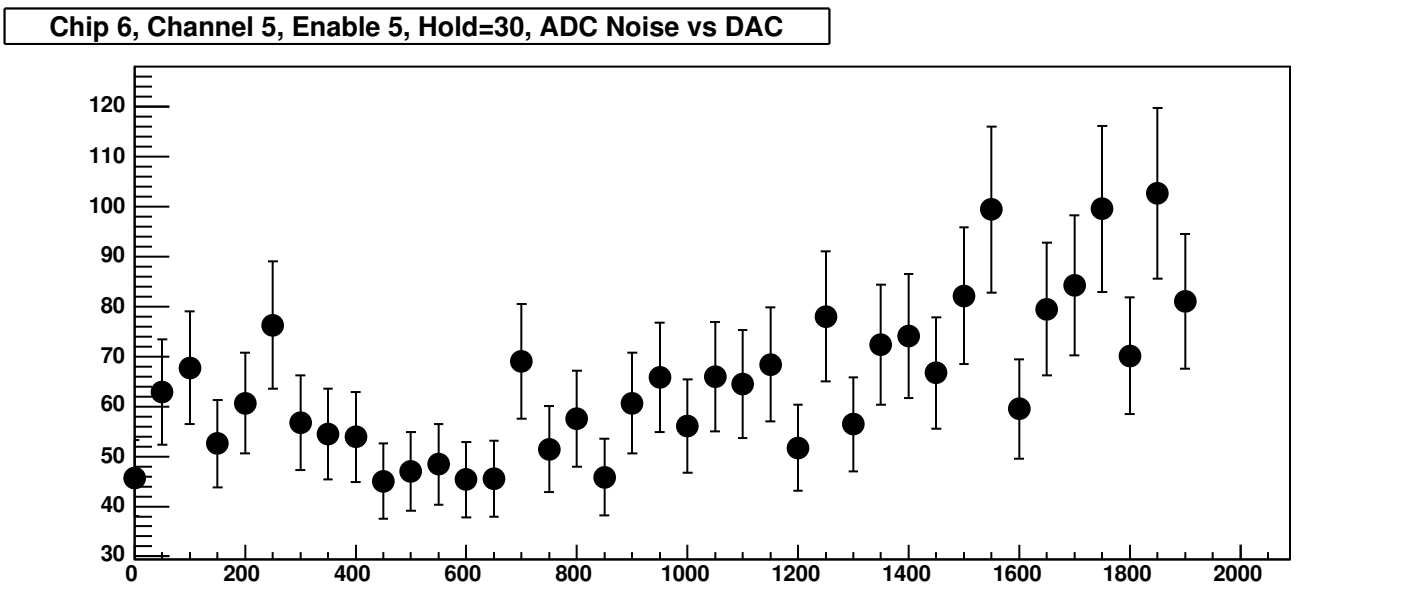

Chip 6, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

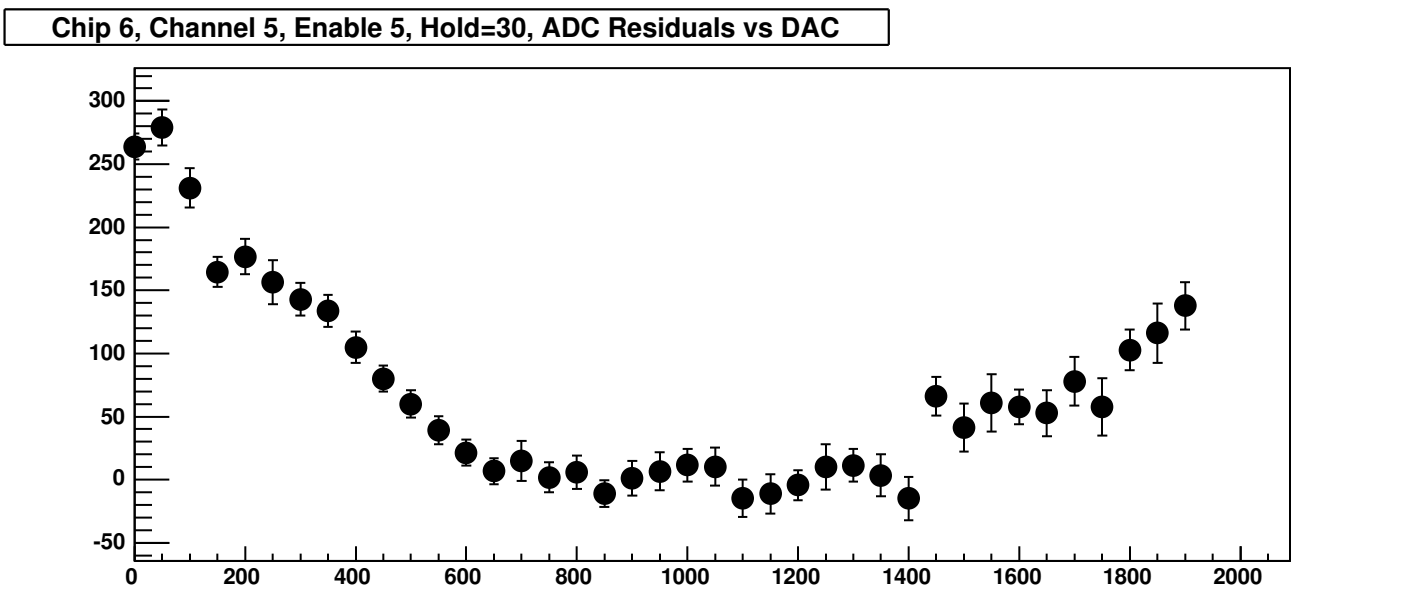

Chip 6, Channel 5, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 21.81 / 11
p0 -1557 ± 21.58
p1 0.06062 ± 0.01893
-1400
-1420
-1440
-1460
-1480
-1500
-1520
-1540
-1560
0
200
400
600
800
1000
1200
1400
1600
1800
2000

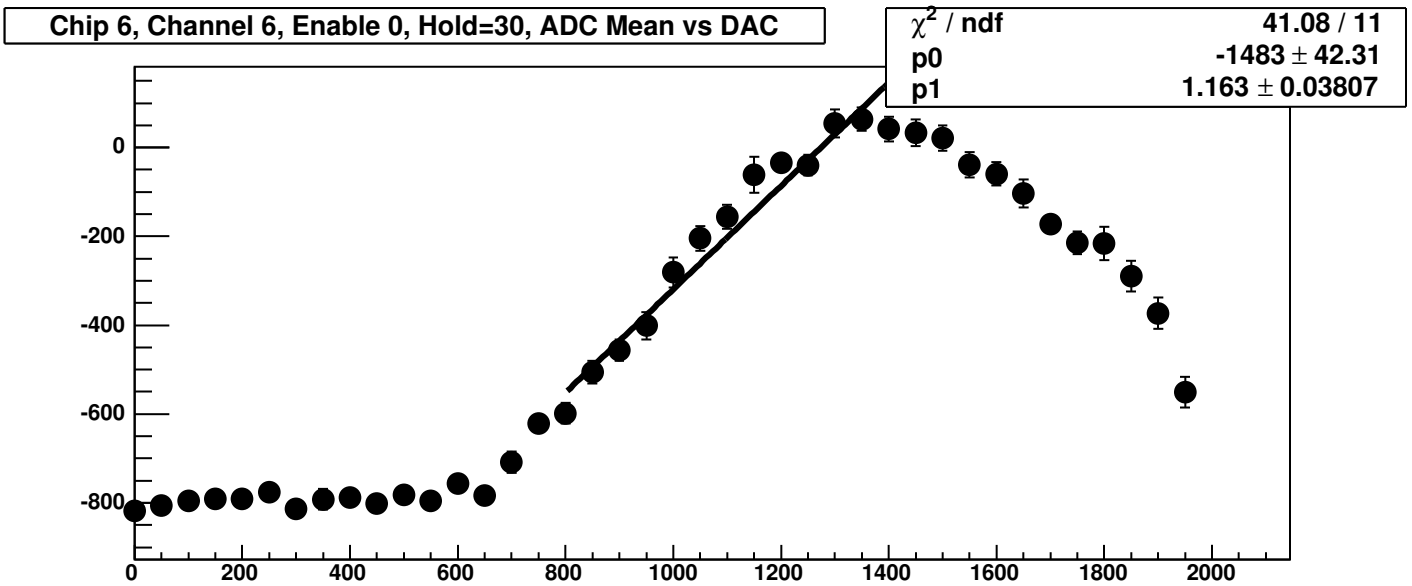

Chip 6, Channel 5, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

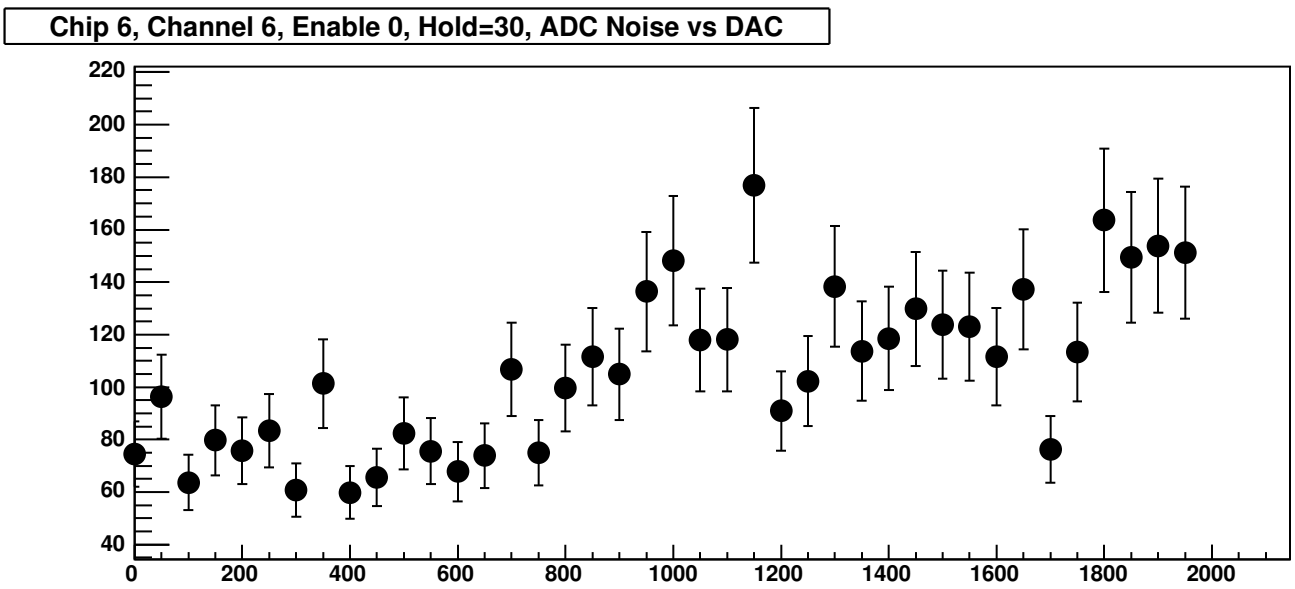

Chip 6, Channel 5, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

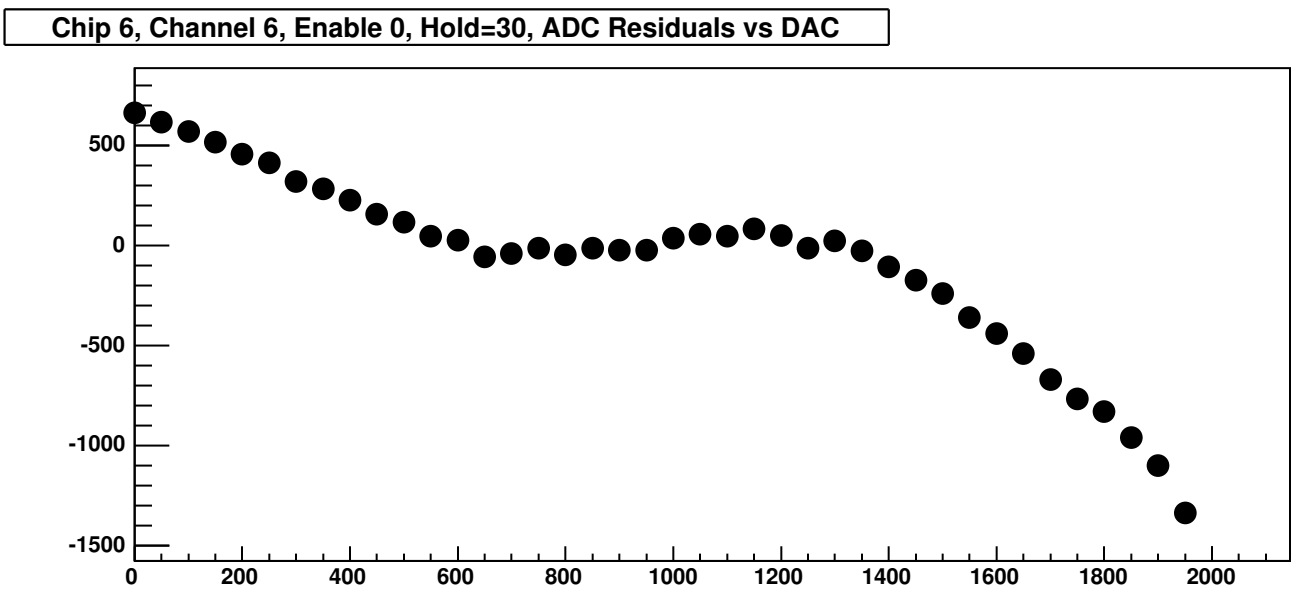

Chip 6, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 6.267 / 11
p0 -1565 ± 22.52
p1 0.3978 ± 0.02073
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

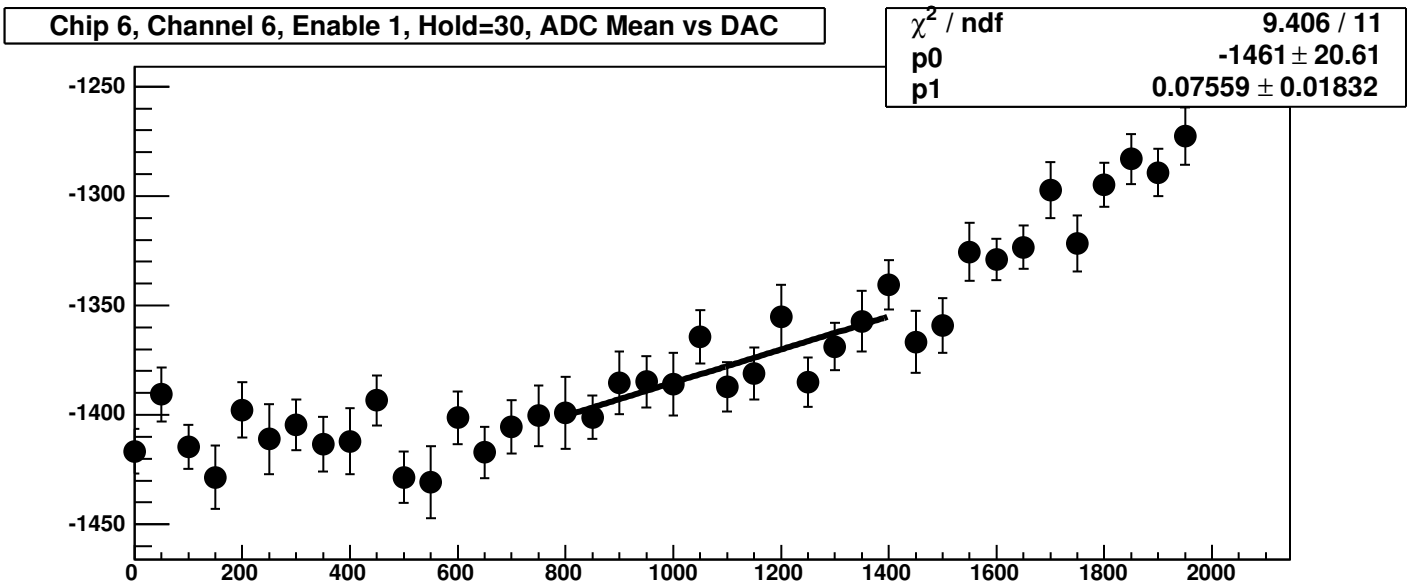

Chip 6, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
120
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

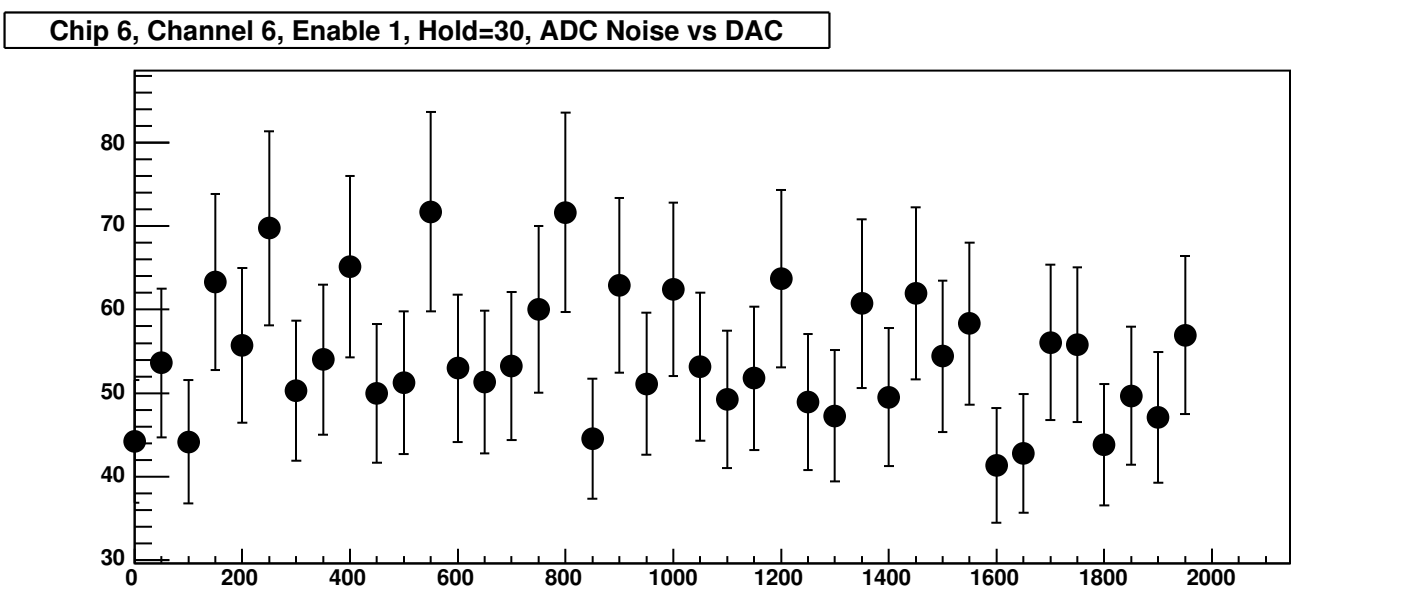

Chip 6, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

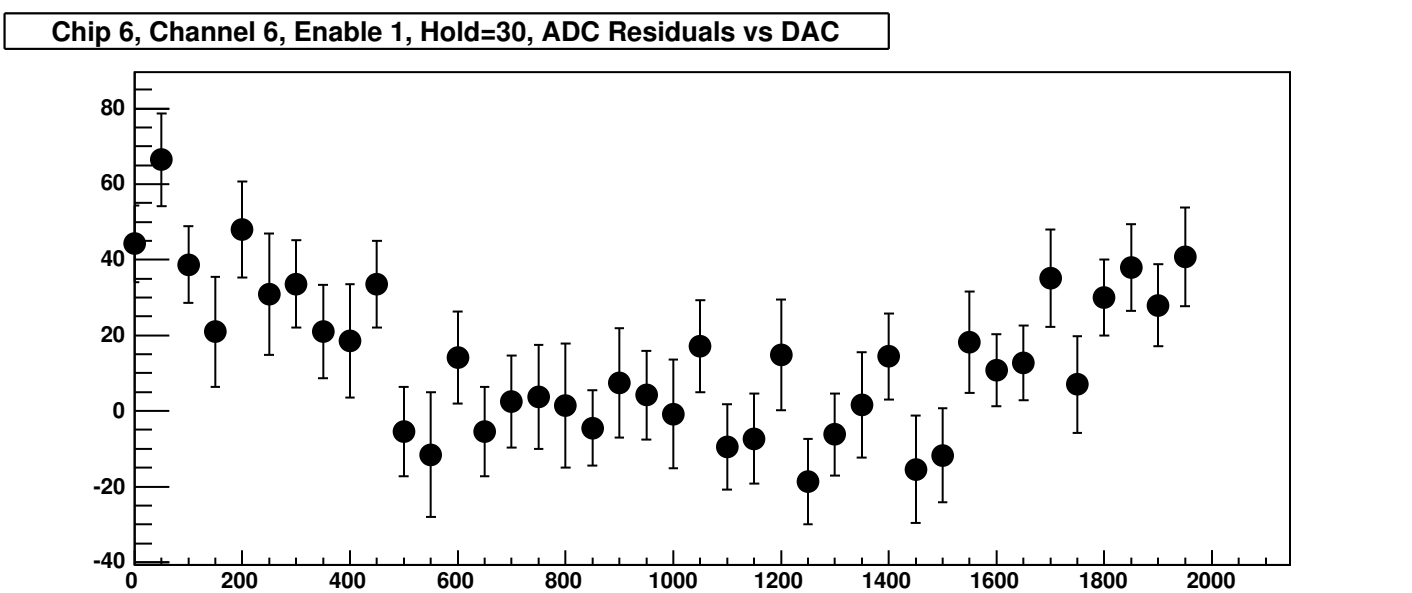

Chip 6, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 41.08 / 11
p0 -1483 ± 42.31
p1 1.163 ± 0.03807
0
-200
-400
-600
-800
0 200 400 600 800 1000 1200 1400 1600 1800 2000

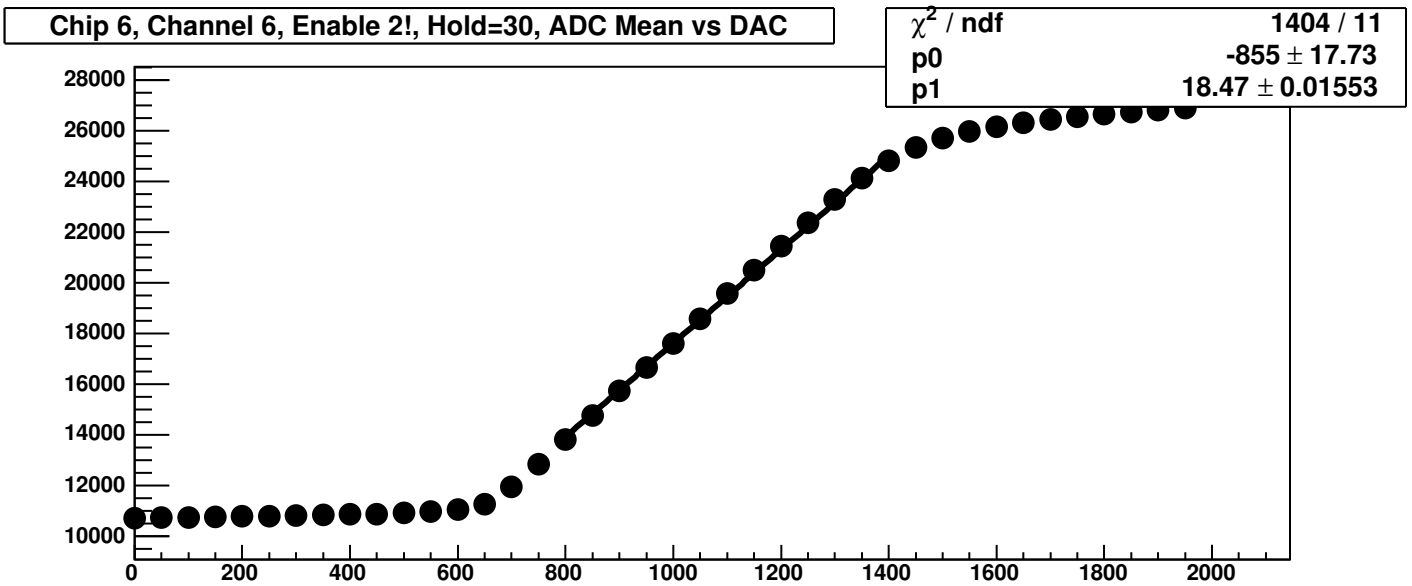

Chip 6, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
0 200 400 600 800 1000 1200 1400 1600 1800 2000

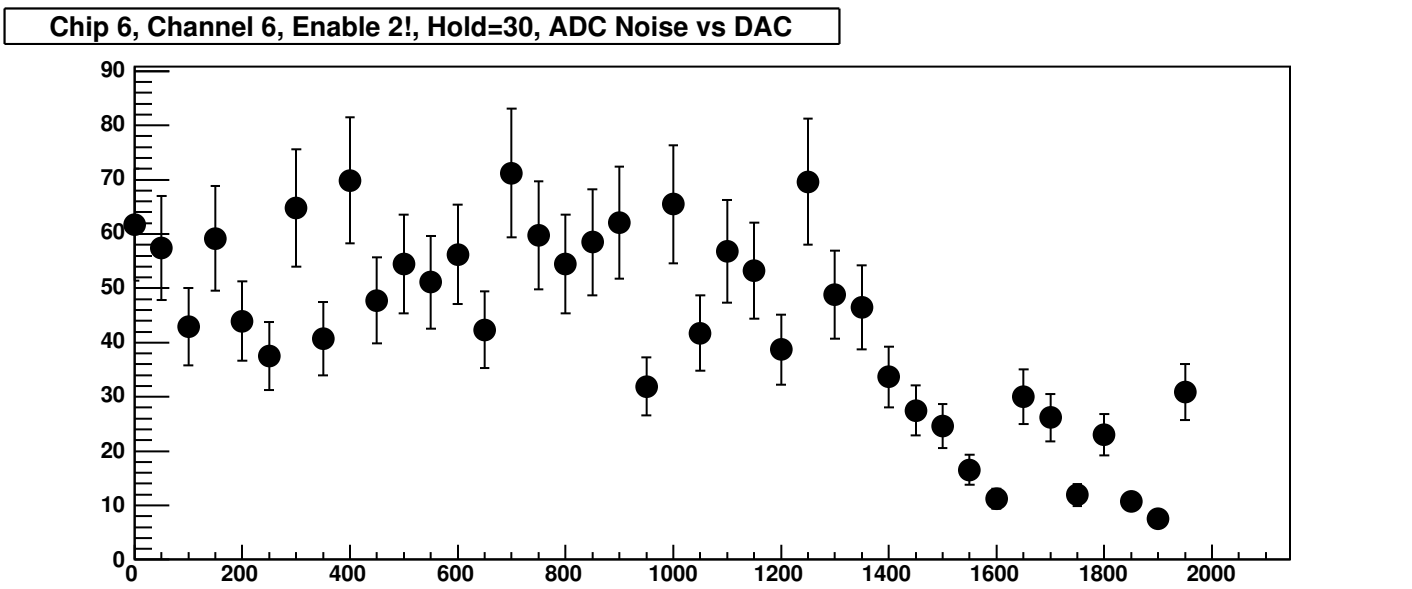

Chip 6, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC
500
0
-500
-1000
-1500
0 200 400 600 800 1000 1200 1400 1600 1800 2000

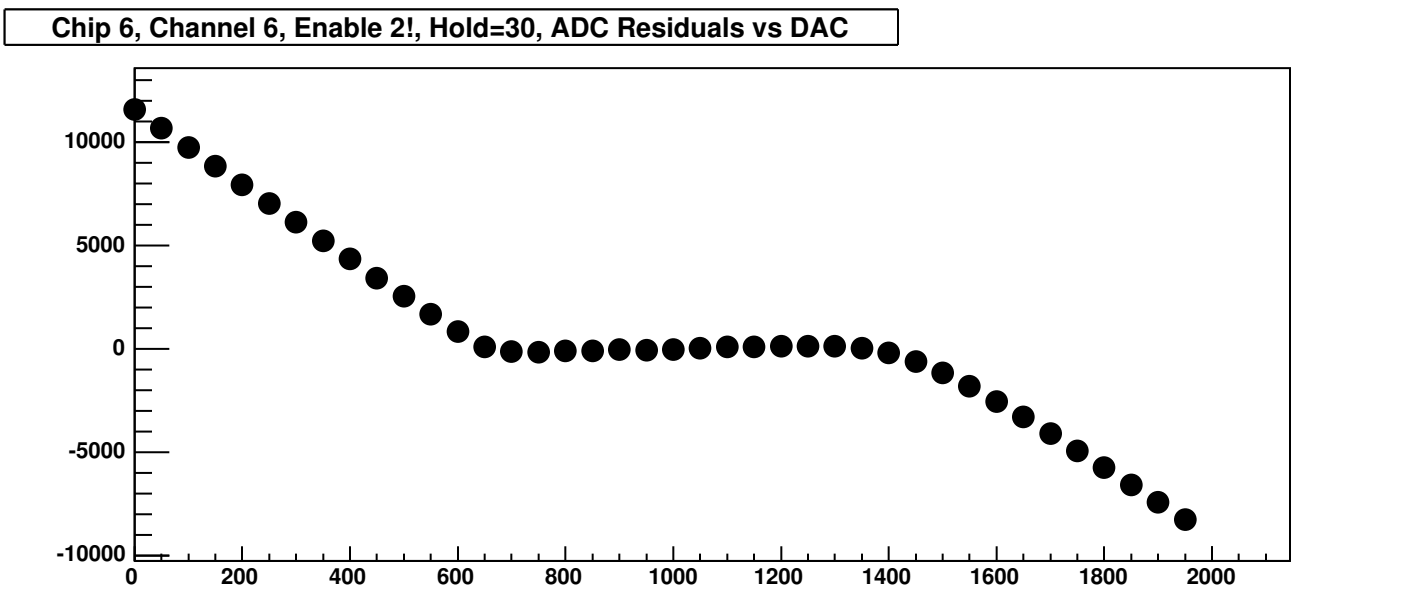

Chip 6, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 9.406 / 11
p0 -1461 ± 20.61
p1 0.07559 ± 0.01832
-1250
-1300
-1350
-1400
-1450
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

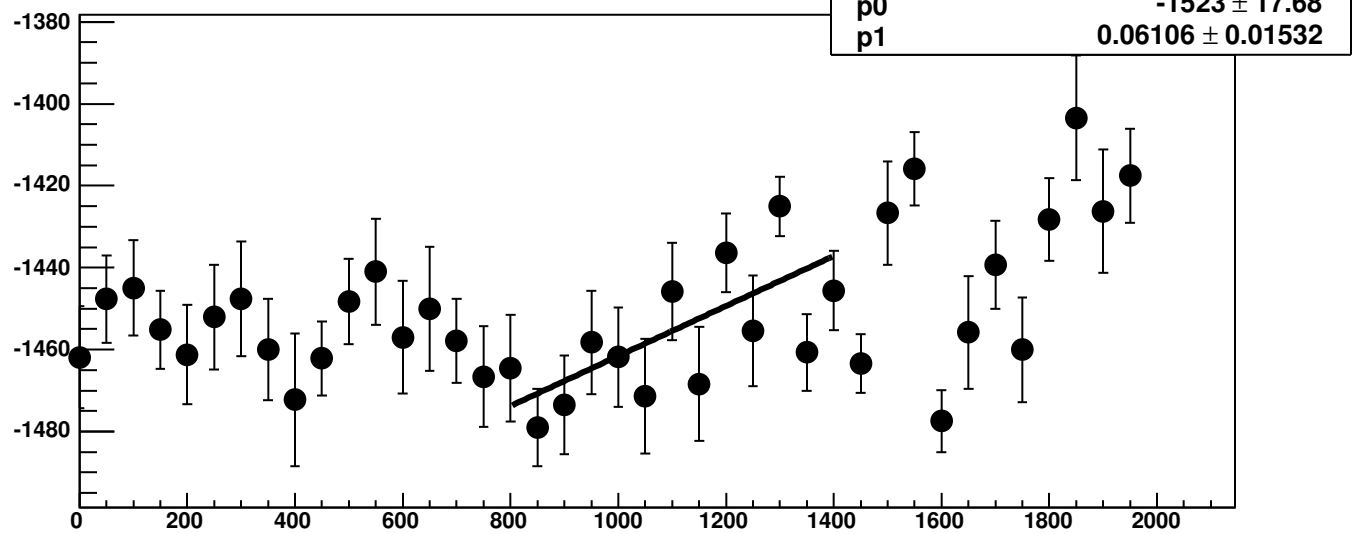

Chip 6, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 6, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 1404 / 11
p0 -855 ± 17.73
p1 18.47 ± 0.01553
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

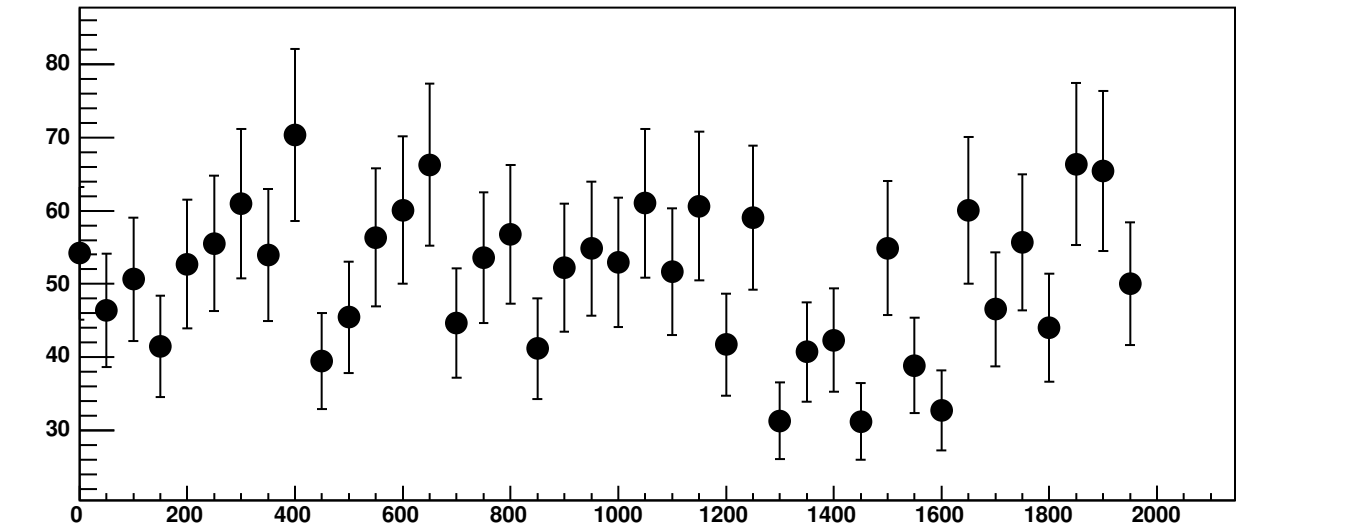

Chip 6, Channel 6, Enable 2!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 6, Enable 2!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

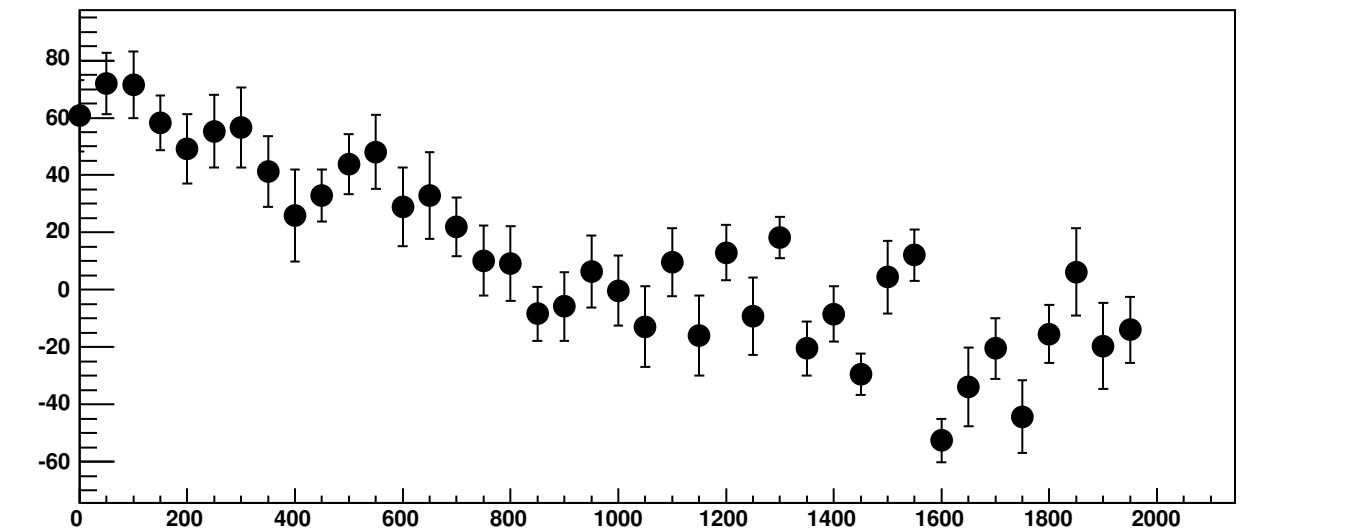
Chip 6, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC

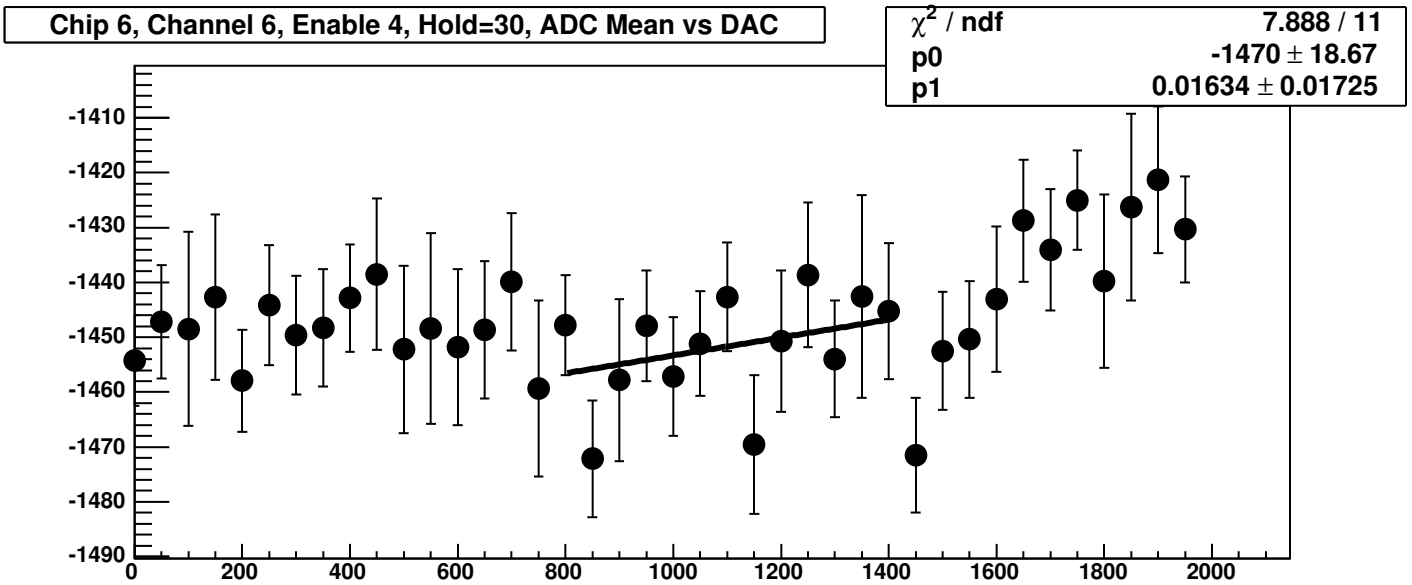


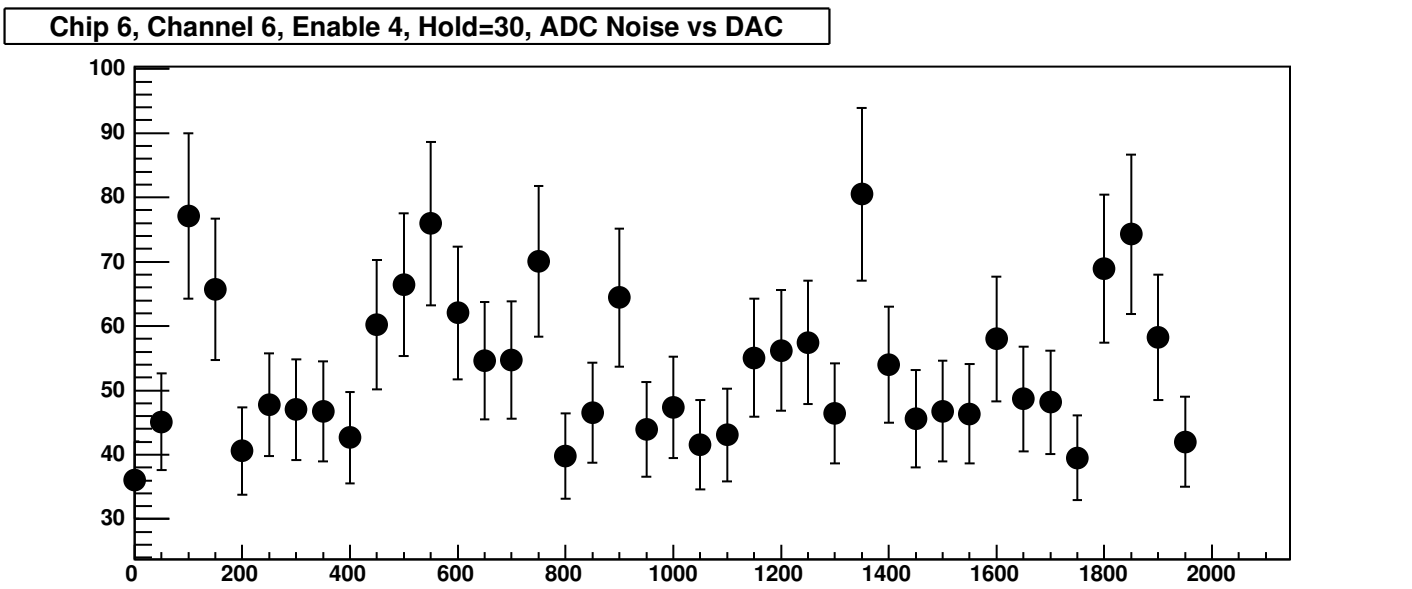
Chip 6, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC

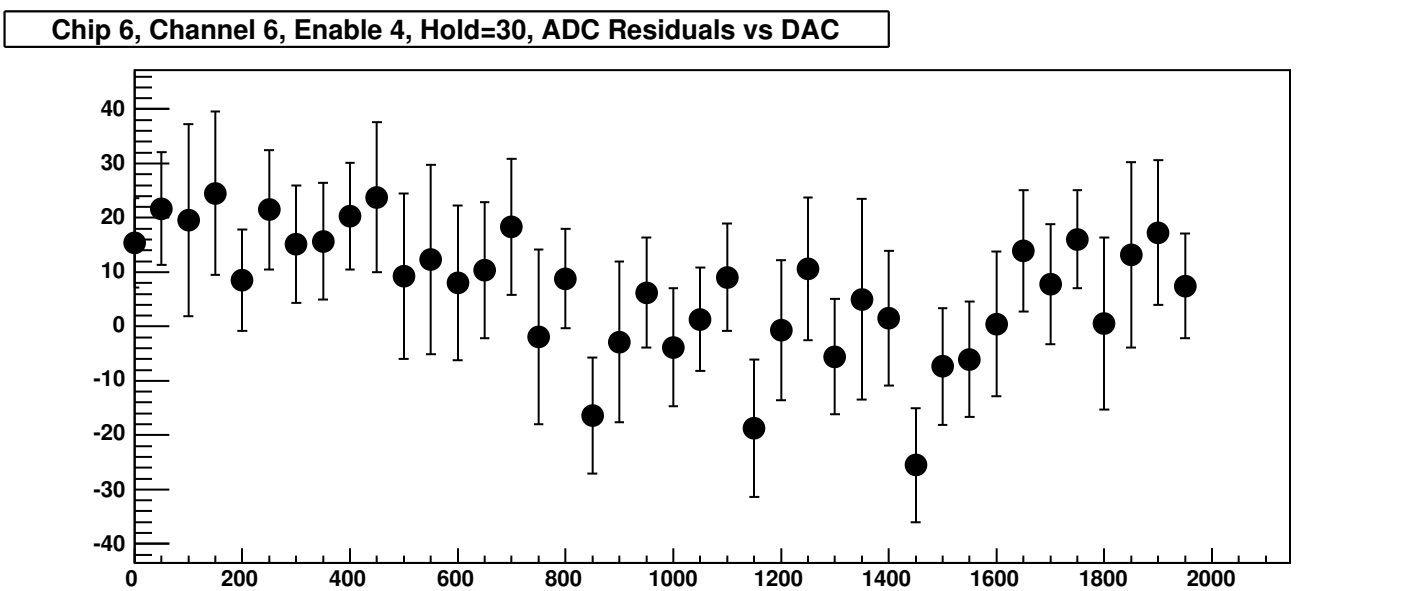


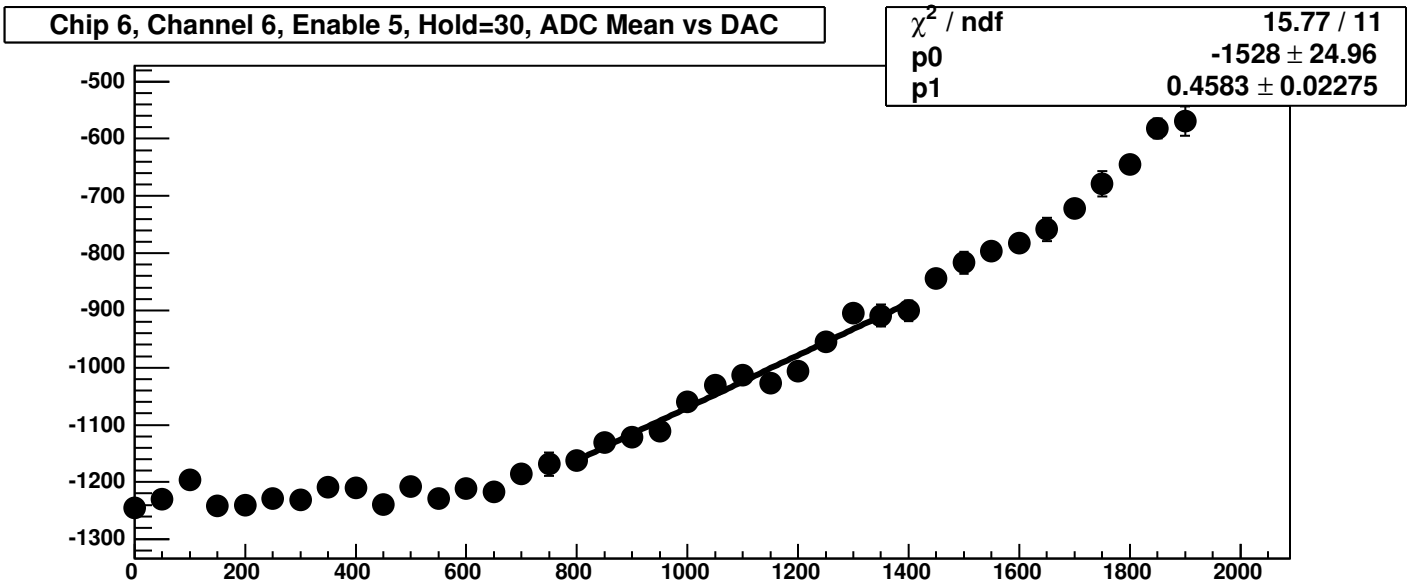
Chip 6, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC

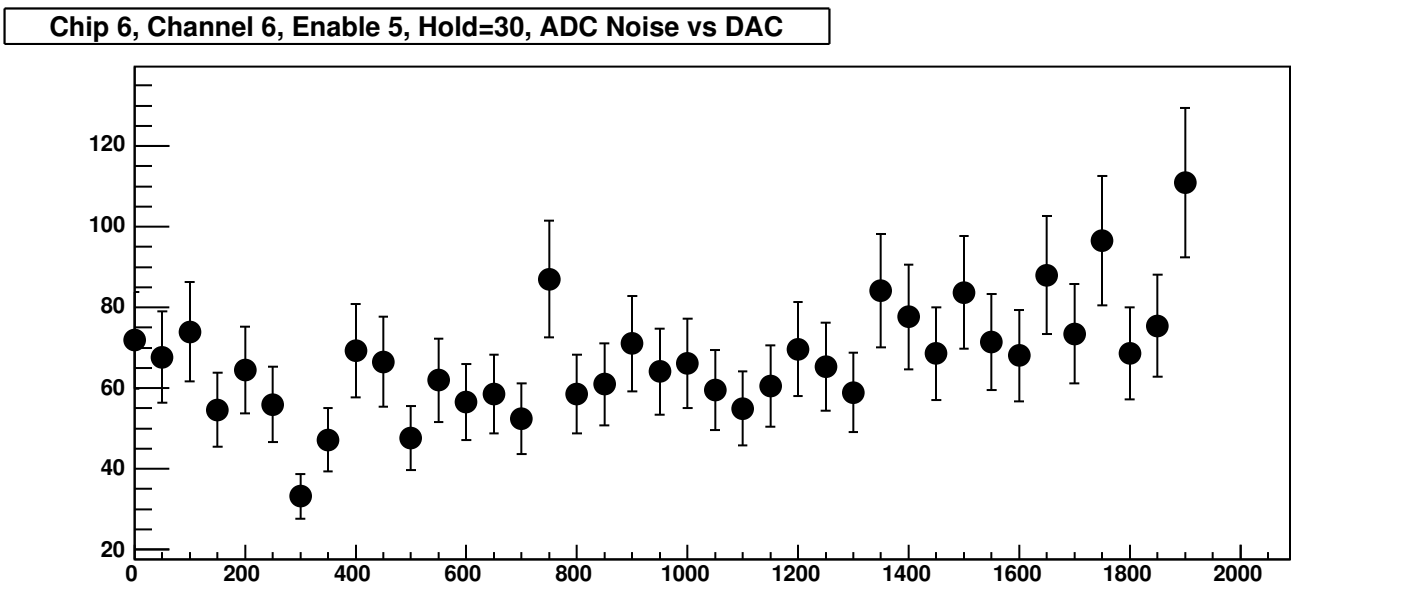


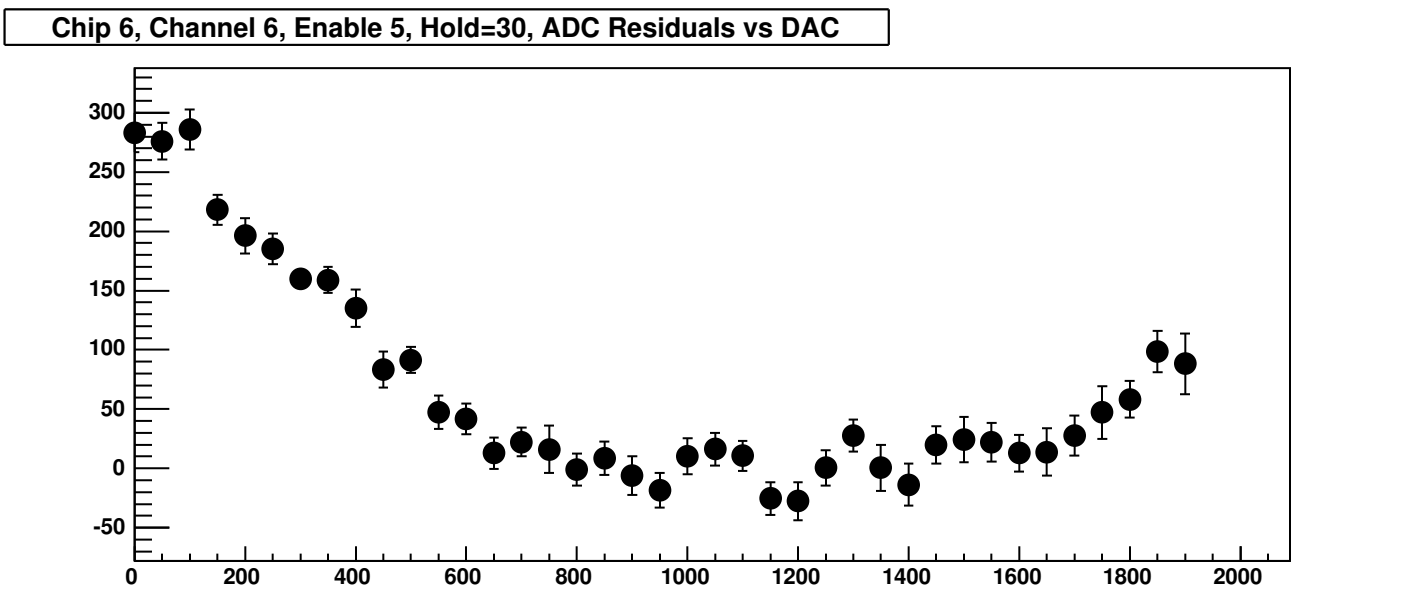

Chip 6, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7.888 / 11
p0 -1470 ± 18.67
p1 0.01634 ± 0.01725
-1410
-1420
-1430
-1440
-1450
-1460
-1470
-1480
-1490
0
200
400
600
800
1000
1200
1400
1600
1800
2000

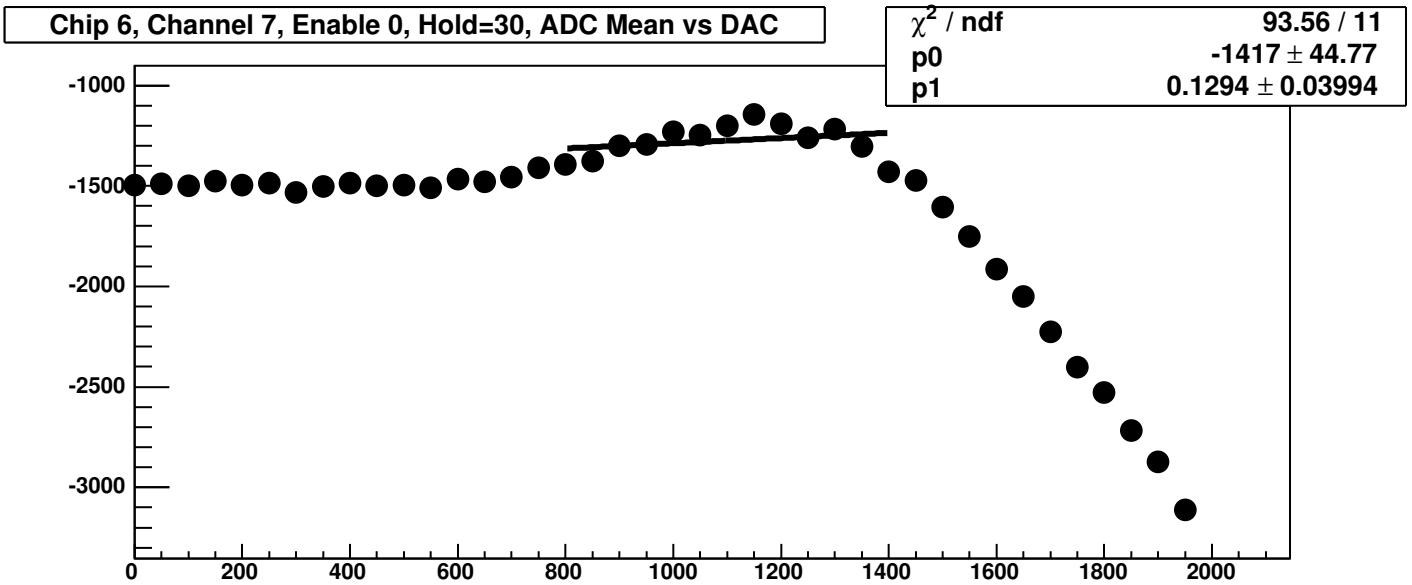

Chip 6, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

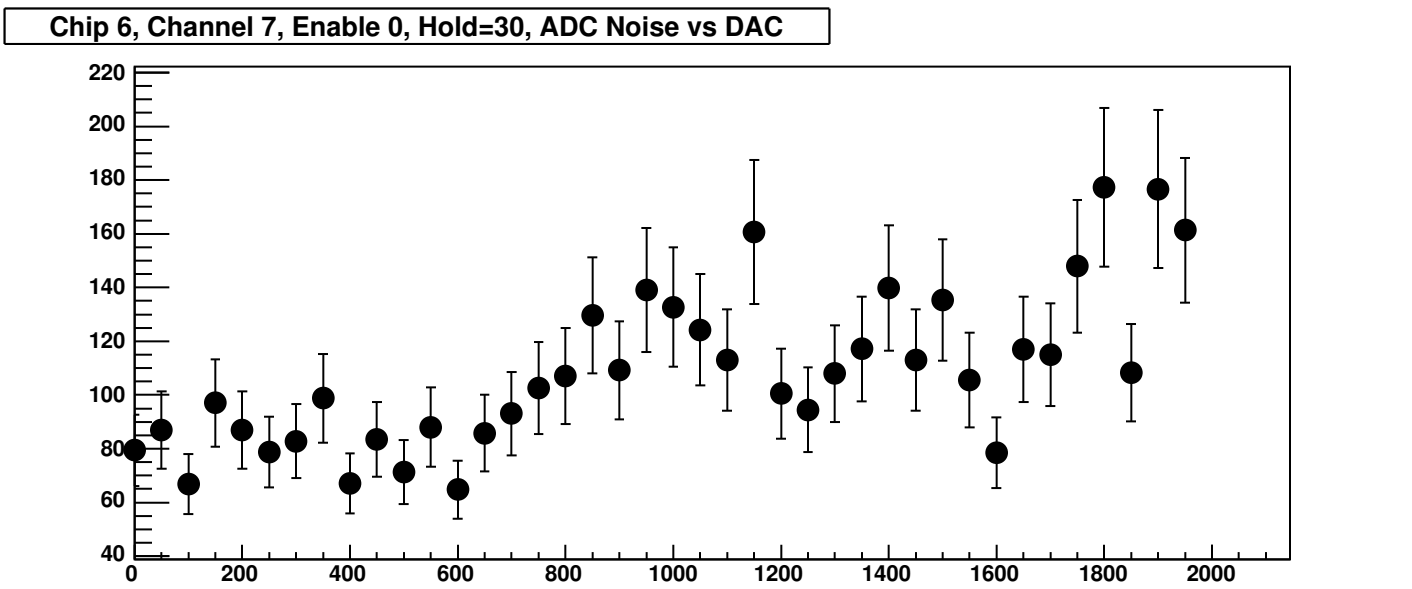

Chip 6, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC
40
30
20
10
0
-10
-20
-30
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

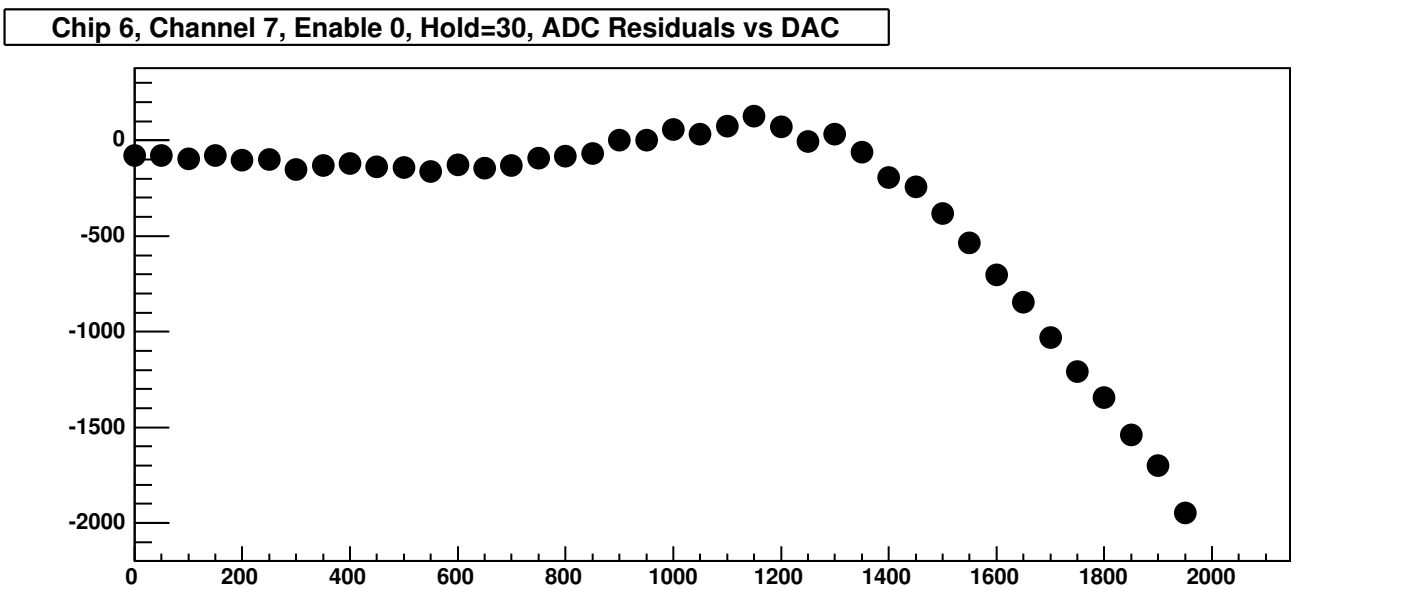

Chip 6, Channel 6, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 15.77 / 11
p0 -1528 ± 24.96
p1 0.4583 ± 0.02275
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

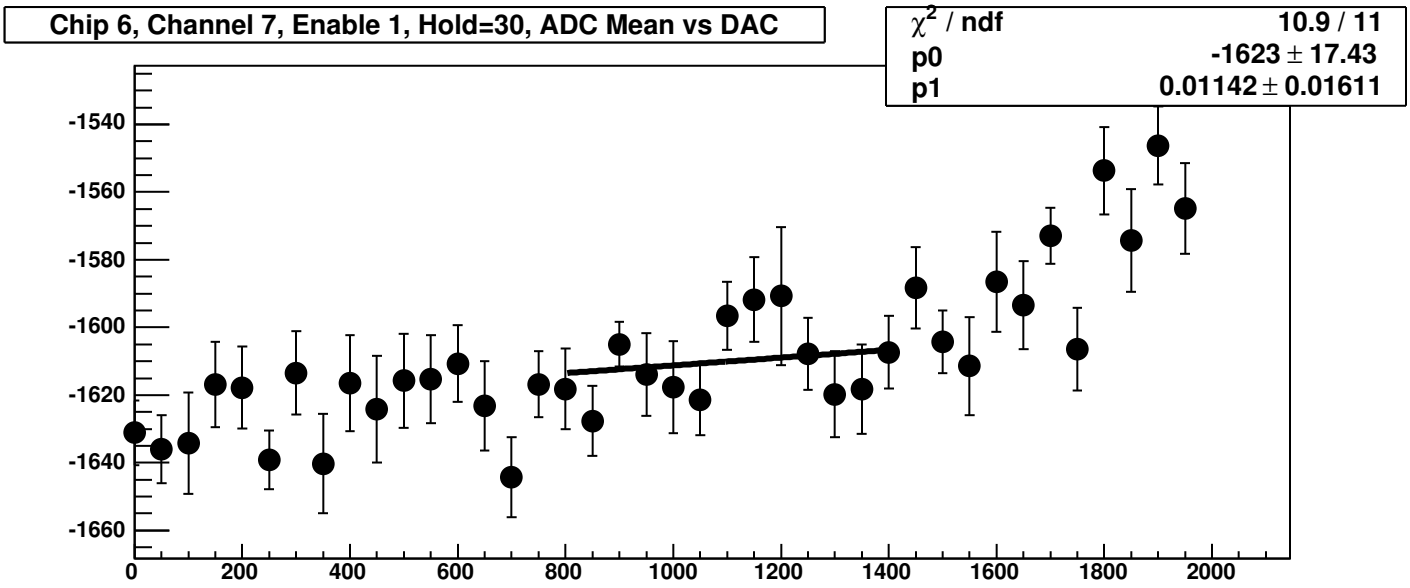

Chip 6, Channel 6, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

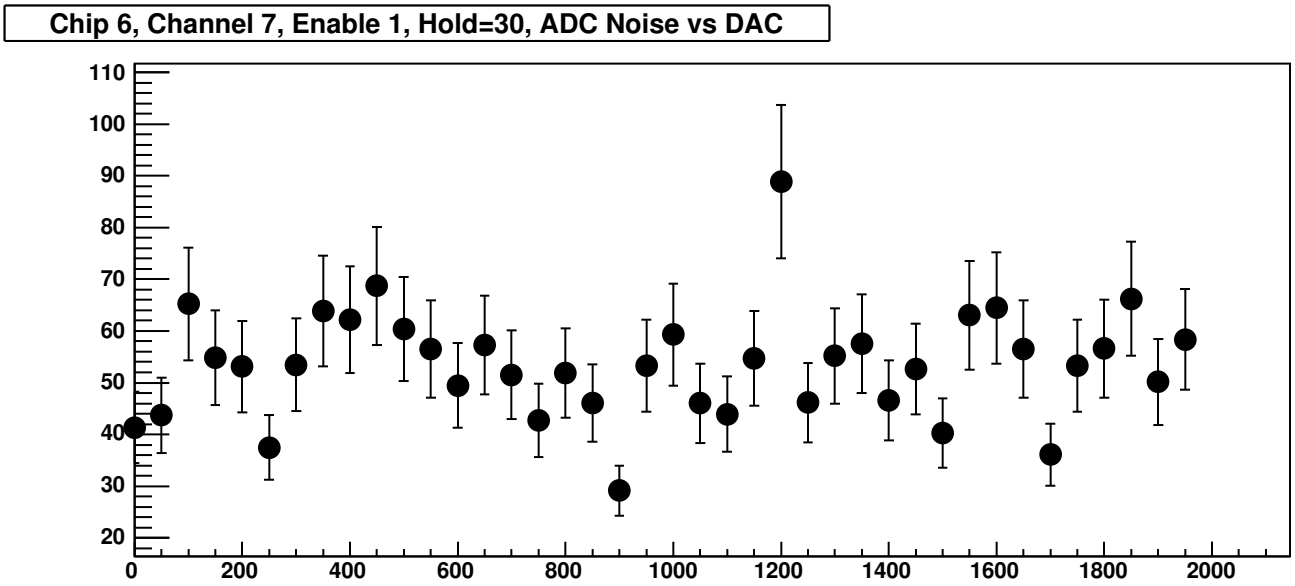

Chip 6, Channel 6, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

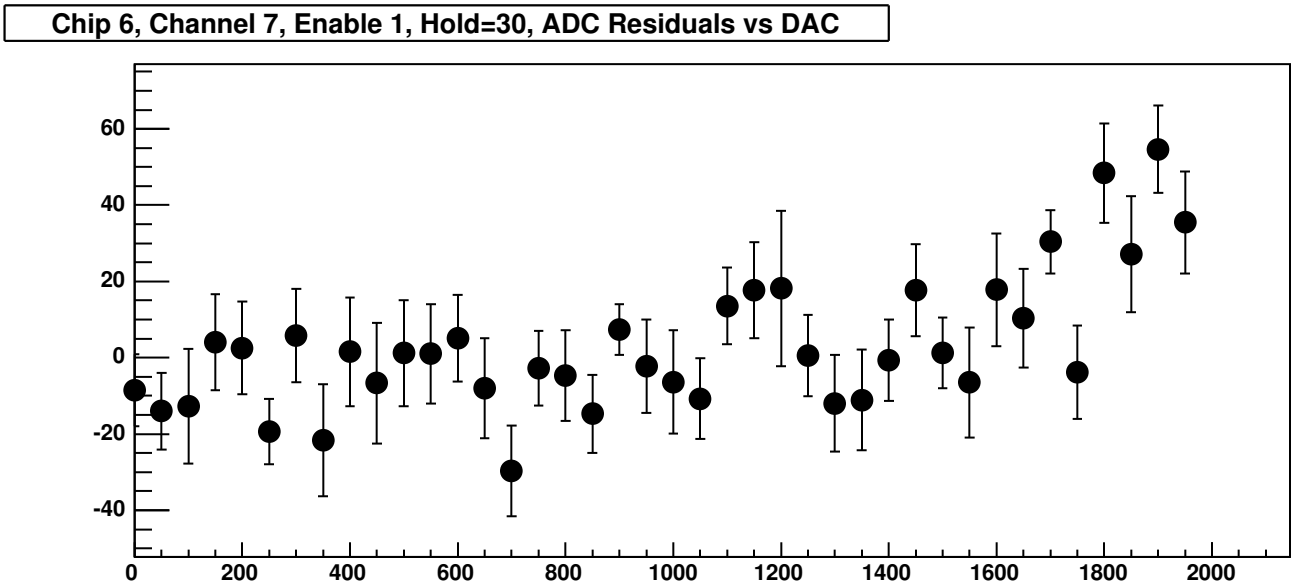

Chip 6, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC
χ^2 / ndf 93.56 / 11
p0 -1417 ± 44.77
p1 0.1294 ± 0.03994
-1000
-1500
-2000
-2500
-3000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

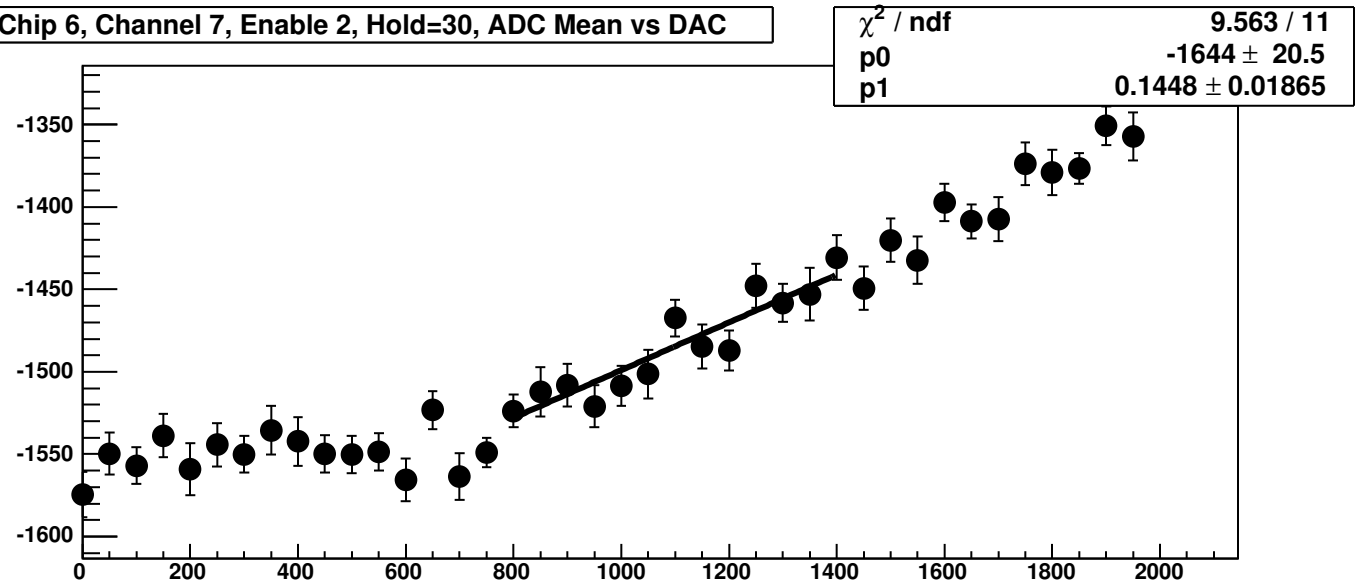

Chip 6, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 10.9 / 11
p0 -1623 ± 17.43
p1 0.01142 ± 0.01611
-1540
-1560
-1580
-1600
-1620
-1640
-1660
0
200
400
600
800
1000
1200
1400
1600
1800
2000

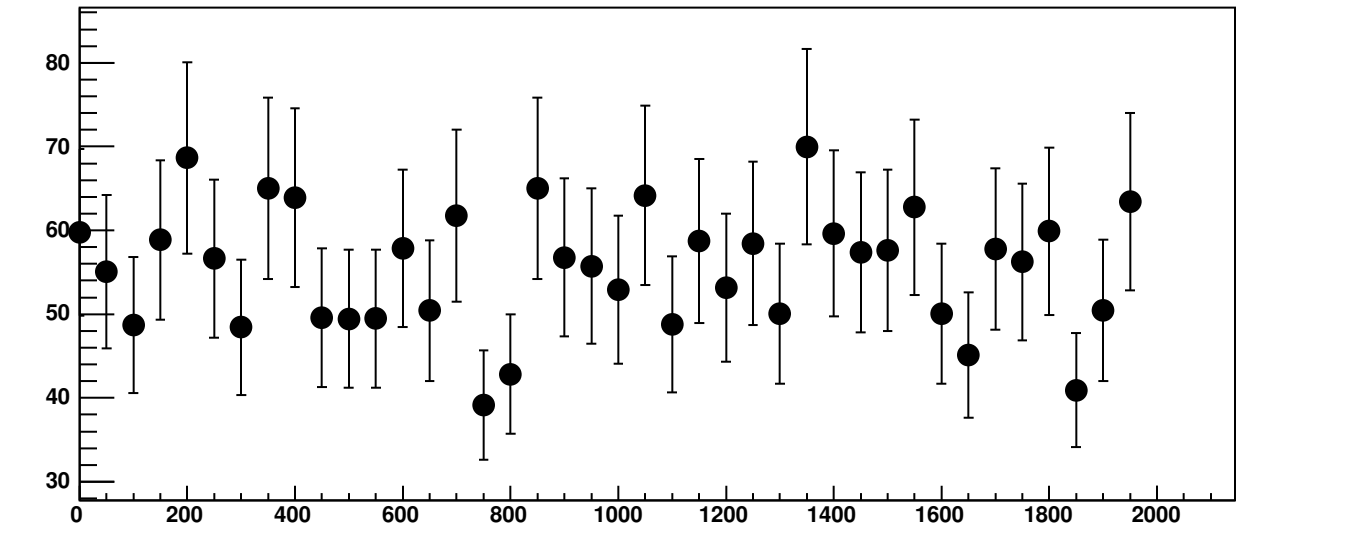

Chip 6, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

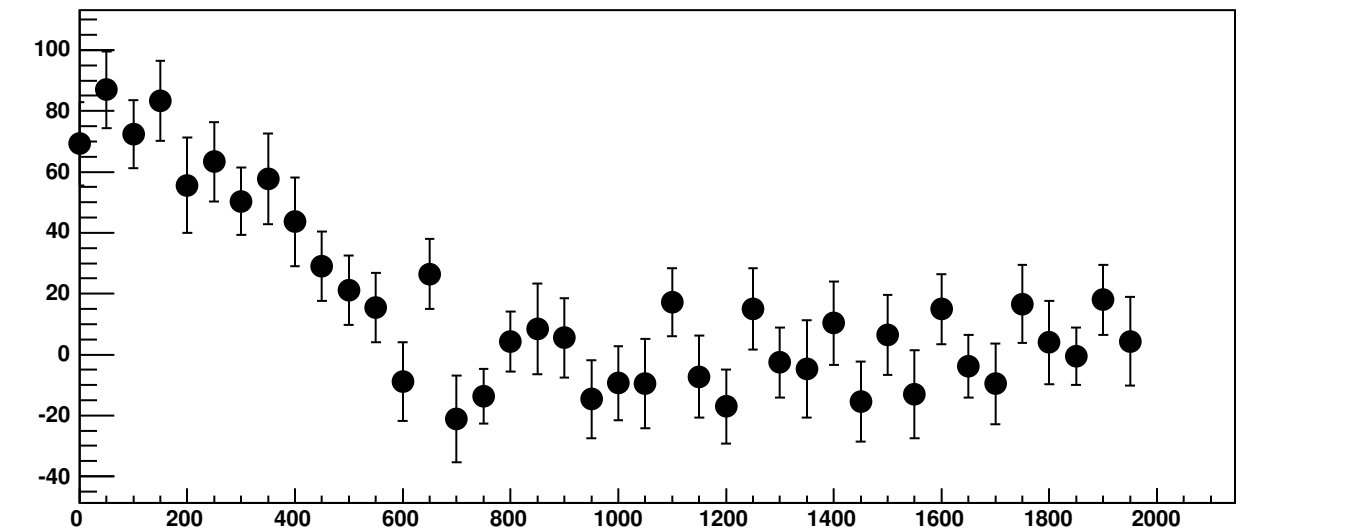
Chip 6, Channel 7, Enable 2, Hold=30, ADC Mean vs DAC

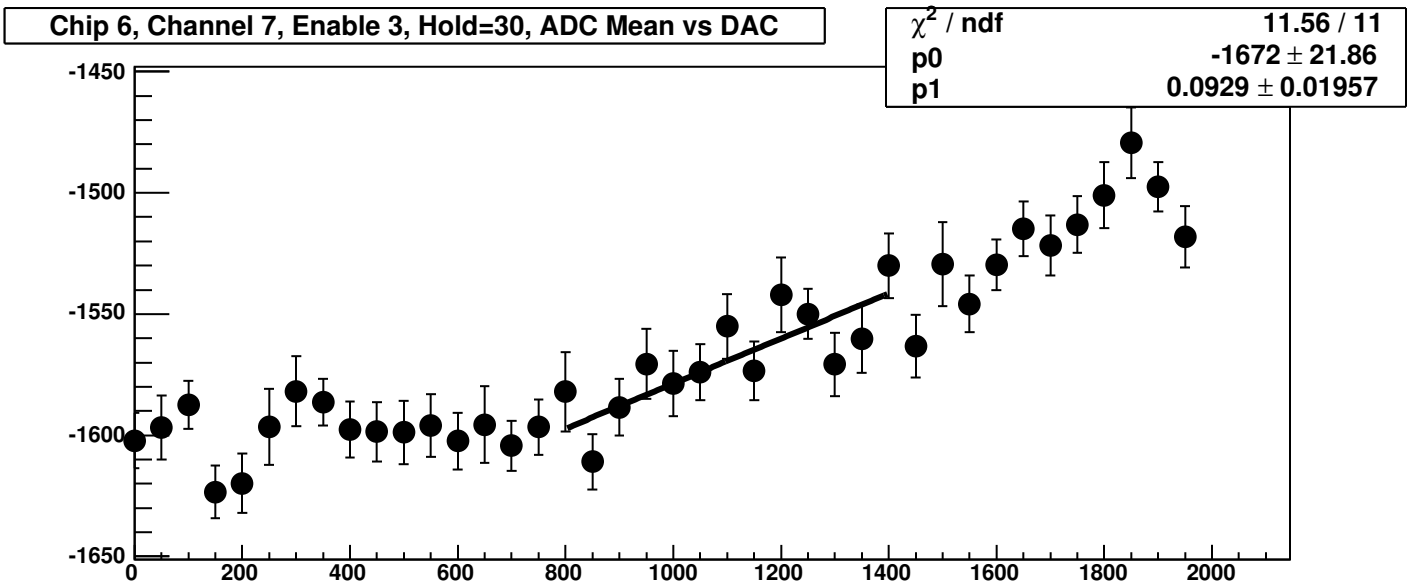


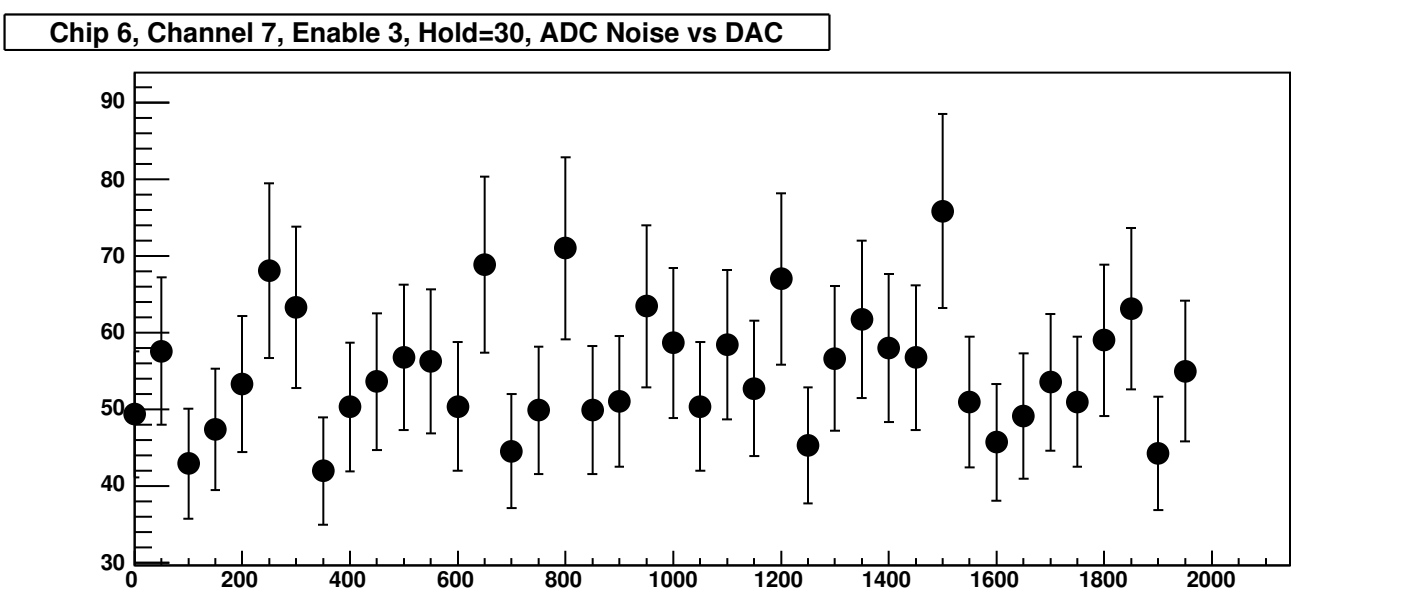
Chip 6, Channel 7, Enable 2, Hold=30, ADC Noise vs DAC

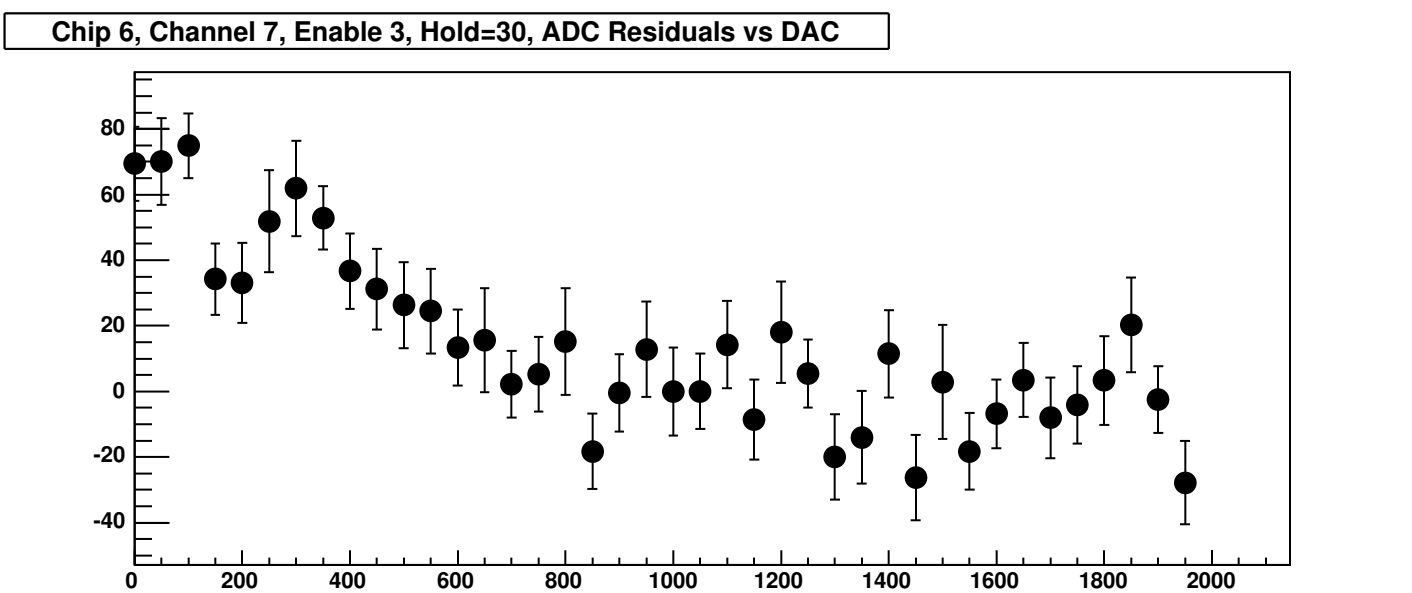


Chip 6, Channel 7, Enable 2, Hold=30, ADC Residuals vs DAC

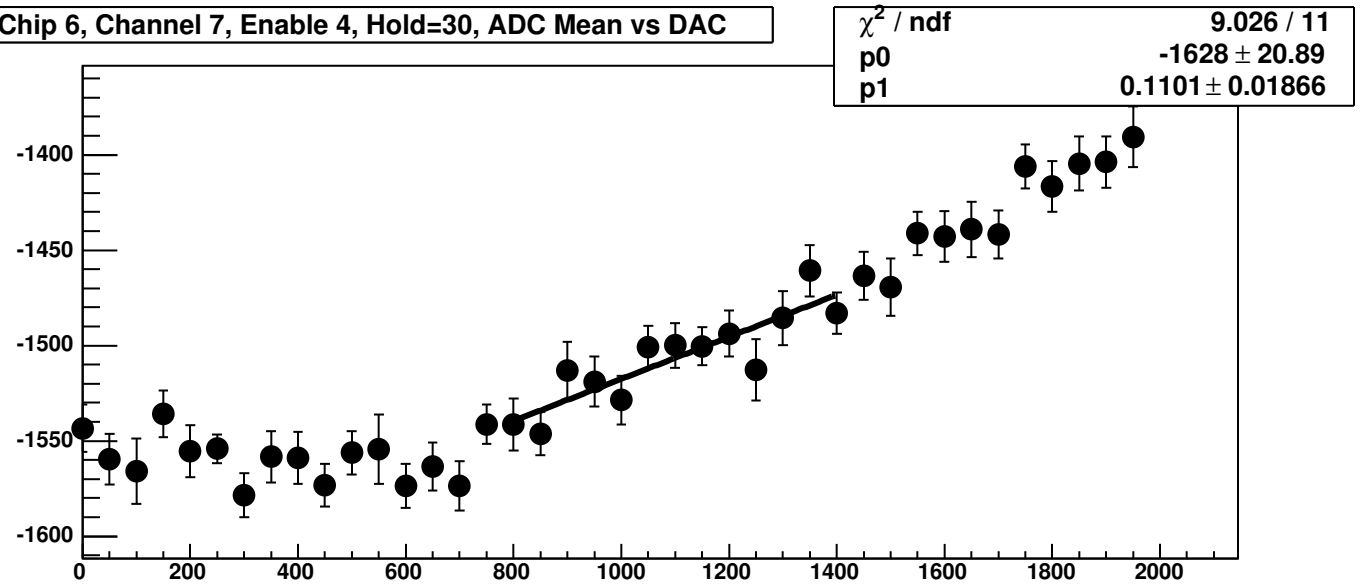



Chip 6, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 11.56 / 11
p0 -1672 ± 21.86
p1 0.0929 ± 0.01957
-1450
-1500
-1550
-1600
-1650
0
200
400
600
800
1000
1200
1400
1600
1800
2000

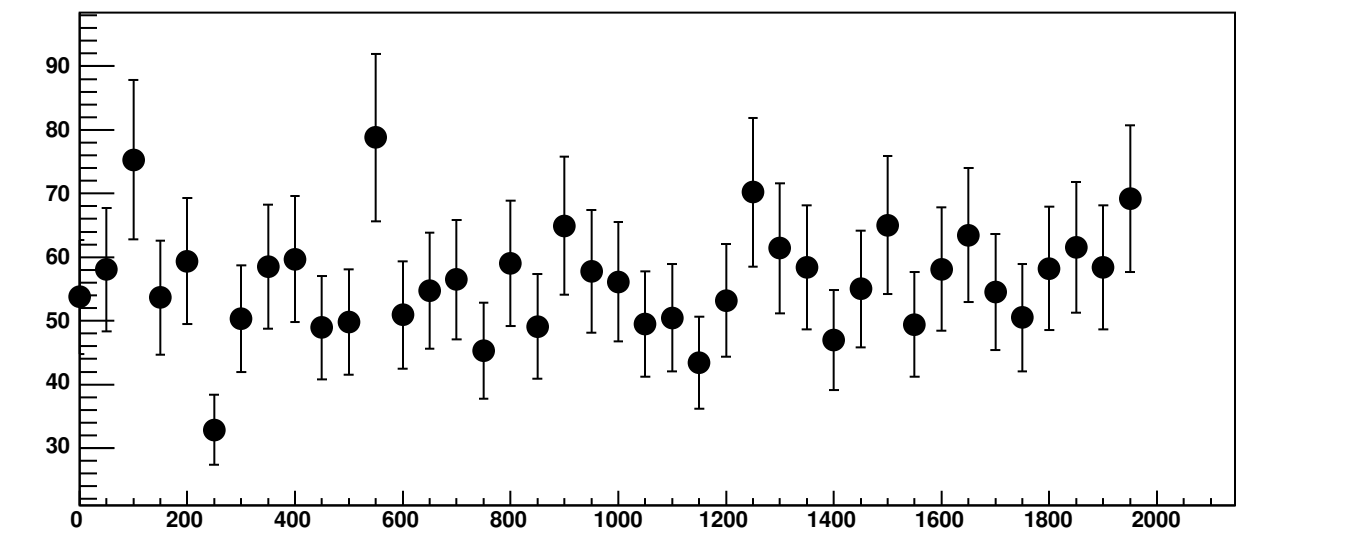

Chip 6, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

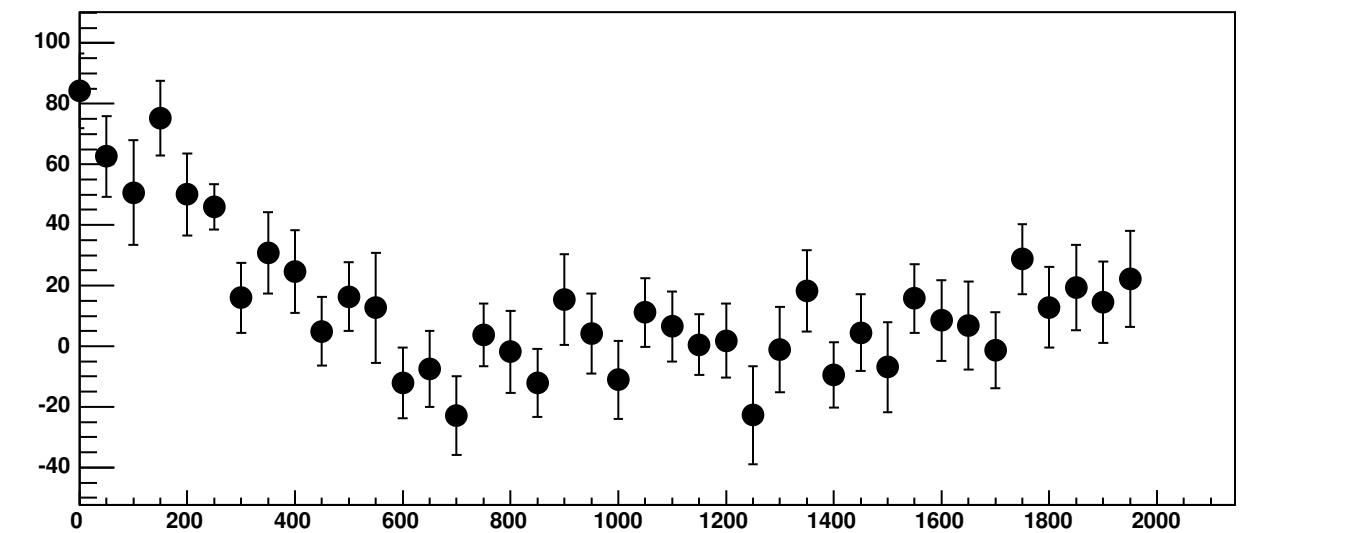
Chip 6, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC



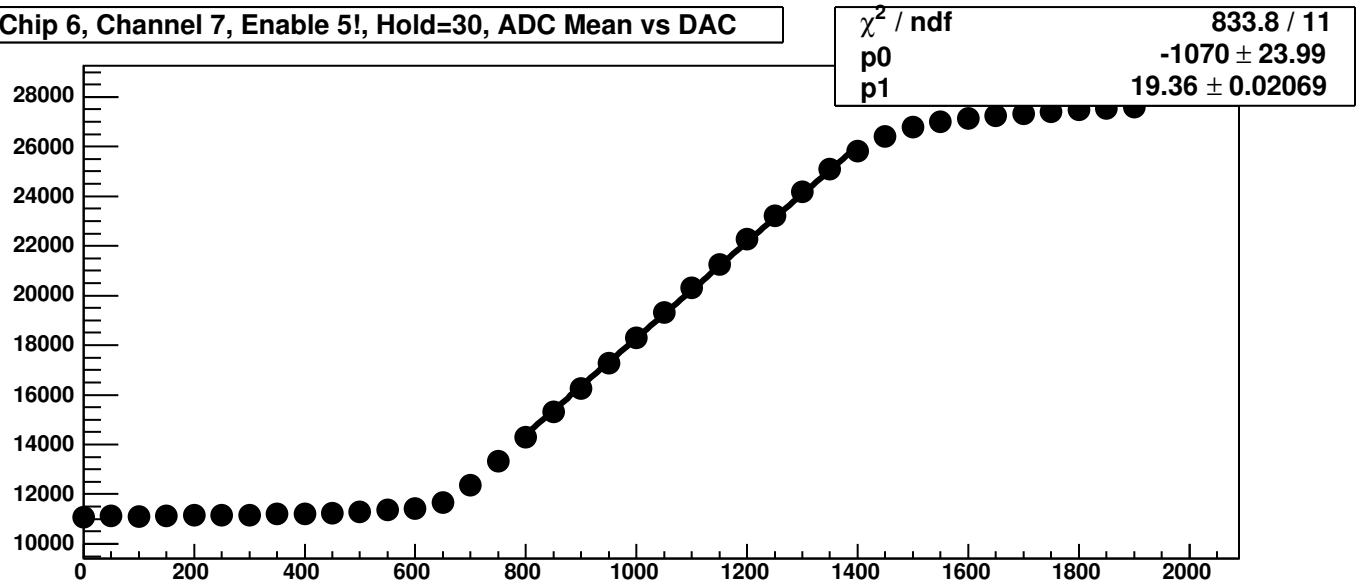
Chip 6, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC



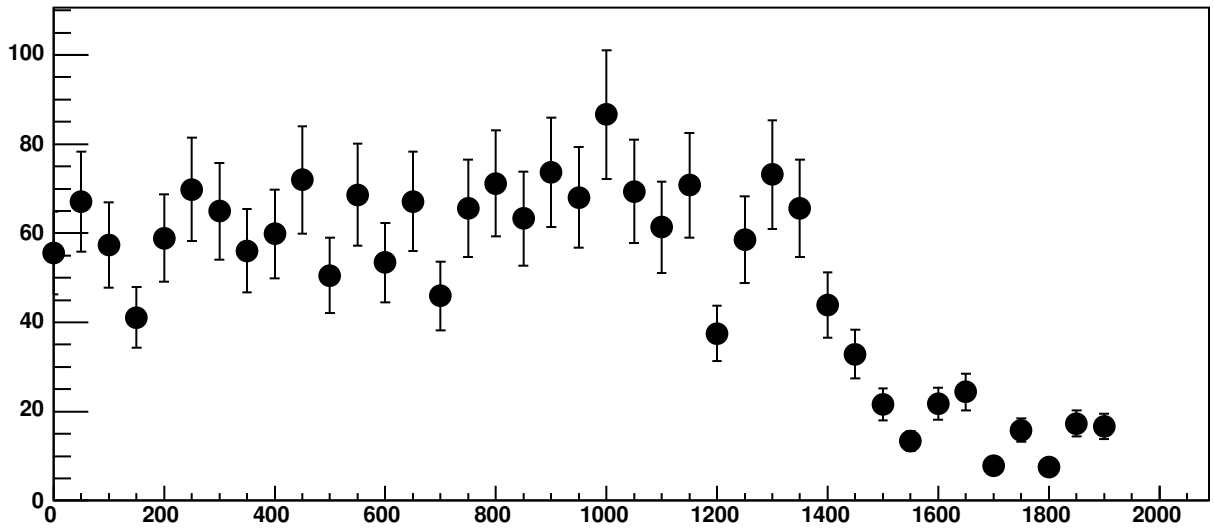
Chip 6, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC



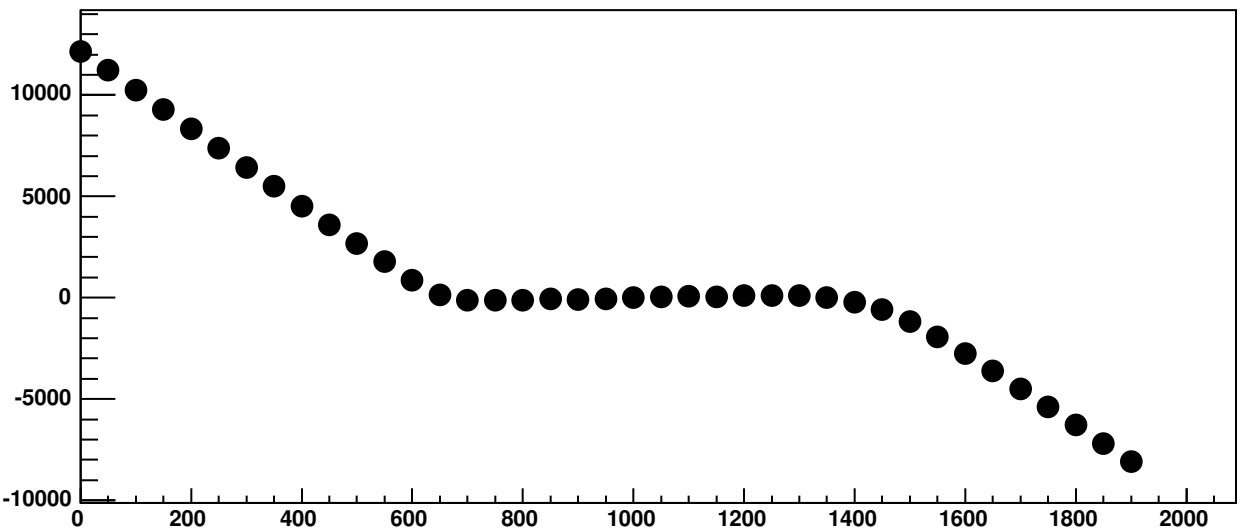
Chip 6, Channel 7, Enable 5!, Hold=30, ADC Mean vs DAC

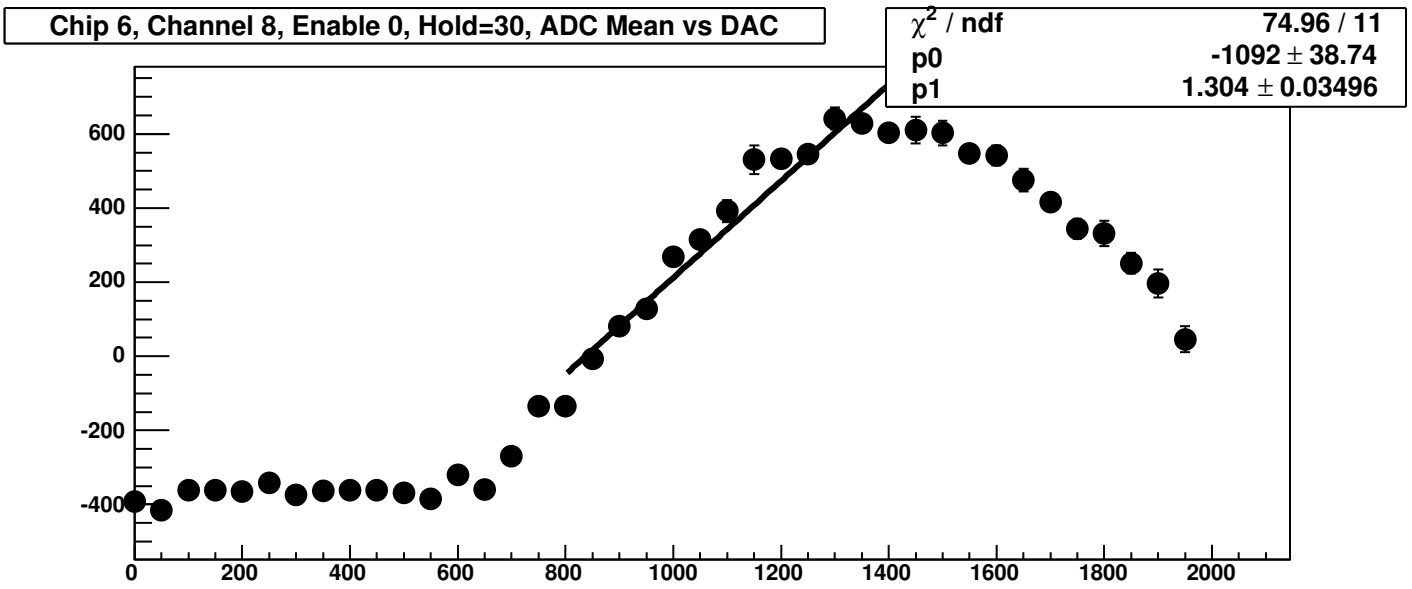


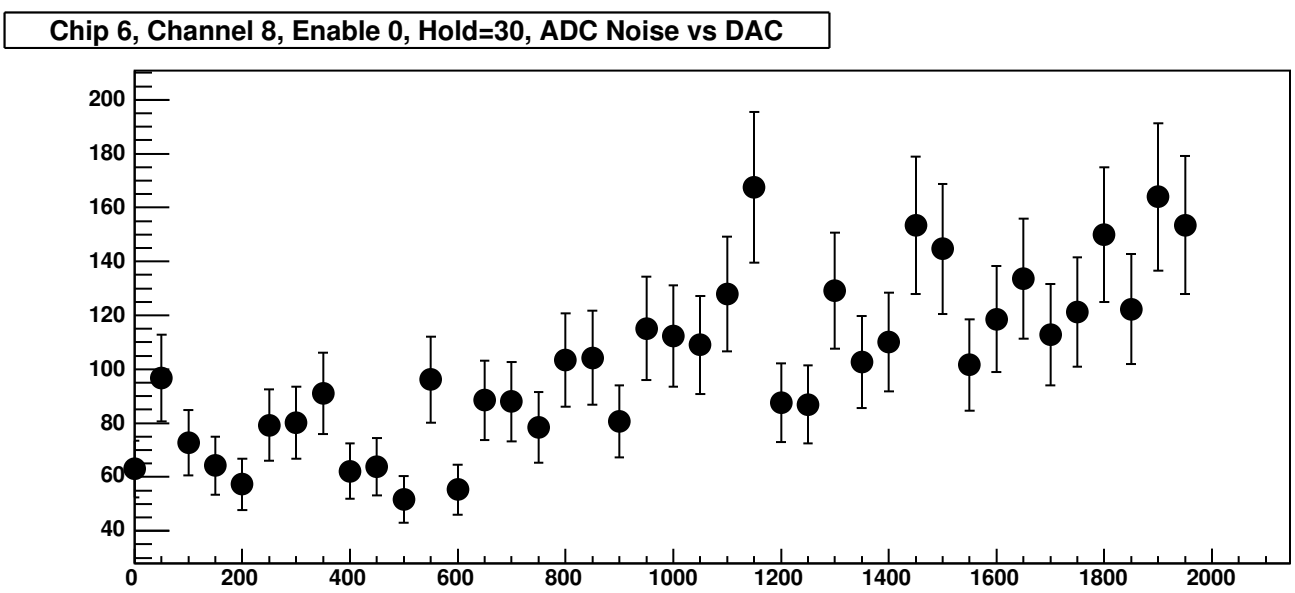
Chip 6, Channel 7, Enable 5!, Hold=30, ADC Noise vs DAC

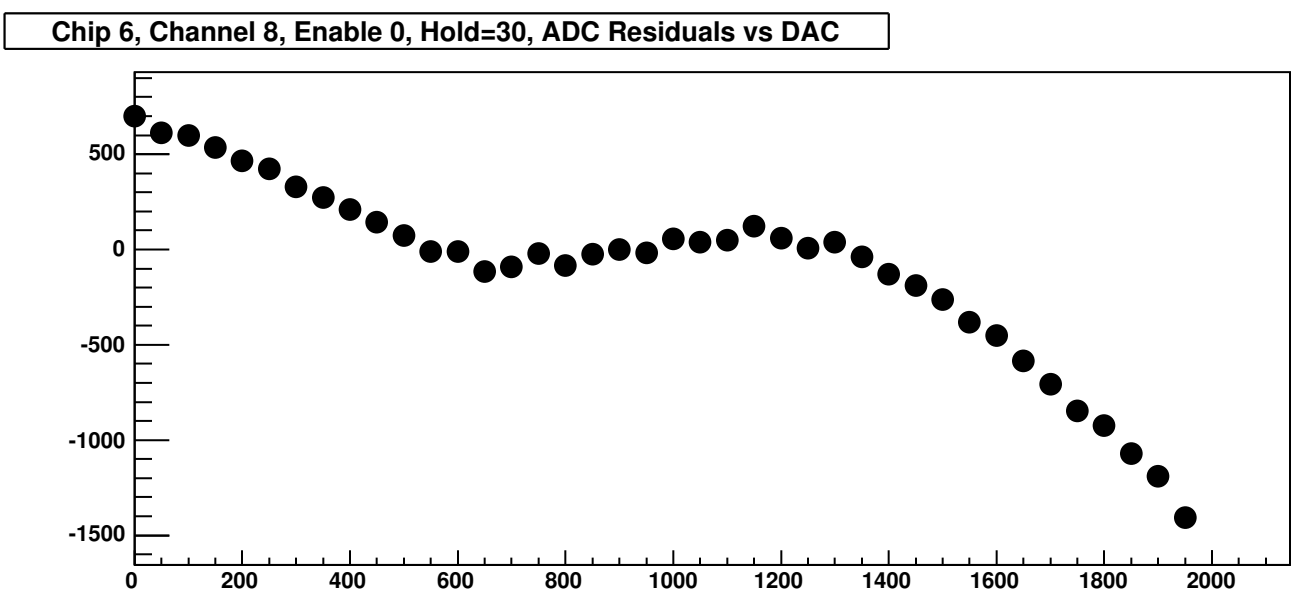


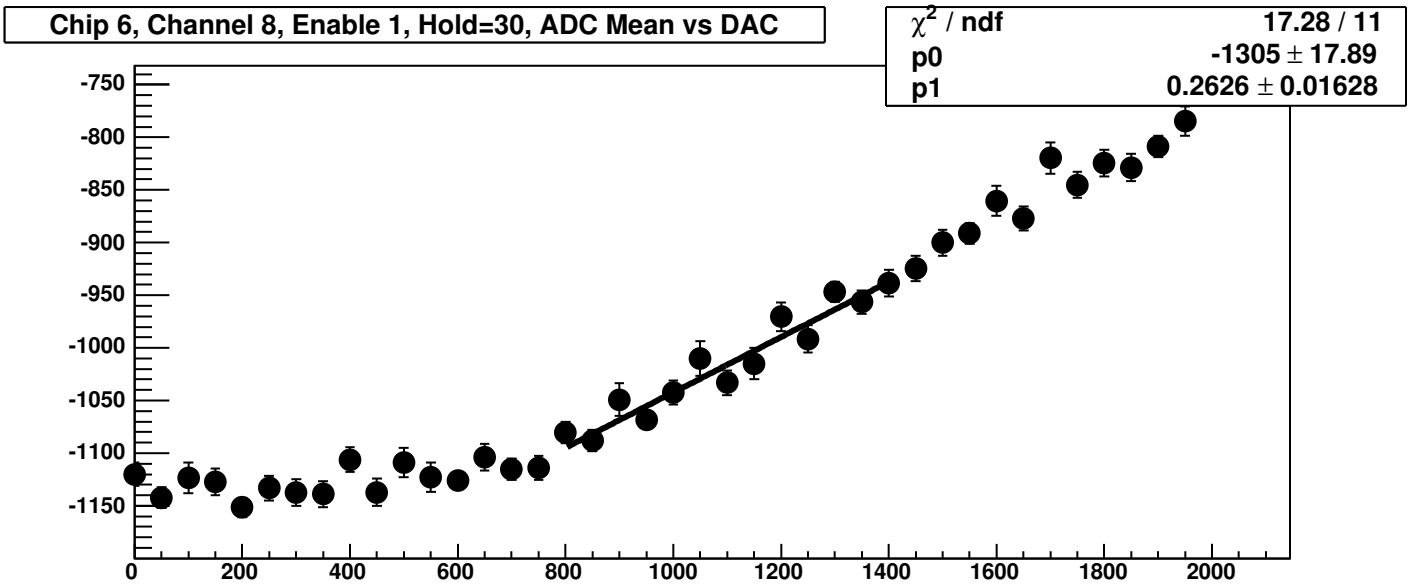
Chip 6, Channel 7, Enable 5!, Hold=30, ADC Residuals vs DAC

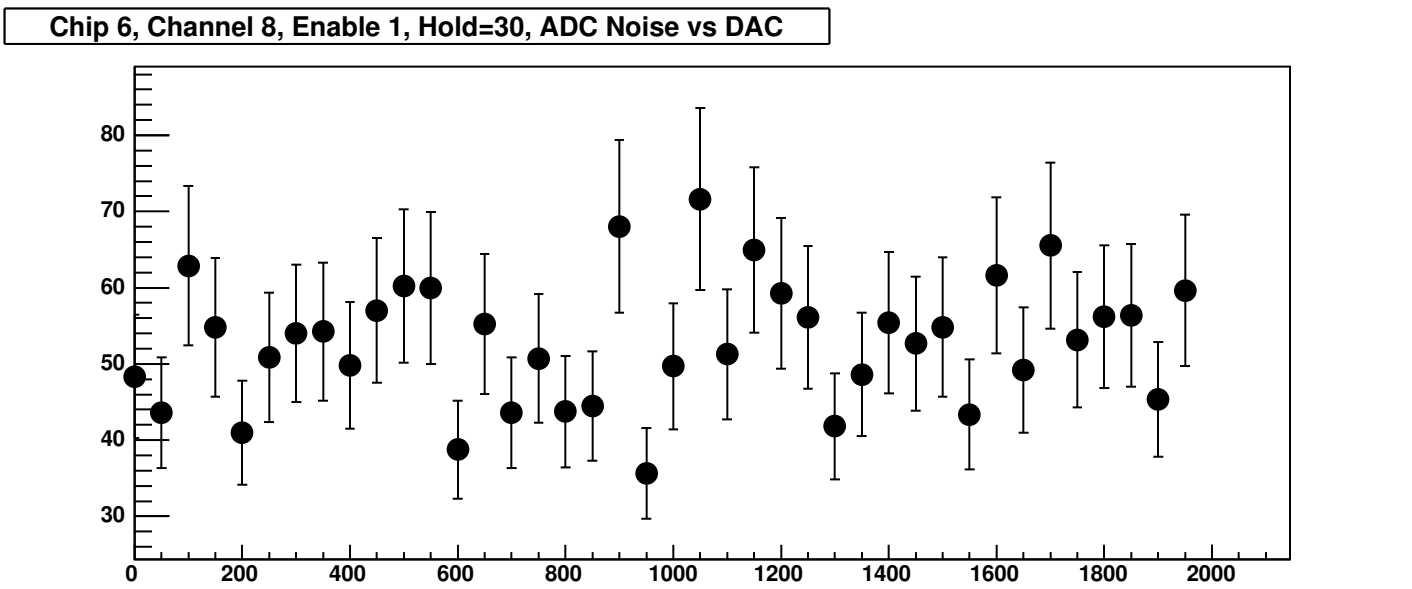


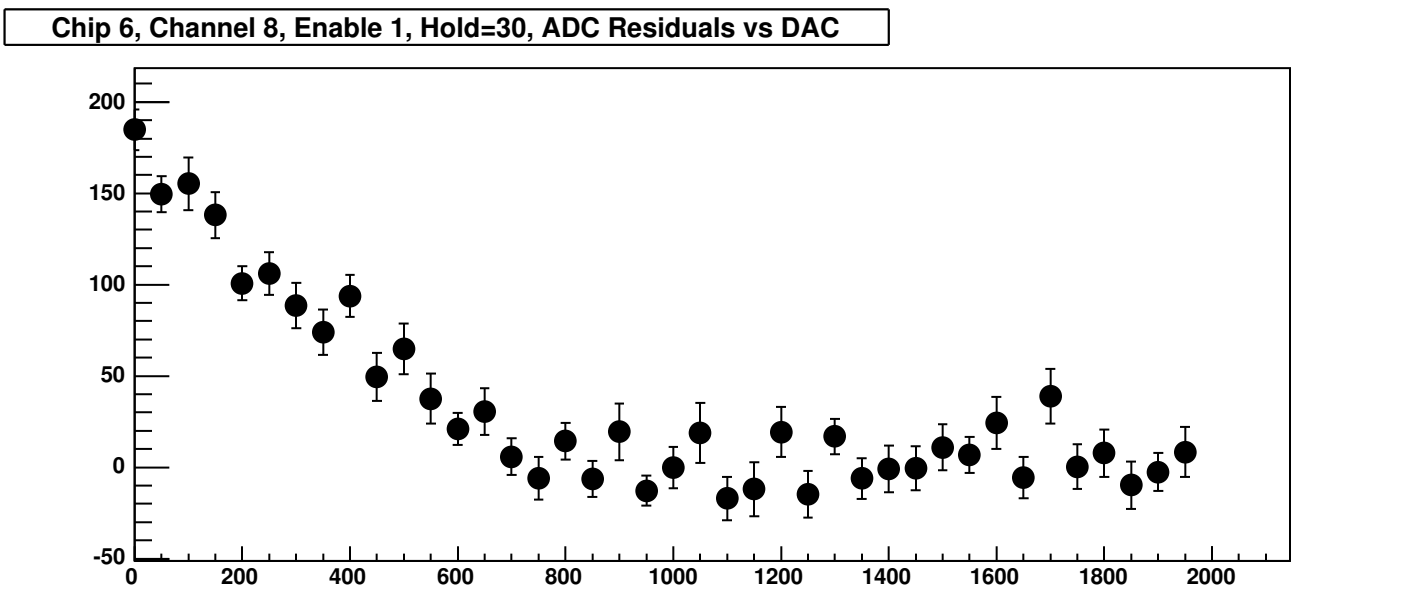

Chip 6, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC
χ^2 / ndf 74.96 / 11
p0 -1092 ± 38.74
p1 1.304 ± 0.03496
600
400
200
0
-200
-400
0 200 400 600 800 1000 1200 1400 1600 1800 2000

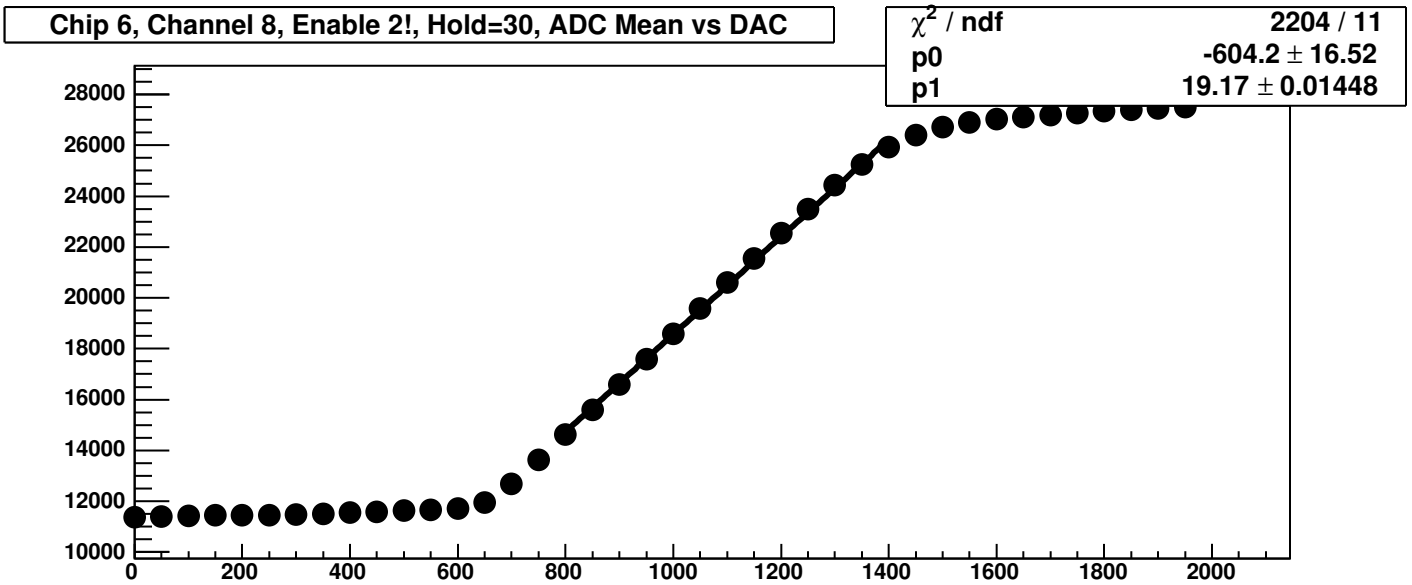

Chip 6, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
0 200 400 600 800 1000 1200 1400 1600 1800 2000

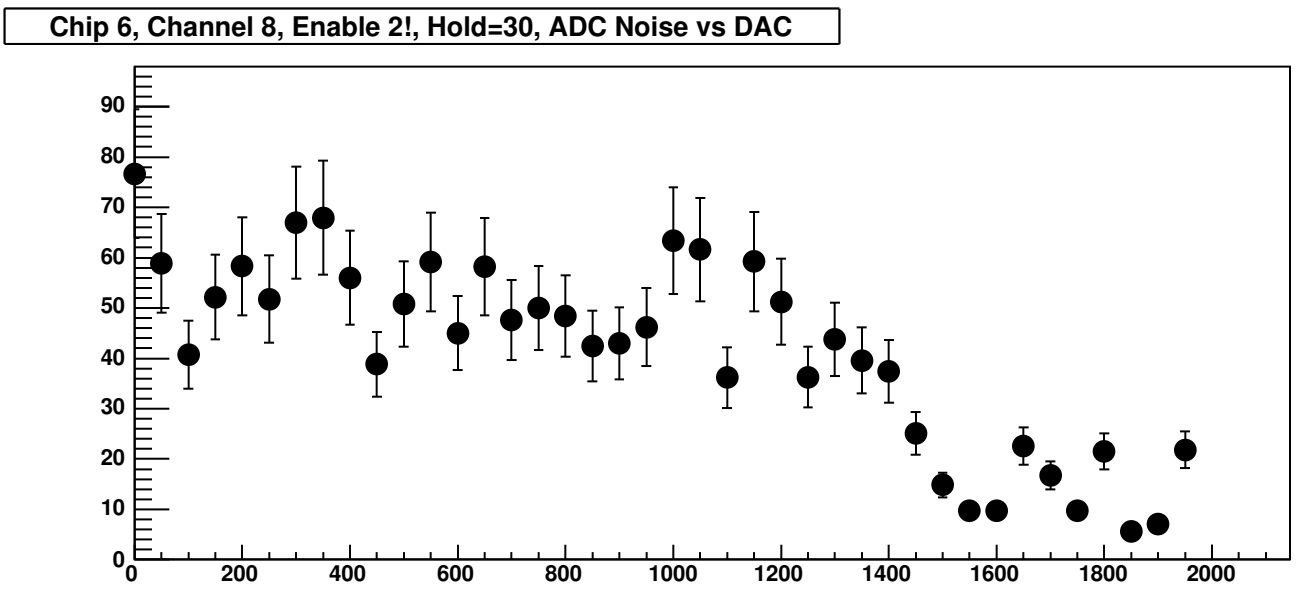

Chip 6, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC
500
0
-500
-1000
-1500
0 200 400 600 800 1000 1200 1400 1600 1800 2000

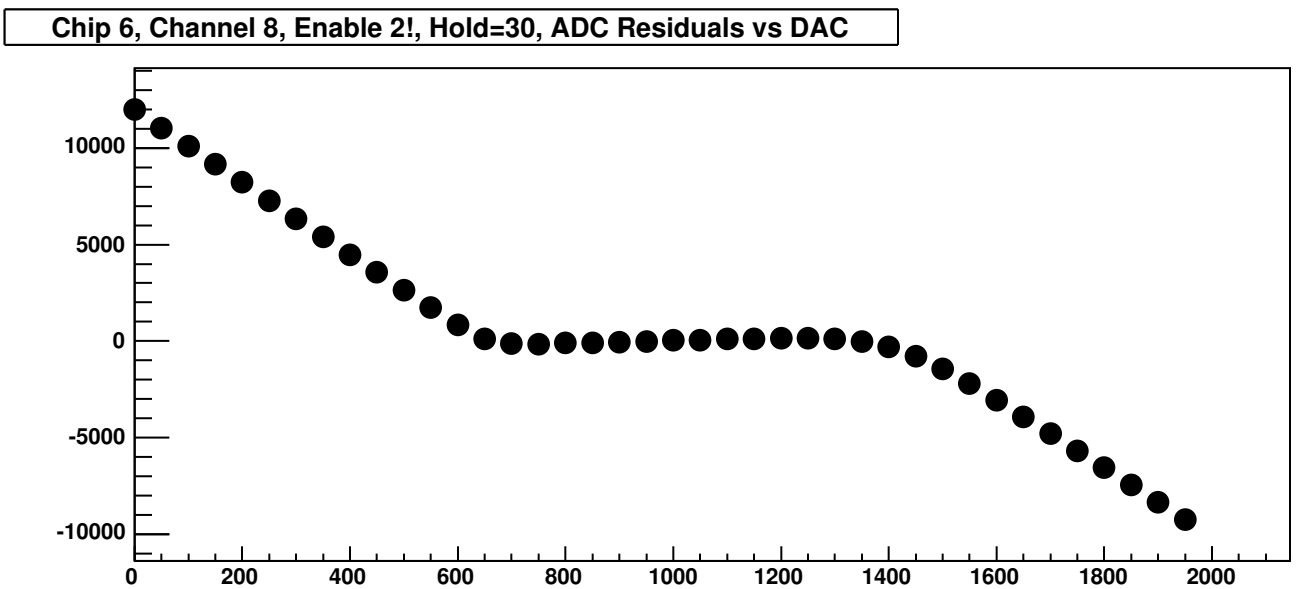

Chip 6, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 17.28 / 11
p0 -1305 ± 17.89
p1 0.2626 ± 0.01628
-750
-800
-850
-900
-950
-1000
-1050
-1100
-1150
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

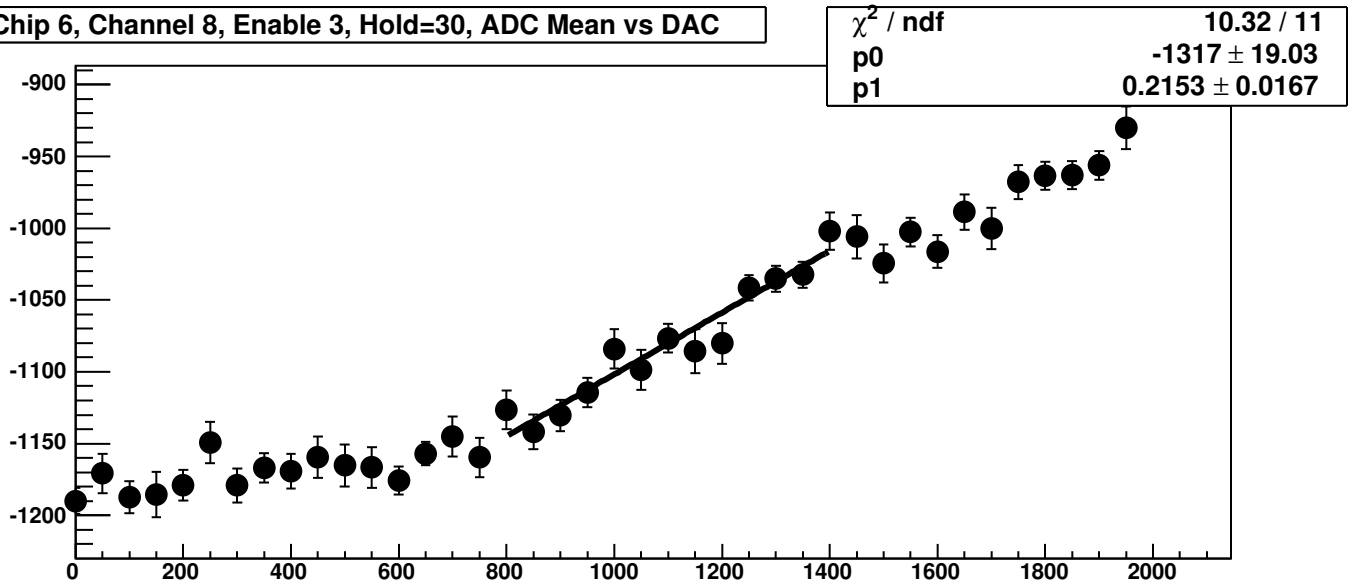

Chip 6, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 8, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 2204 / 11
p0 -604.2 ± 16.52
p1 19.17 ± 0.01448
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

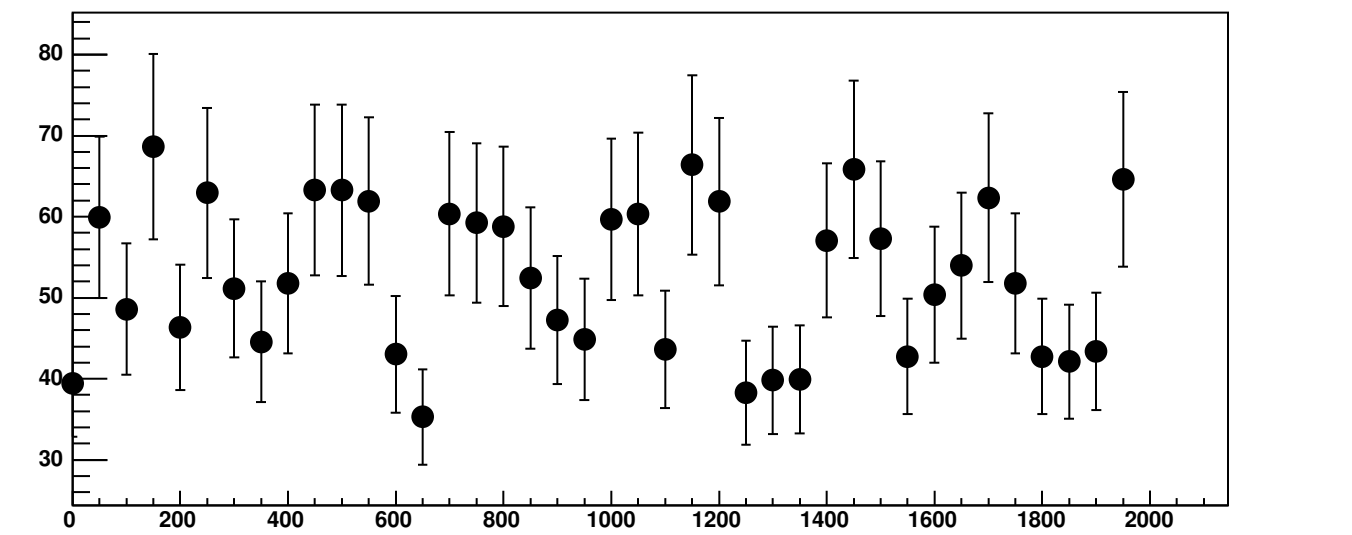

Chip 6, Channel 8, Enable 2!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 8, Enable 2!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

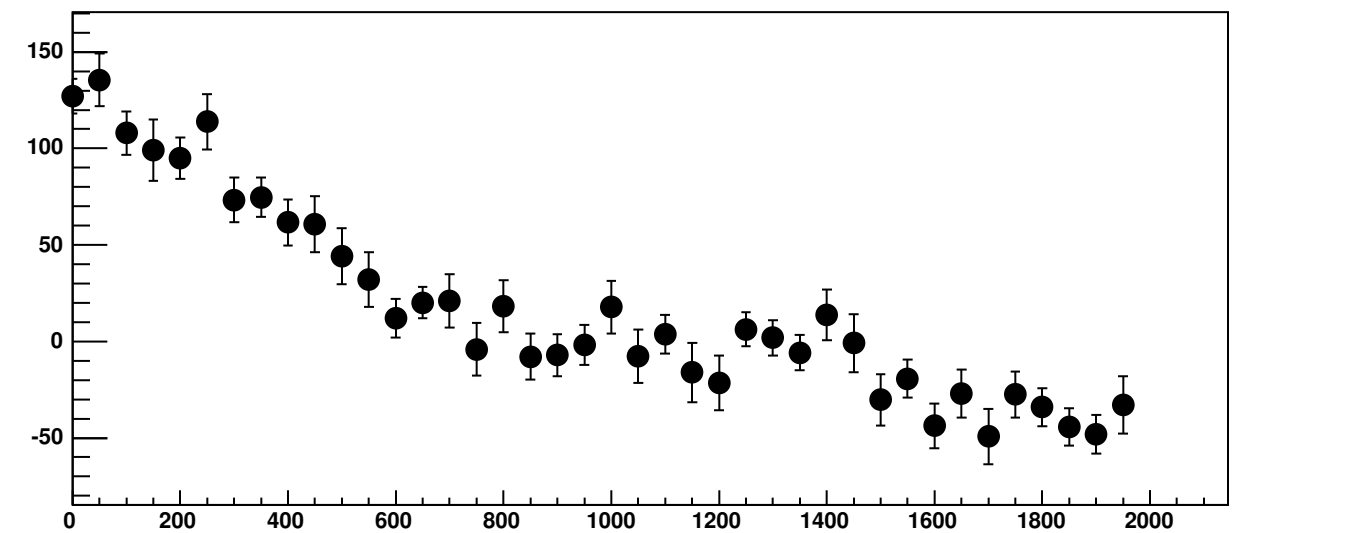
Chip 6, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC

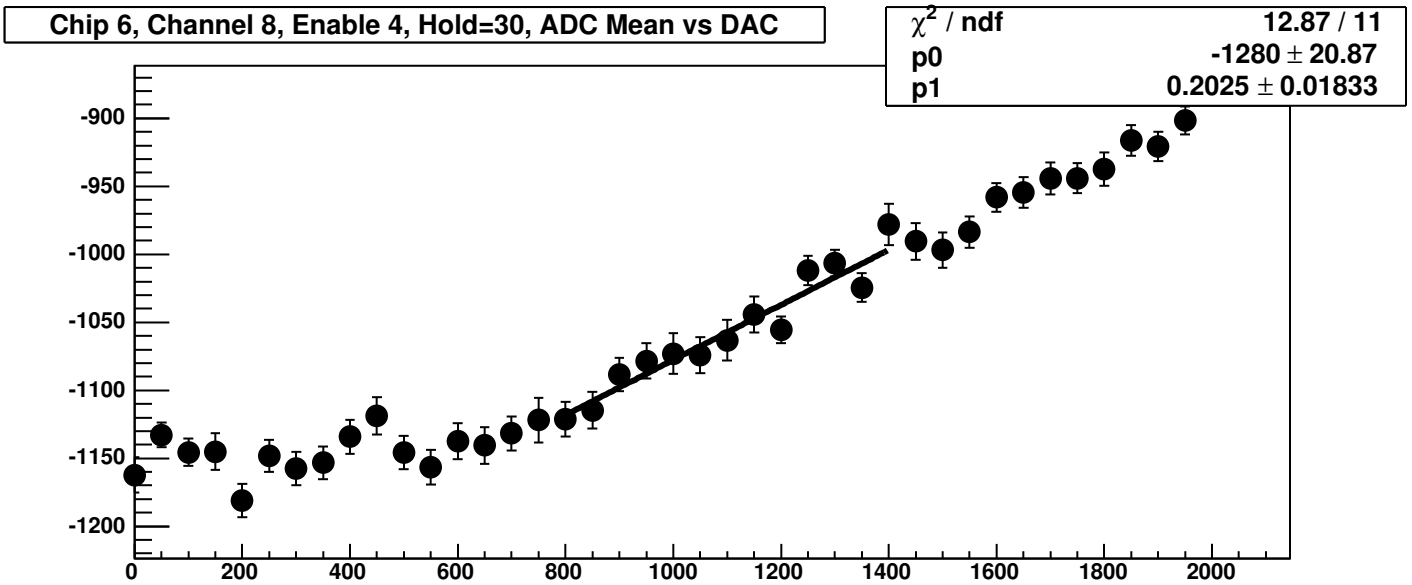


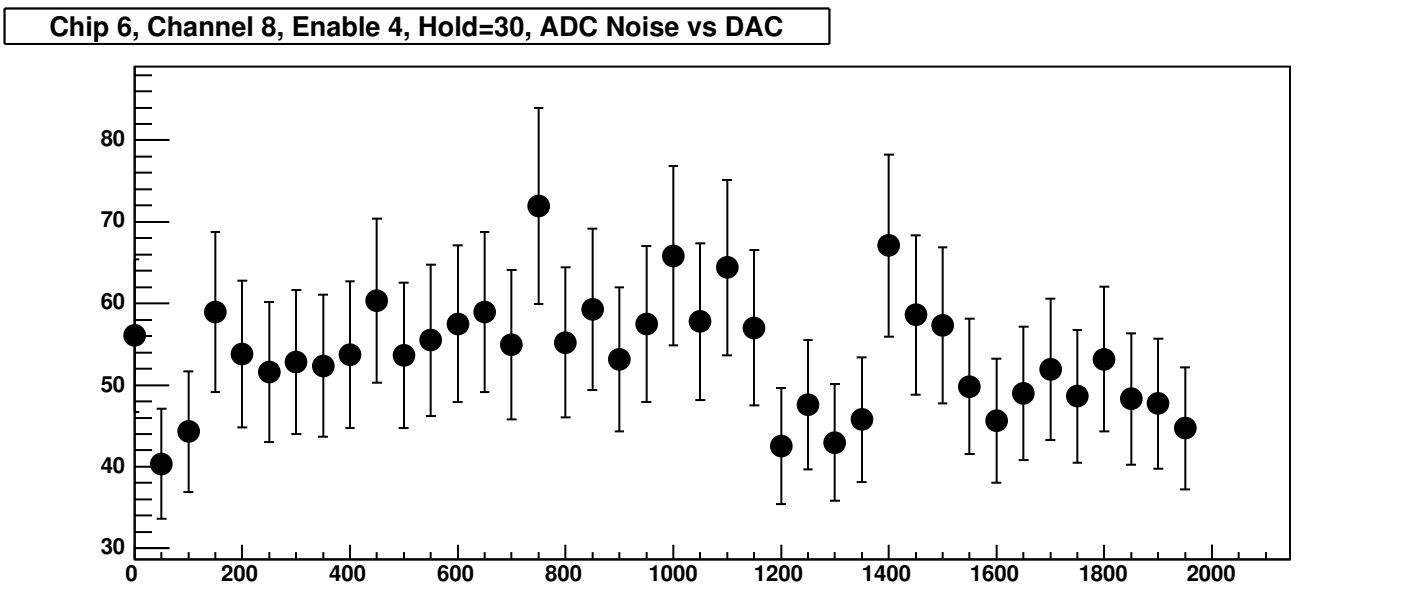
Chip 6, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC

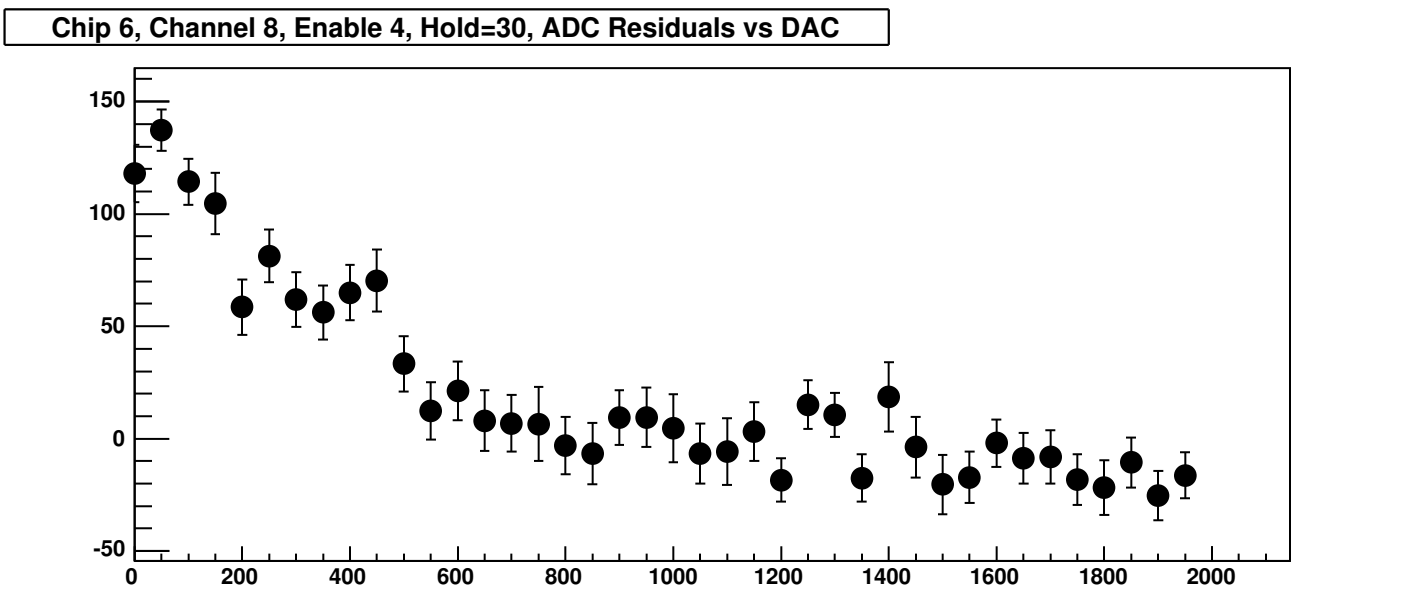


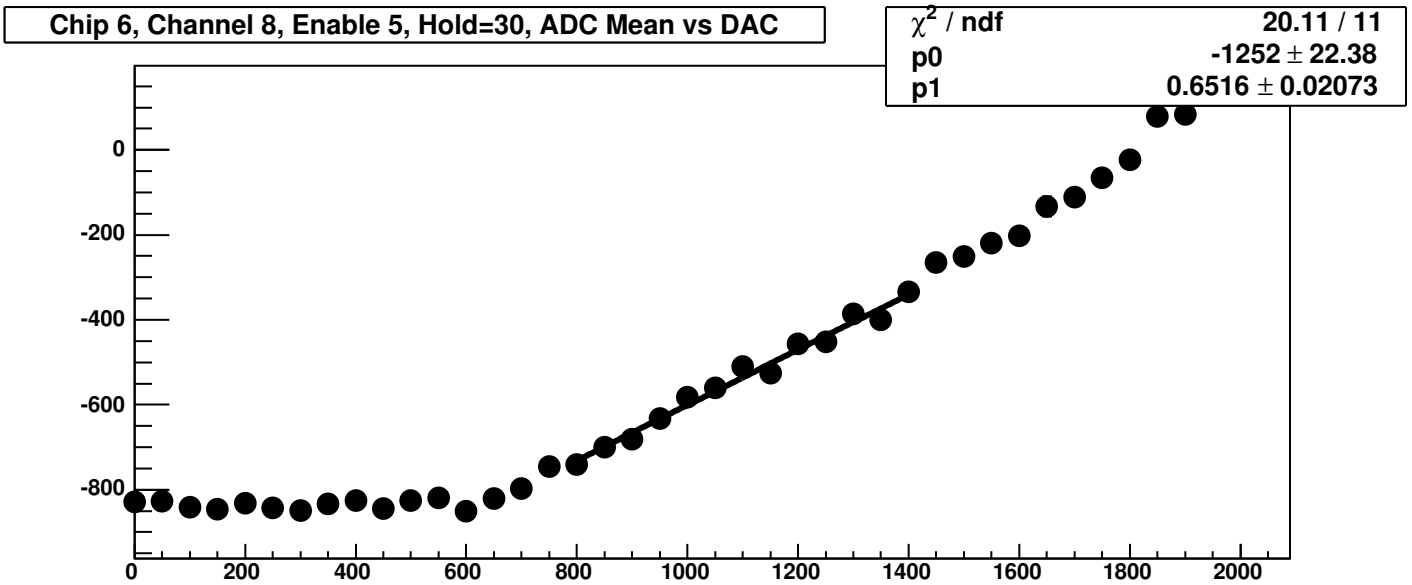
Chip 6, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC

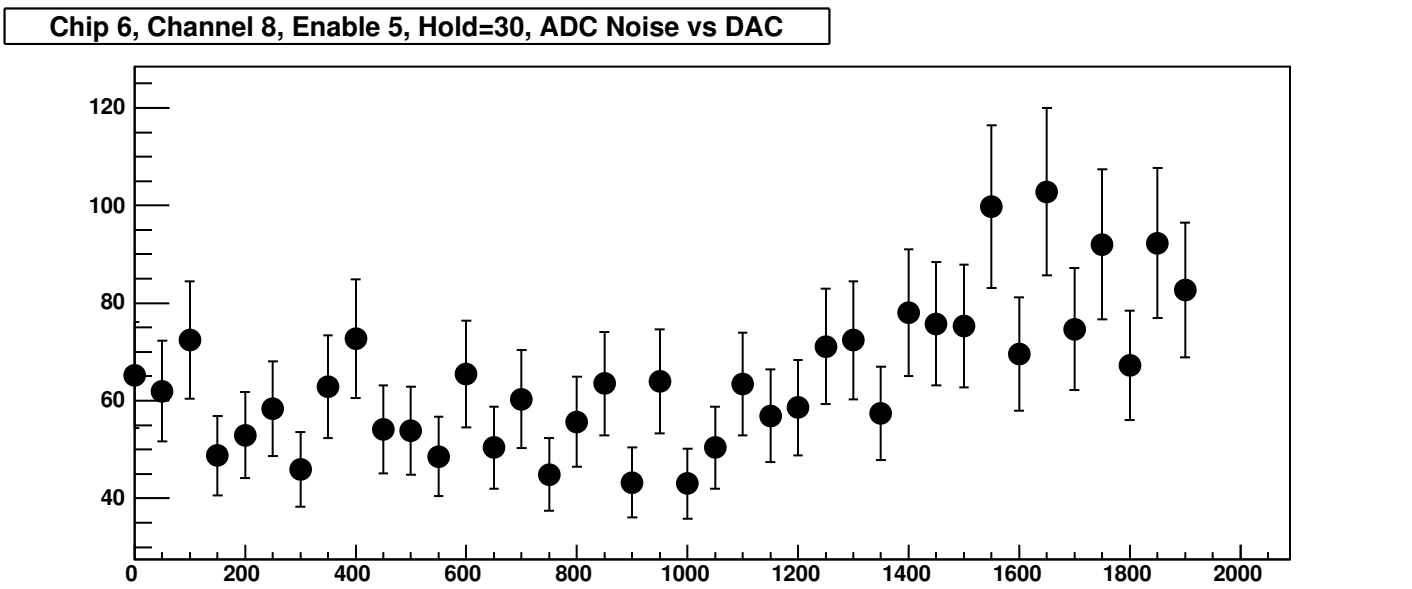


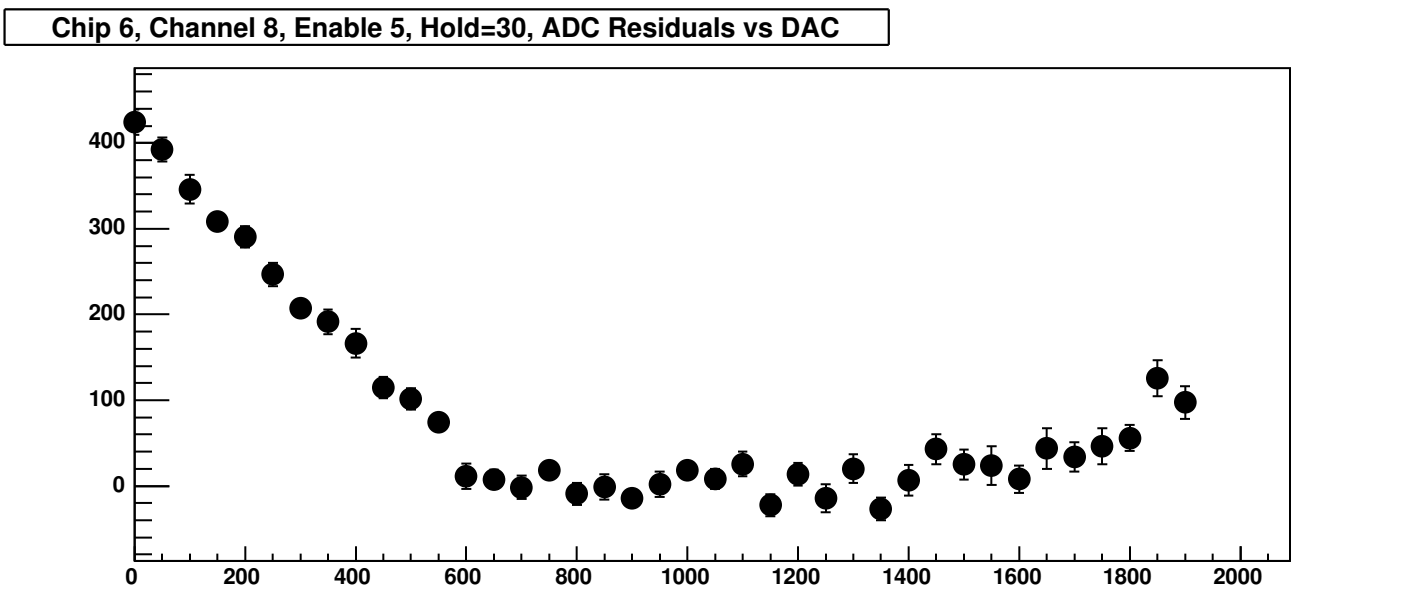

Chip 6, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 12.87 / 11
p0 -1280 ± 20.87
p1 0.2025 ± 0.01833
-900
-950
-1000
-1050
-1100
-1150
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

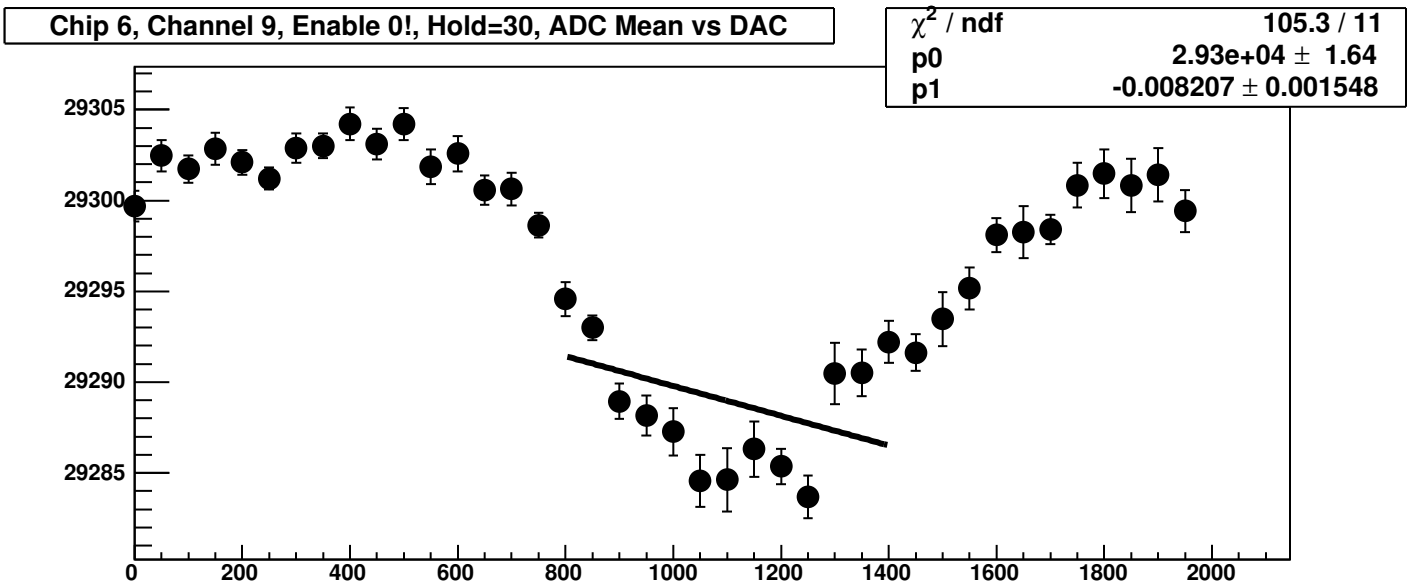

Chip 6, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

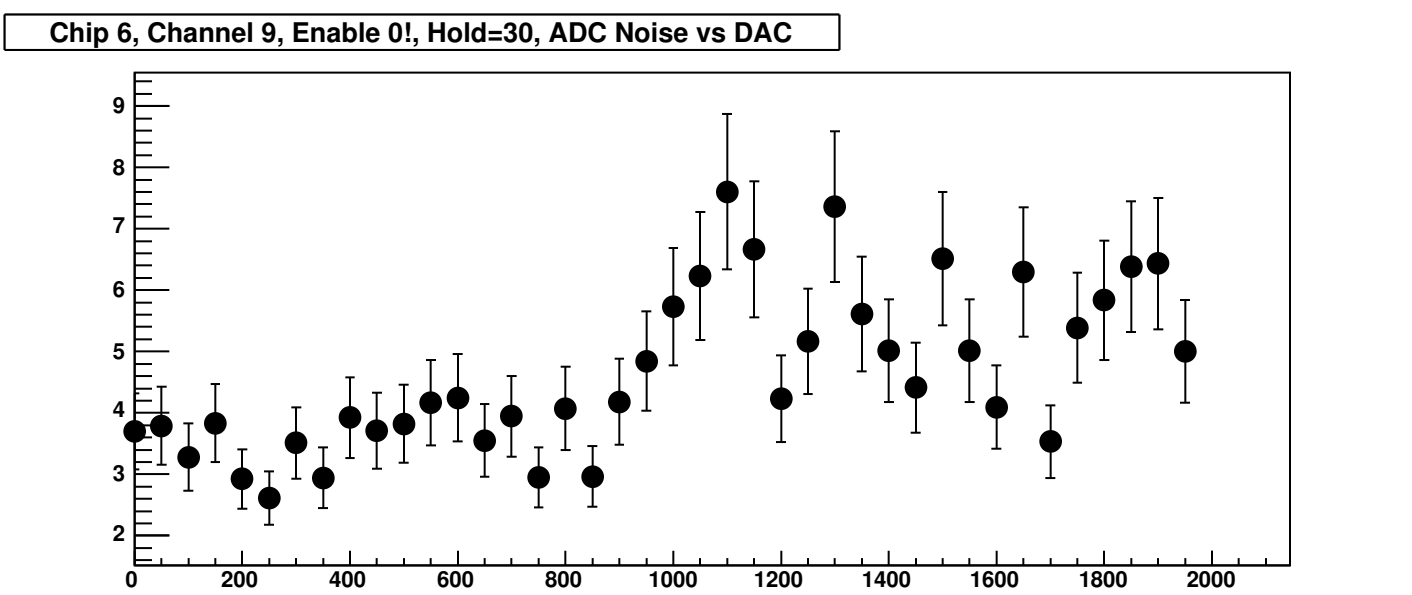

Chip 6, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

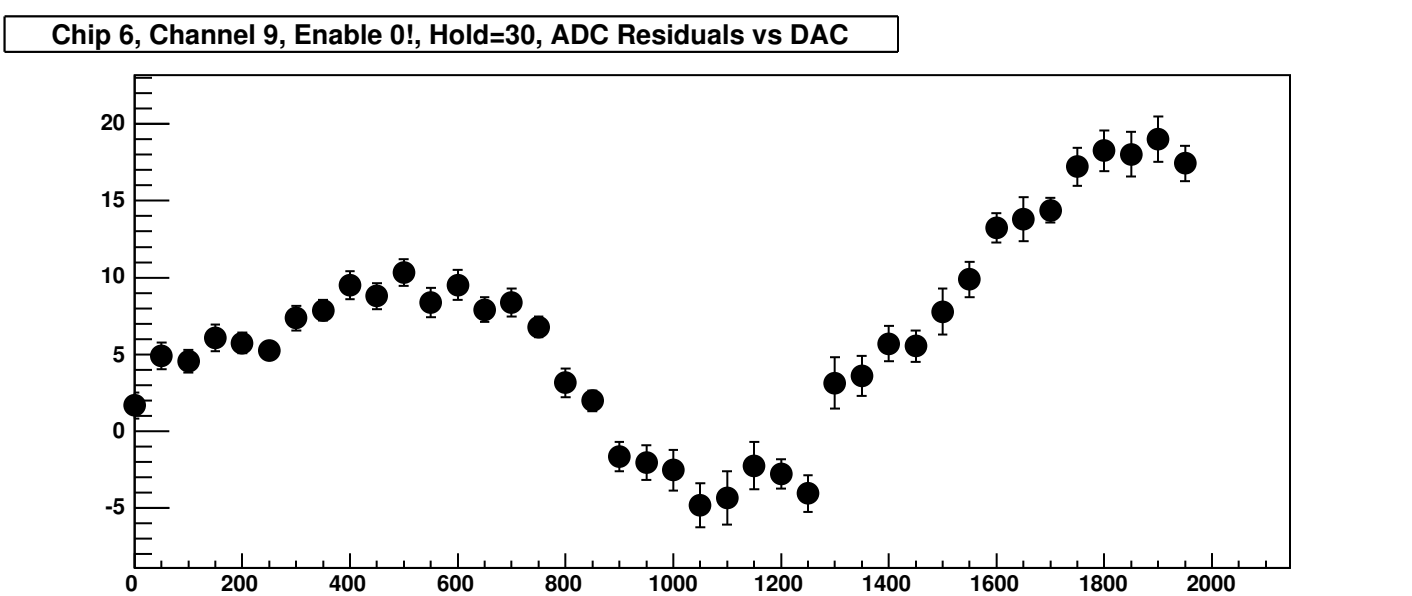

Chip 6, Channel 8, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 20.11 / 11
p0 -1252 ± 22.38
p1 0.6516 ± 0.02073
0
-200
-400
-600
-800
0 200 400 600 800 1000 1200 1400 1600 1800 2000


Chip 6, Channel 8, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0 200 400 600 800 1000 1200 1400 1600 1800 2000

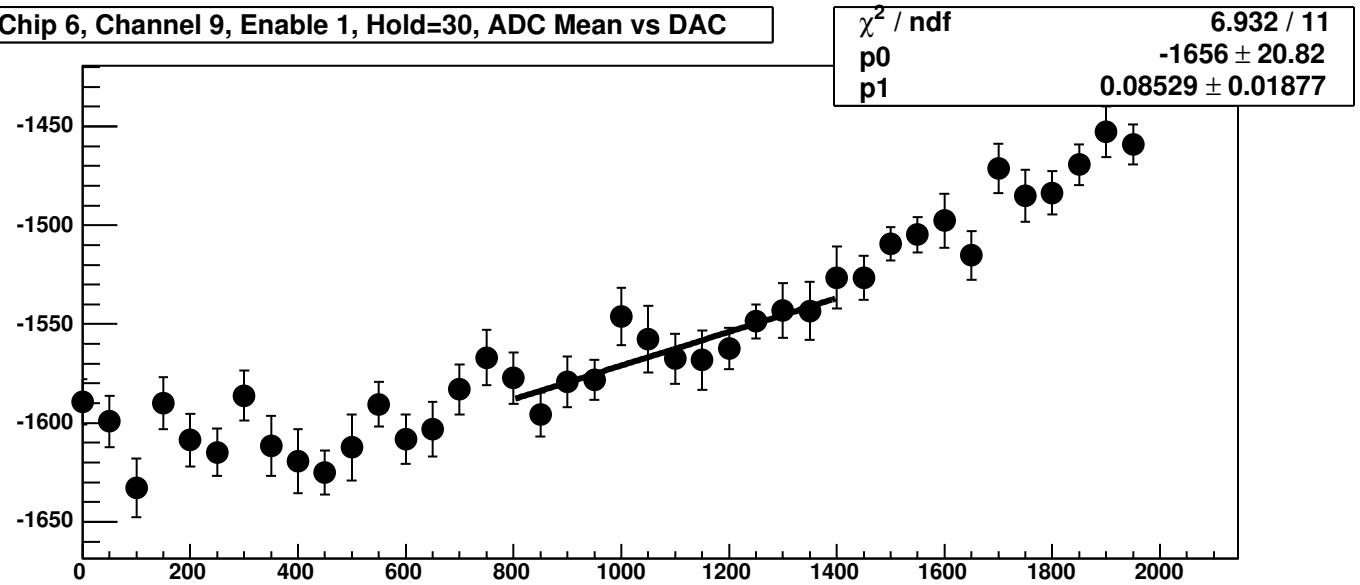

Chip 6, Channel 8, Enable 5, Hold=30, ADC Residuals vs DAC
400
300
200
100
0
0 200 400 600 800 1000 1200 1400 1600 1800 2000


Chip 6, Channel 9, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 105.3 / 11
p0 2.93e+04 ± 1.64
p1 -0.008207 ± 0.001548
29305
29300
29295
29290
29285
0
200
400
600
800
1000
1200
1400
1600
1800
2000

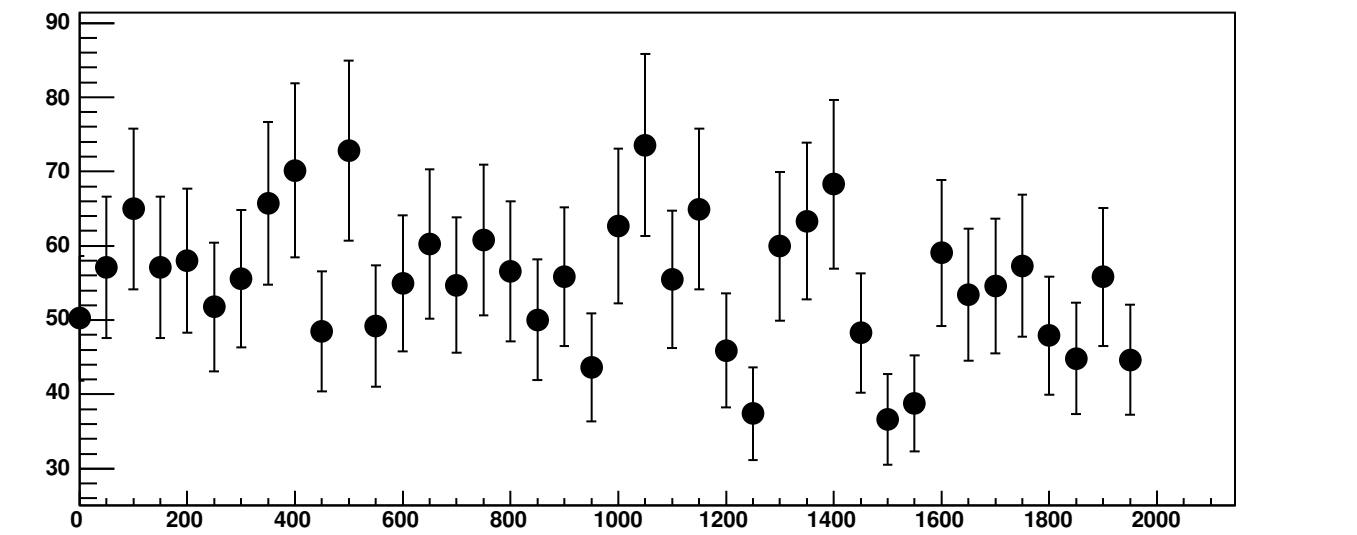

Chip 6, Channel 9, Enable 0!, Hold=30, ADC Noise vs DAC
9
8
7
6
5
4
3
2
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 9, Enable 0!, Hold=30, ADC Residuals vs DAC
20
15
10
5
0
-5
0
200
400
600
800
1000
1200
1400
1600
1800
2000

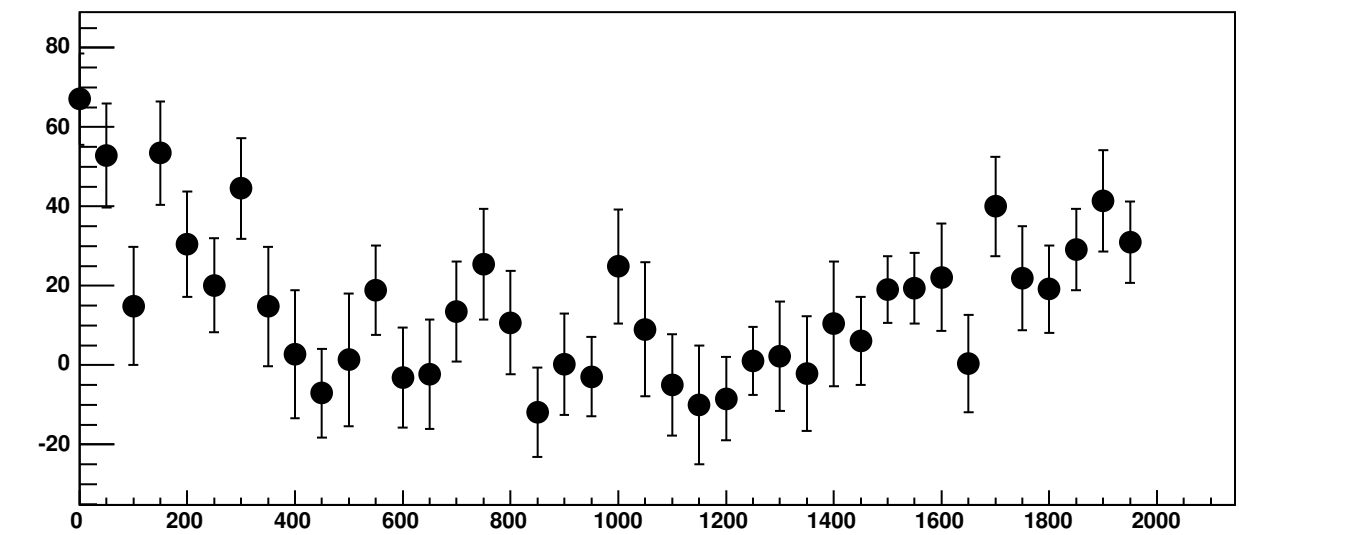
Chip 6, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC



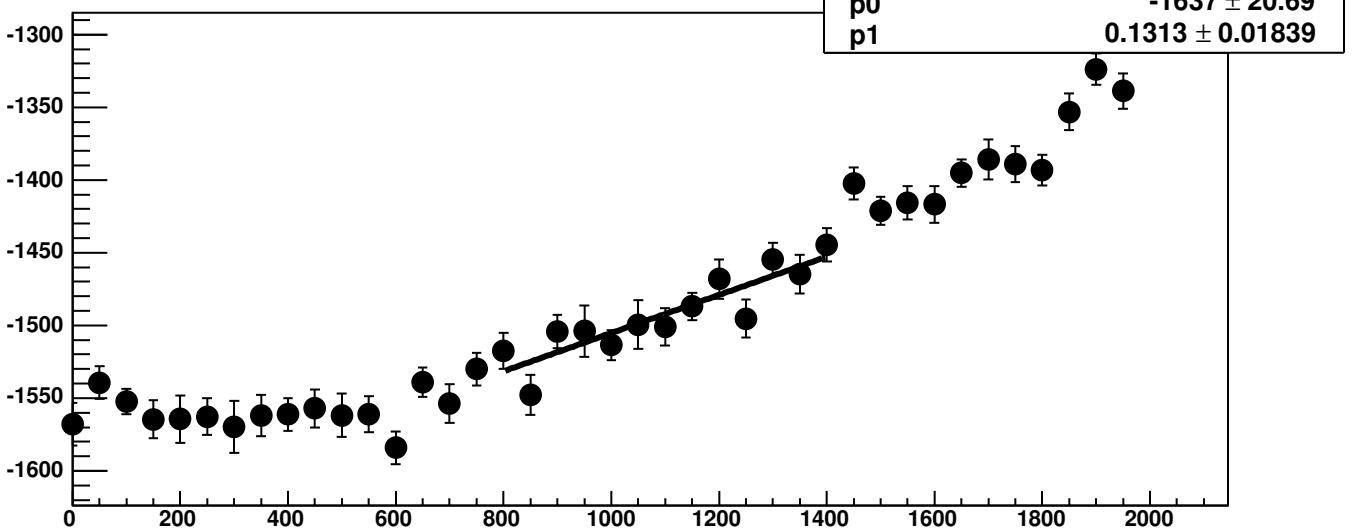
Chip 6, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC



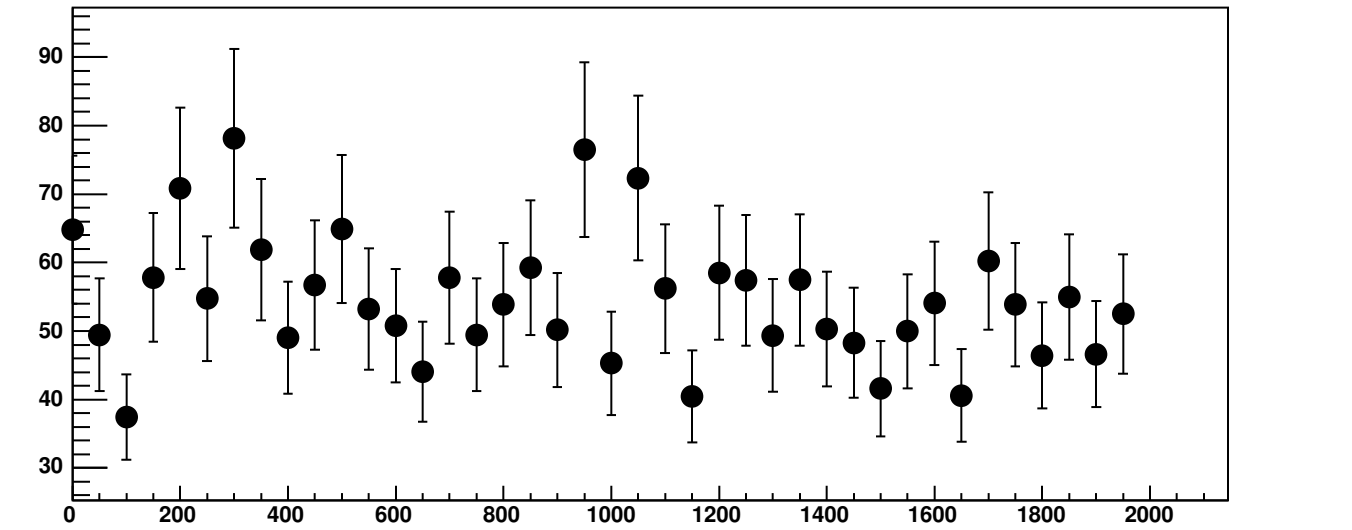
Chip 6, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC



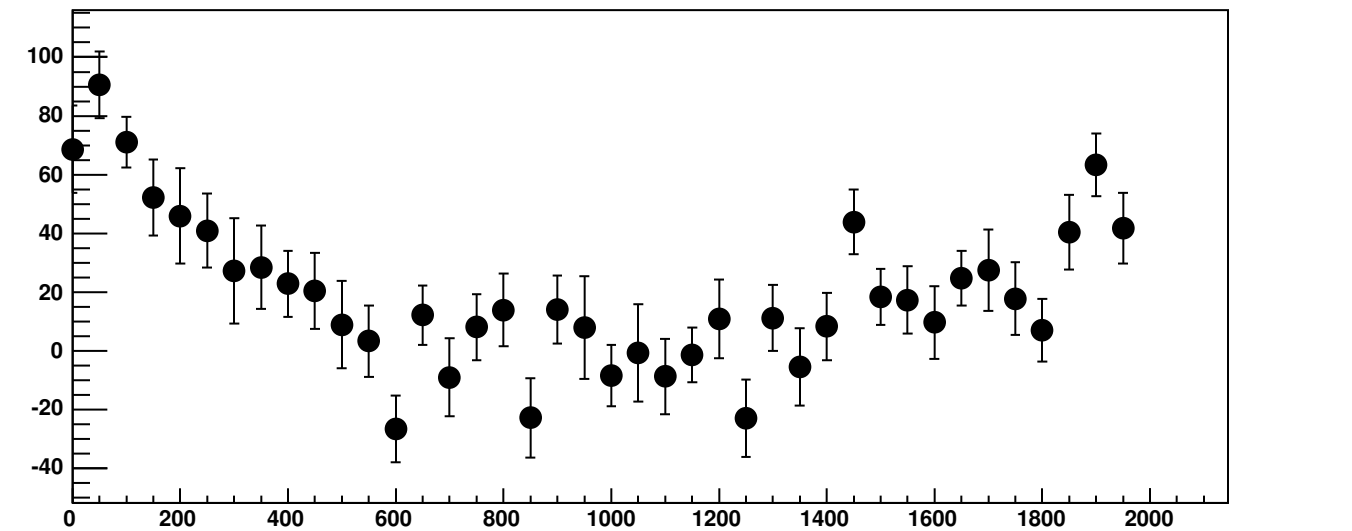
Chip 6, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC

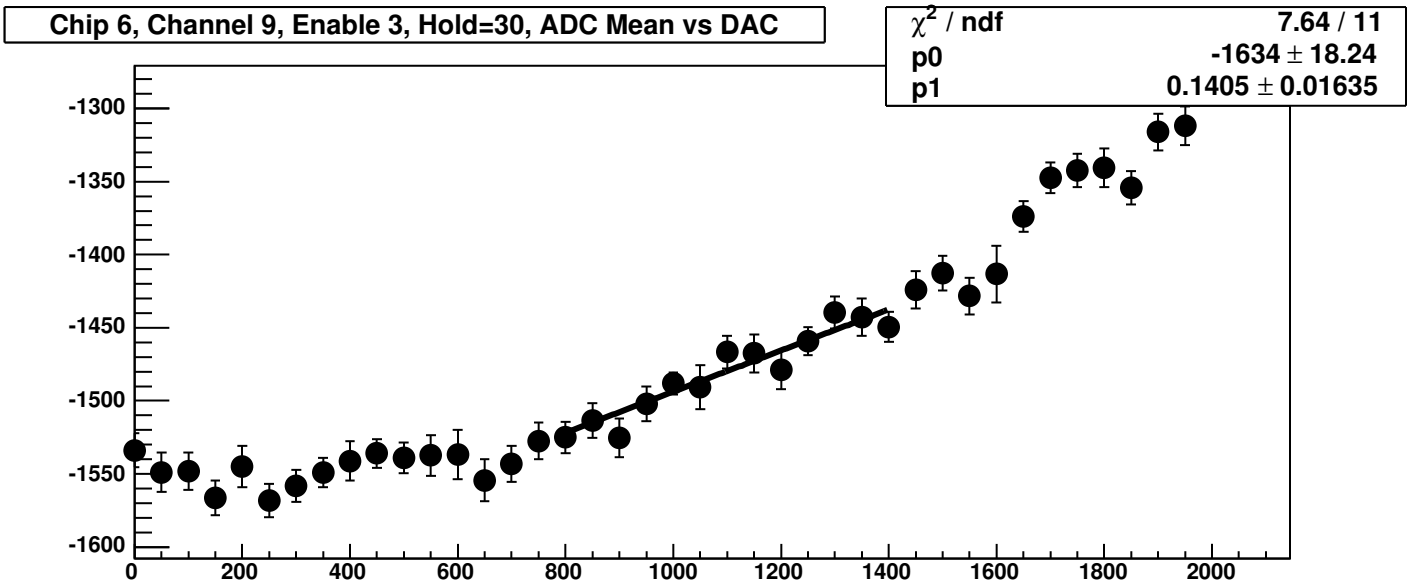


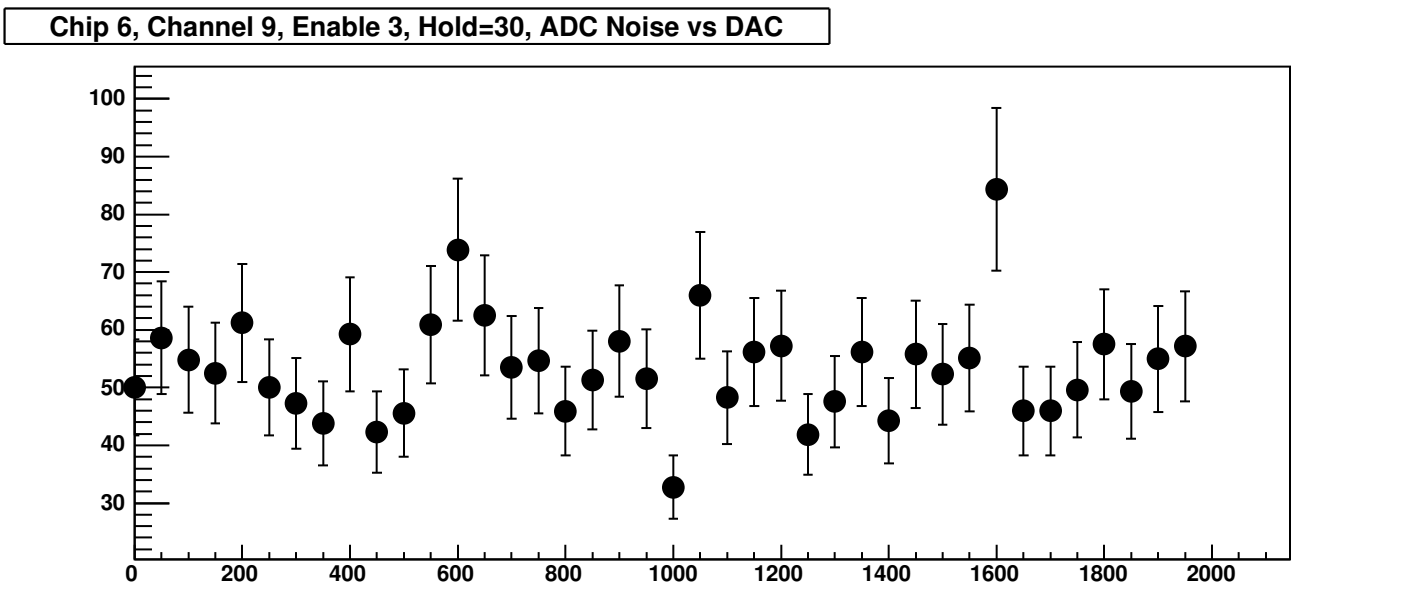
Chip 6, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC

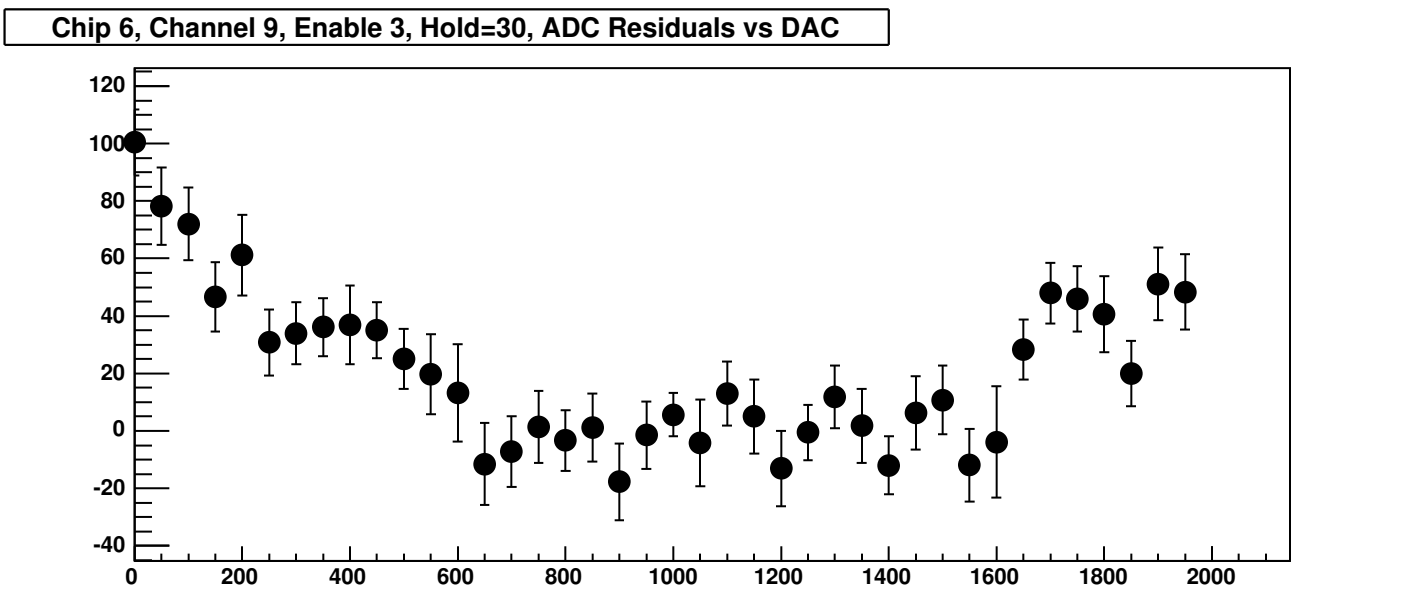


Chip 6, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC

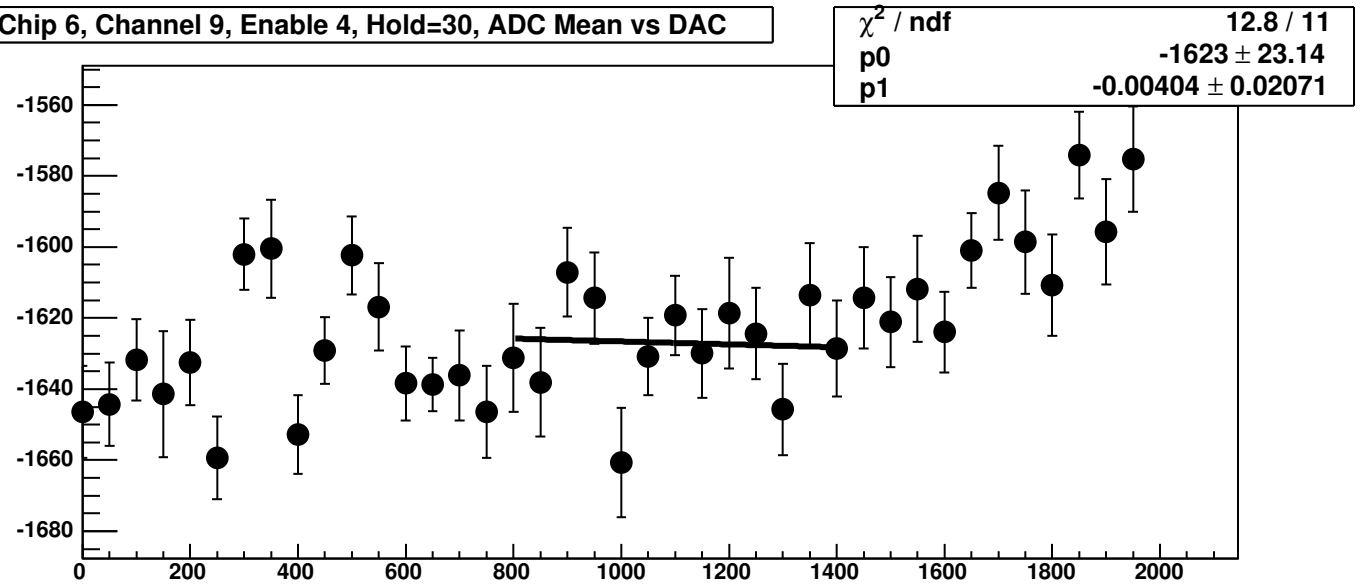



Chip 6, Channel 9, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7.64 / 11
p0 -1634 ± 18.24
p1 0.1405 ± 0.01635
-1300
-1350
-1400
-1450
-1500
-1550
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

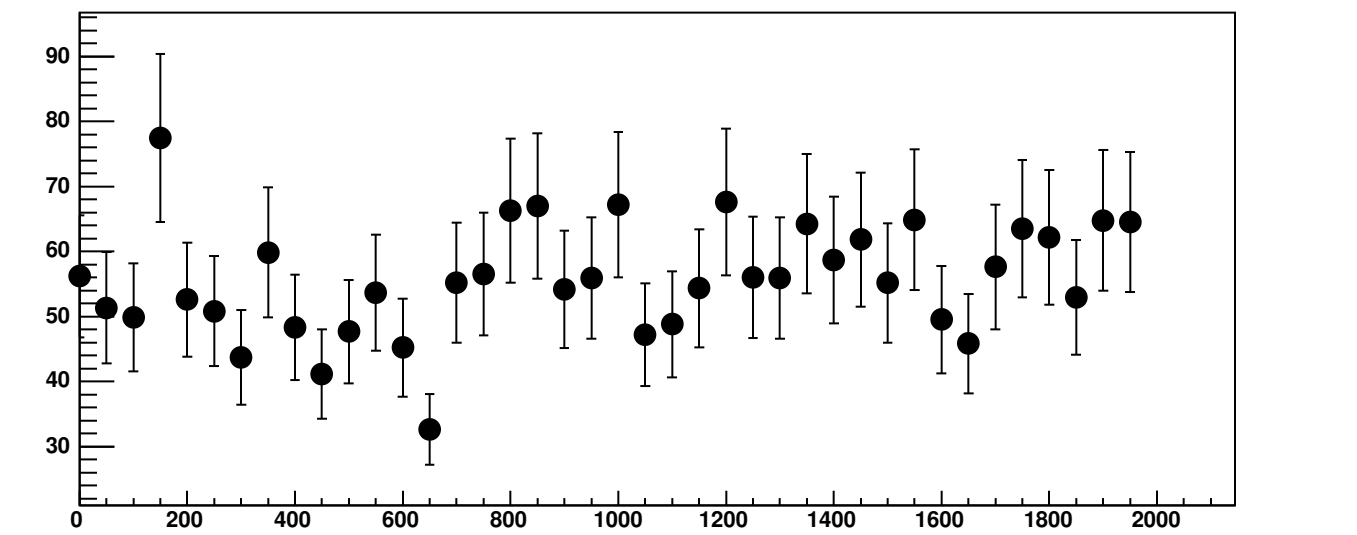

Chip 6, Channel 9, Enable 3, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 9, Enable 3, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

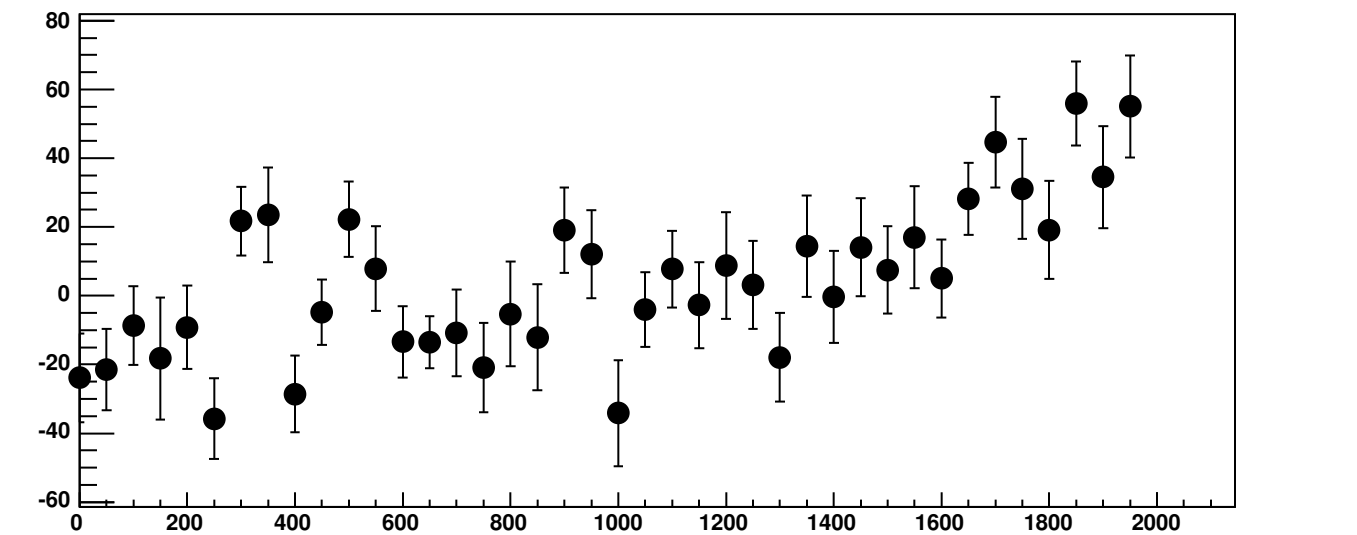
Chip 6, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC



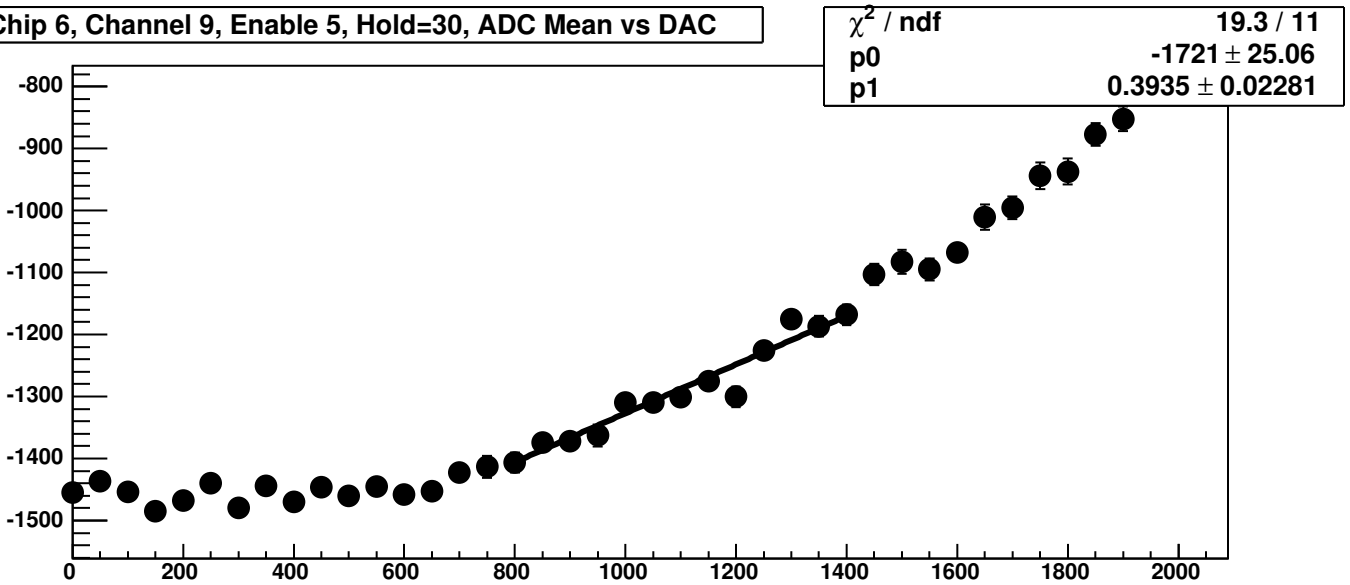
Chip 6, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC



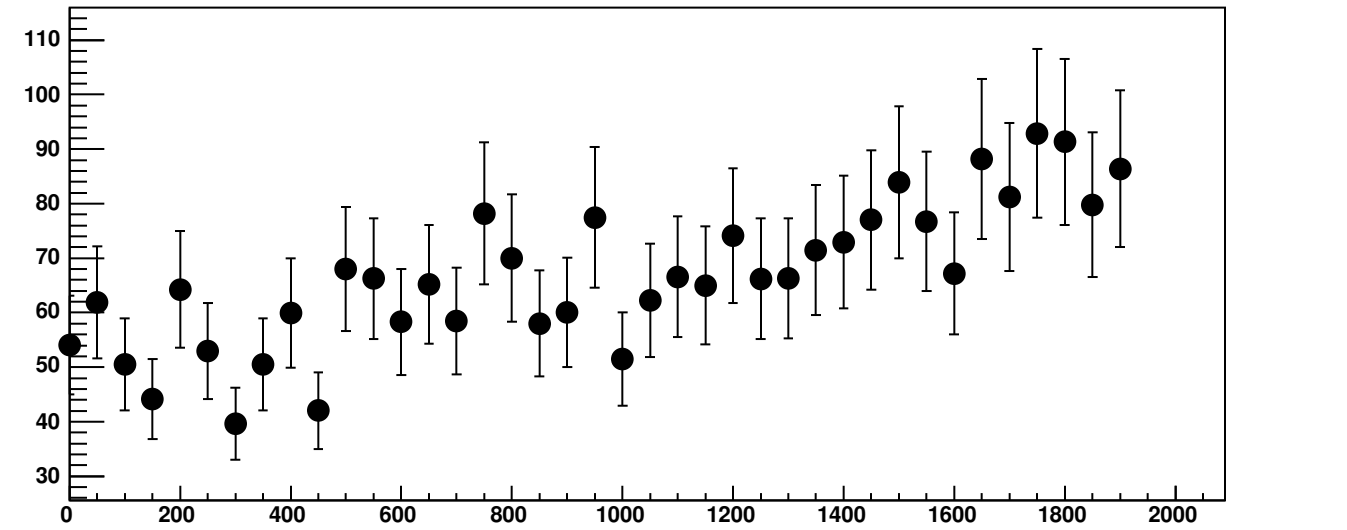
Chip 6, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC



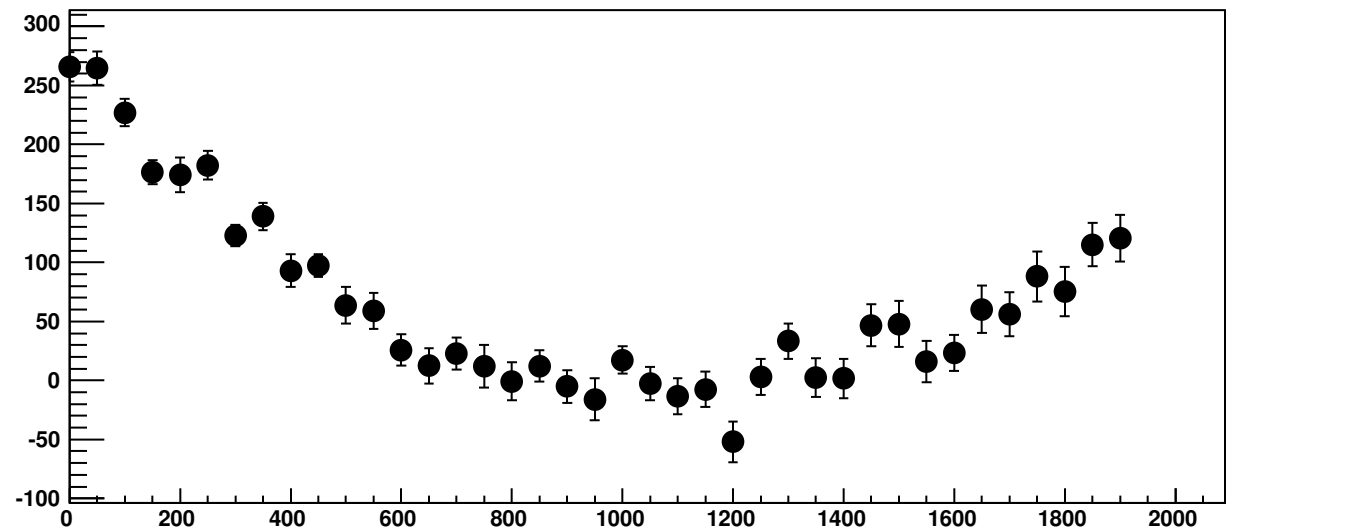
Chip 6, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC

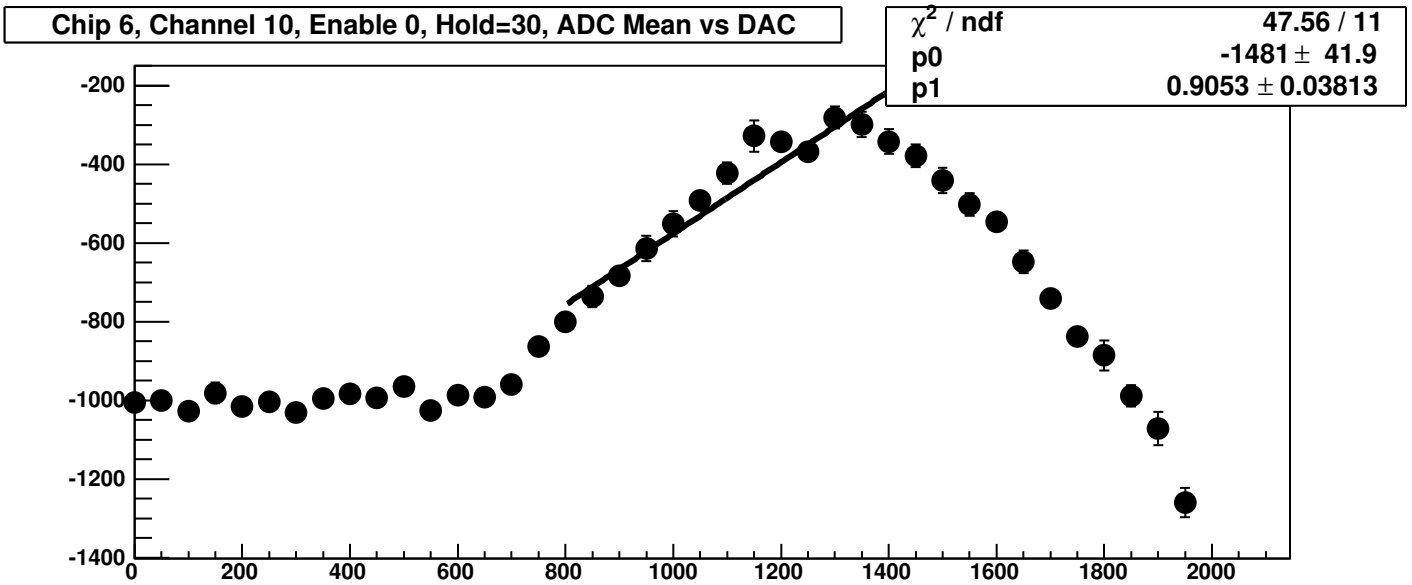


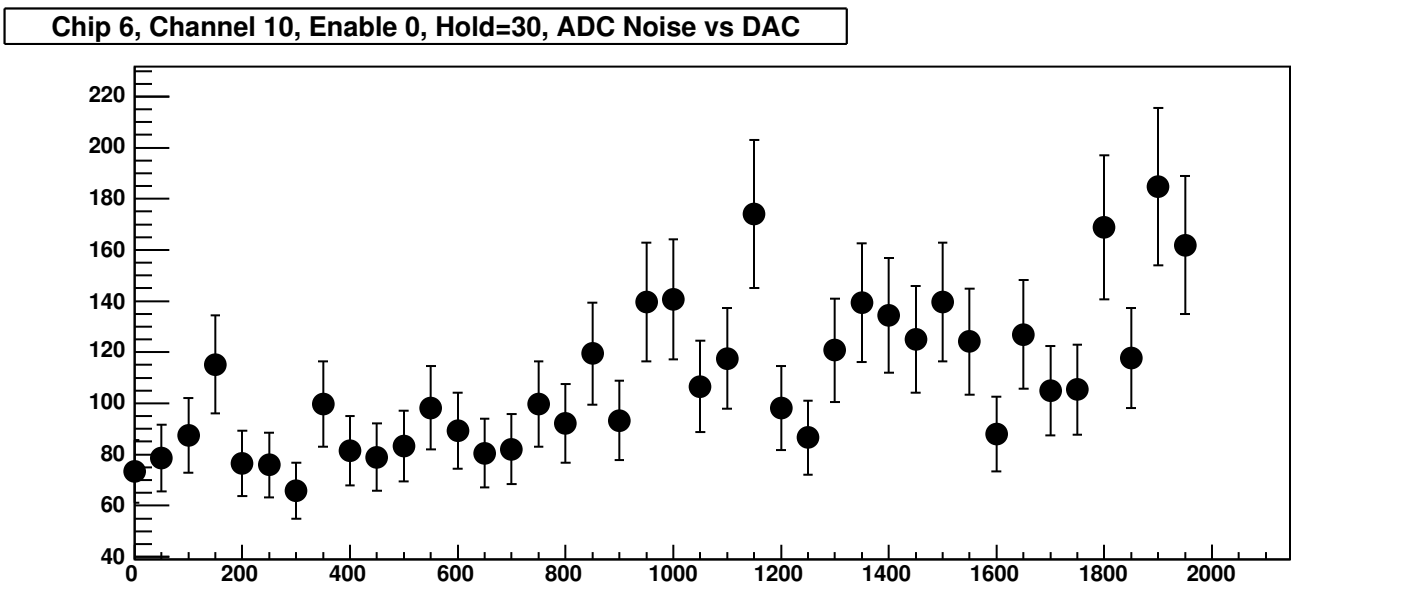
Chip 6, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC

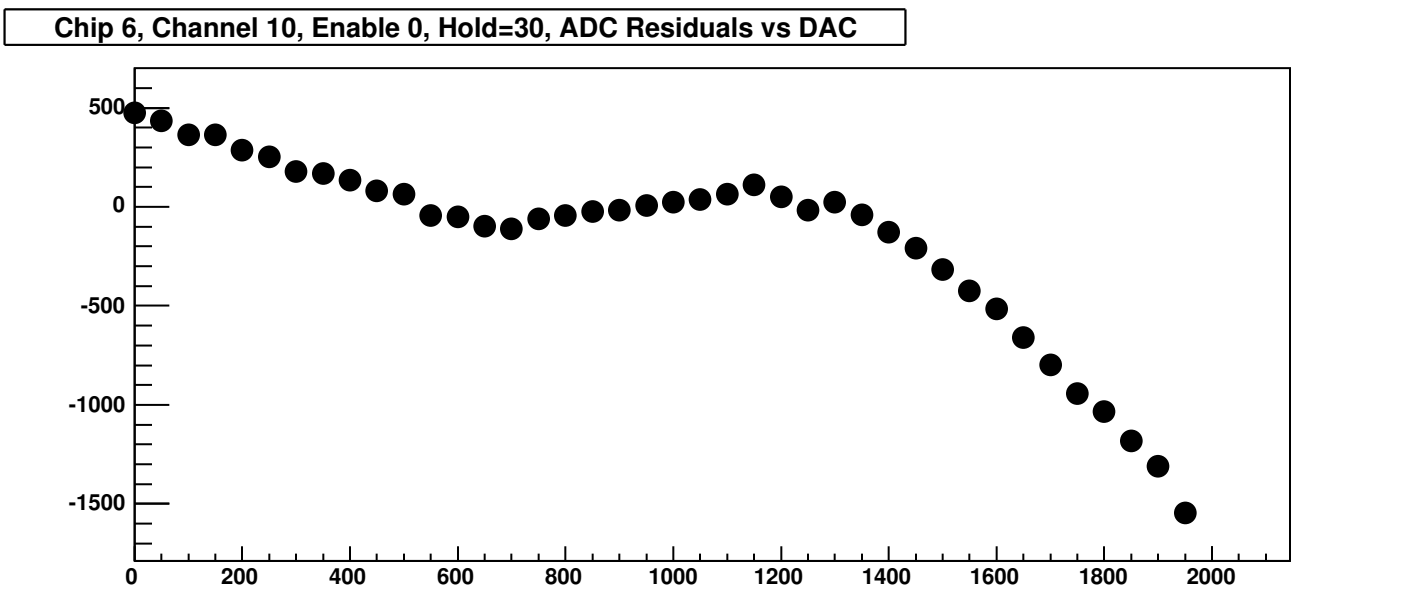


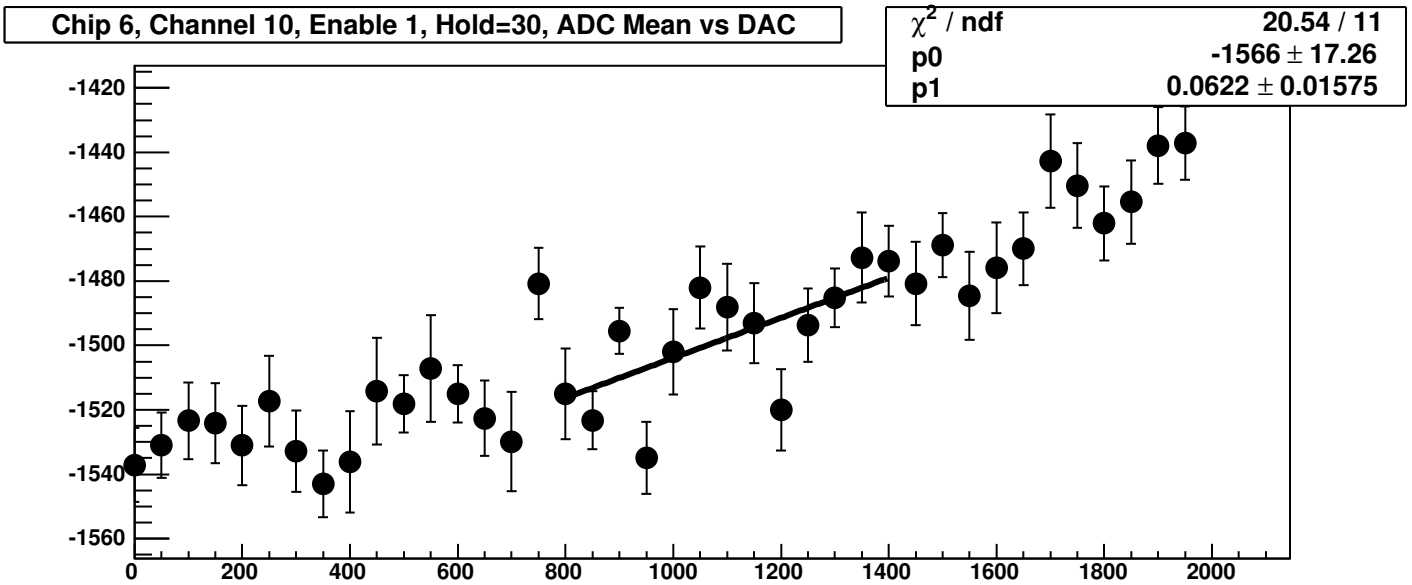
Chip 6, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC

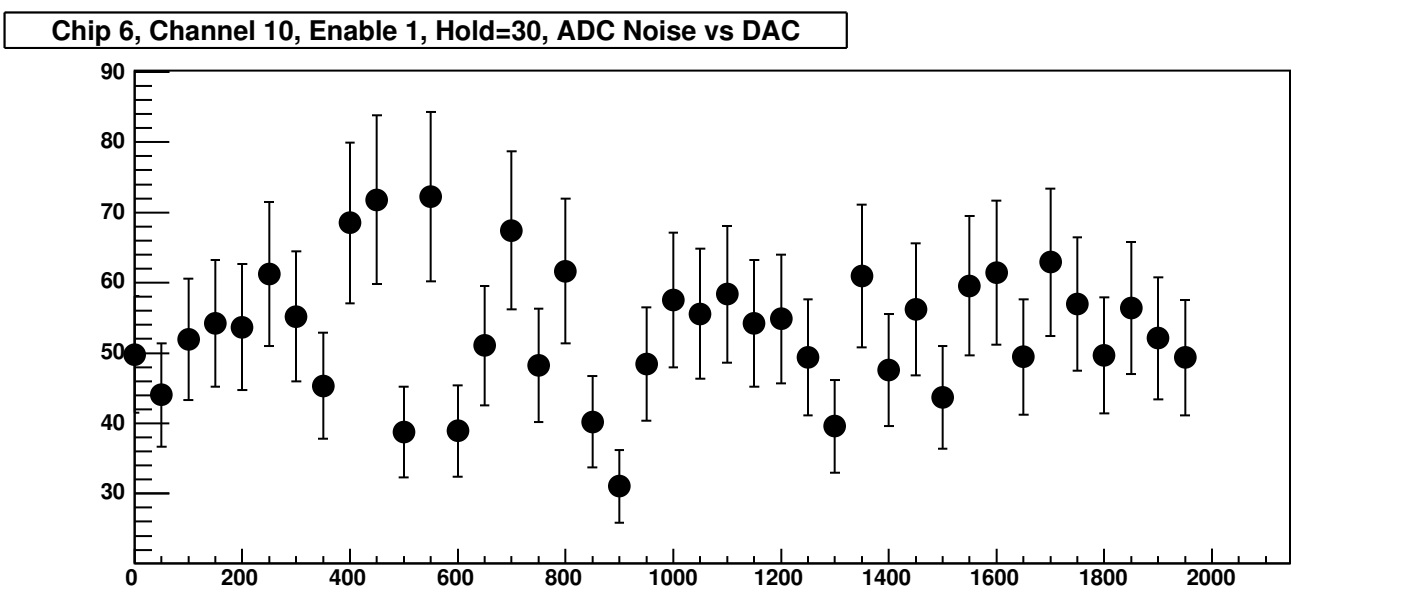


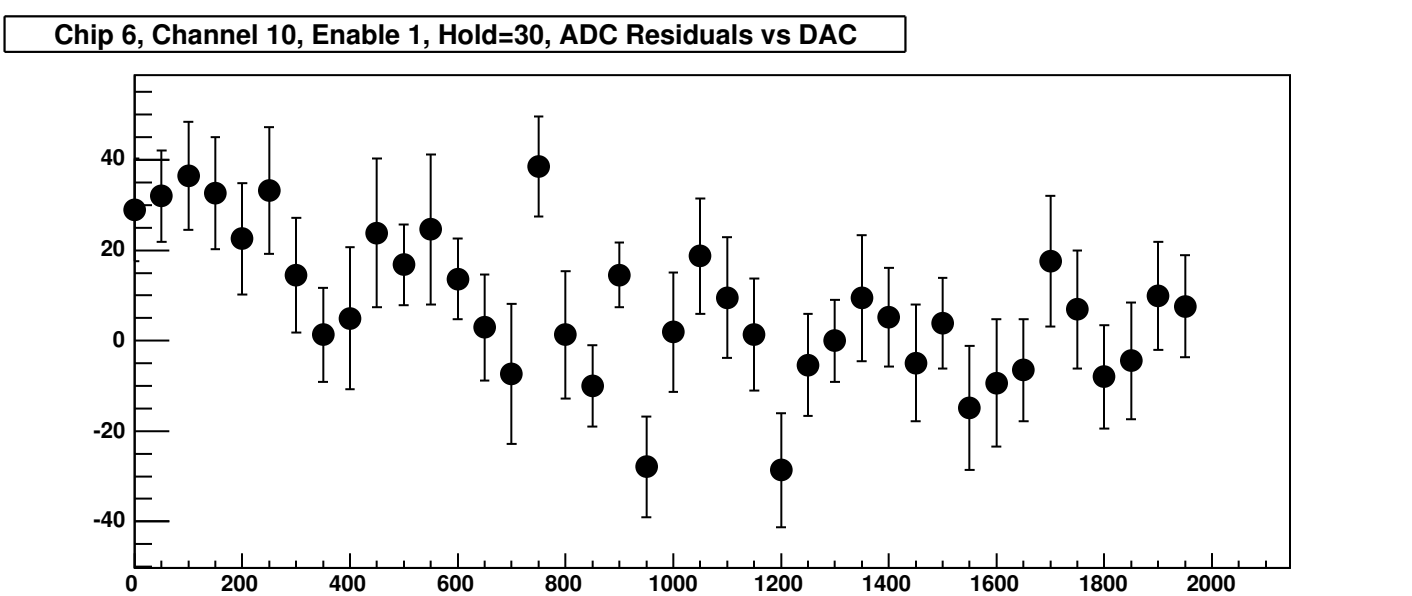

Chip 6, Channel 10, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 47.56 / 11
p0 -1481 ± 41.9
p1 0.9053 ± 0.03813
-200
-400
-600
-800
-1000
-1200
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

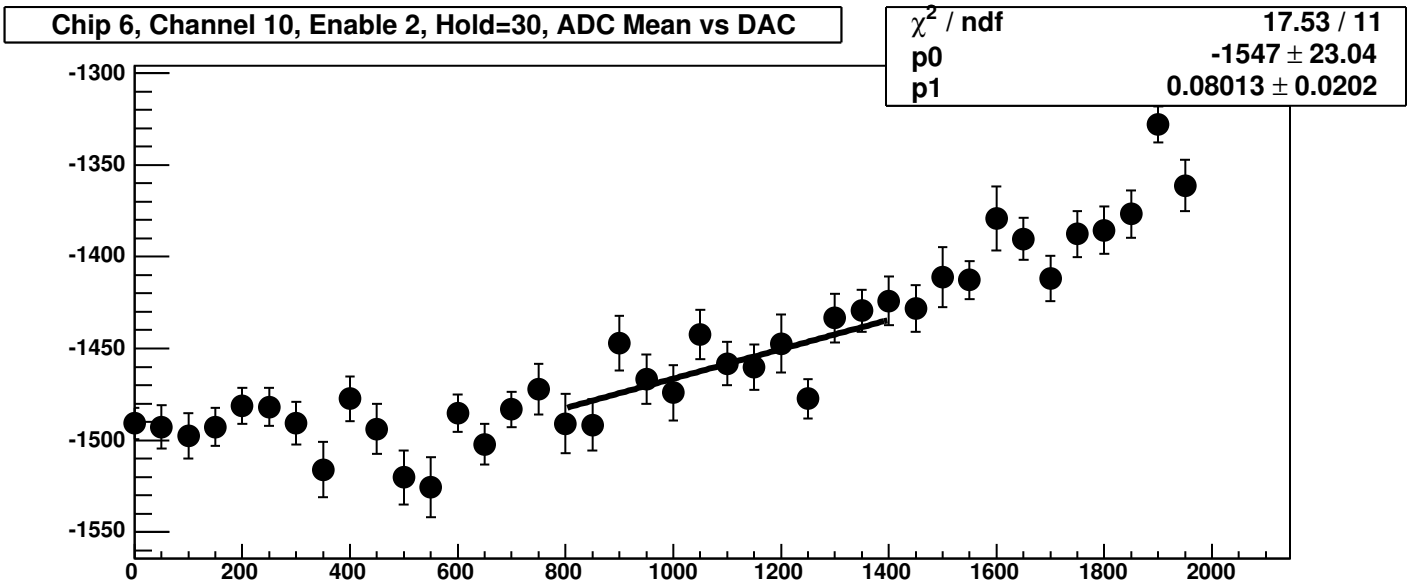

Chip 6, Channel 10, Enable 0, Hold=30, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

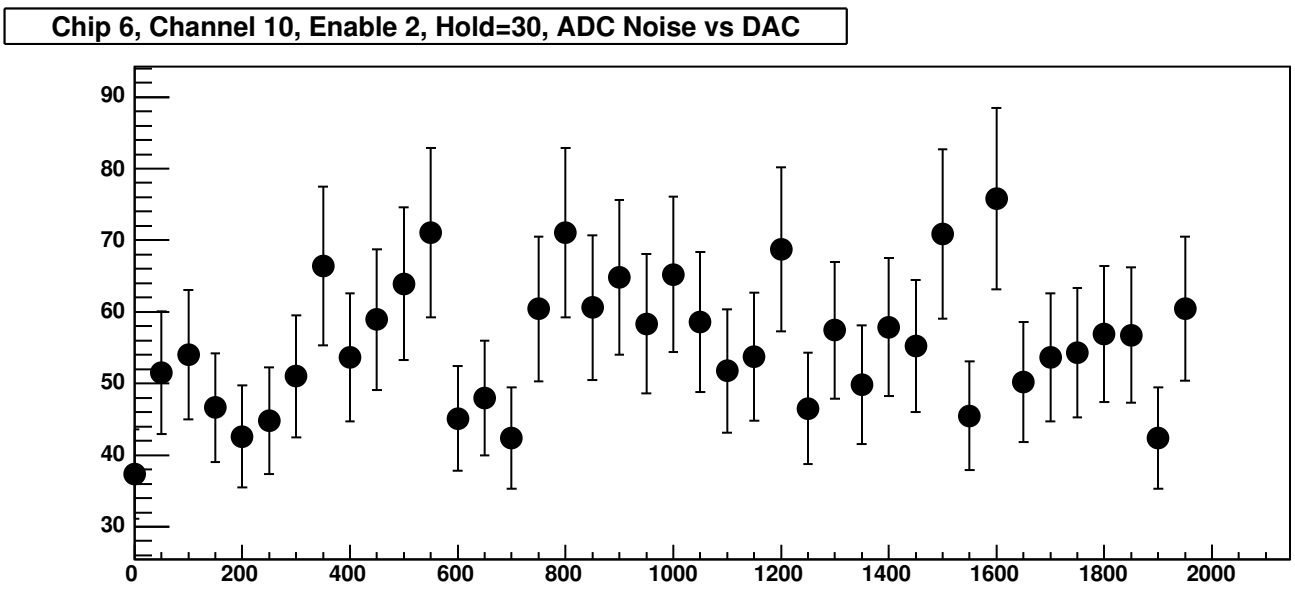

Chip 6, Channel 10, Enable 0, Hold=30, ADC Residuals vs DAC
500
0
-500
-1000
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

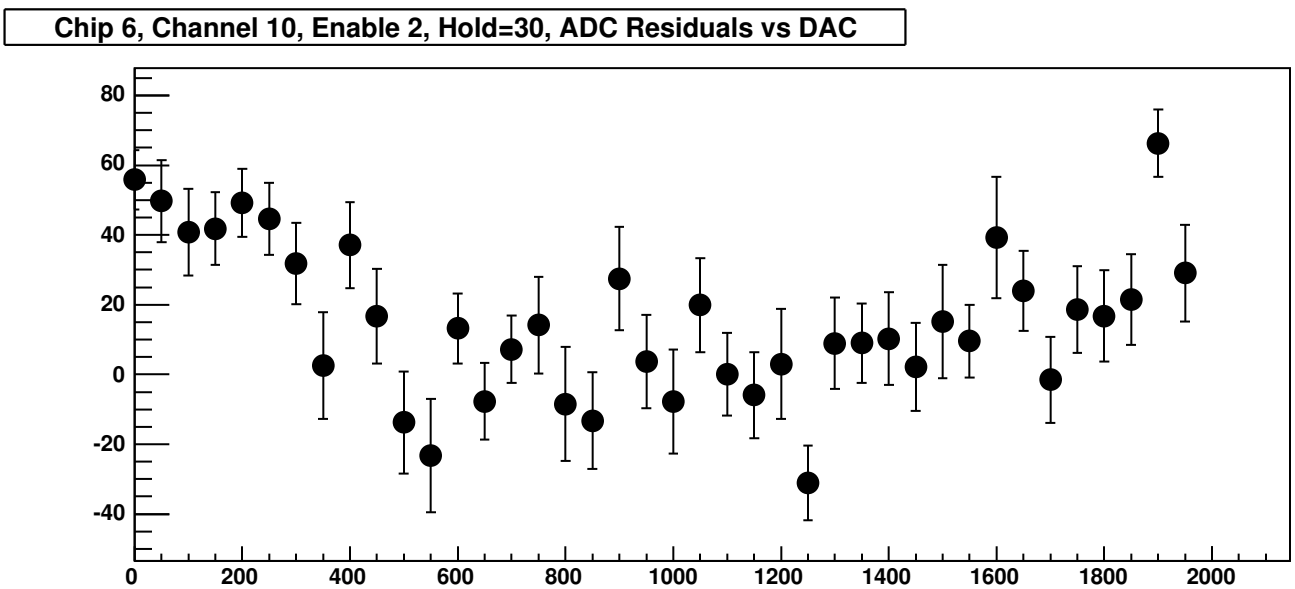

Chip 6, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 20.54 / 11
p0 -1566 ± 17.26
p1 0.0622 ± 0.01575
-1420
-1440
-1460
-1480
-1500
-1520
-1540
-1560
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

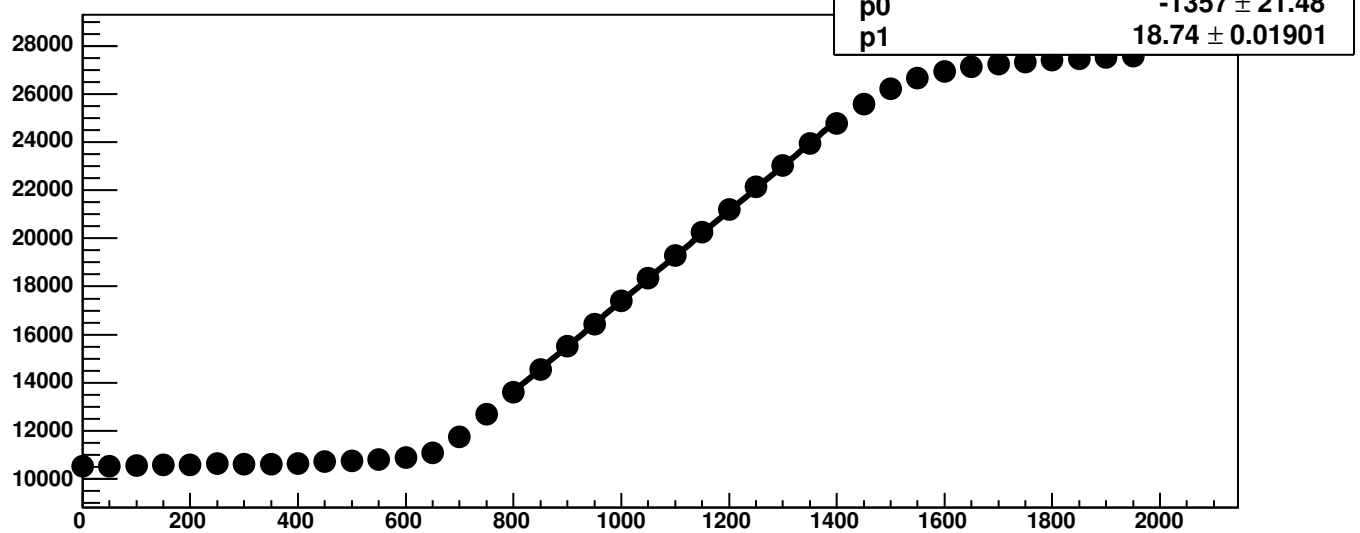

Chip 6, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 17.53 / 11
p0 -1547 ± 23.04
p1 0.08013 ± 0.0202
-1300
-1350
-1400
-1450
-1500
-1550
0
200
400
600
800
1000
1200
1400
1600
1800
2000

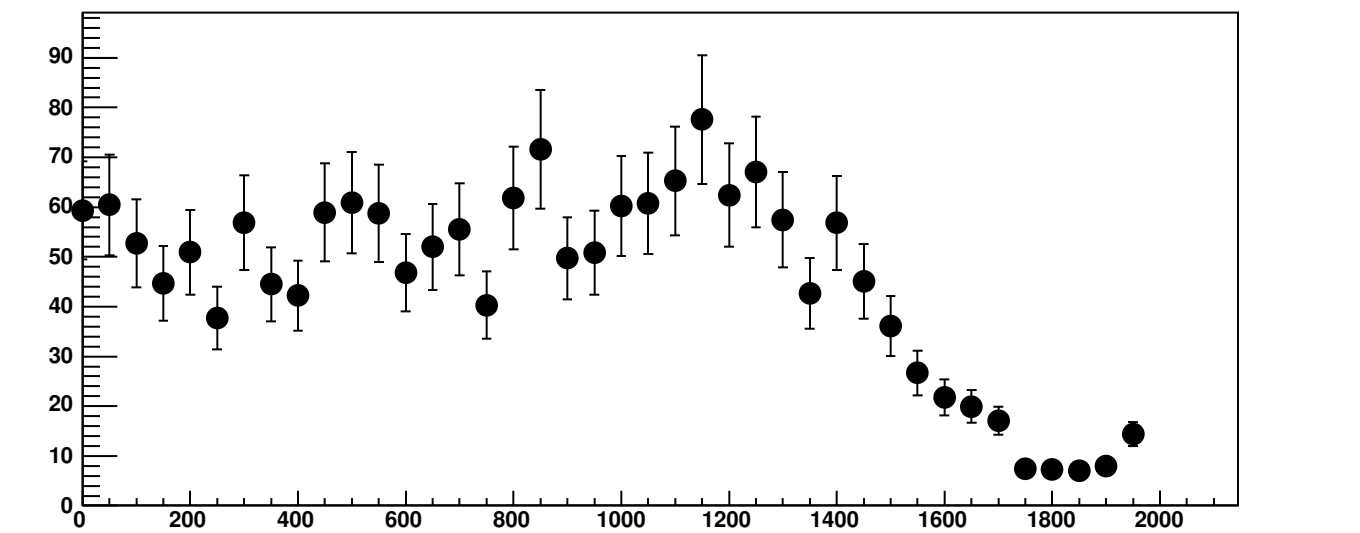

Chip 6, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

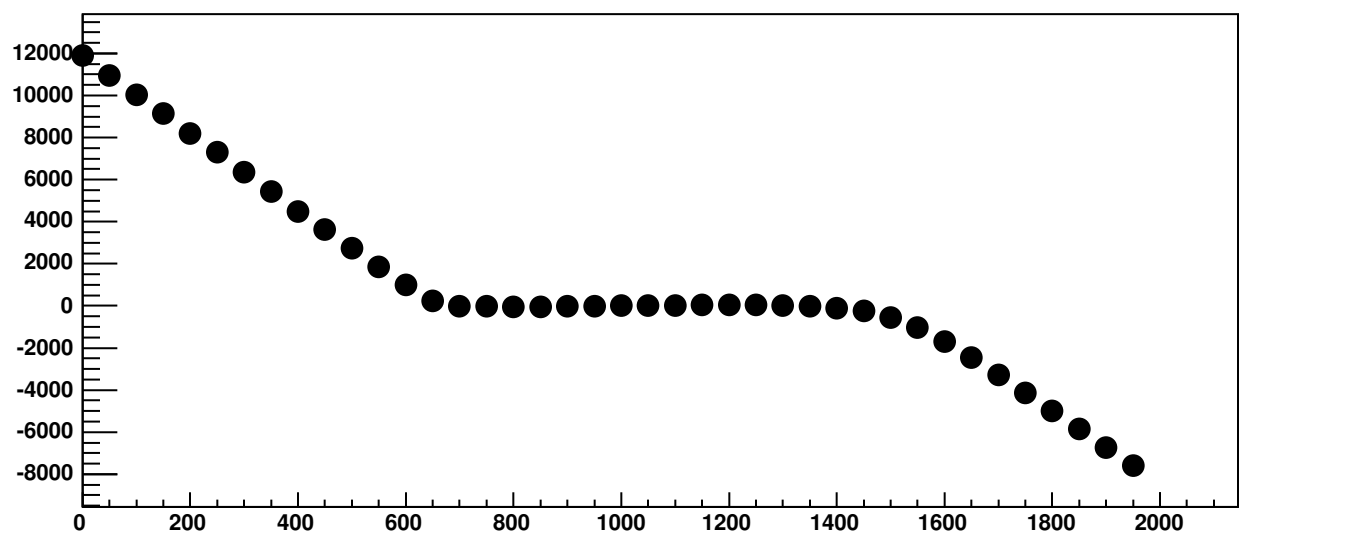
Chip 6, Channel 10, Enable 3!, Hold=30, ADC Mean vs DAC



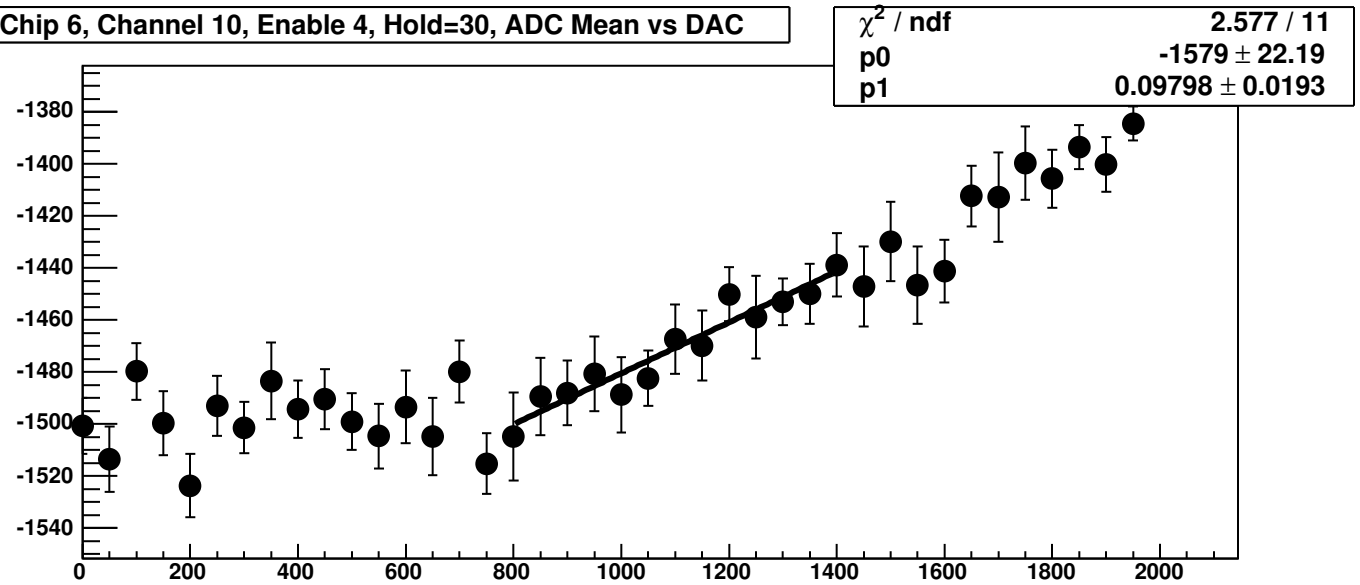
Chip 6, Channel 10, Enable 3!, Hold=30, ADC Noise vs DAC



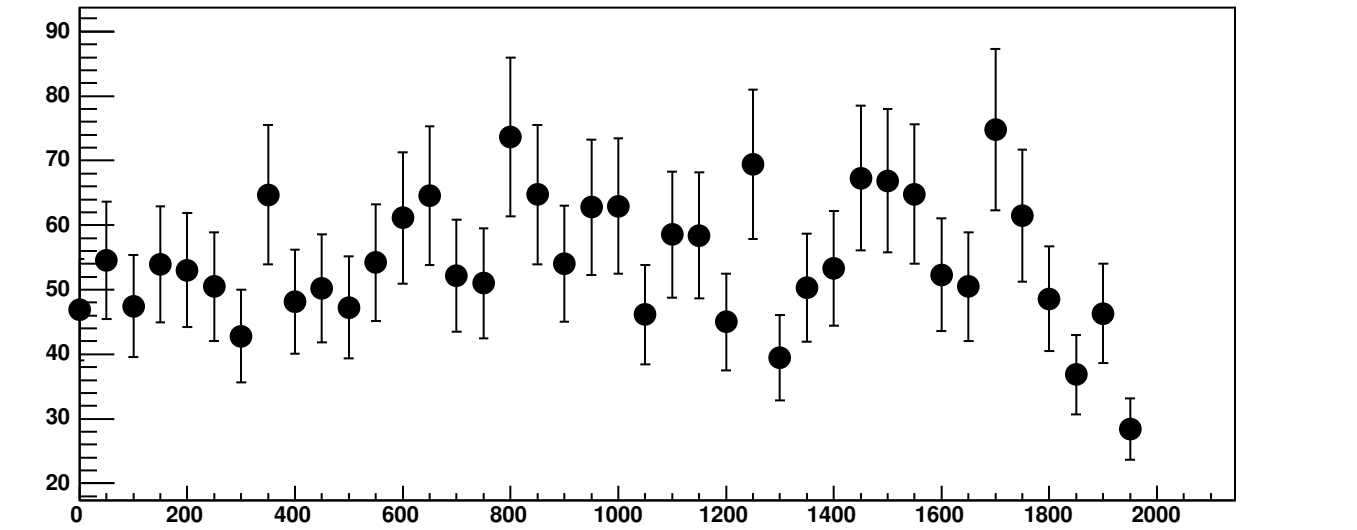
Chip 6, Channel 10, Enable 3!, Hold=30, ADC Residuals vs DAC



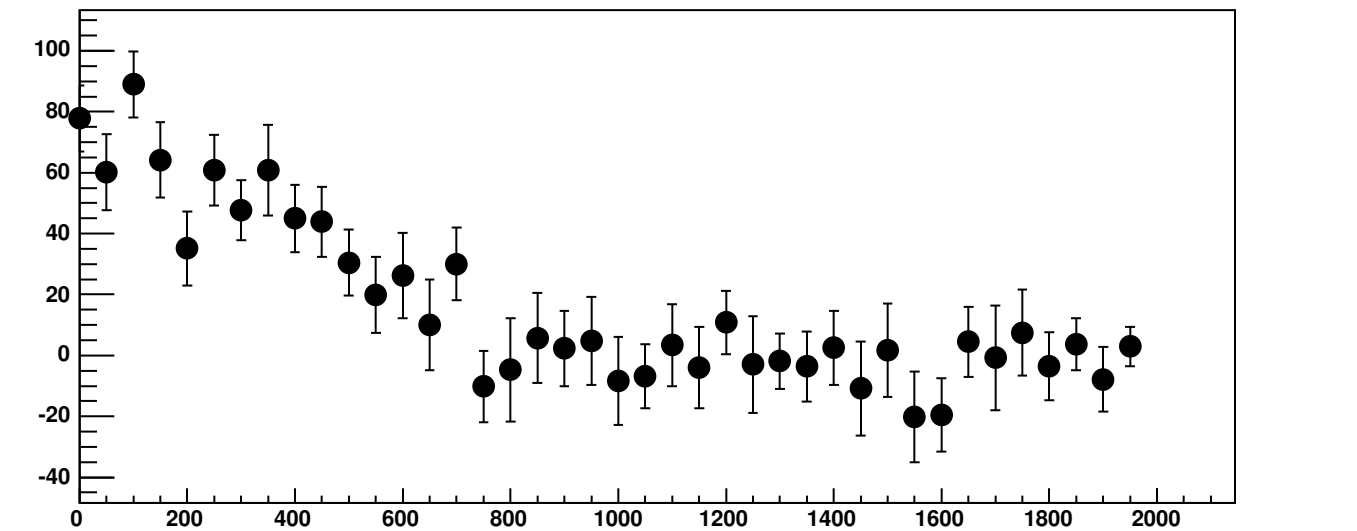
Chip 6, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC

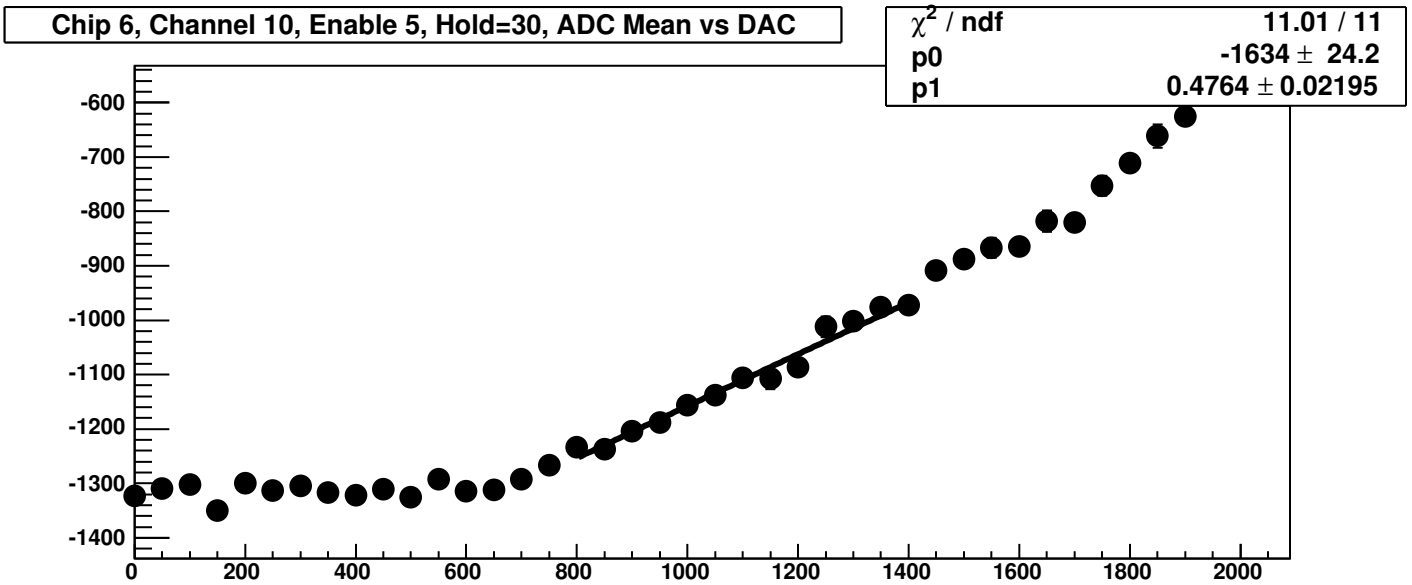


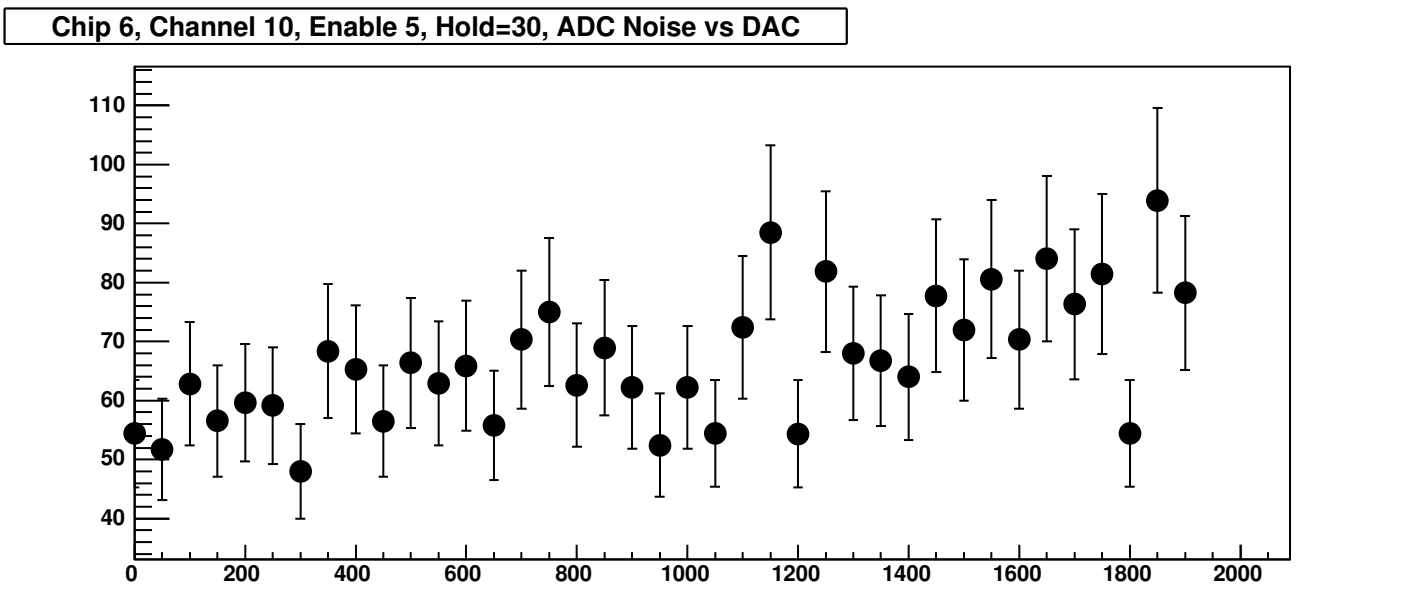
Chip 6, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC

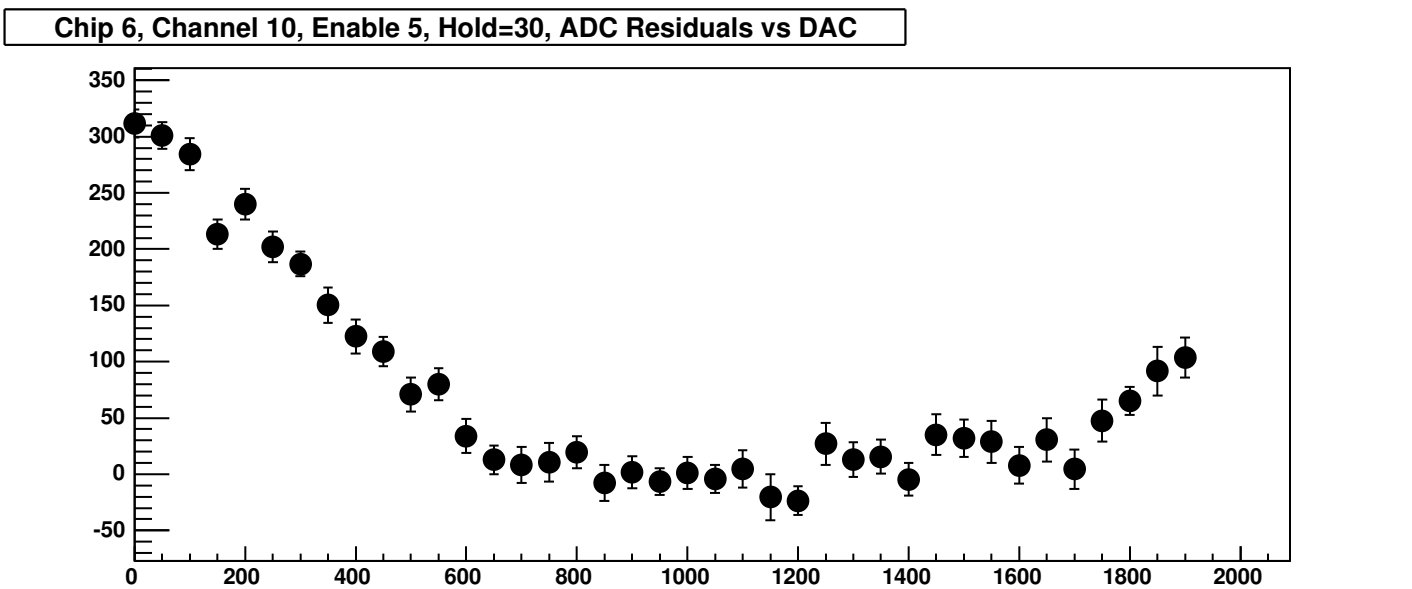


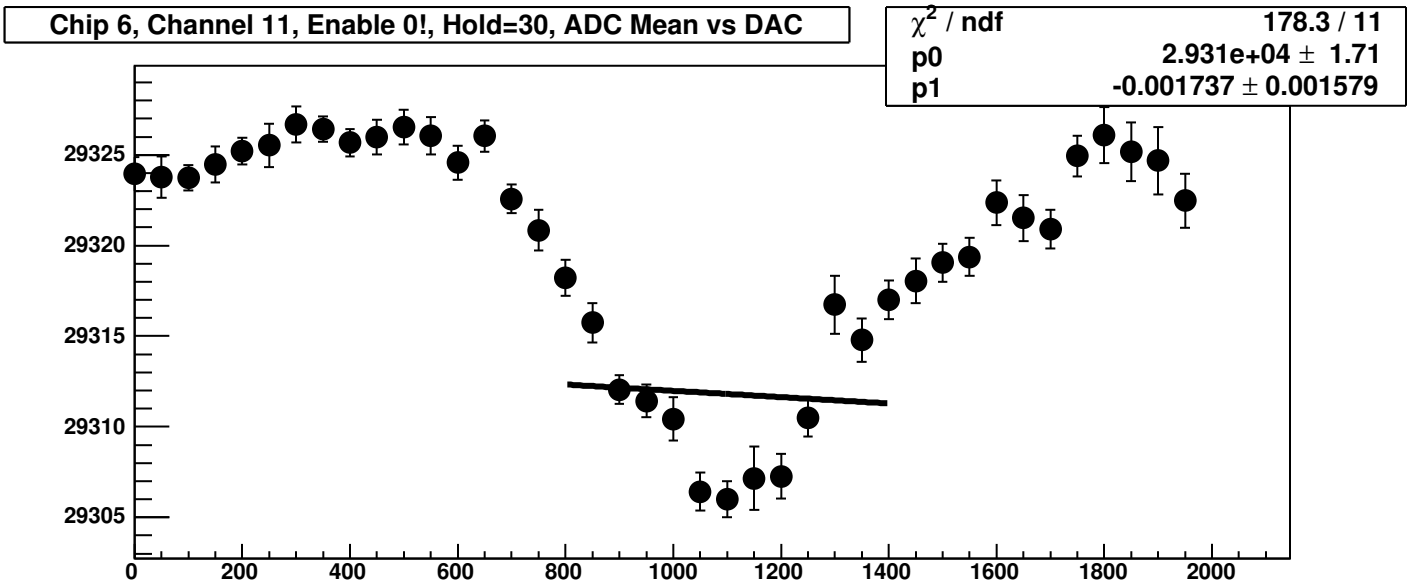
Chip 6, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC

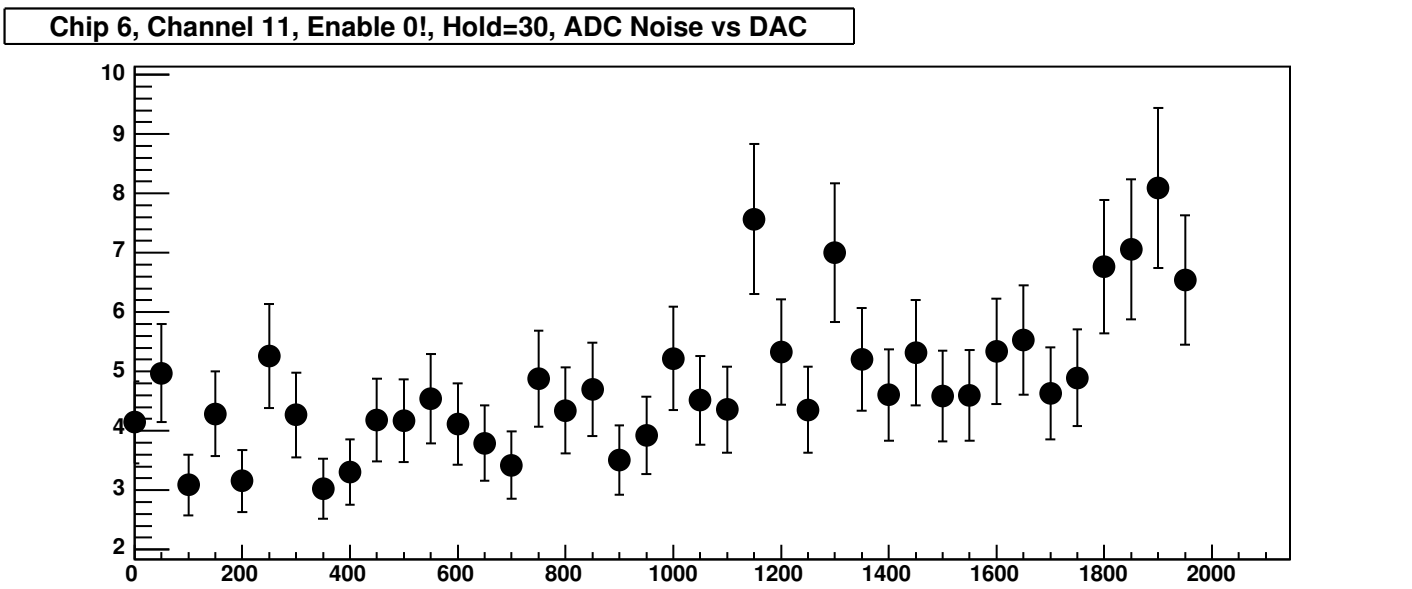


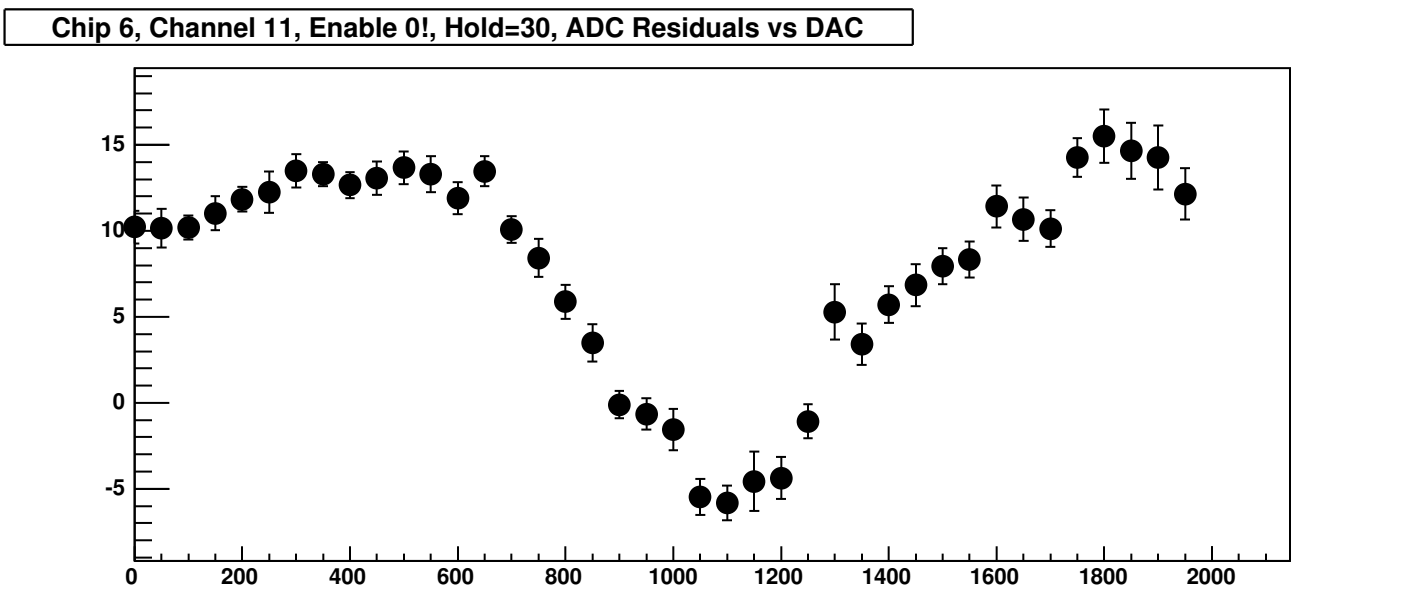

Chip 6, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 11.01 / 11
p0 -1634 ± 24.2
p1 0.4764 ± 0.02195
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

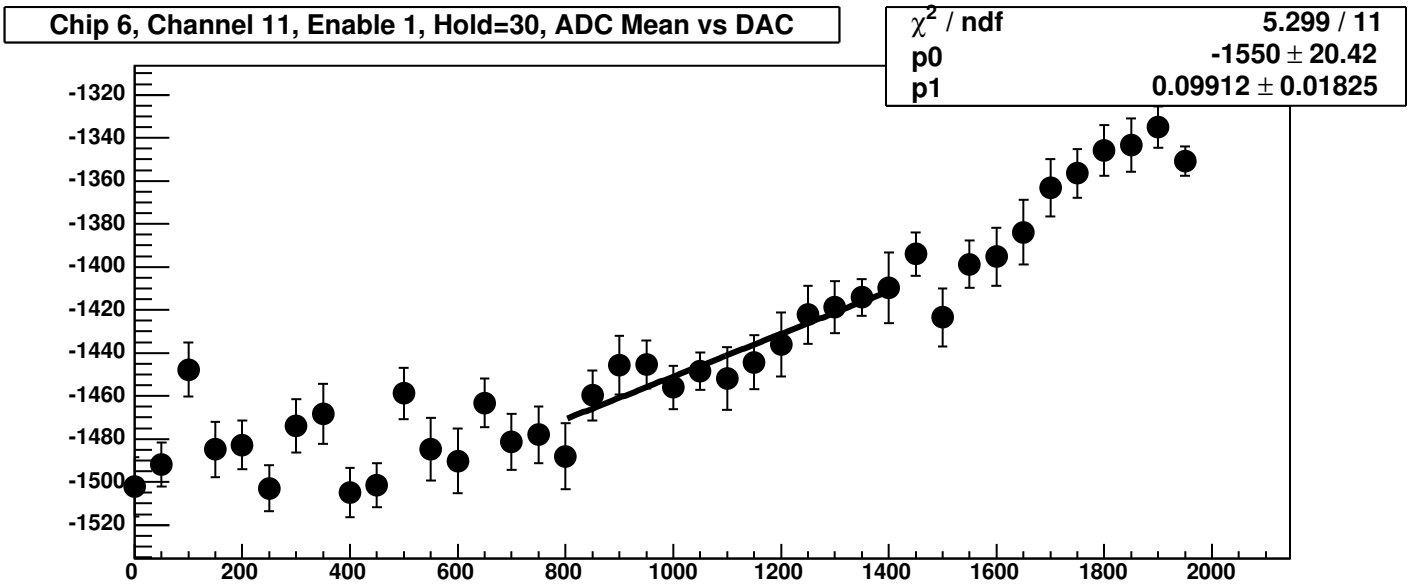

Chip 6, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

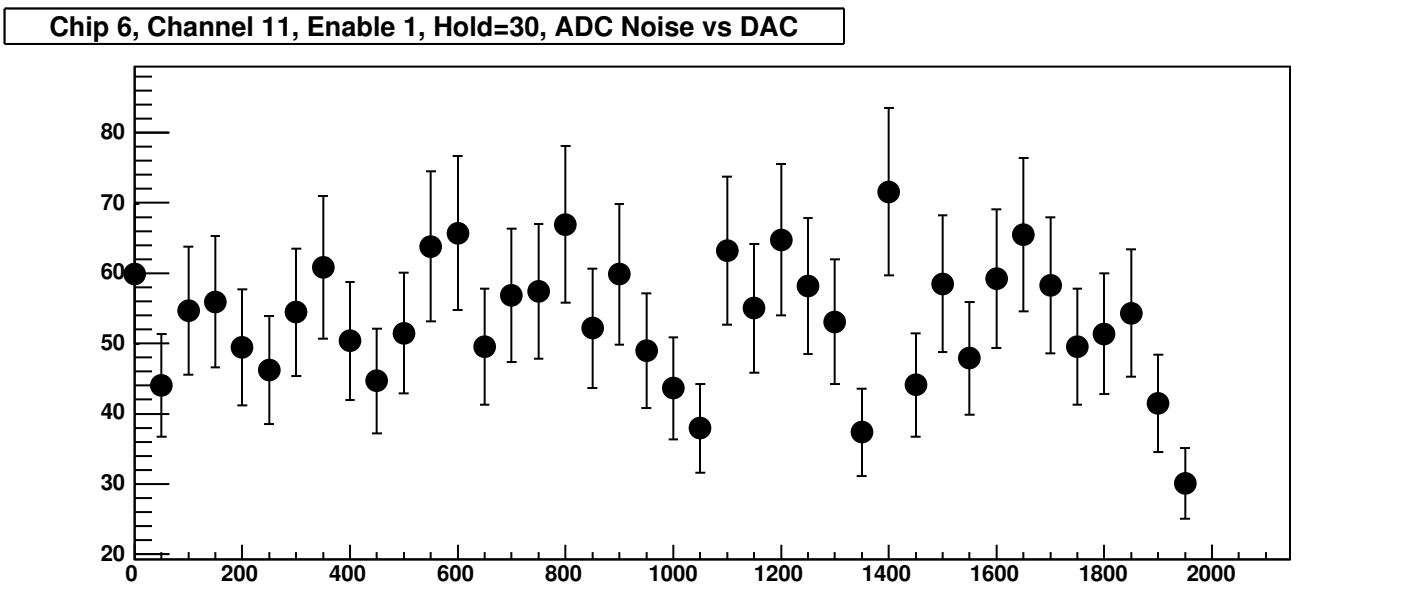

Chip 6, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

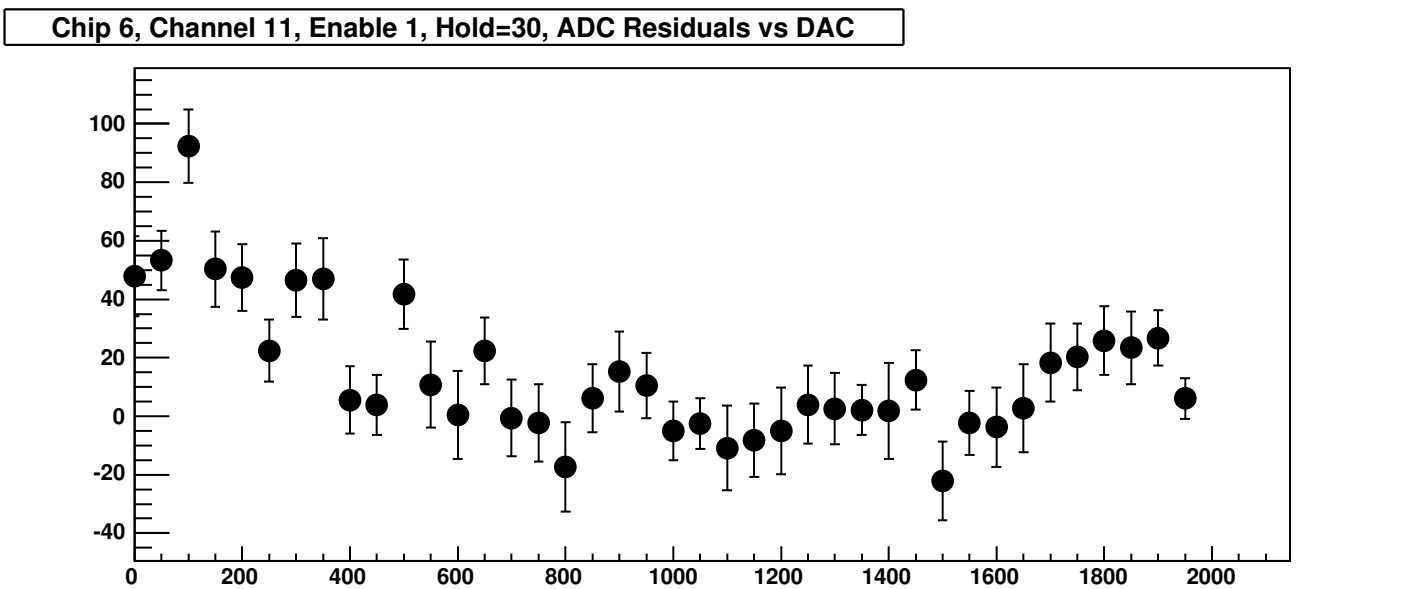

Chip 6, Channel 11, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 178.3 / 11
p0 2.931e+04 ± 1.71
p1 -0.001737 ± 0.001579
29325
29320
29315
29310
29305
0
200
400
600
800
1000
1200
1400
1600
1800
2000

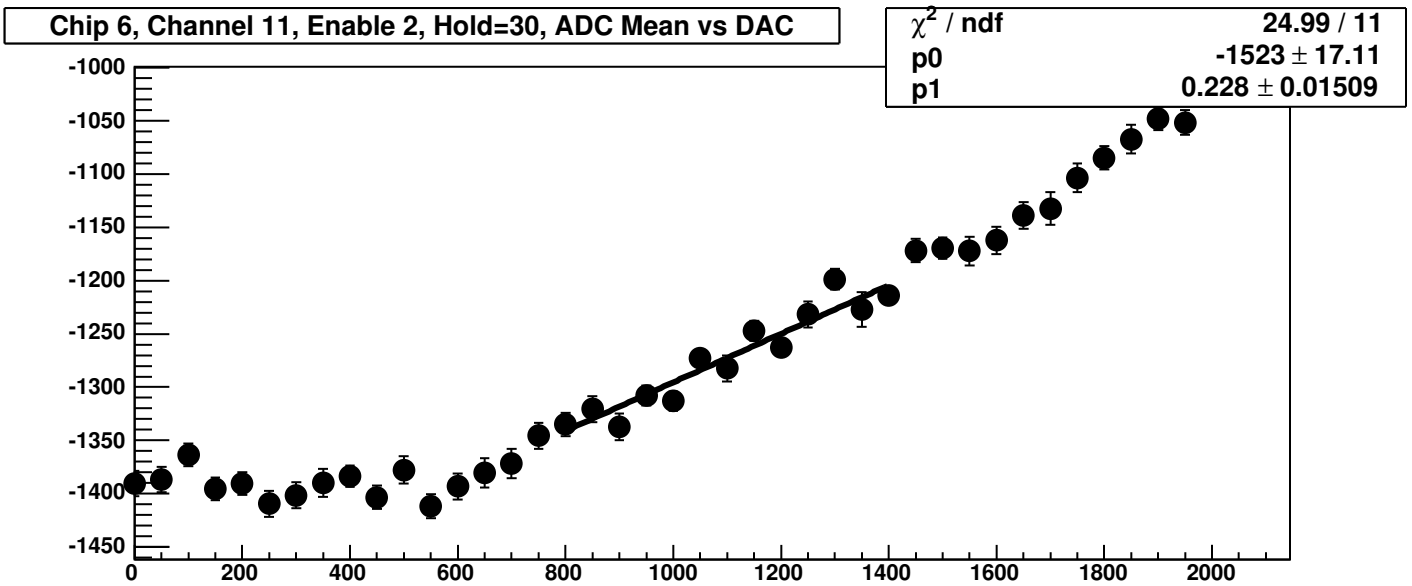

Chip 6, Channel 11, Enable 0!, Hold=30, ADC Noise vs DAC
10
9
8
7
6
5
4
3
2
0
200
400
600
800
1000
1200
1400
1600
1800
2000

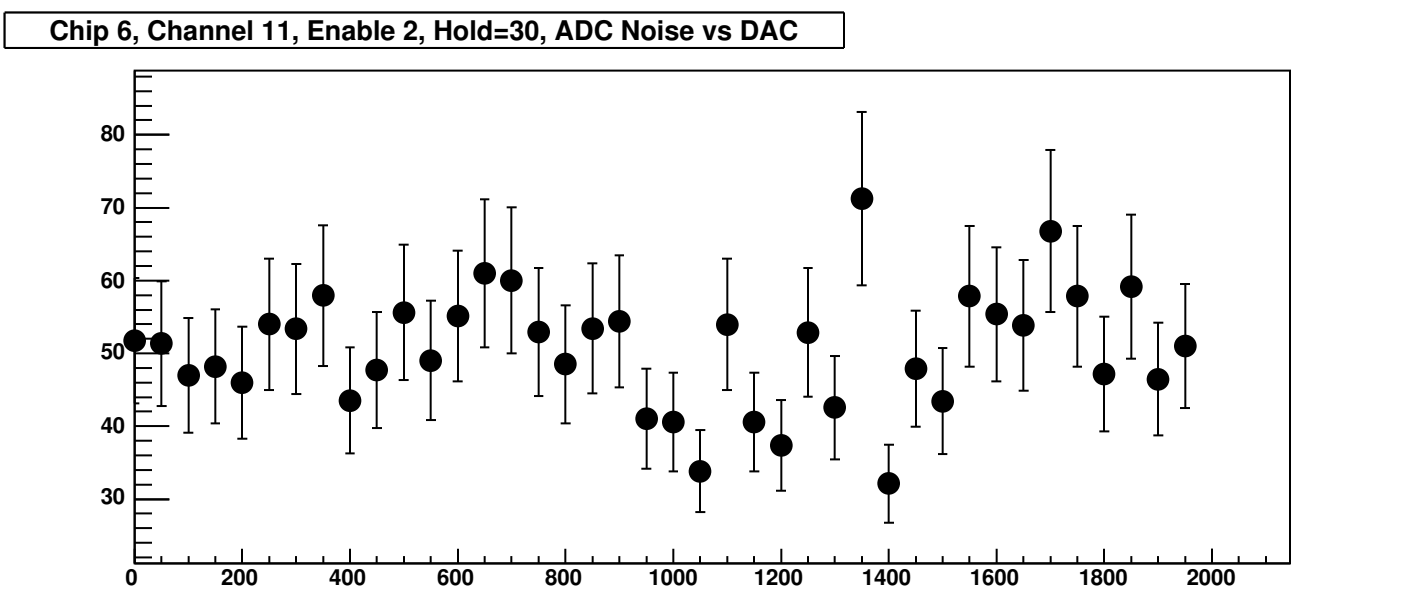

Chip 6, Channel 11, Enable 0!, Hold=30, ADC Residuals vs DAC
15
10
5
0
-5
0
200
400
600
800
1000
1200
1400
1600
1800
2000

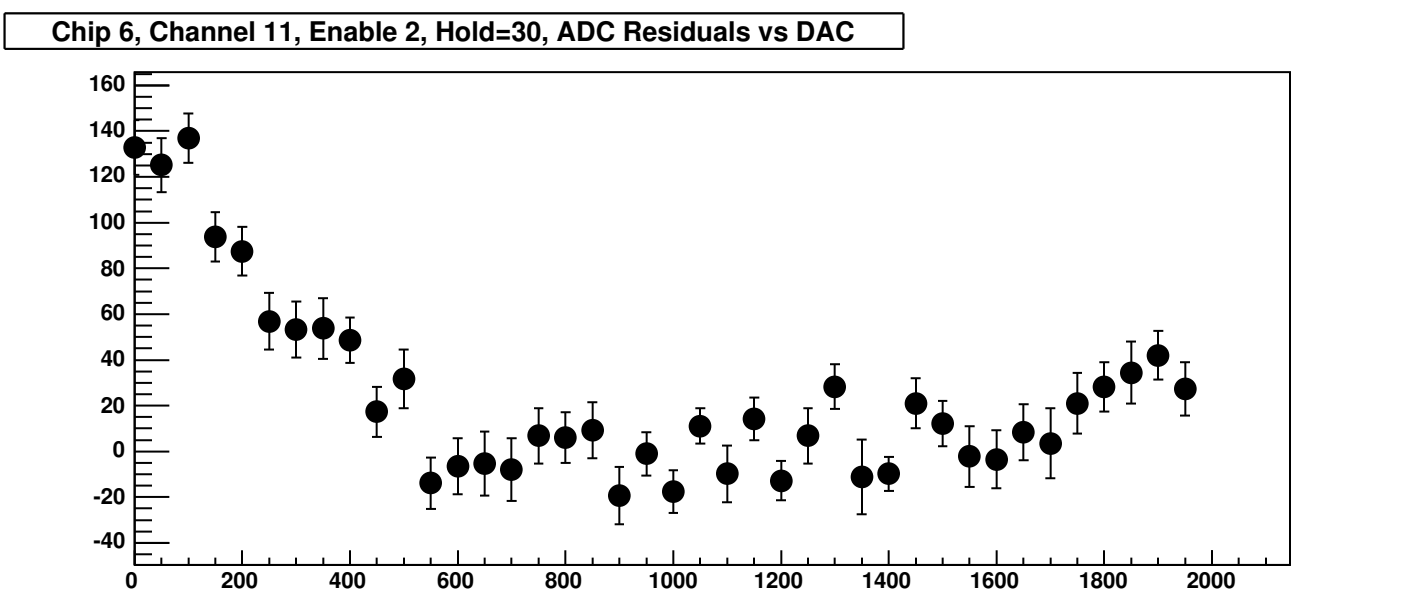

Chip 6, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 5.299 / 11
p0 -1550 ± 20.42
p1 0.09912 ± 0.01825
-1320
-1340
-1360
-1380
-1400
-1420
-1440
-1460
-1480
-1500
-1520
0
200
400
600
800
1000
1200
1400
1600
1800
2000

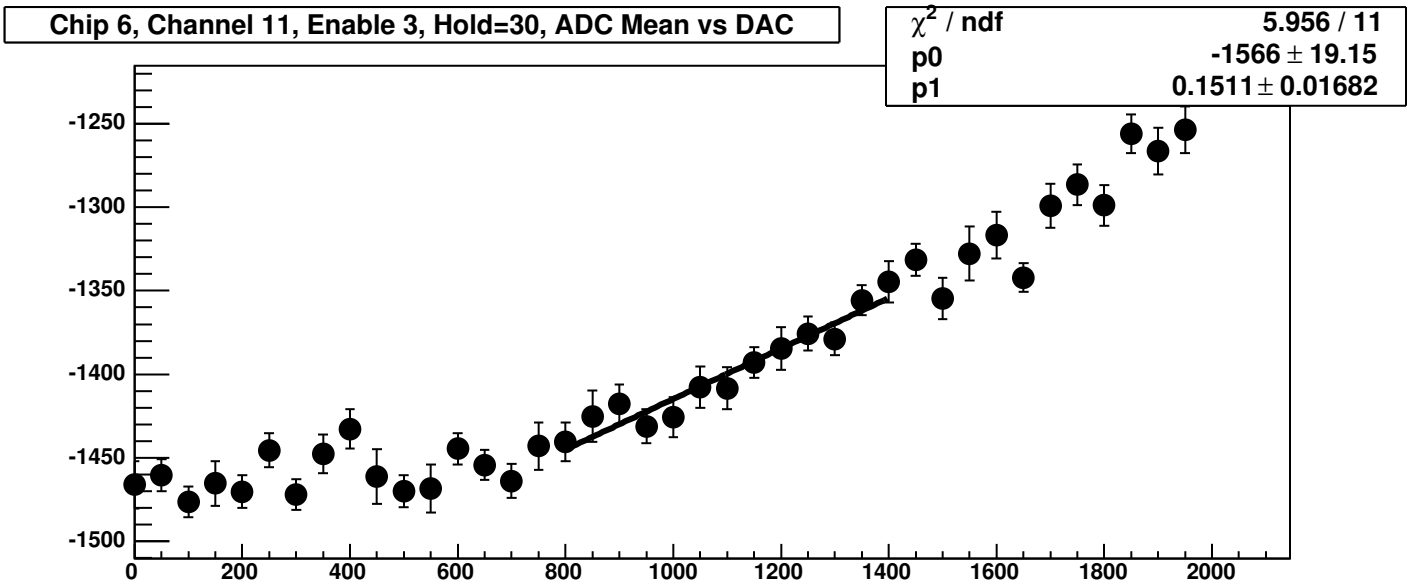

Chip 6, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

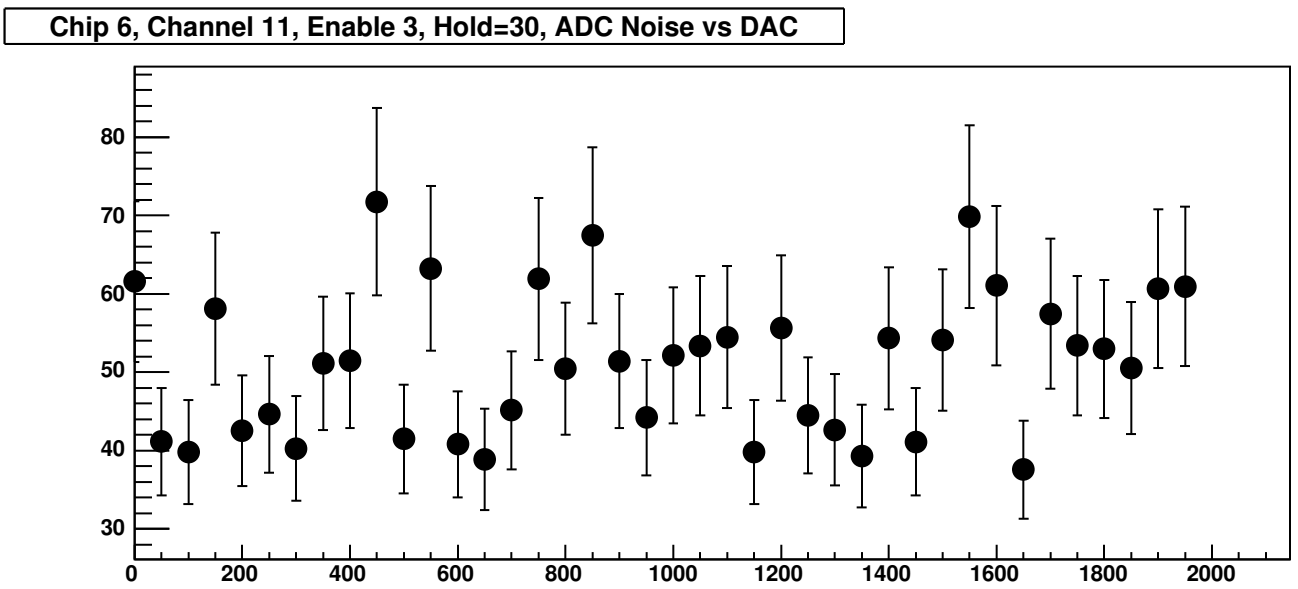

Chip 6, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

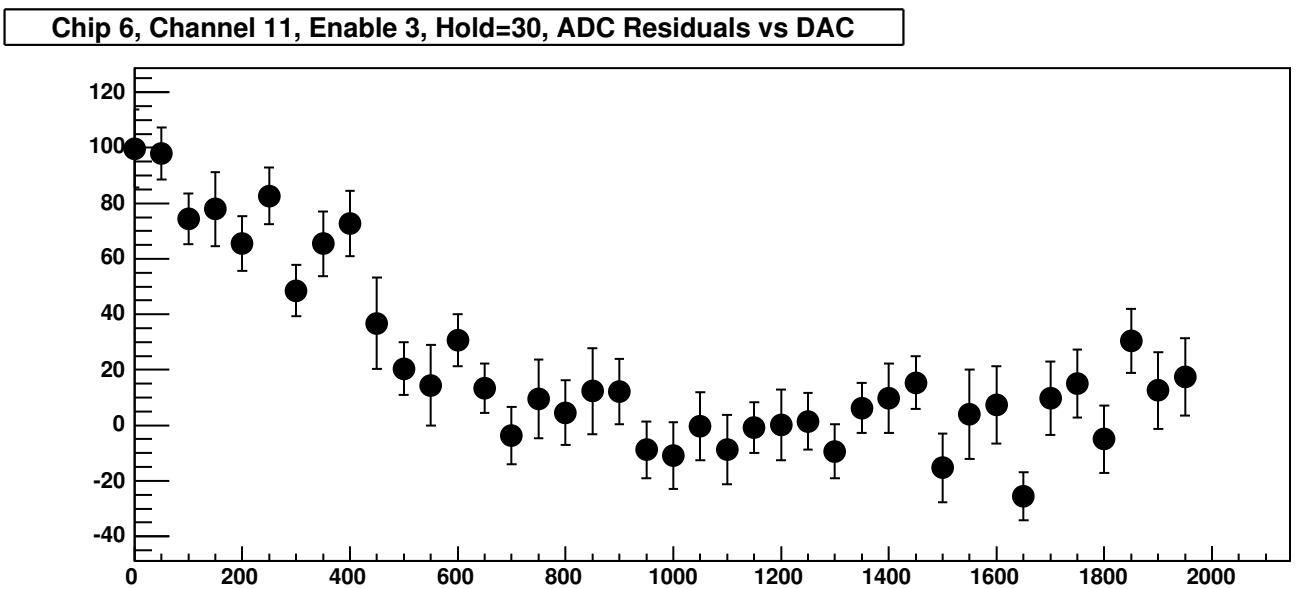

Chip 6, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 24.99 / 11
p0 -1523 ± 17.11
p1 0.228 ± 0.01509
-1000
-1050
-1100
-1150
-1200
-1250
-1300
-1350
-1400
-1450
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

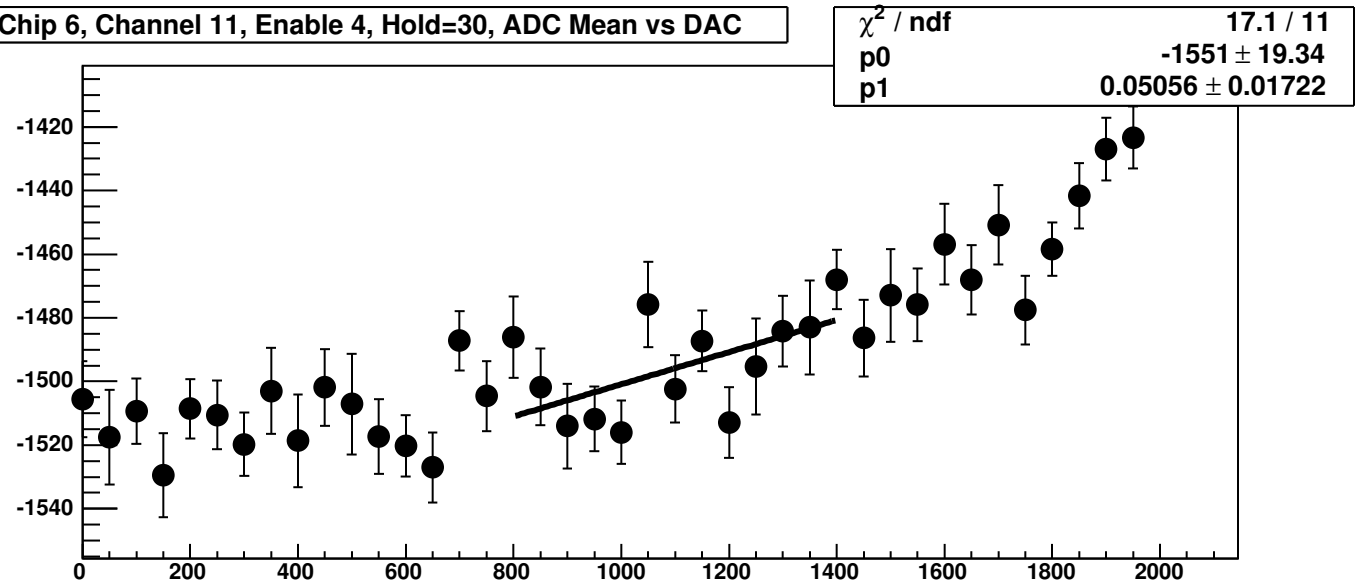

Chip 6, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC
160
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 11, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5.956 / 11
p0 -1566 ± 19.15
p1 0.1511 ± 0.01682
-1250
-1300
-1350
-1400
-1450
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

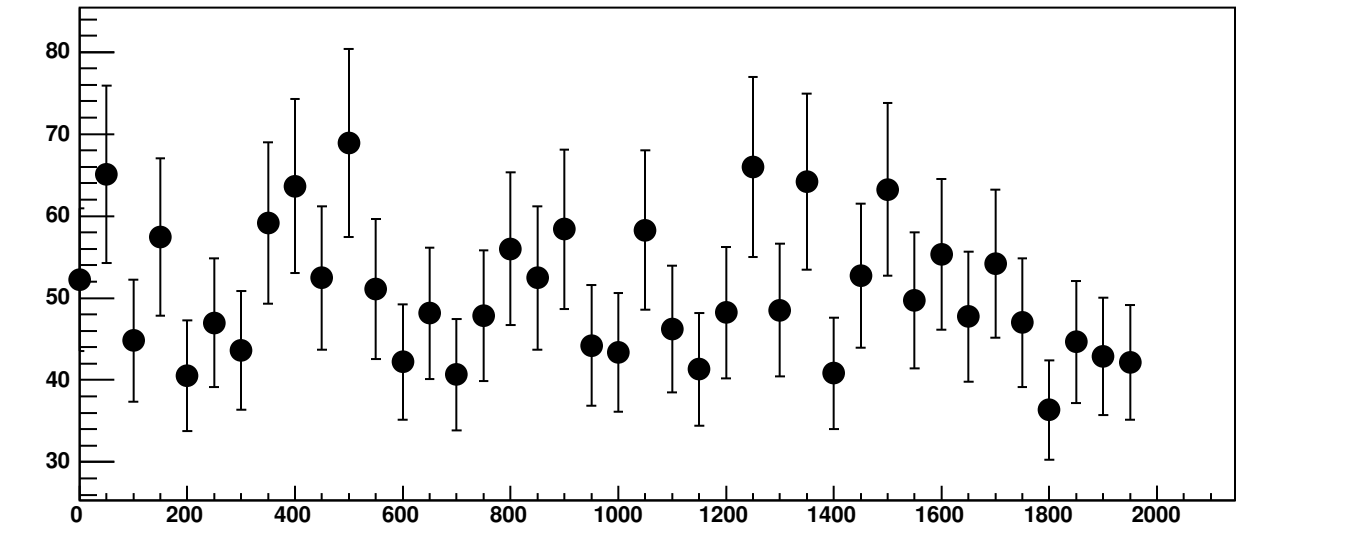

Chip 6, Channel 11, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 11, Enable 3, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

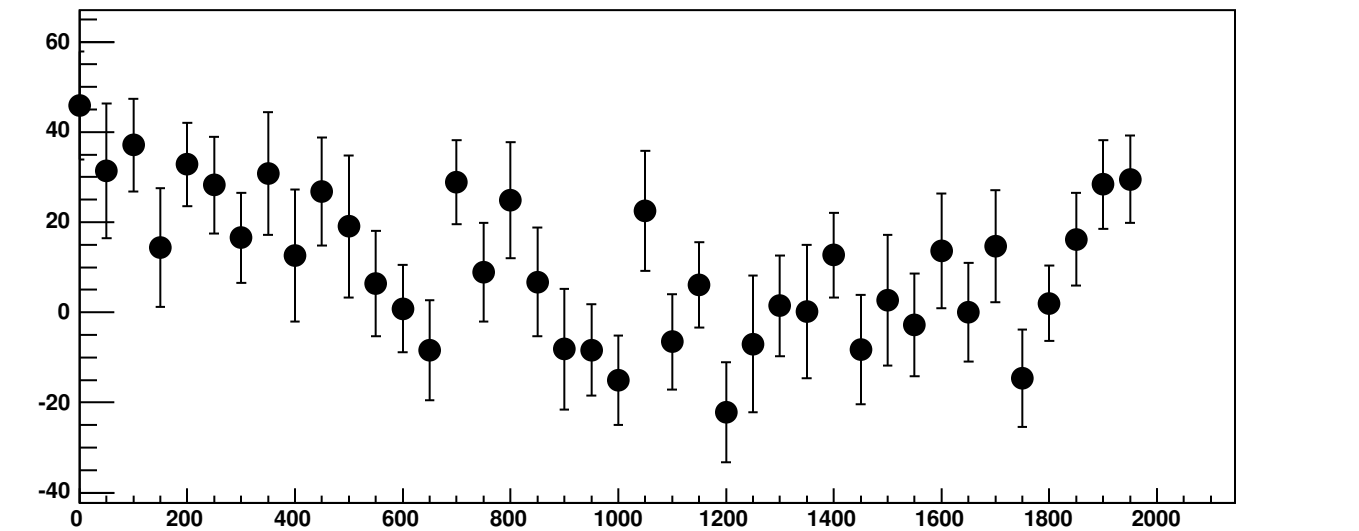
Chip 6, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC

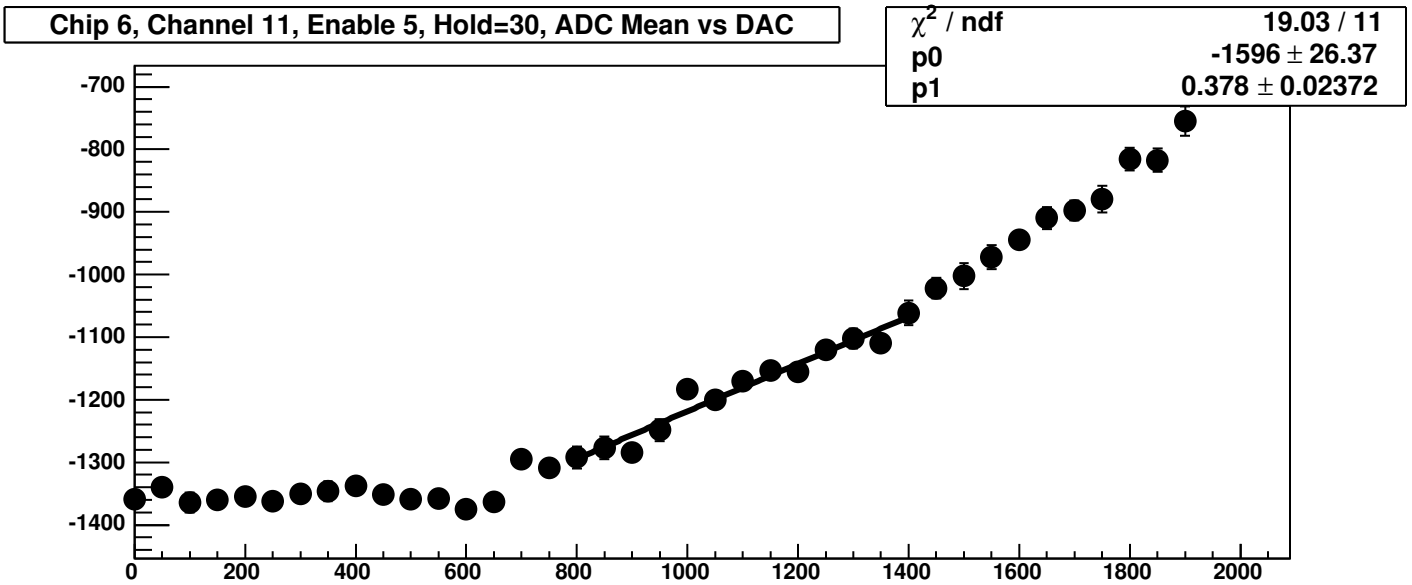


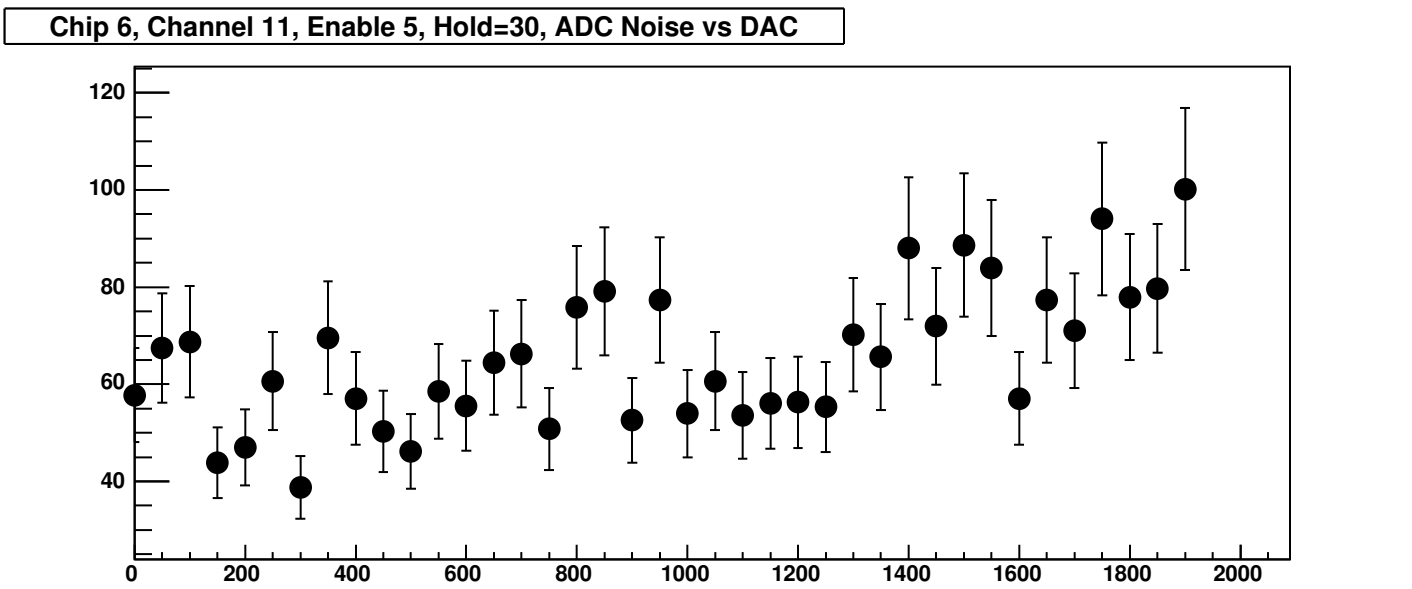
Chip 6, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC

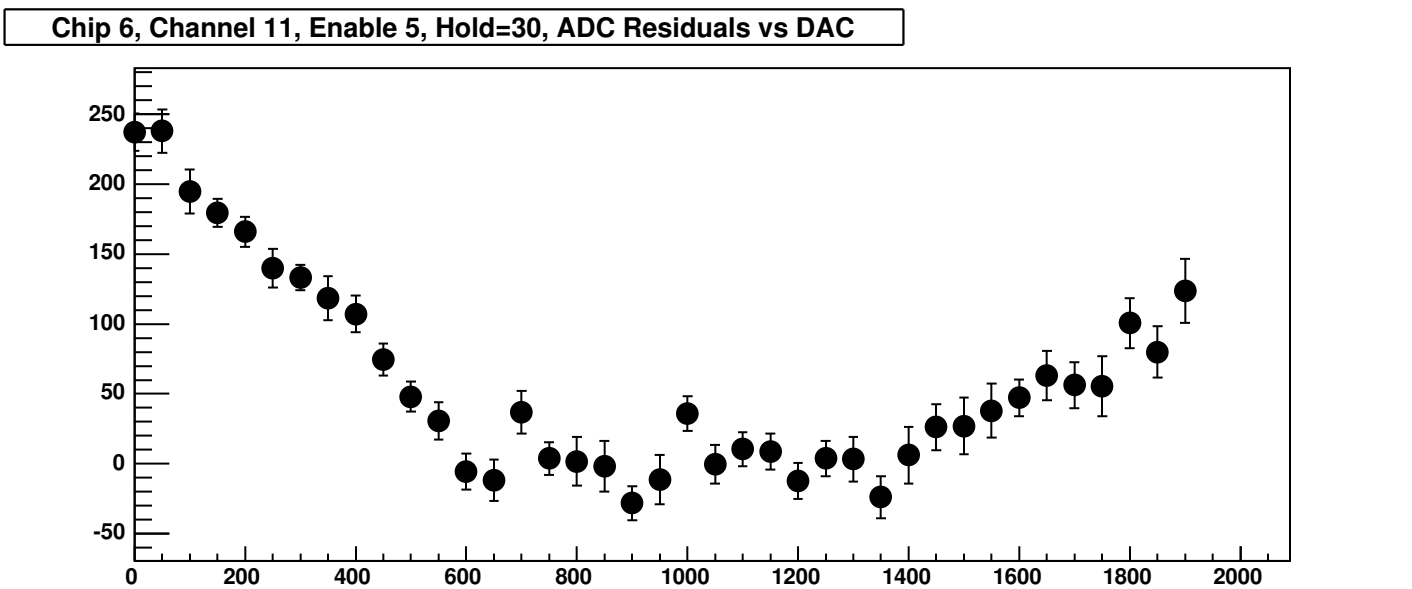


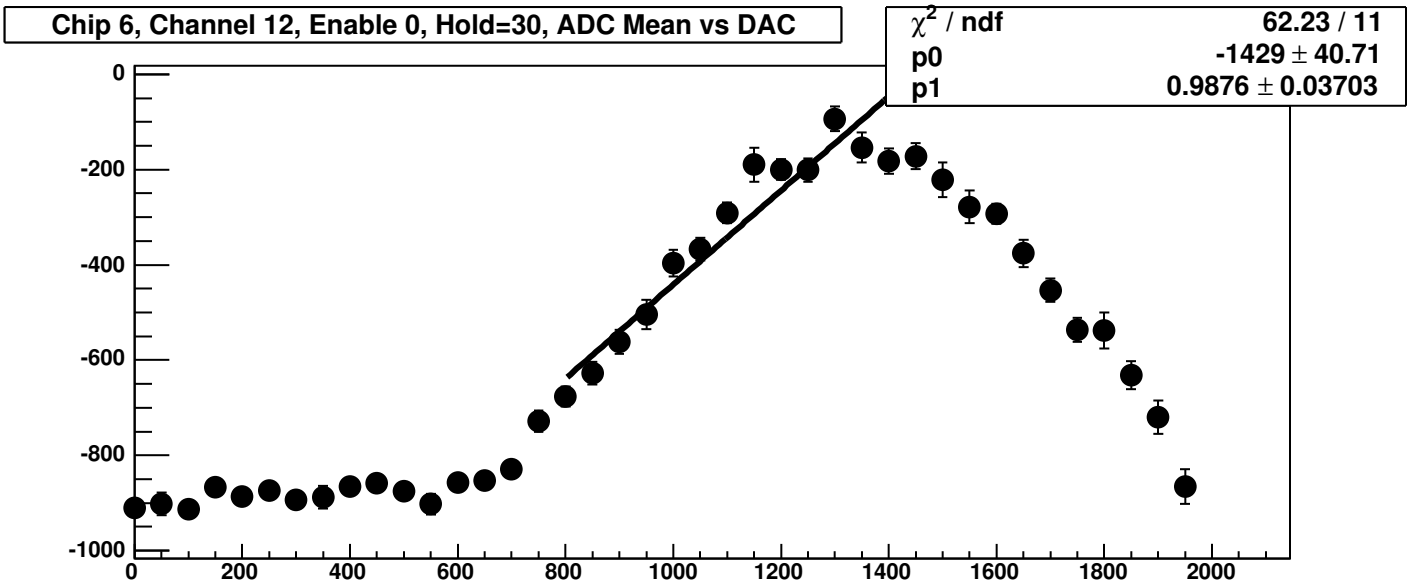
Chip 6, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC

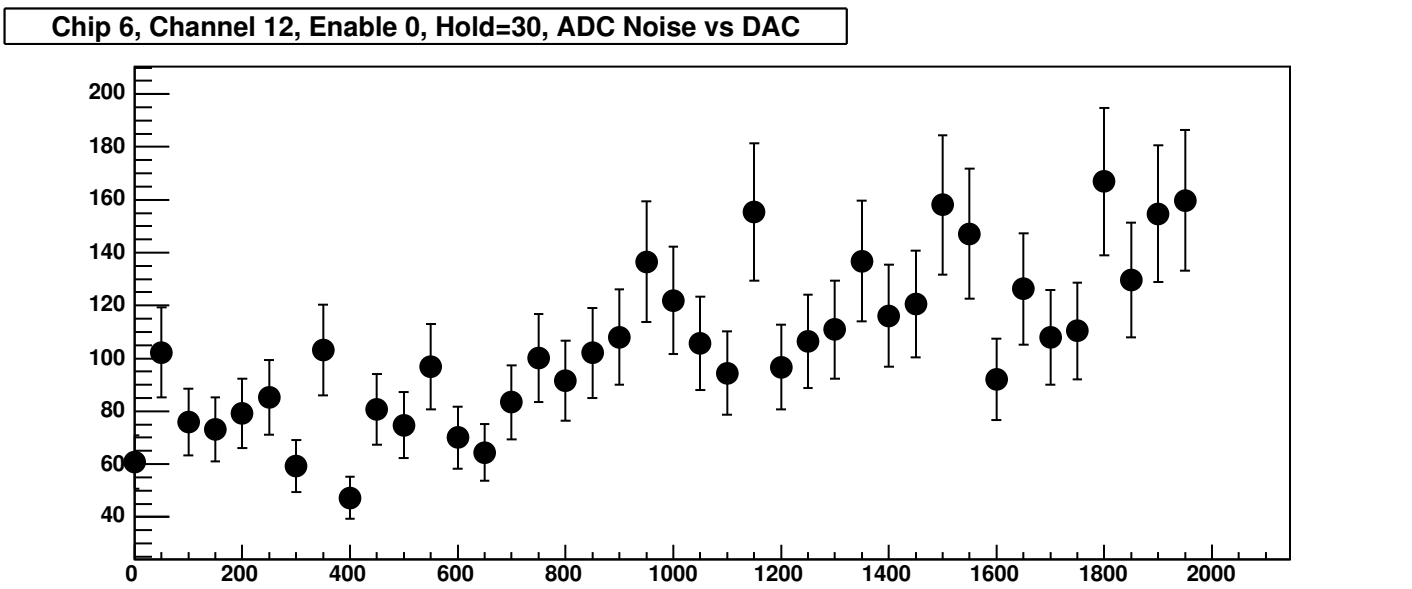


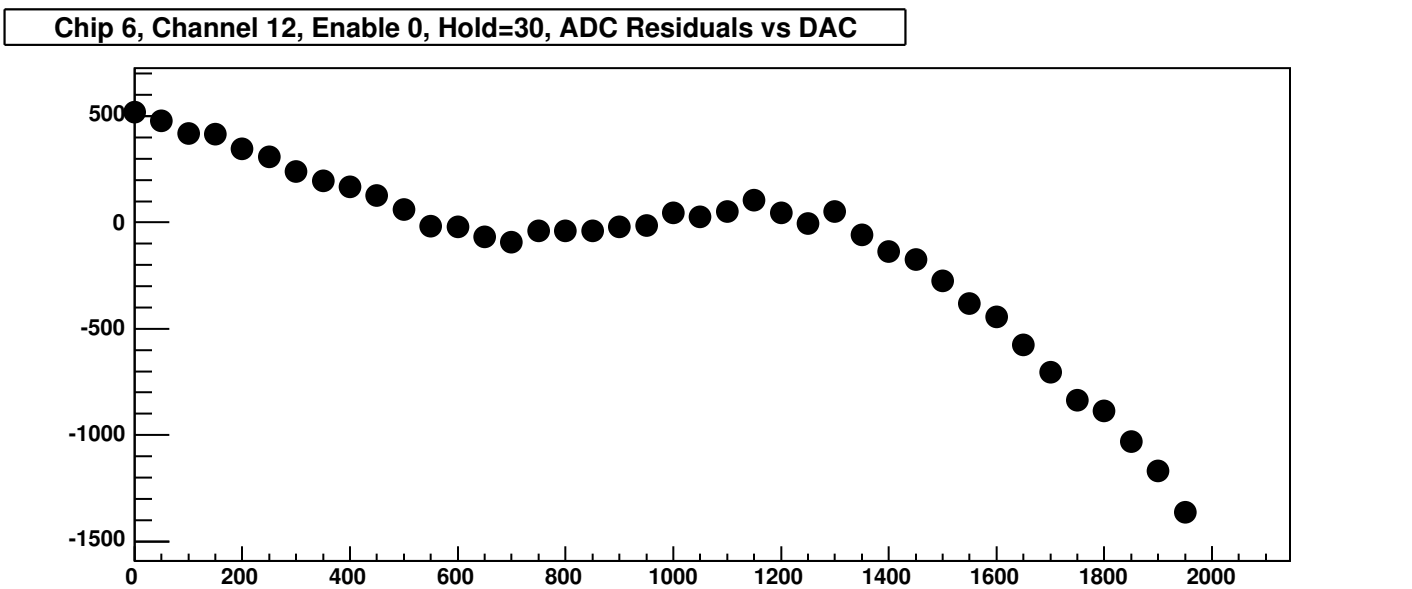

Chip 6, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 19.03 / 11
p0 -1596 ± 26.37
p1 0.378 ± 0.02372
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

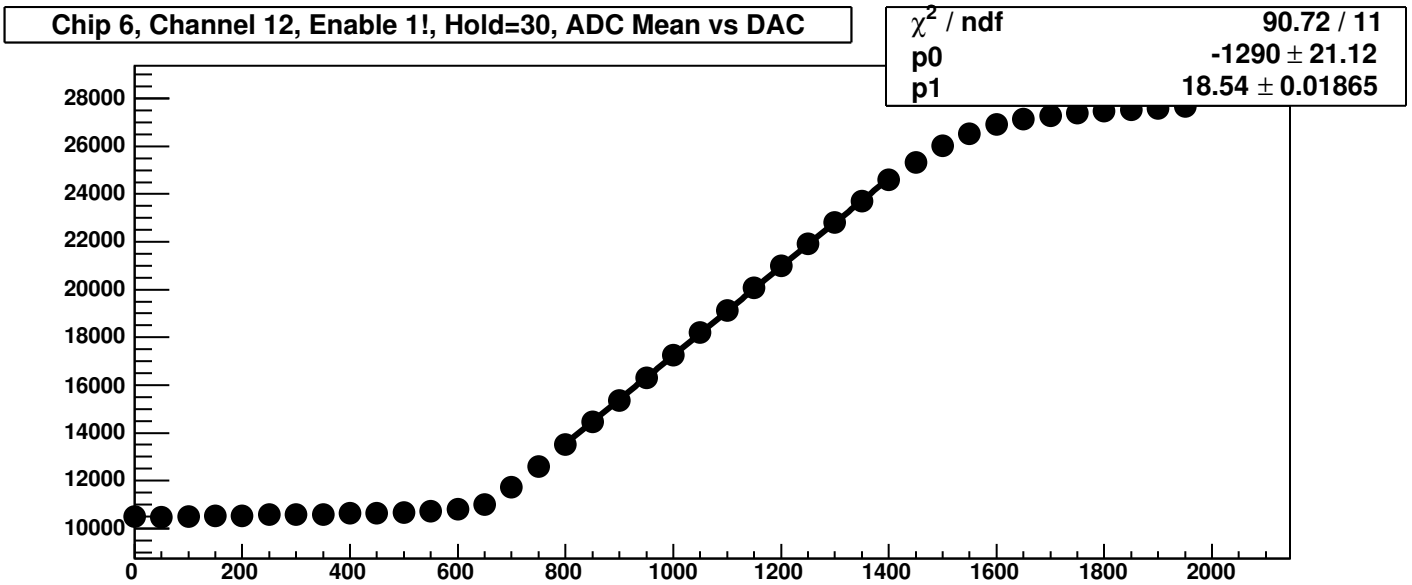

Chip 6, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

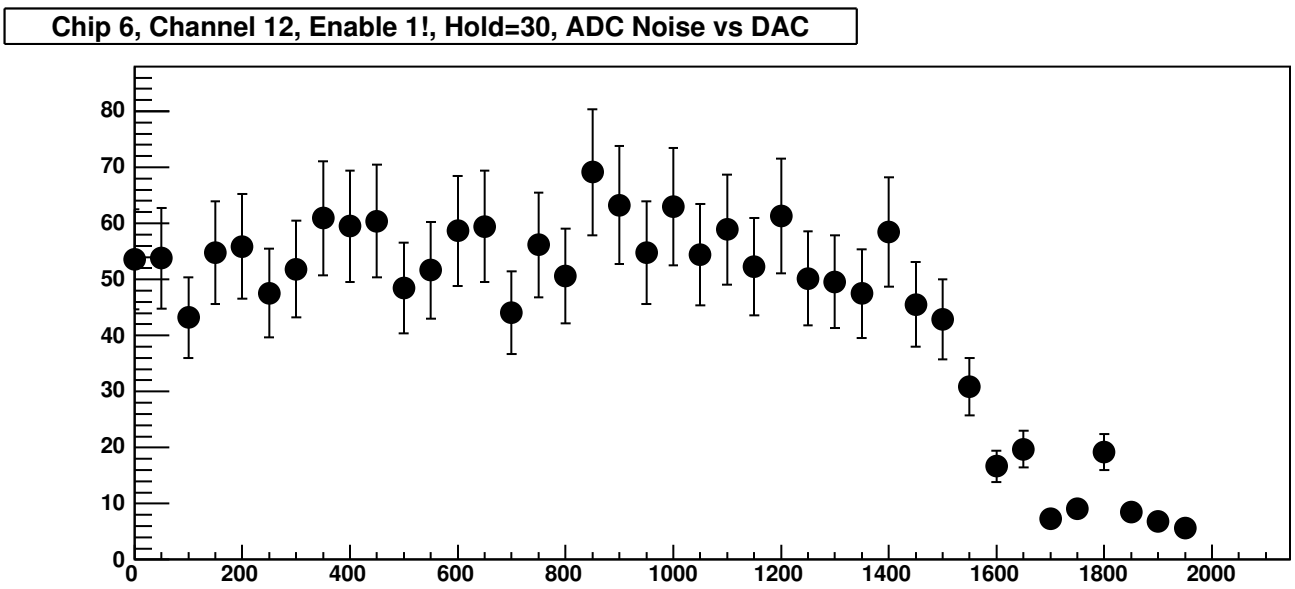

Chip 6, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

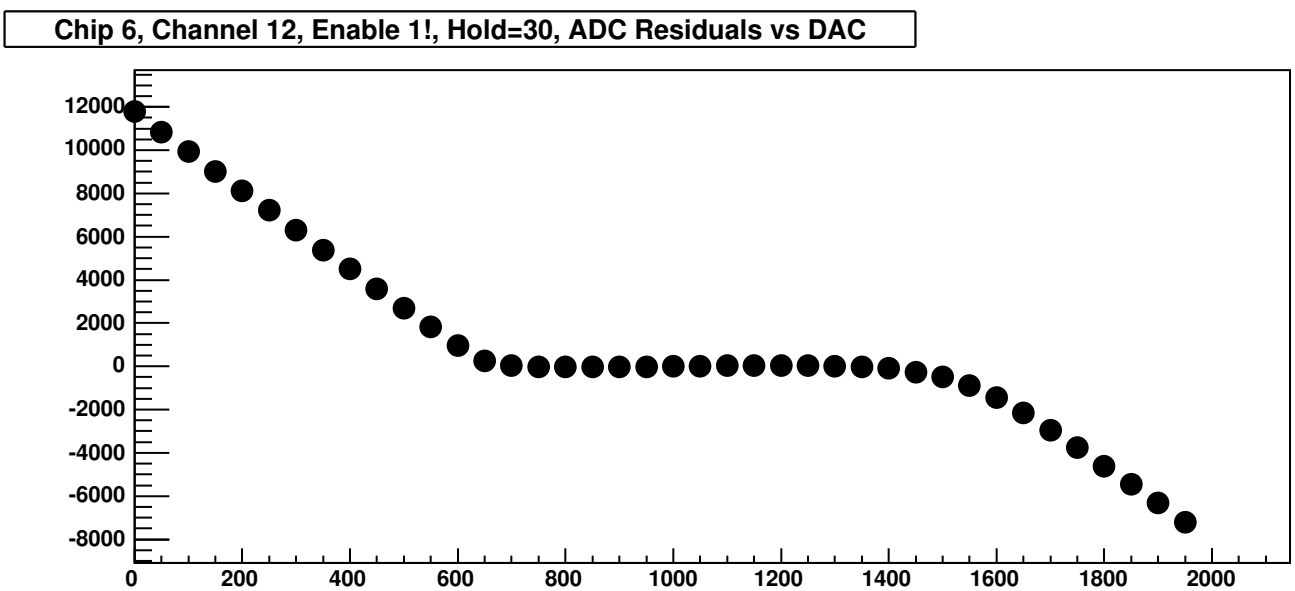

Chip 6, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 62.23 / 11
p0 -1429 ± 40.71
p1 0.9876 ± 0.03703
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

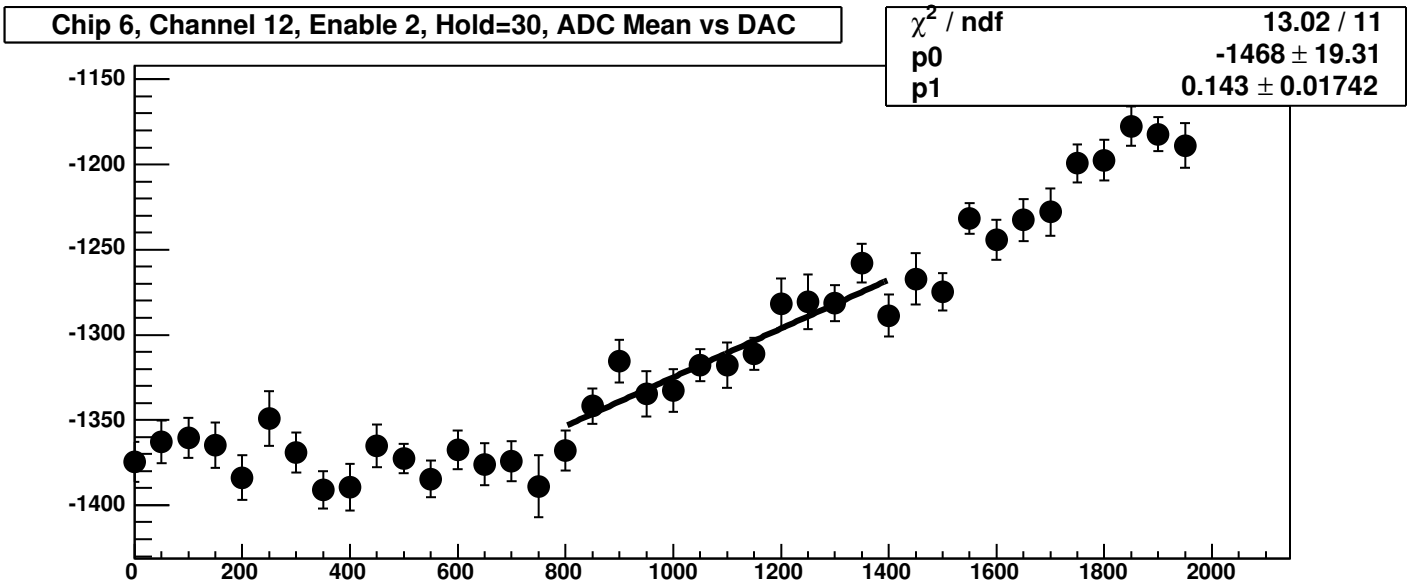

Chip 6, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

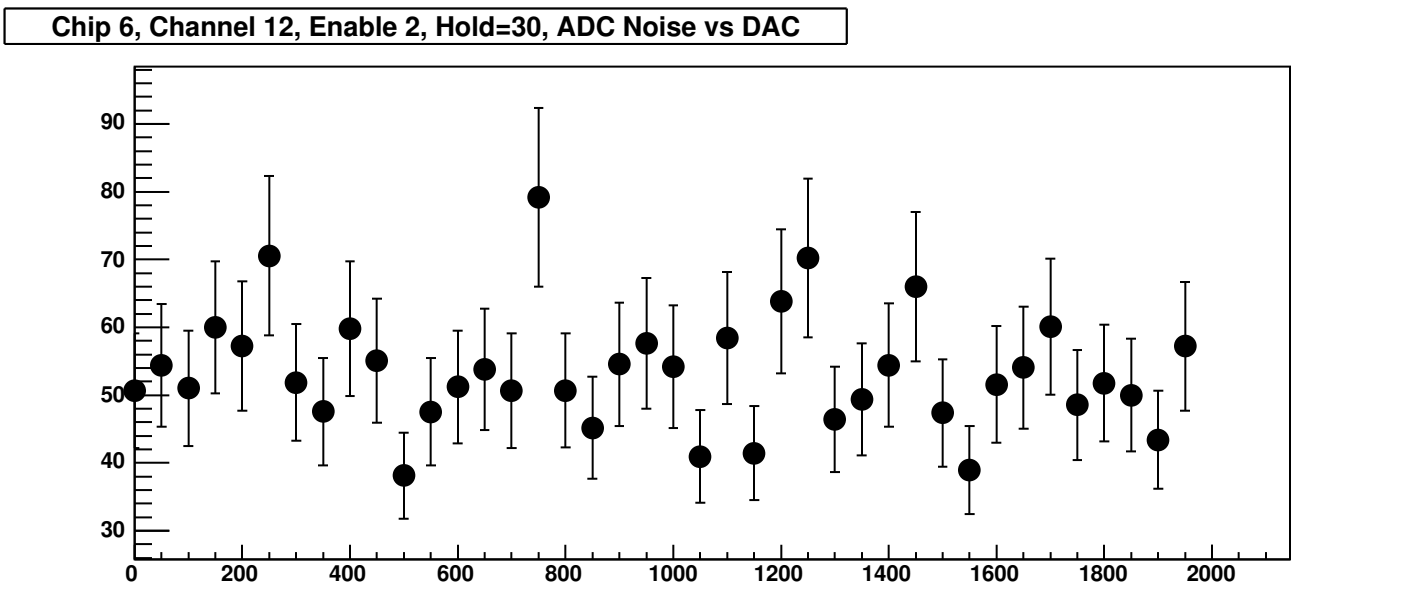

Chip 6, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC
500
0
-500
-1000
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

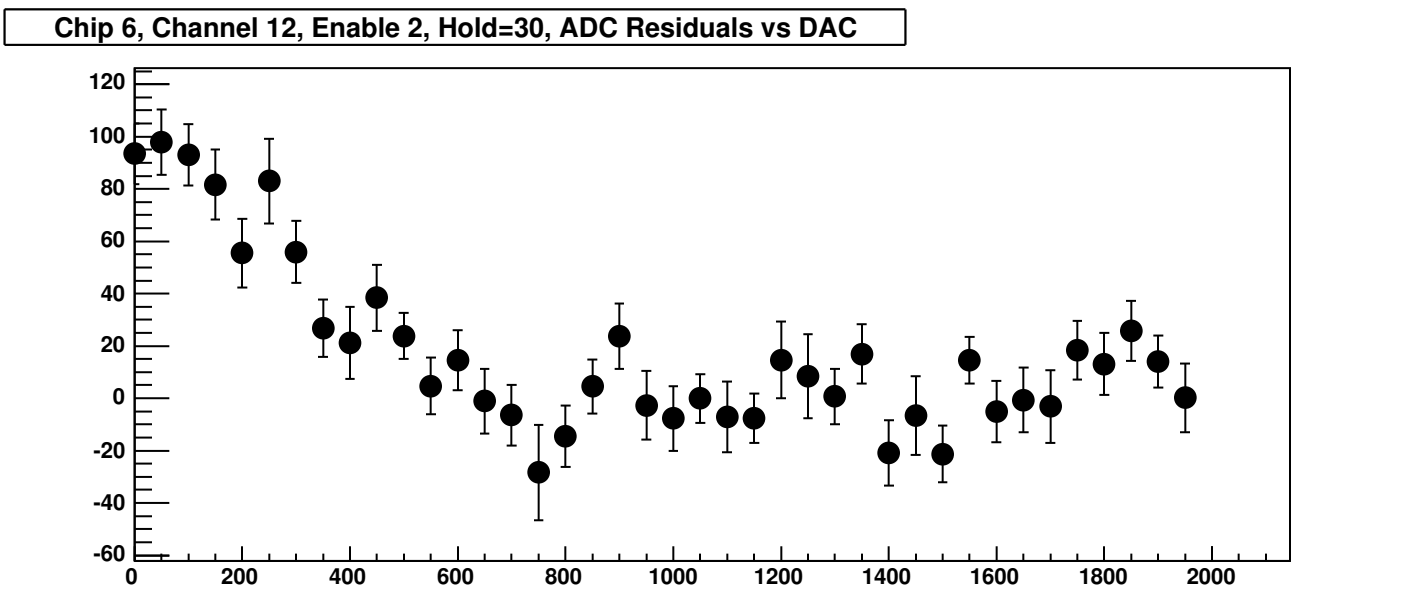

Chip 6, Channel 12, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 90.72 / 11
p0 -1290 ± 21.12
p1 18.54 ± 0.01865
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

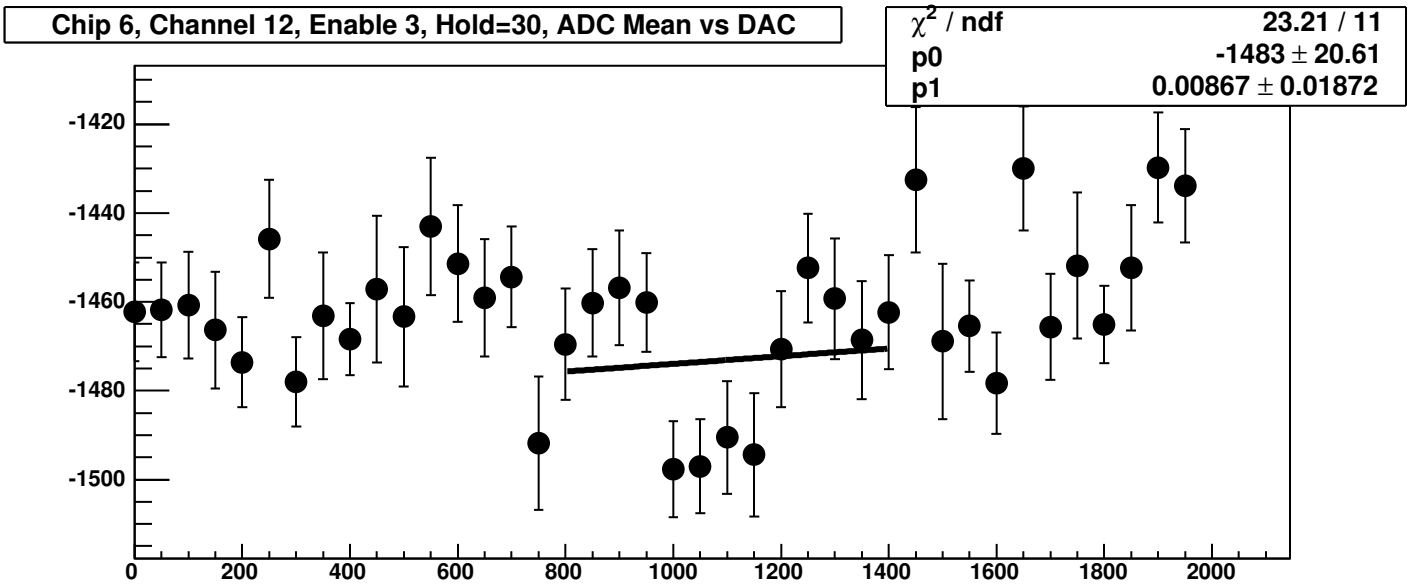

Chip 6, Channel 12, Enable 1!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

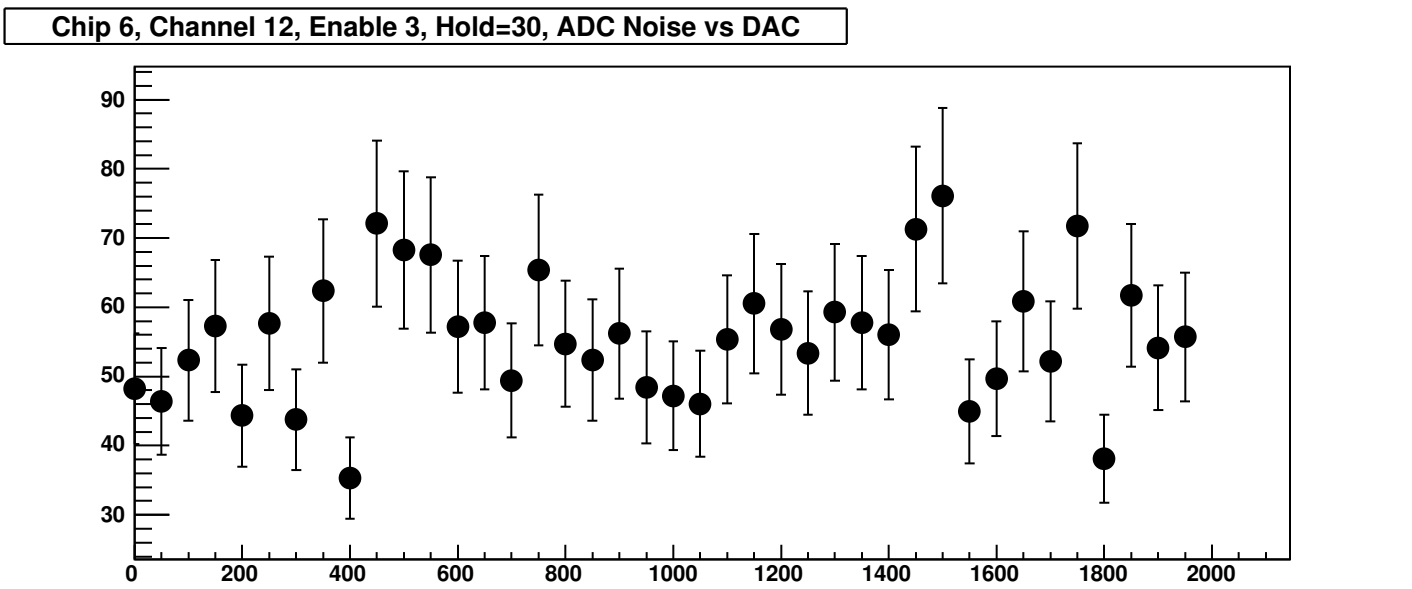

Chip 6, Channel 12, Enable 1!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

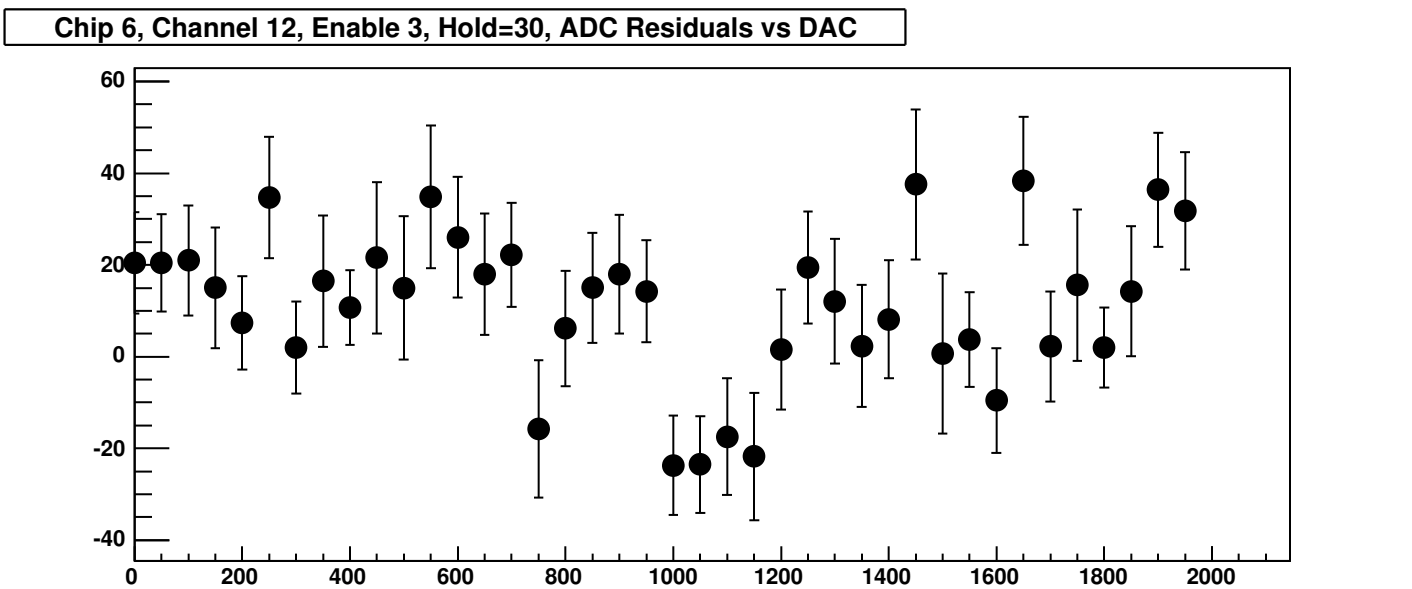

Chip 6, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 13.02 / 11
p0 -1468 ± 19.31
p1 0.143 ± 0.01742
-1150
-1200
-1250
-1300
-1350
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

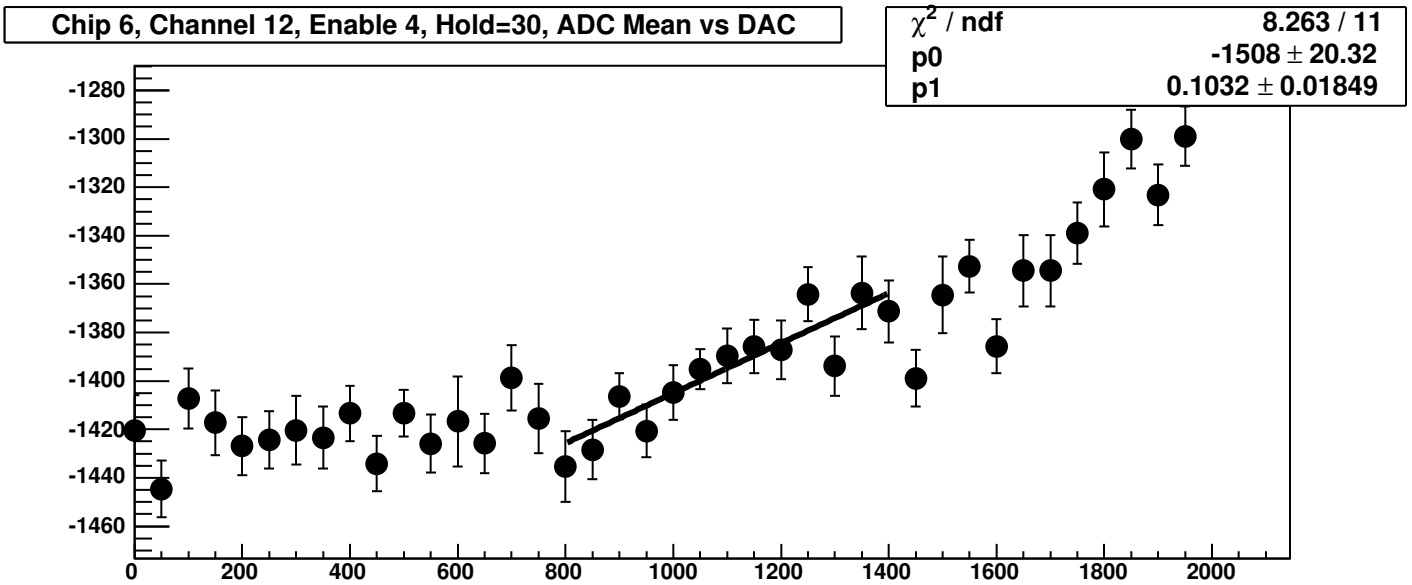

Chip 6, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

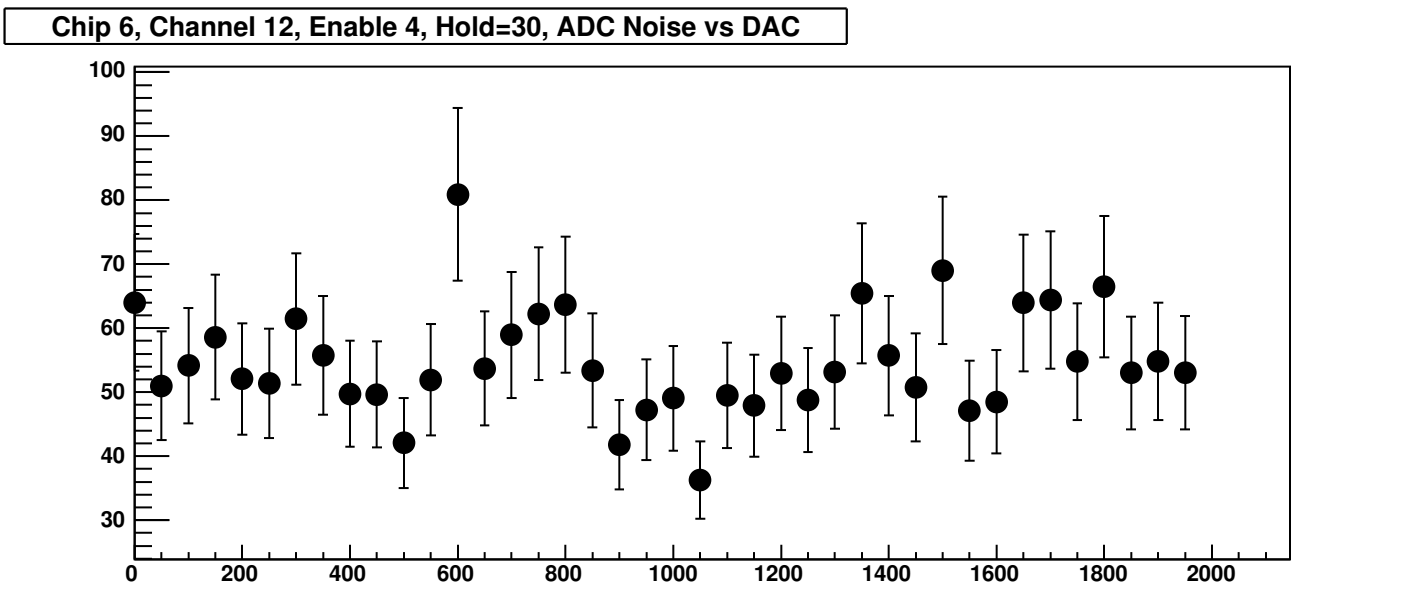

Chip 6, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

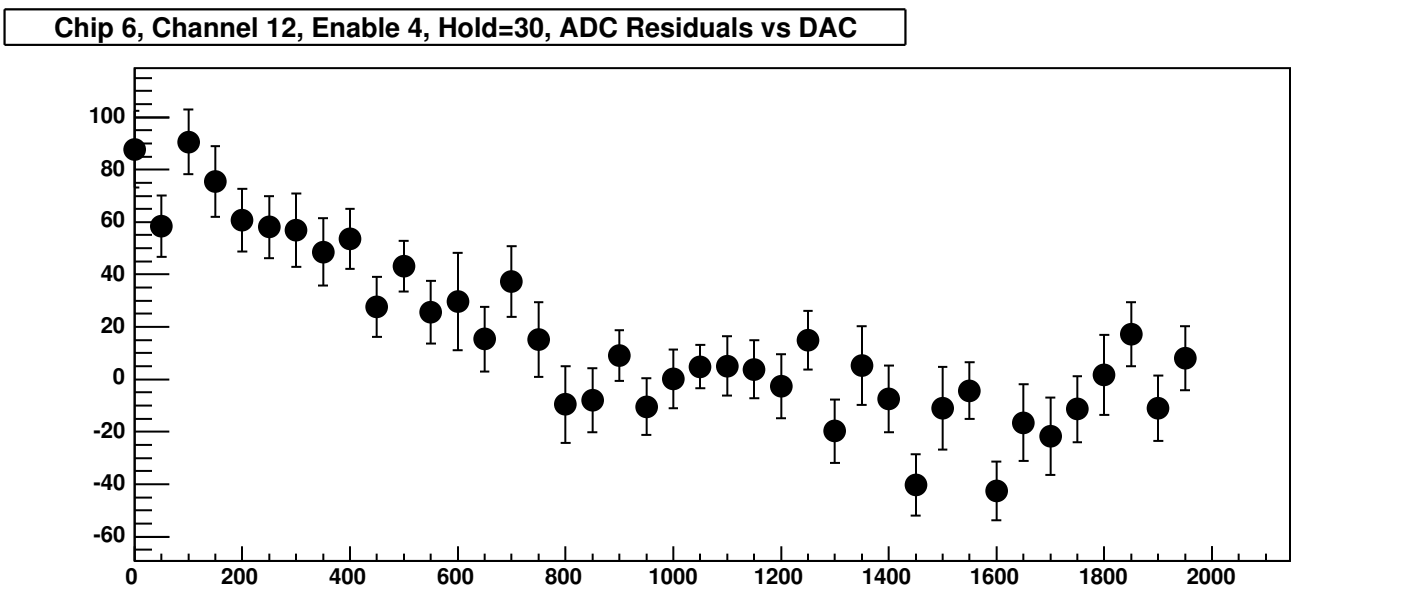

Chip 6, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 23.21 / 11
p0 -1483 ± 20.61
p1 0.00867 ± 0.01872
-1420
-1440
-1460
-1480
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

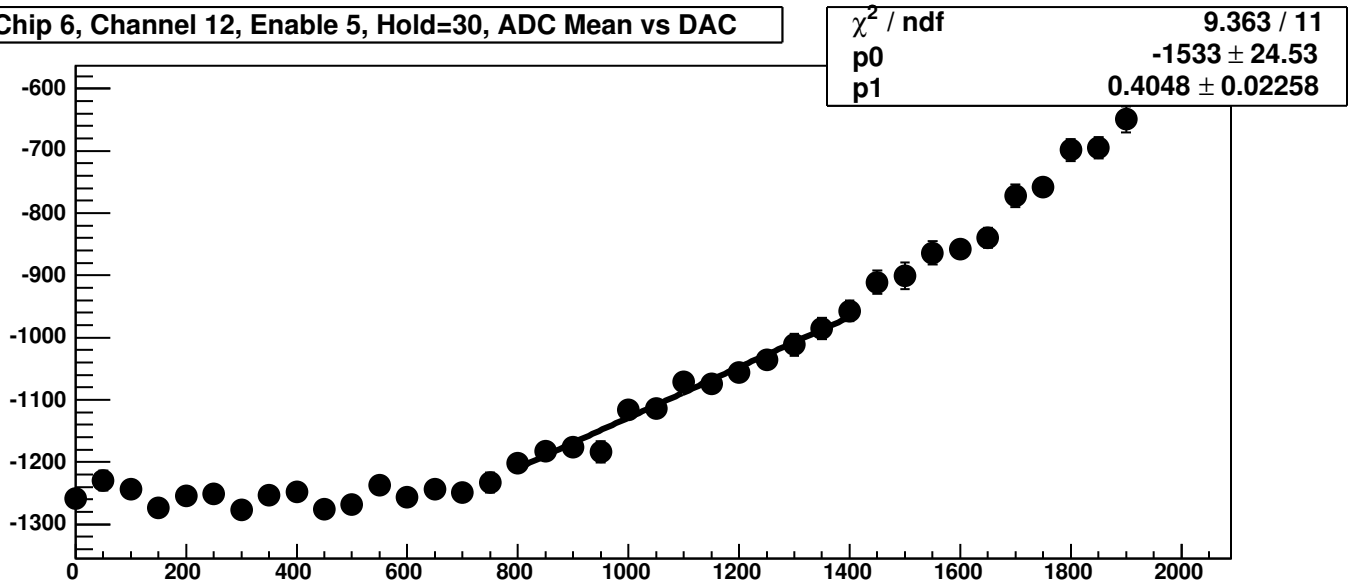

Chip 6, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 12, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 8.263 / 11
p0 -1508 ± 20.32
p1 0.1032 ± 0.01849
-1280
-1300
-1320
-1340
-1360
-1380
-1400
-1420
-1440
-1460
0
200
400
600
800
1000
1200
1400
1600
1800
2000

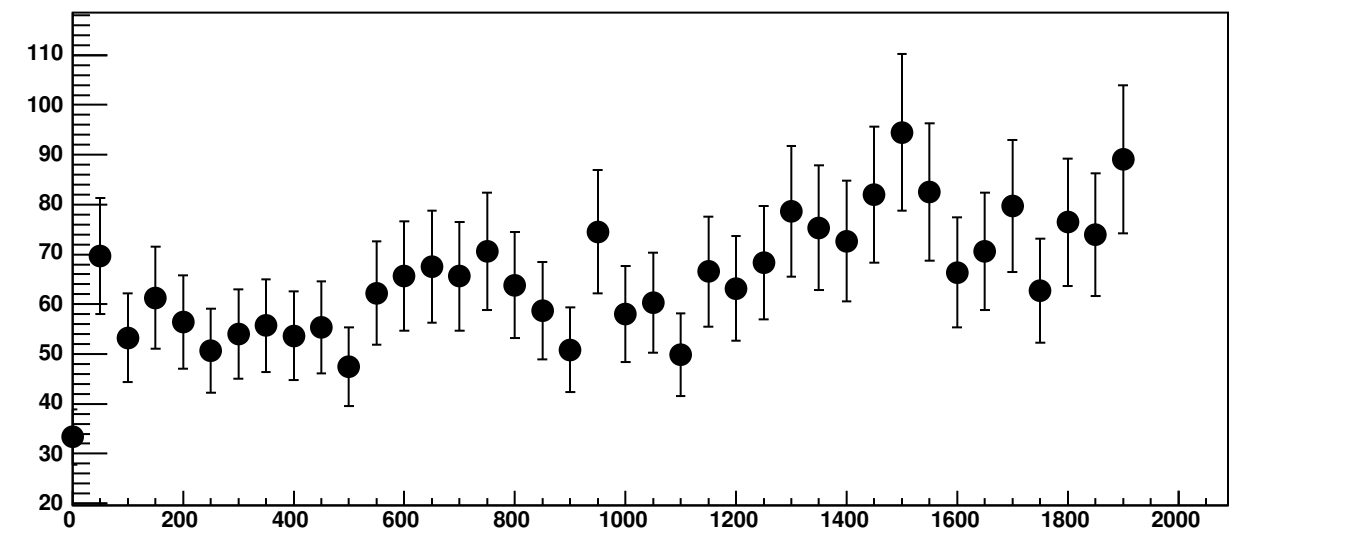

Chip 6, Channel 12, Enable 4, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 12, Enable 4, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

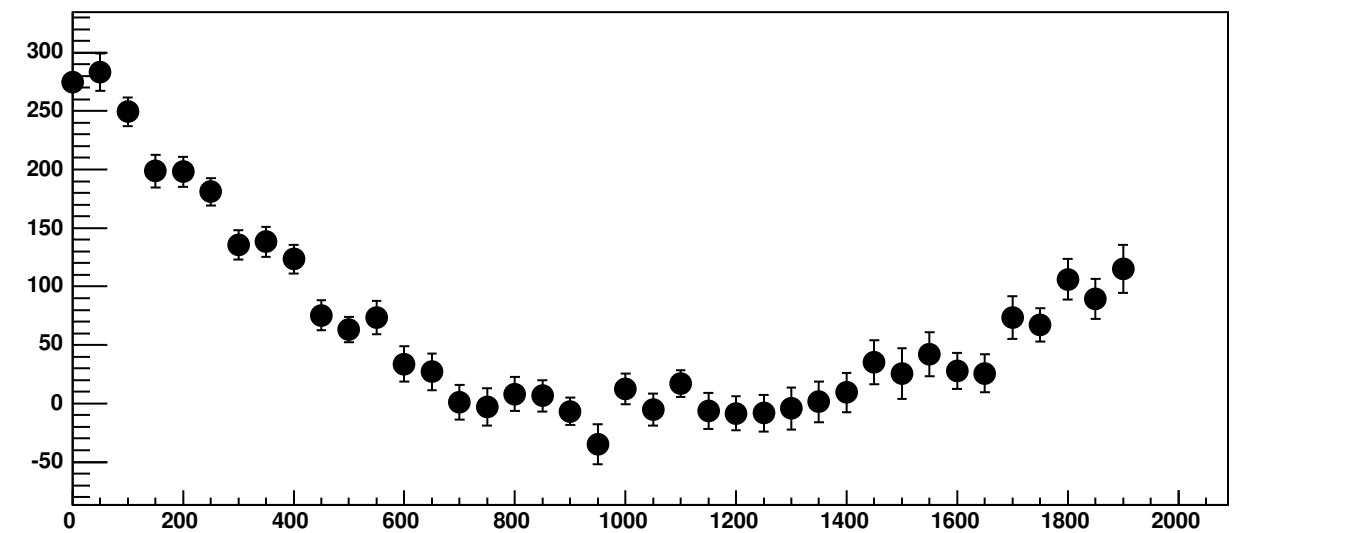
Chip 6, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC

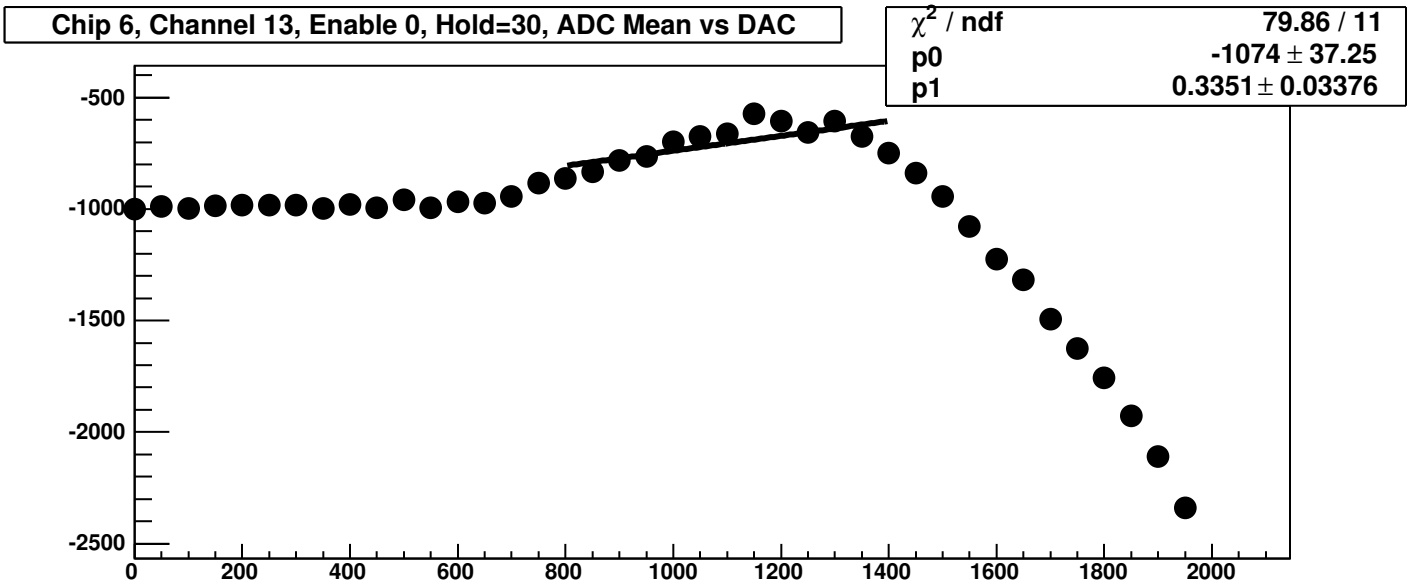


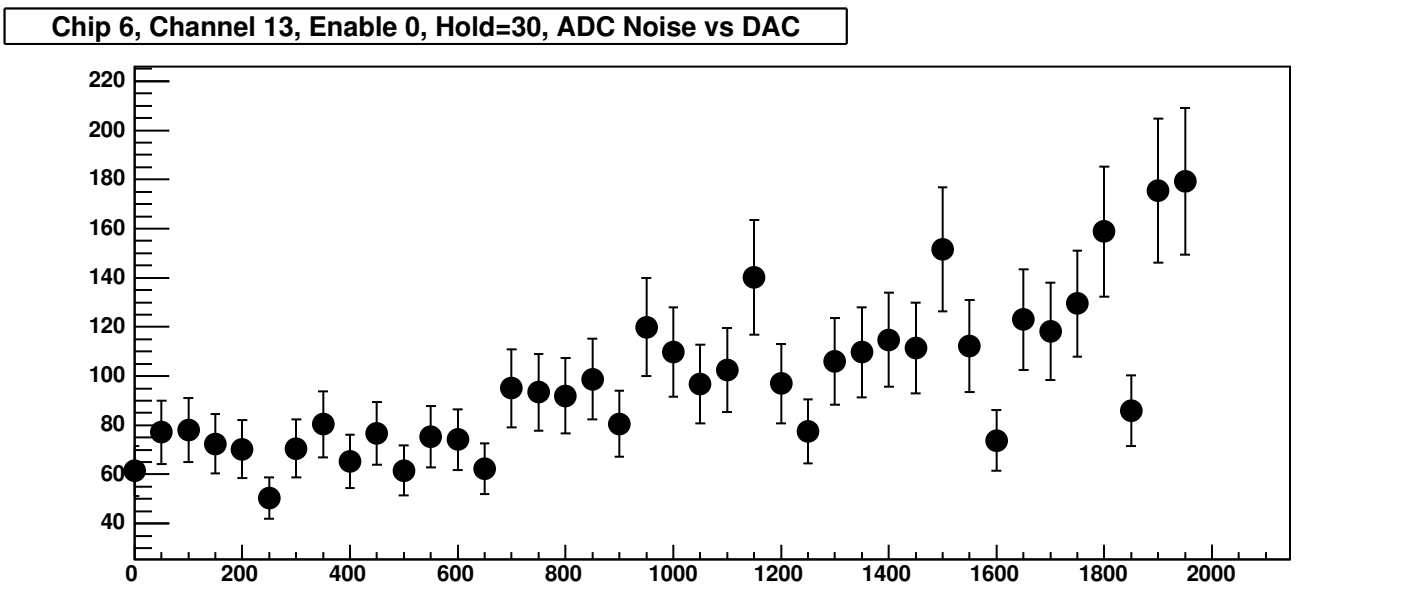
Chip 6, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC

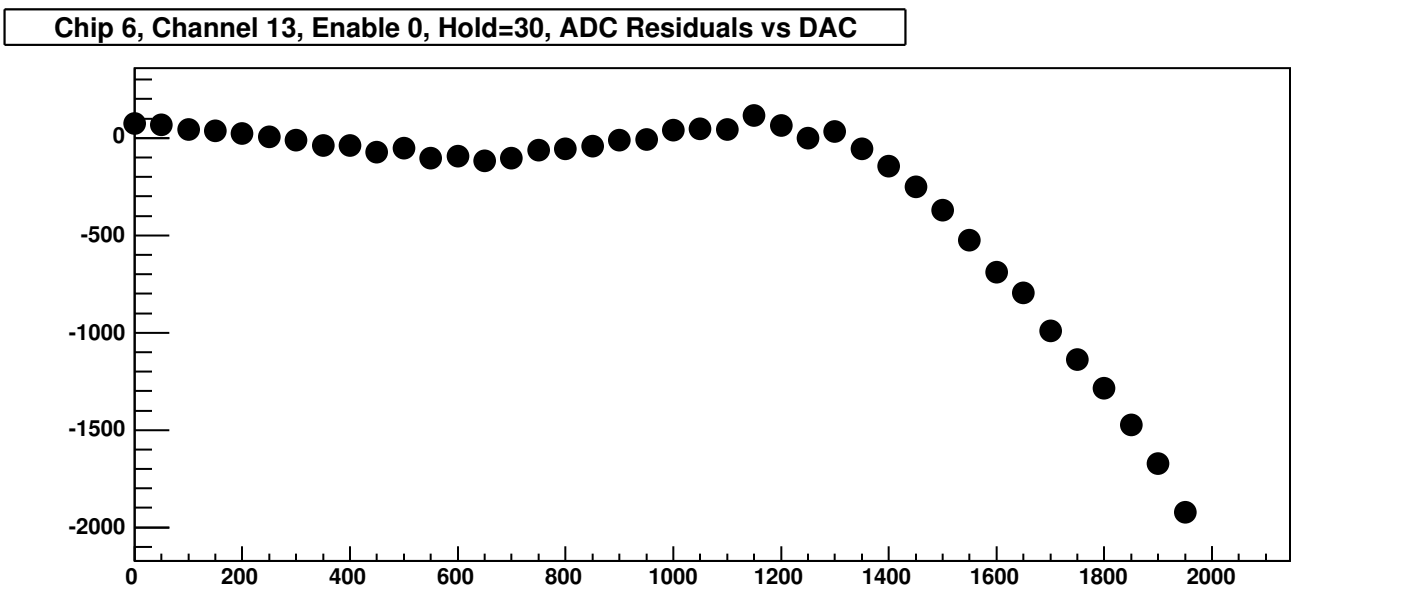


Chip 6, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC

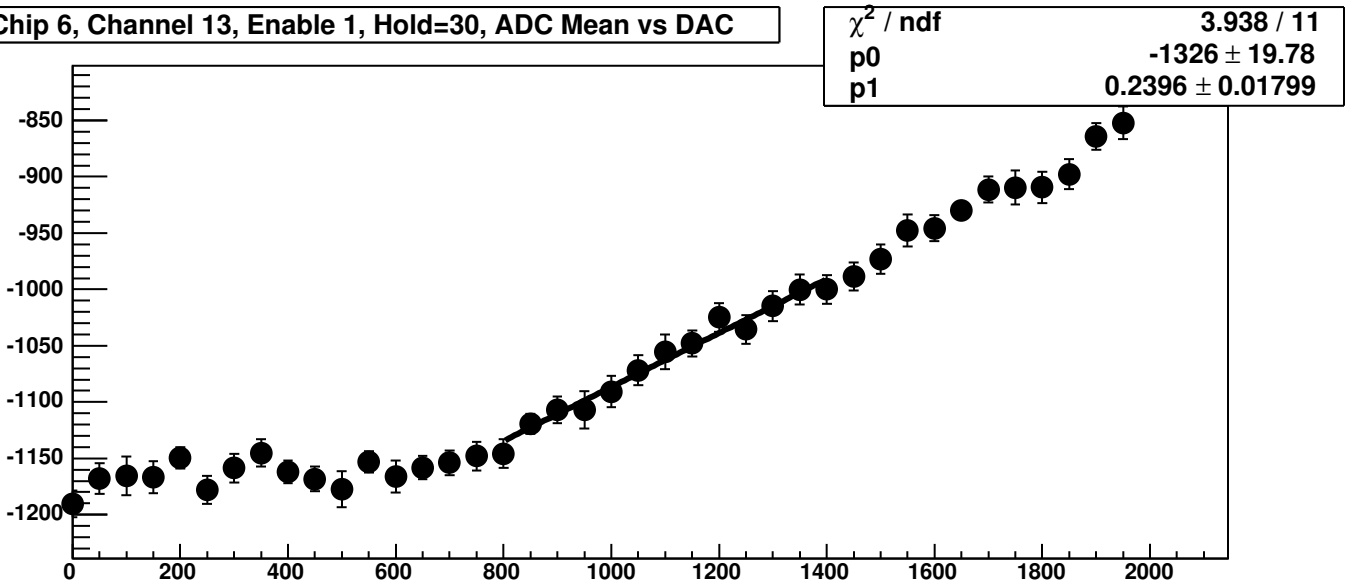



Chip 6, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 79.86 / 11
p0 -1074 ± 37.25
p1 0.3351 ± 0.03376
-500
-1000
-1500
-2000
-2500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

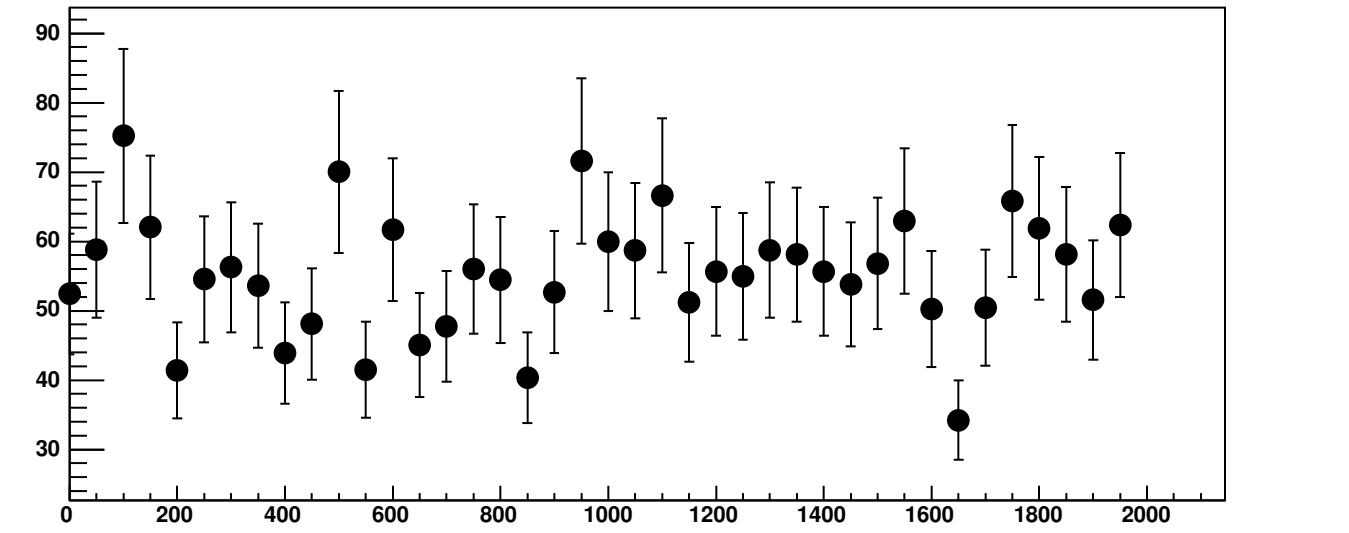

Chip 6, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

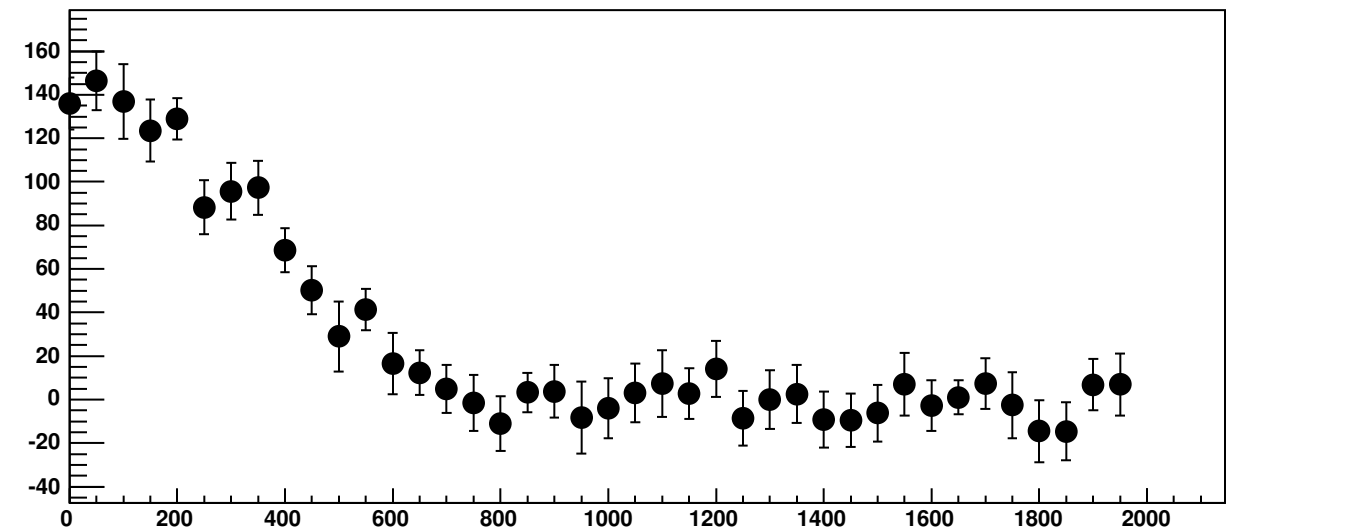
Chip 6, Channel 13, Enable 1, Hold=30, ADC Mean vs DAC

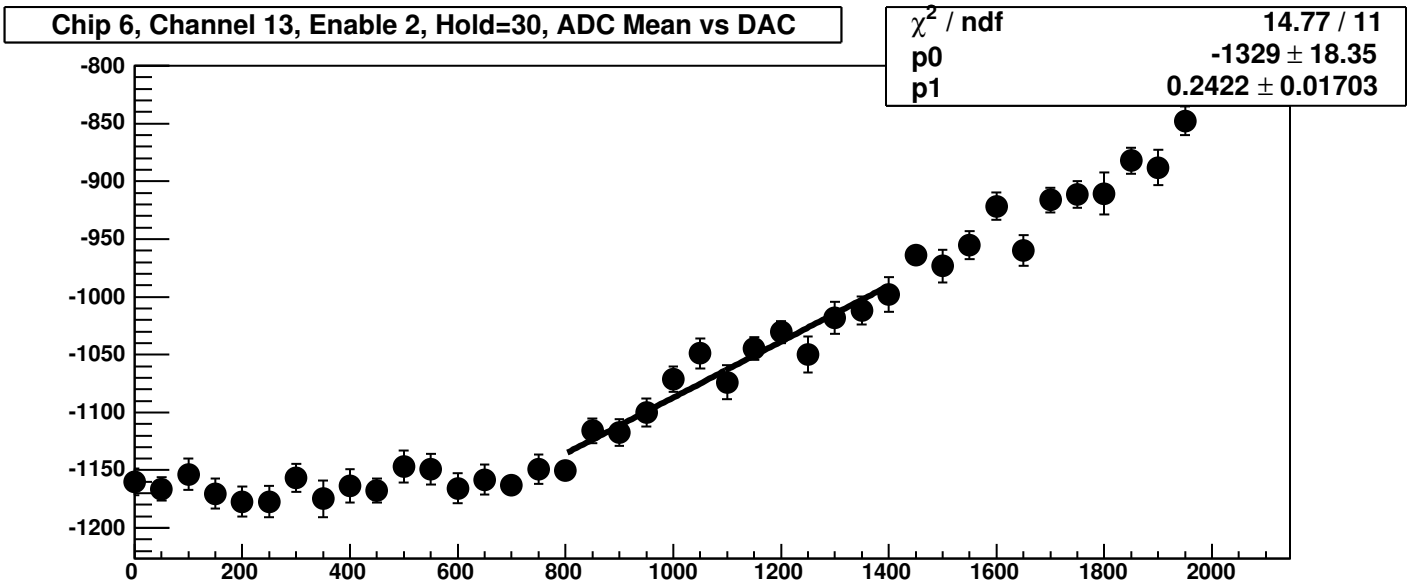


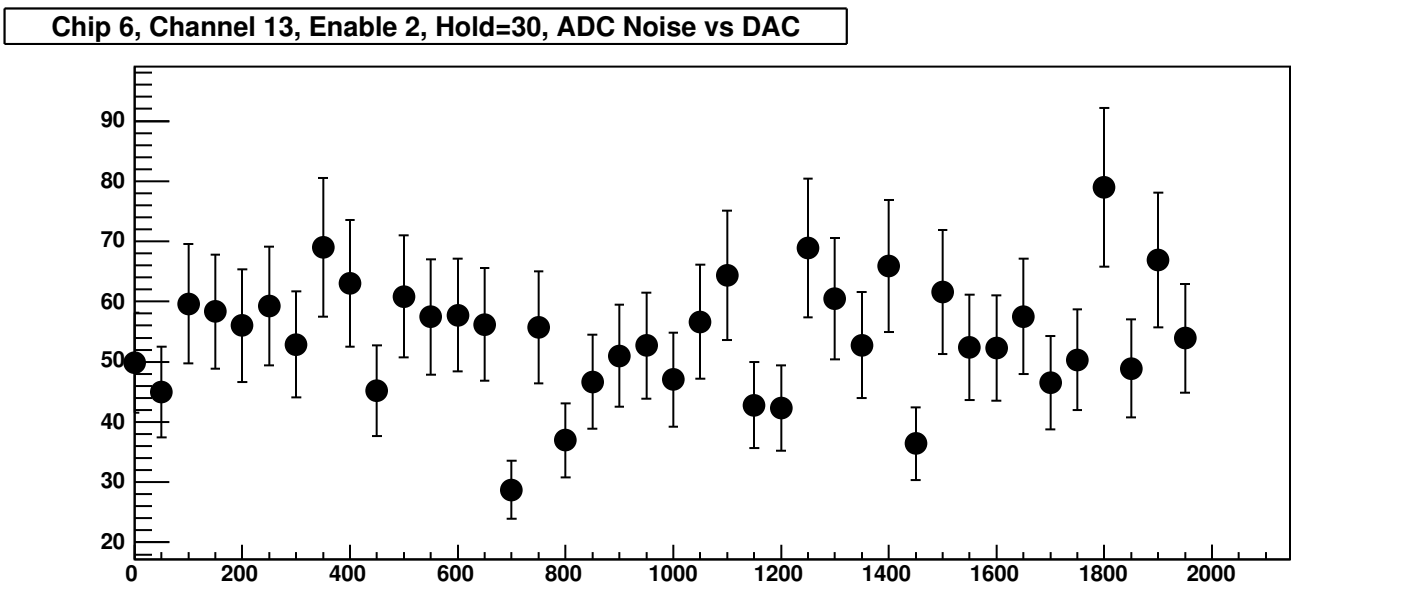
Chip 6, Channel 13, Enable 1, Hold=30, ADC Noise vs DAC

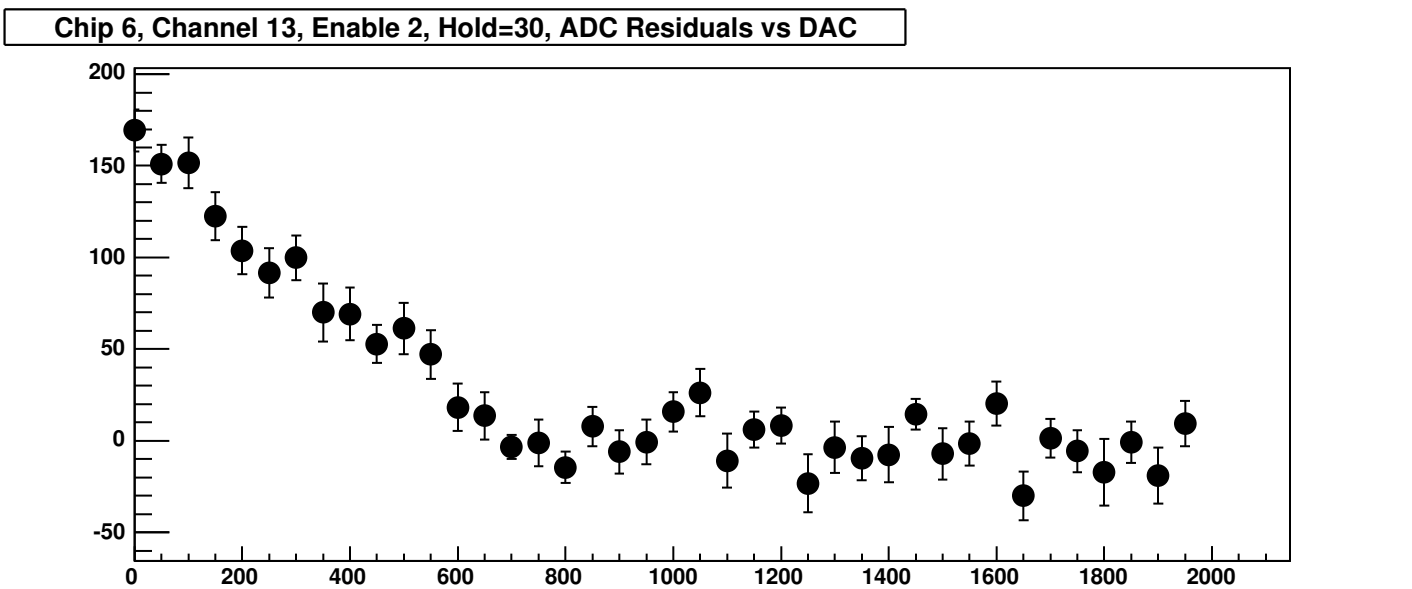


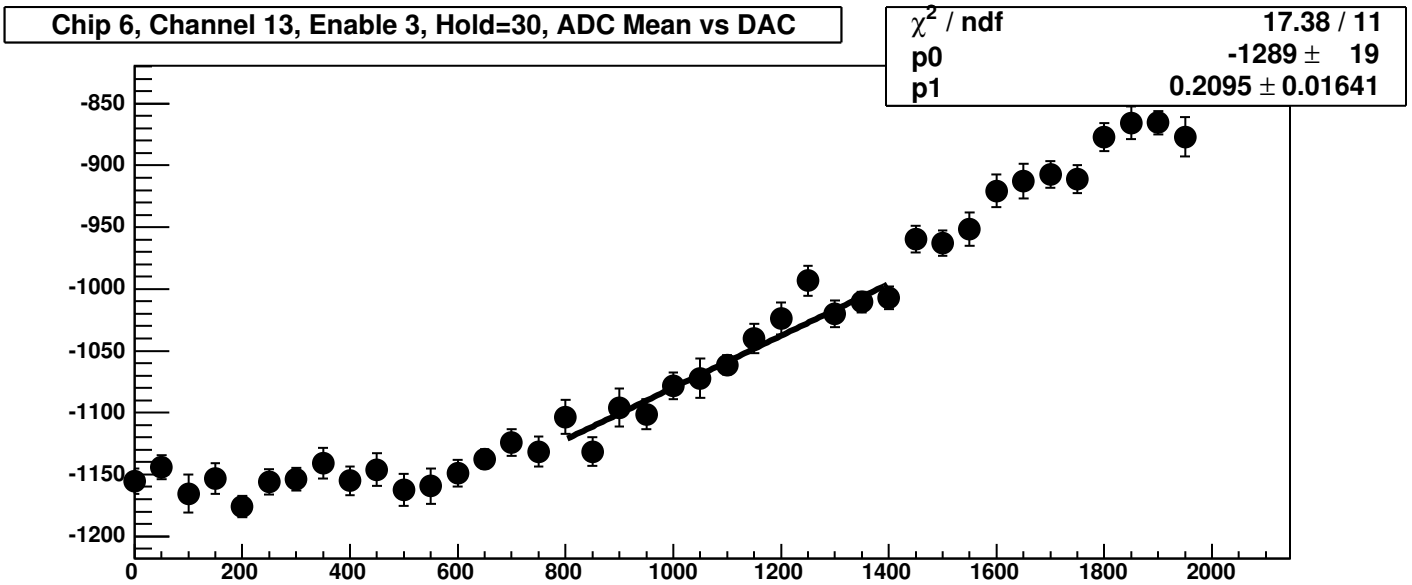
Chip 6, Channel 13, Enable 1, Hold=30, ADC Residuals vs DAC

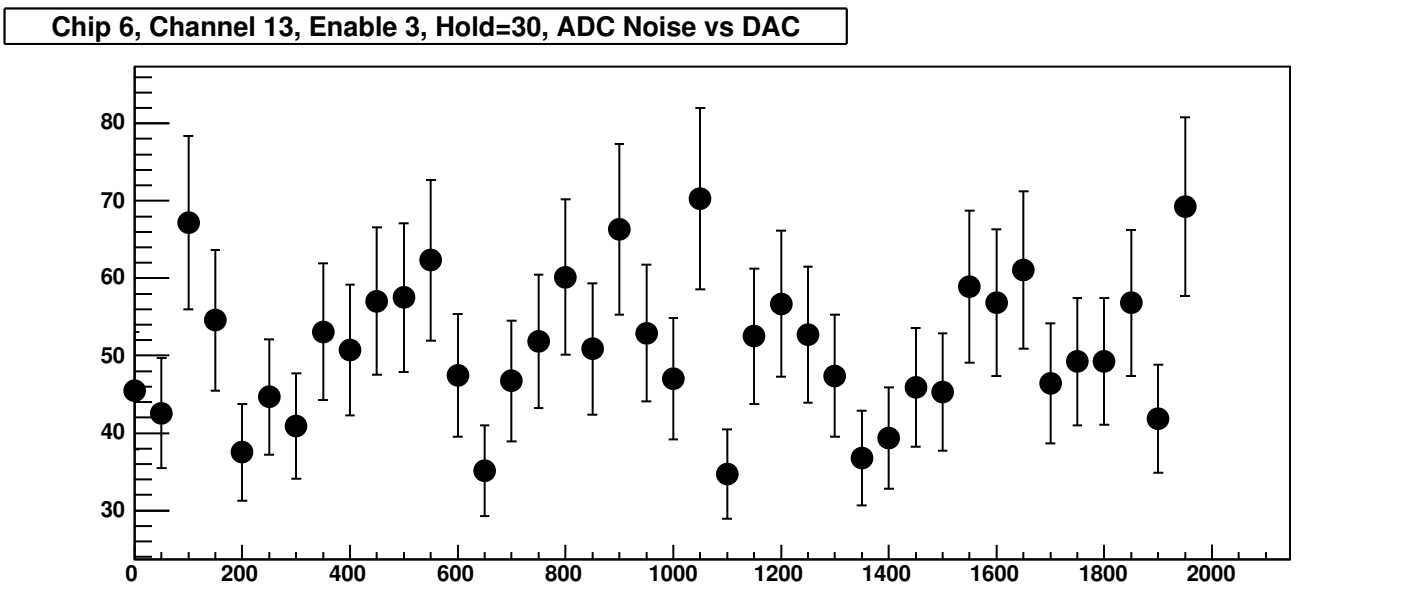


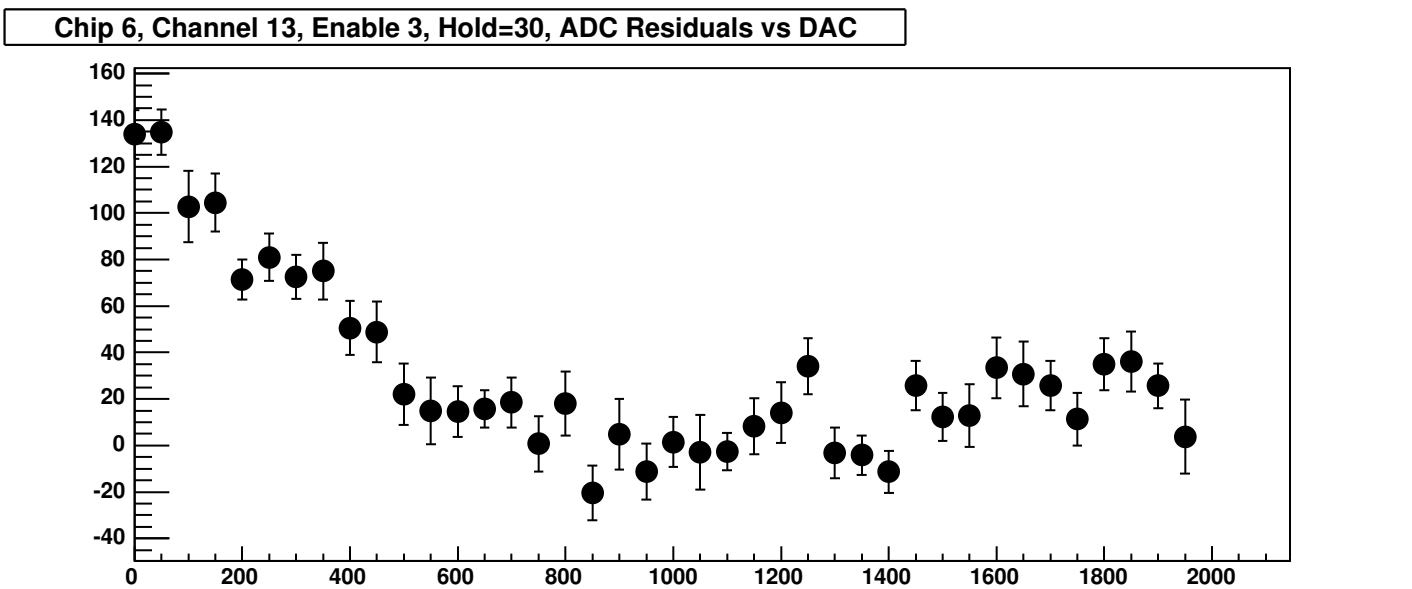

Chip 6, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 14.77 / 11
p0 -1329 ± 18.35
p1 0.2422 ± 0.01703
-800
-850
-900
-950
-1000
-1050
-1100
-1150
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

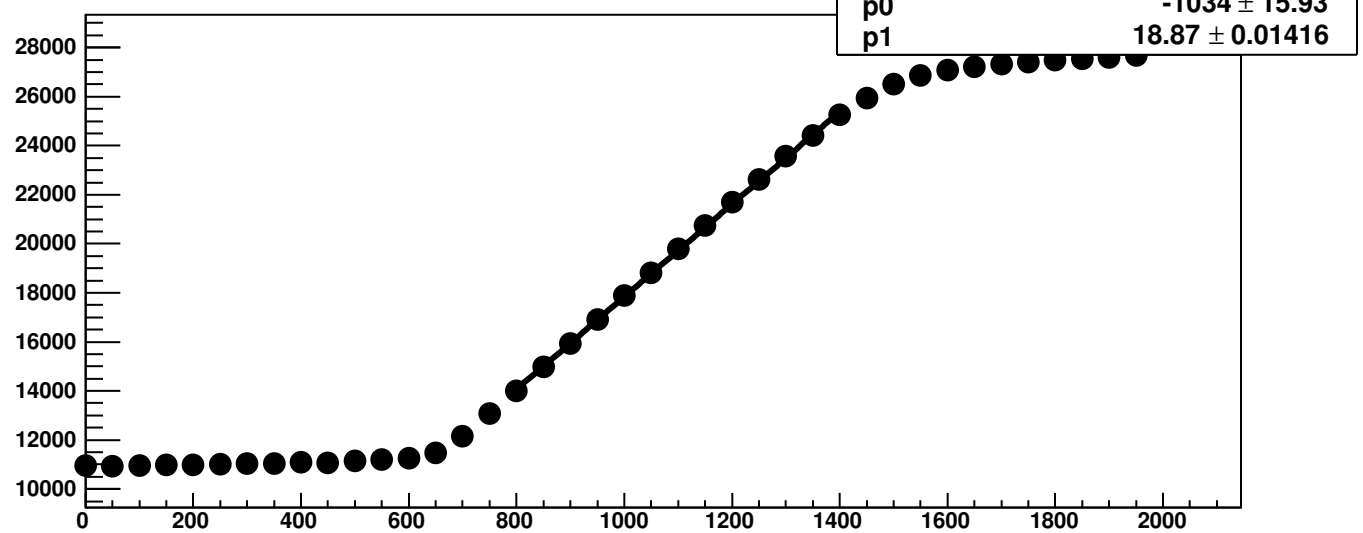

Chip 6, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 17.38 / 11
p0 -1289 ± 19
p1 0.2095 ± 0.01641
-850
-900
-950
-1000
-1050
-1100
-1150
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

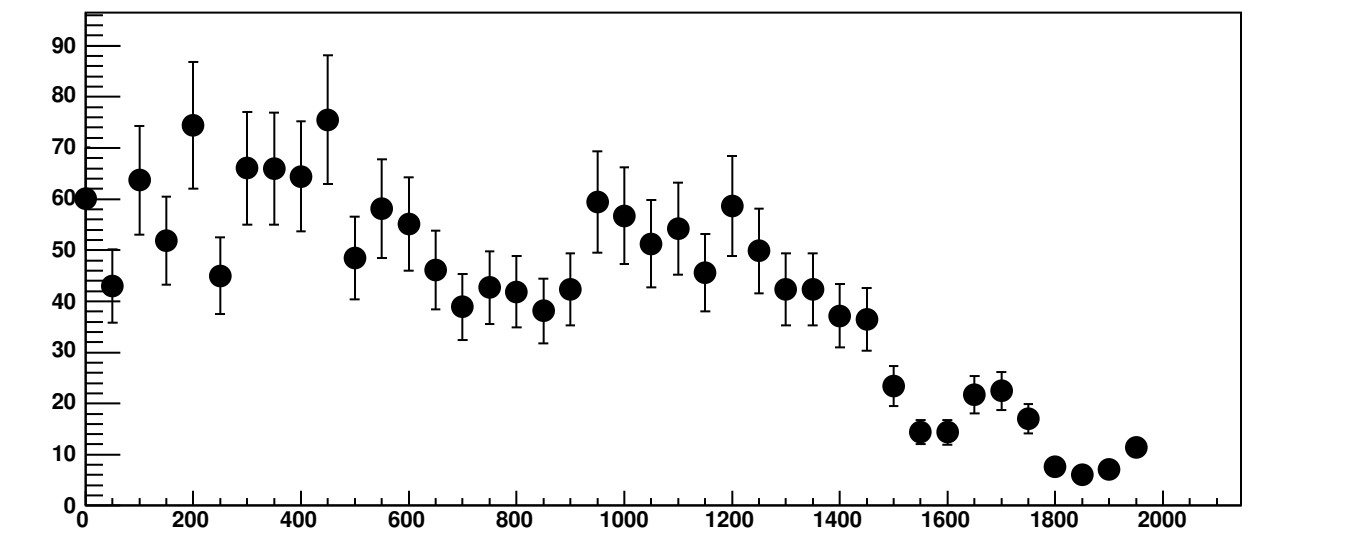

Chip 6, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
160
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

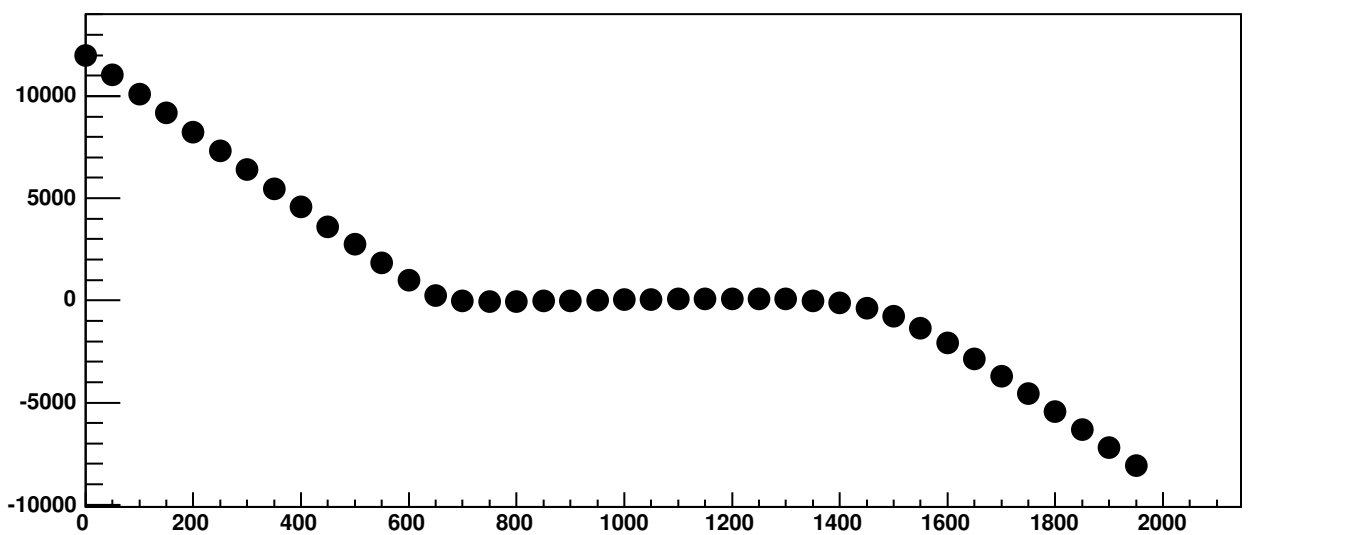
Chip 6, Channel 13, Enable 4!, Hold=30, ADC Mean vs DAC

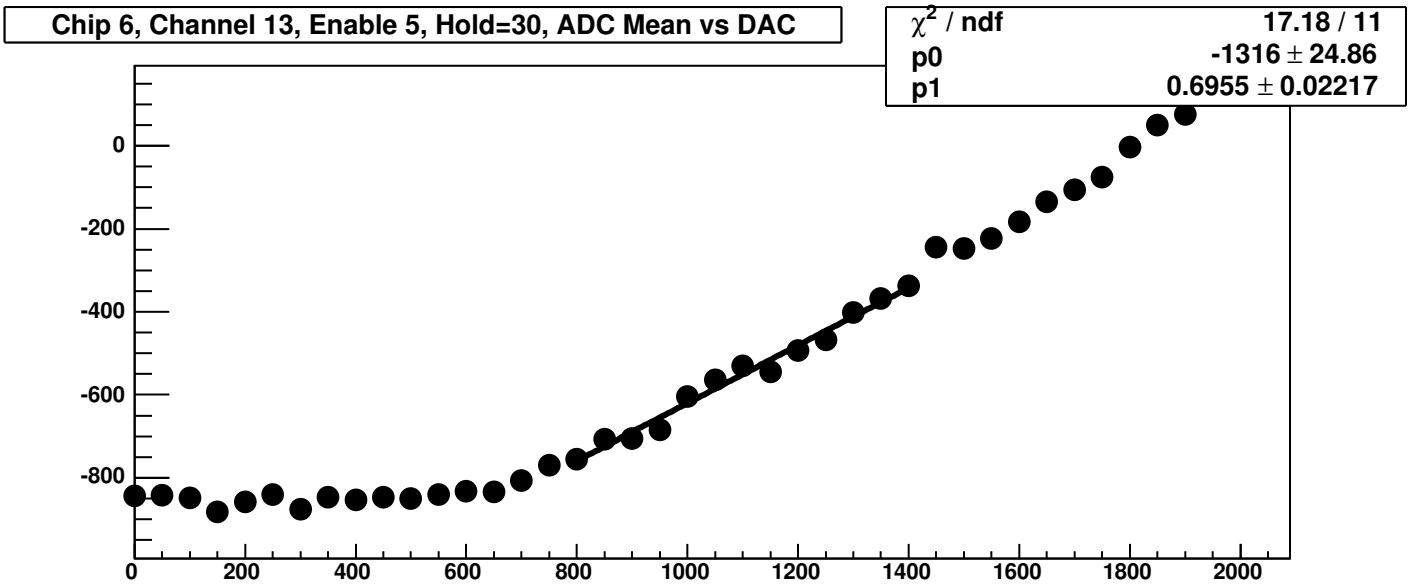


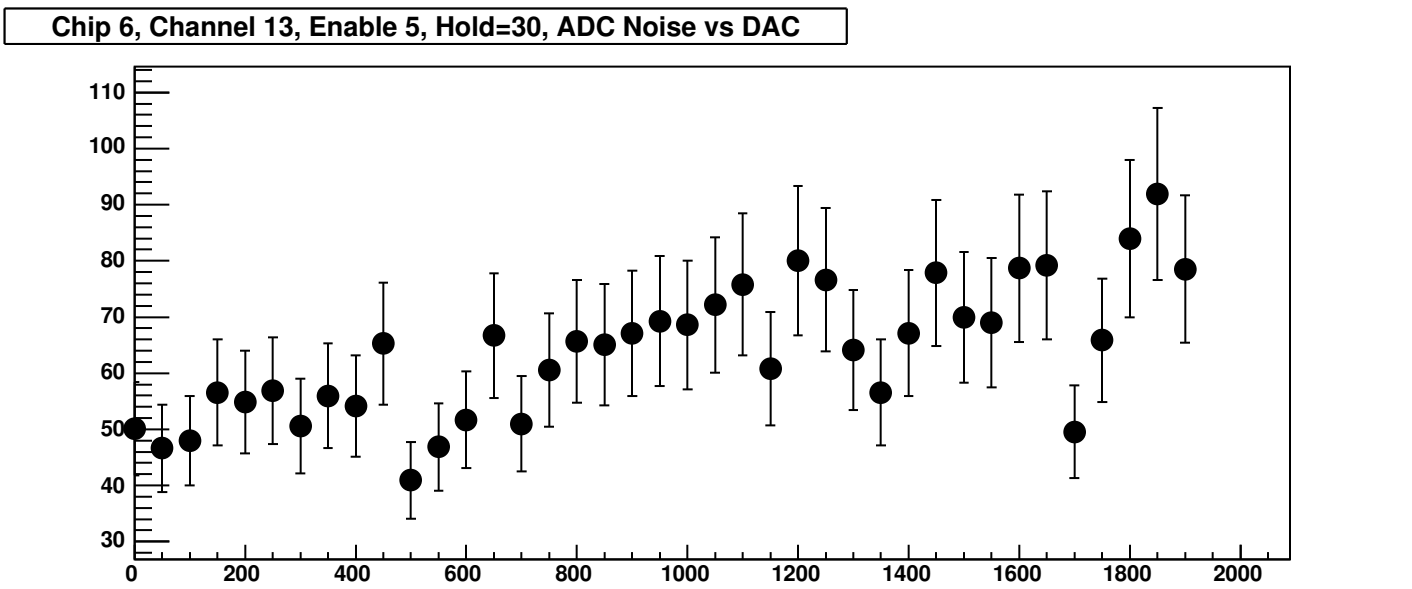
Chip 6, Channel 13, Enable 4!, Hold=30, ADC Noise vs DAC

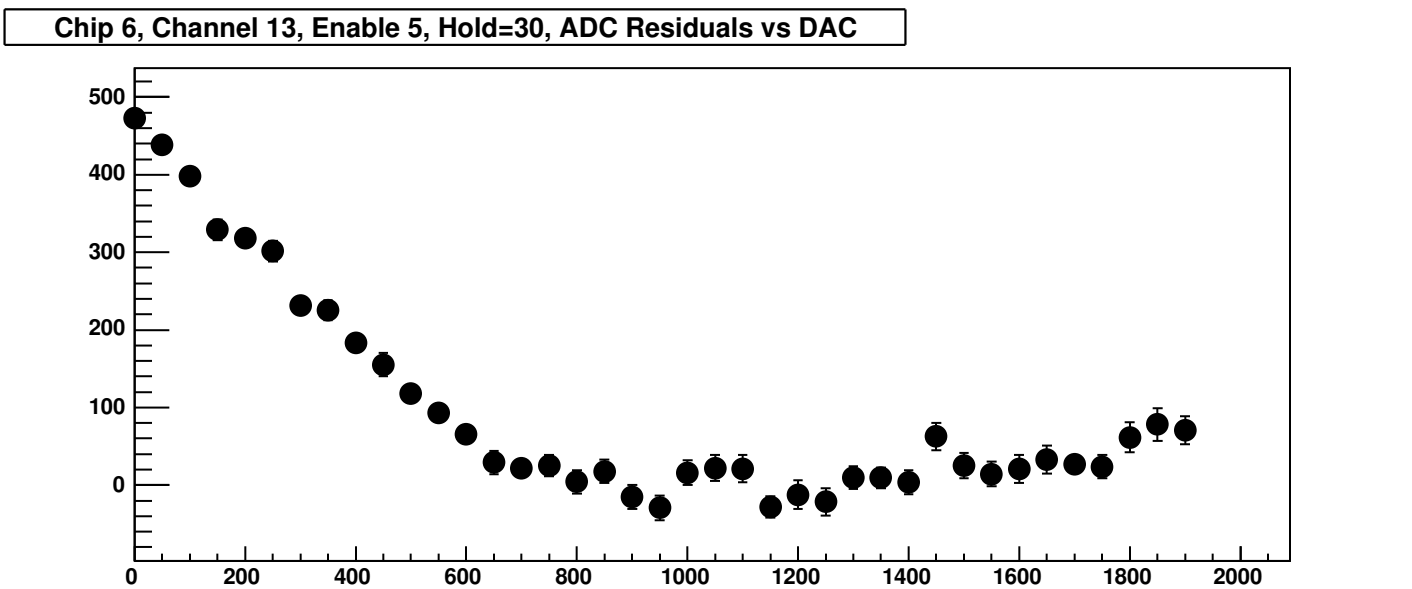


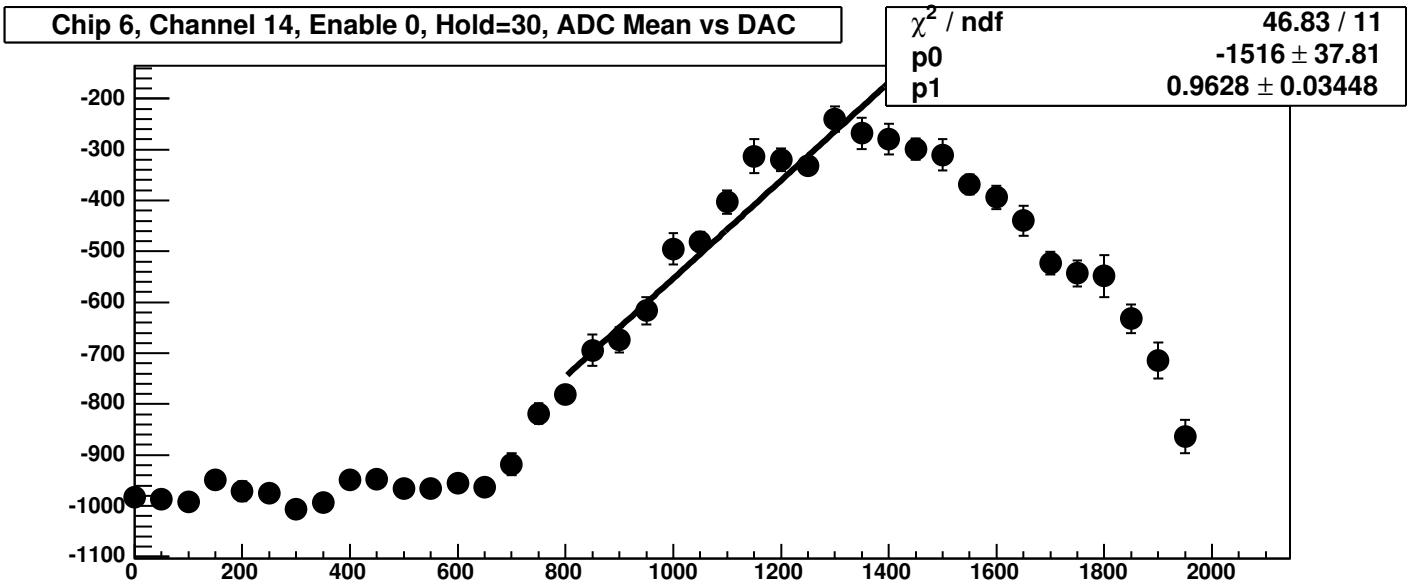
Chip 6, Channel 13, Enable 4!, Hold=30, ADC Residuals vs DAC

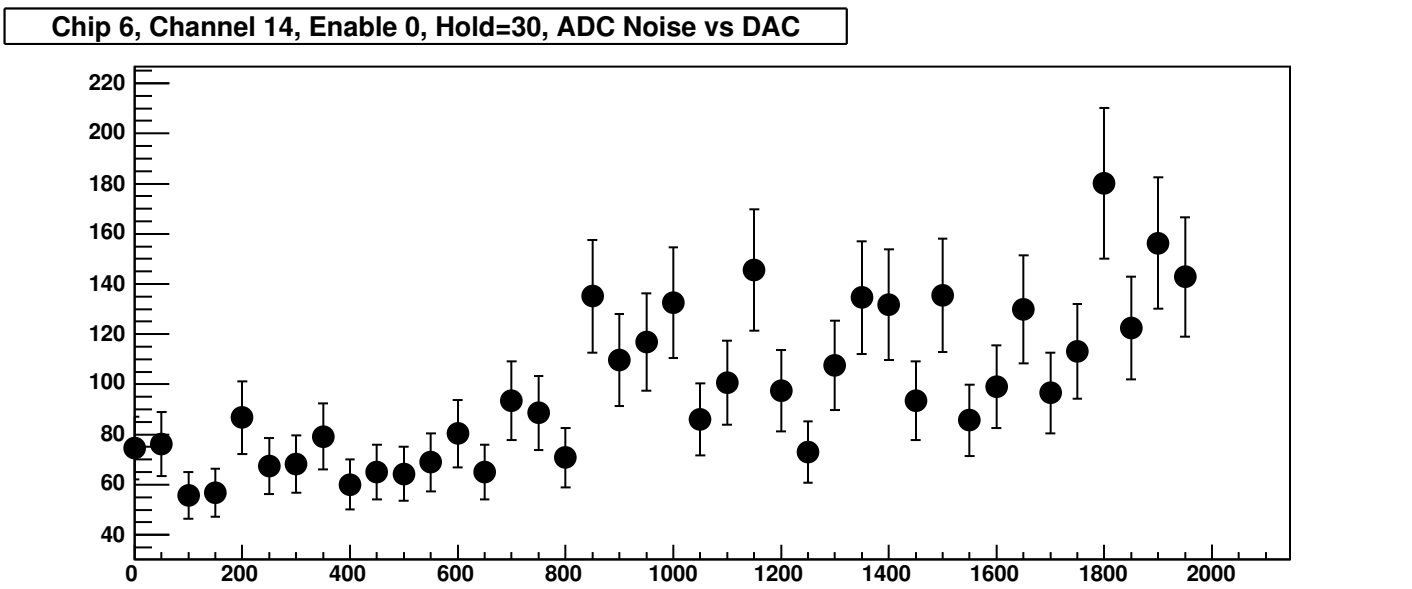


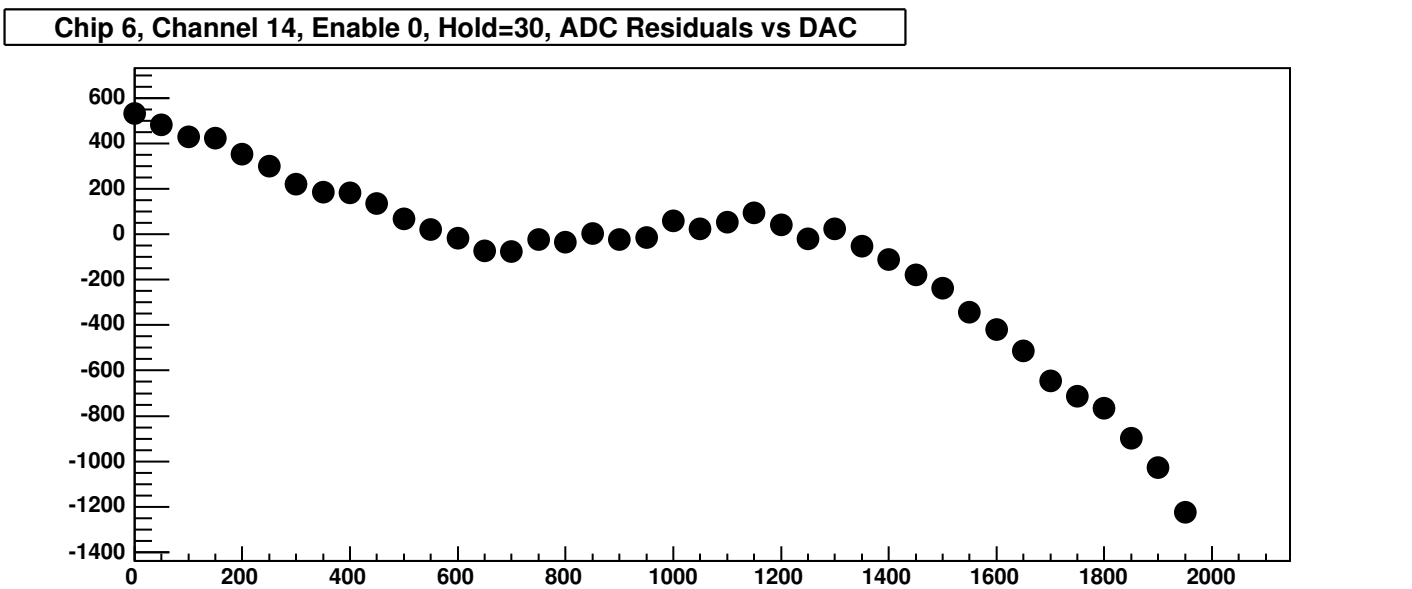

Chip 6, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 17.18 / 11
p0 -1316 ± 24.86
p1 0.6955 ± 0.02217
0
-200
-400
-600
-800
0 200 400 600 800 1000 1200 1400 1600 1800 2000


Chip 6, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0 200 400 600 800 1000 1200 1400 1600 1800 2000

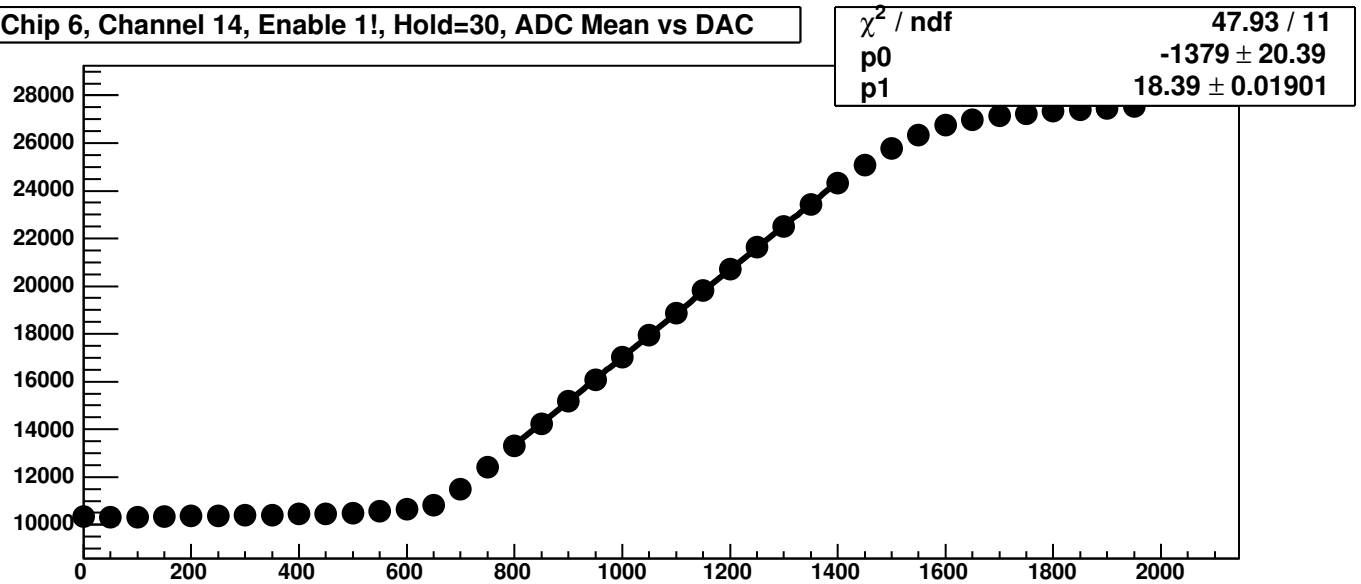

Chip 6, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
500
400
300
200
100
0
0 200 400 600 800 1000 1200 1400 1600 1800 2000


Chip 6, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 46.83 / 11
p0 -1516 ± 37.81
p1 0.9628 ± 0.03448
-200
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

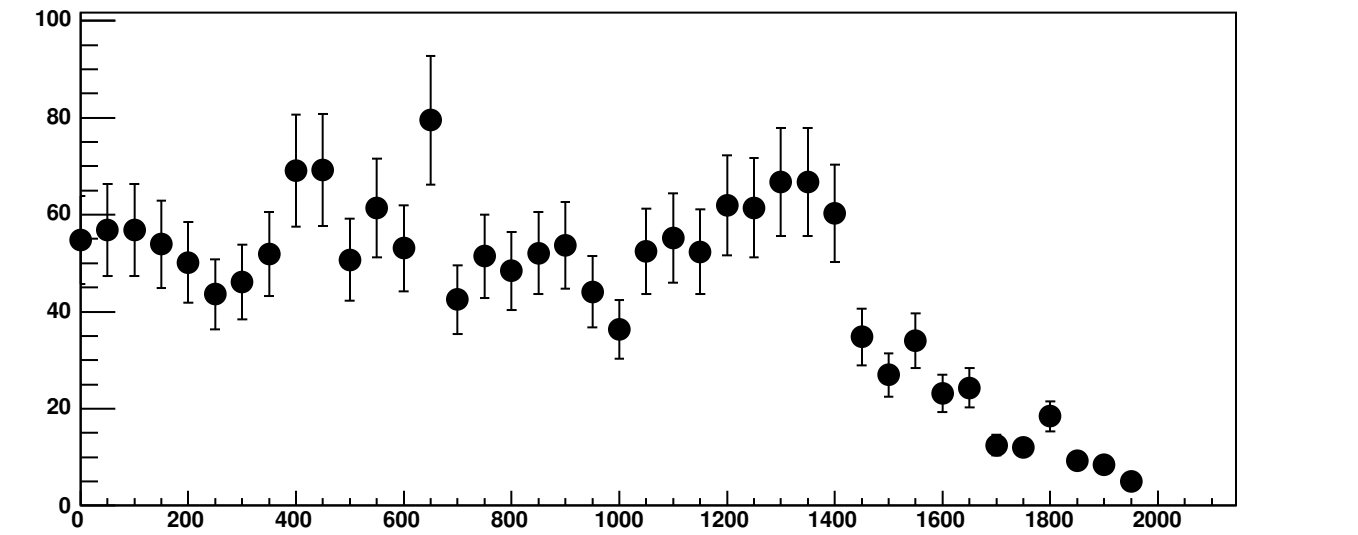

Chip 6, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
600
400
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

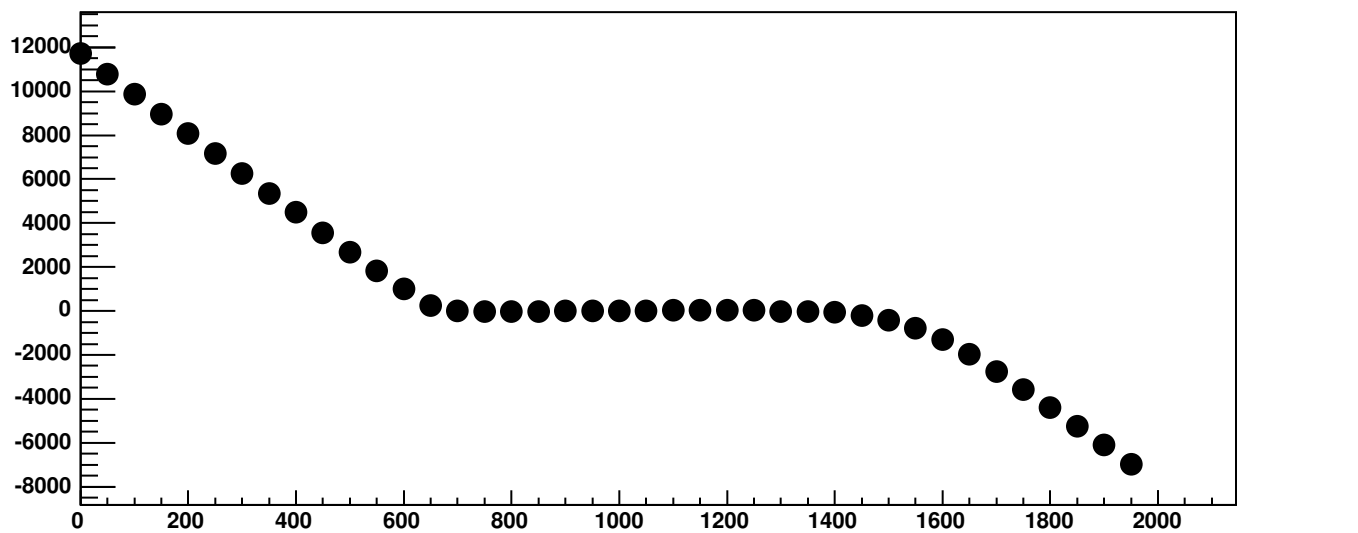
Chip 6, Channel 14, Enable 1!, Hold=30, ADC Mean vs DAC

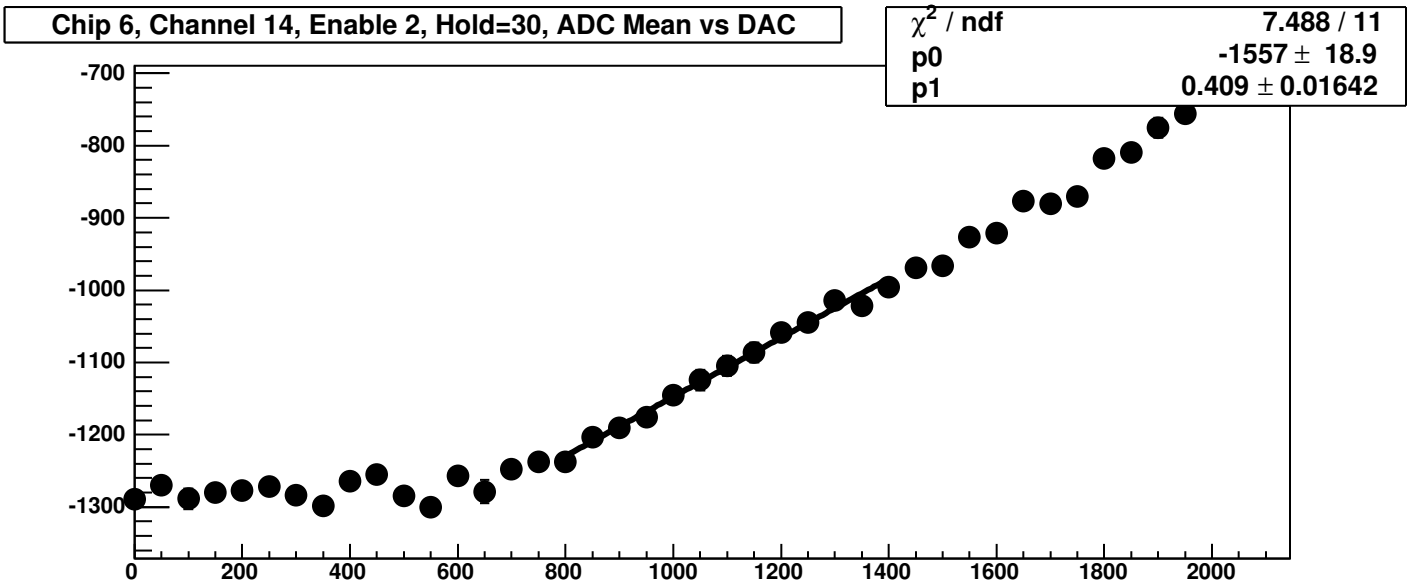


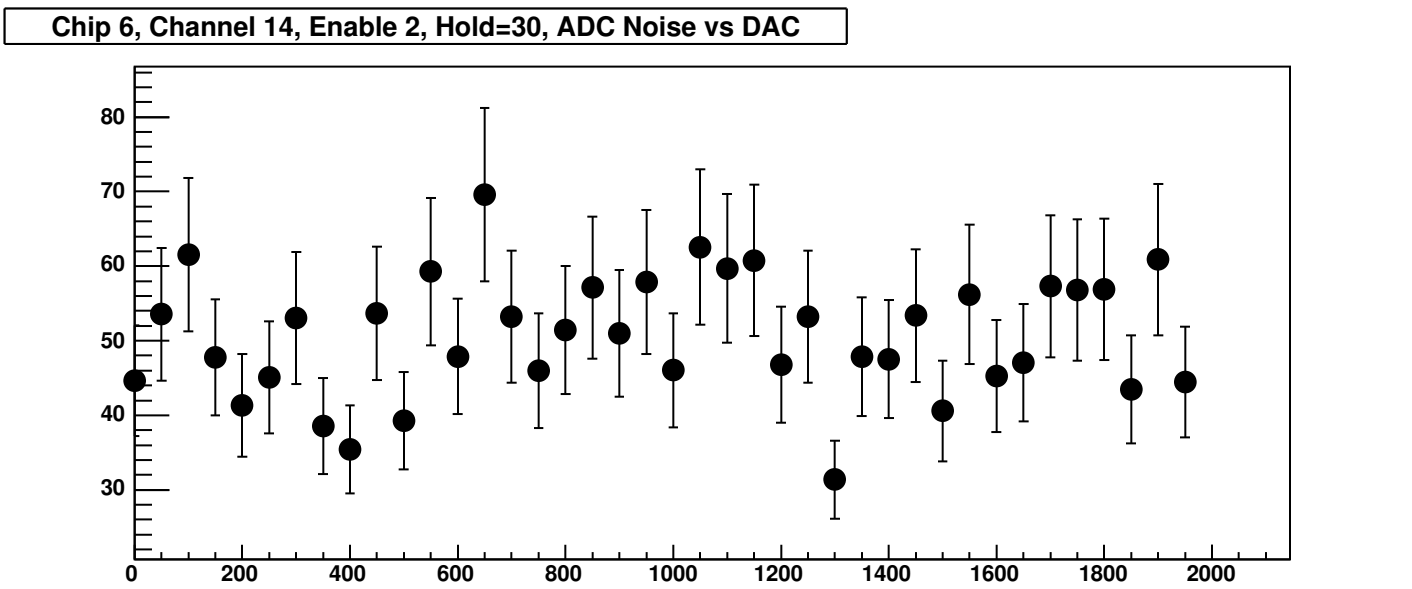
Chip 6, Channel 14, Enable 1!, Hold=30, ADC Noise vs DAC

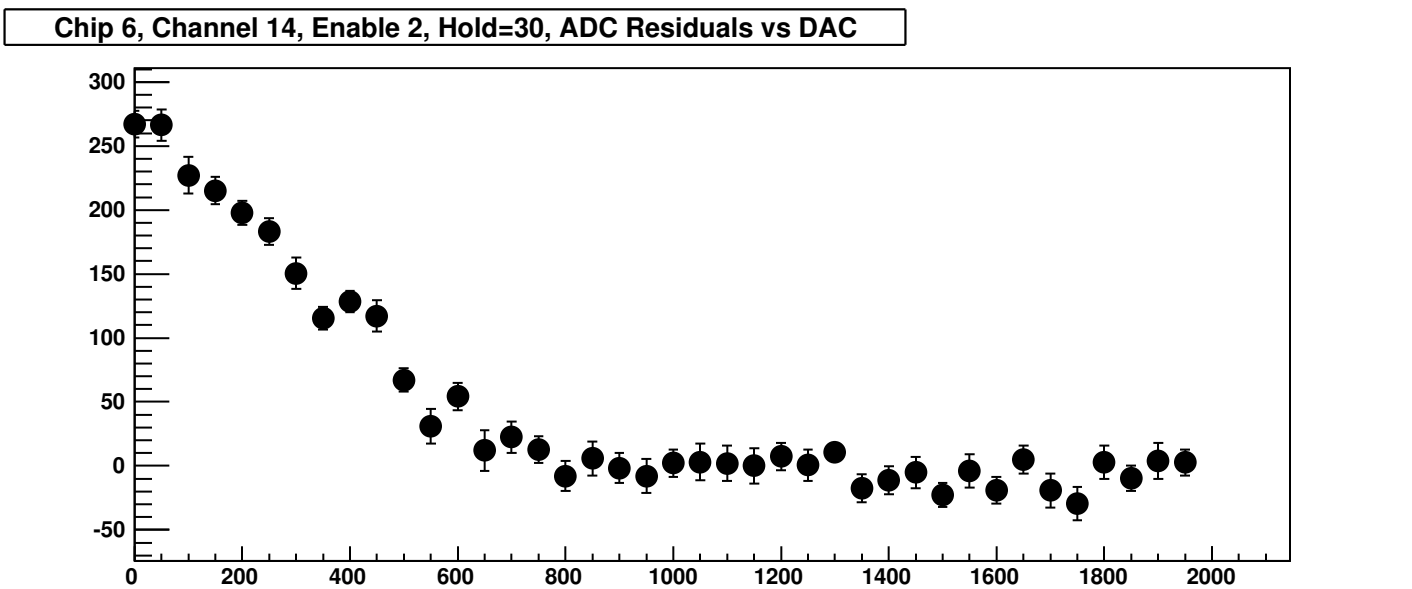


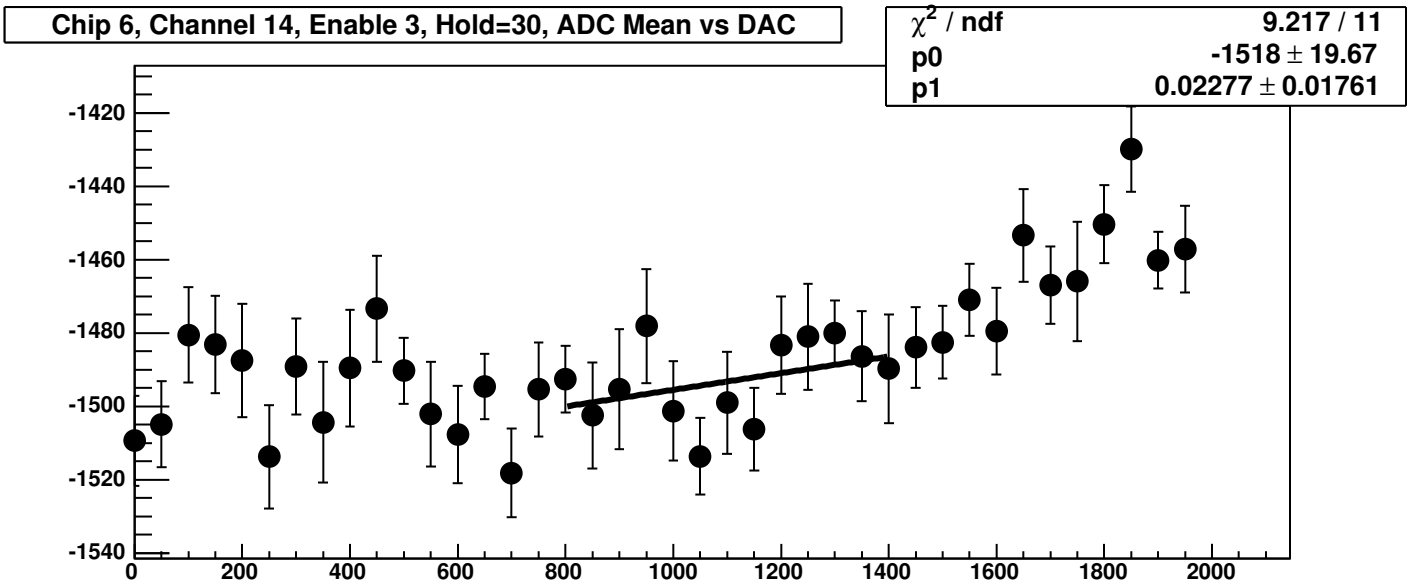
Chip 6, Channel 14, Enable 1!, Hold=30, ADC Residuals vs DAC

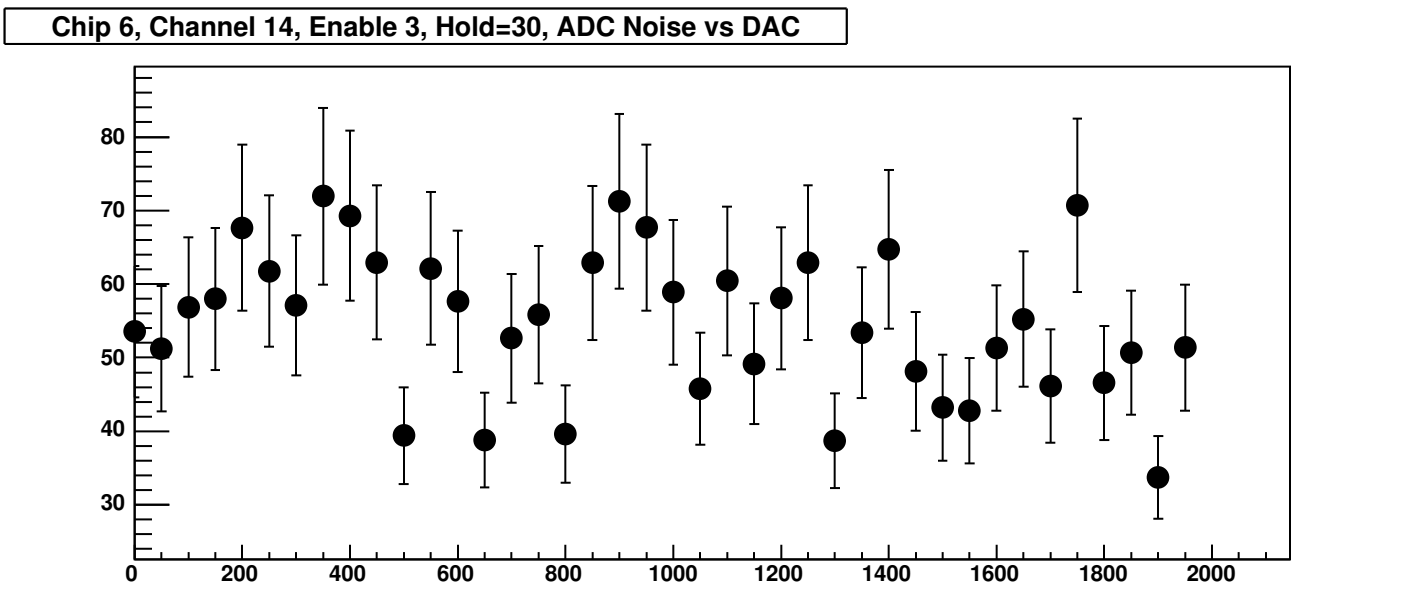


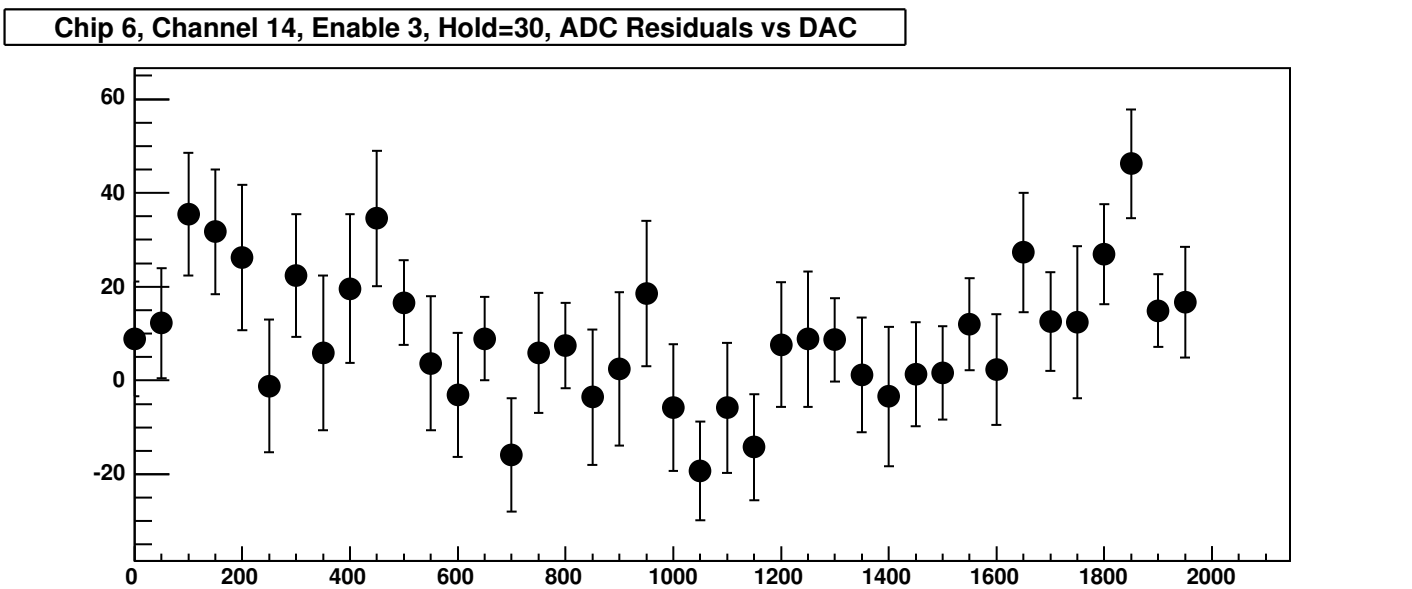

Chip 6, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 7.488 / 11
p0 -1557 ± 18.9
p1 0.409 ± 0.01642
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

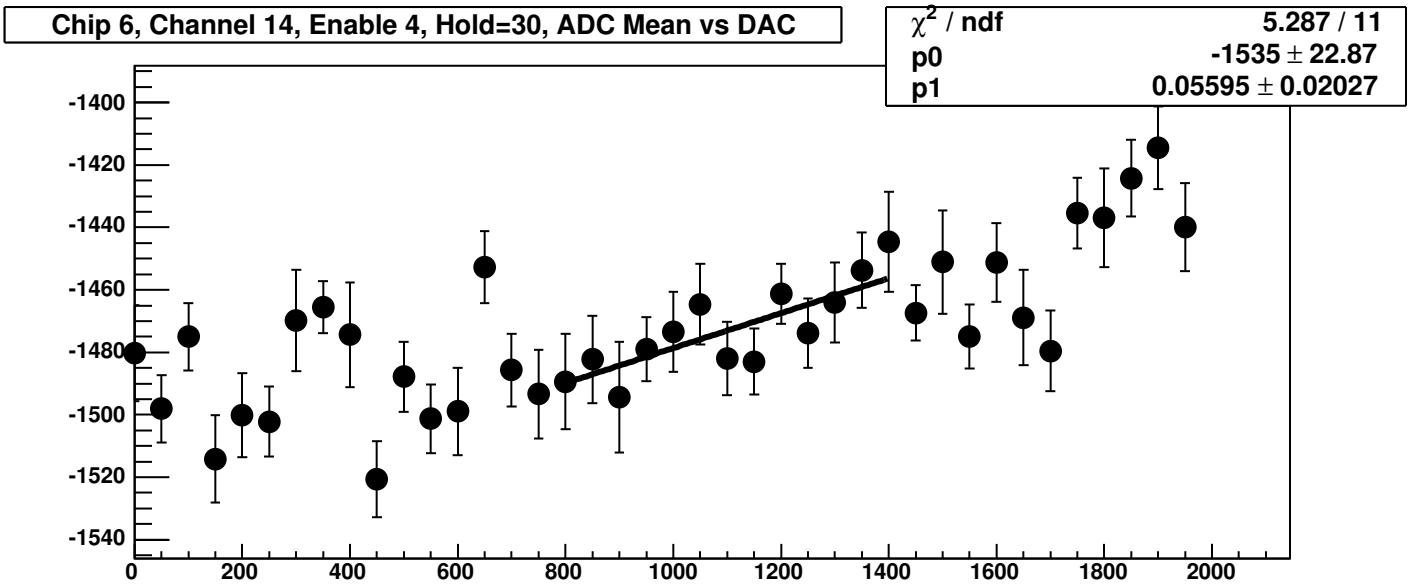

Chip 6, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

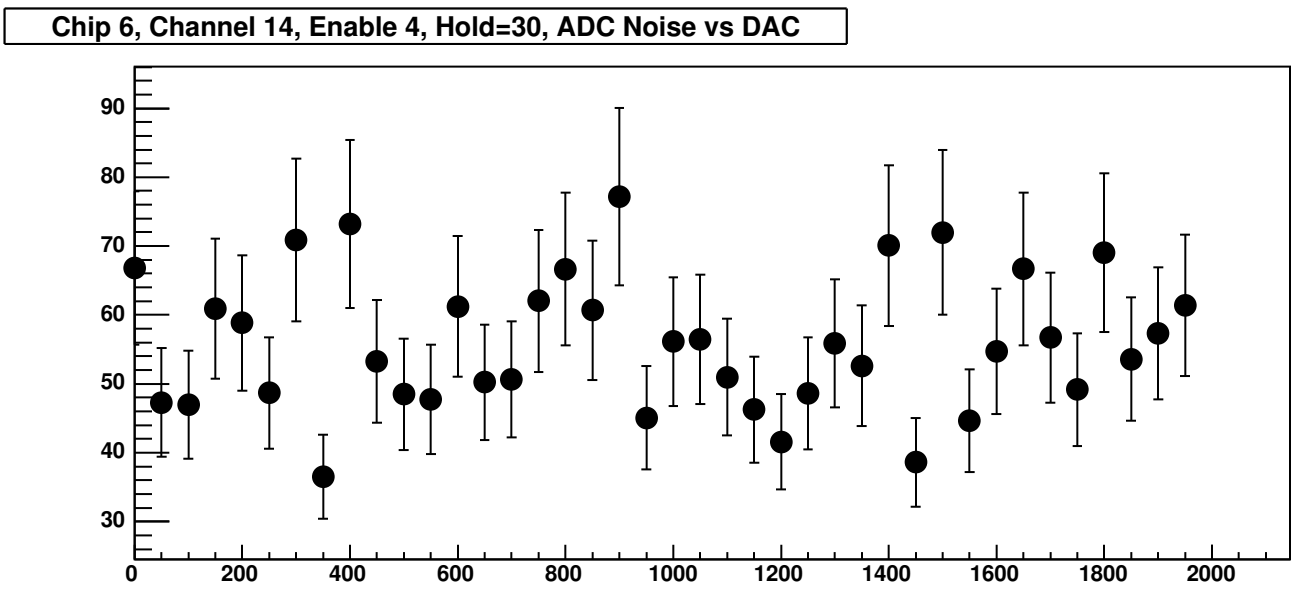

Chip 6, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

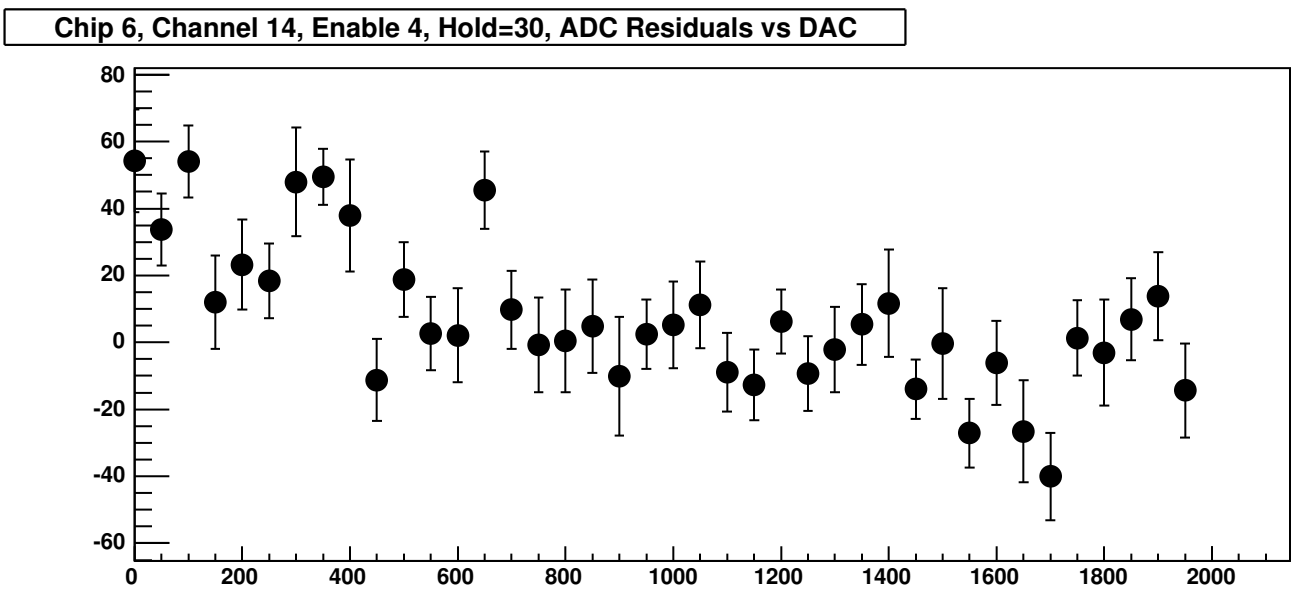

Chip 6, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 9.217 / 11
p0 -1518 ± 19.67
p1 0.02277 ± 0.01761
-1420
-1440
-1460
-1480
-1500
-1520
-1540
0
200
400
600
800
1000
1200
1400
1600
1800
2000

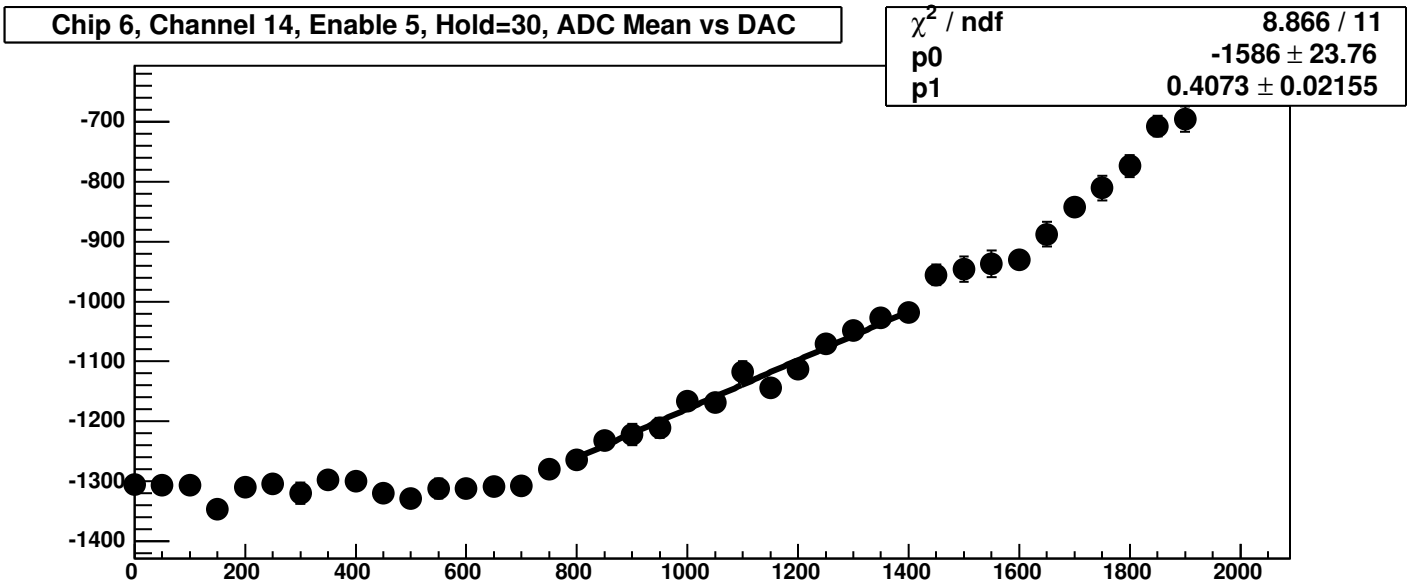

Chip 6, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

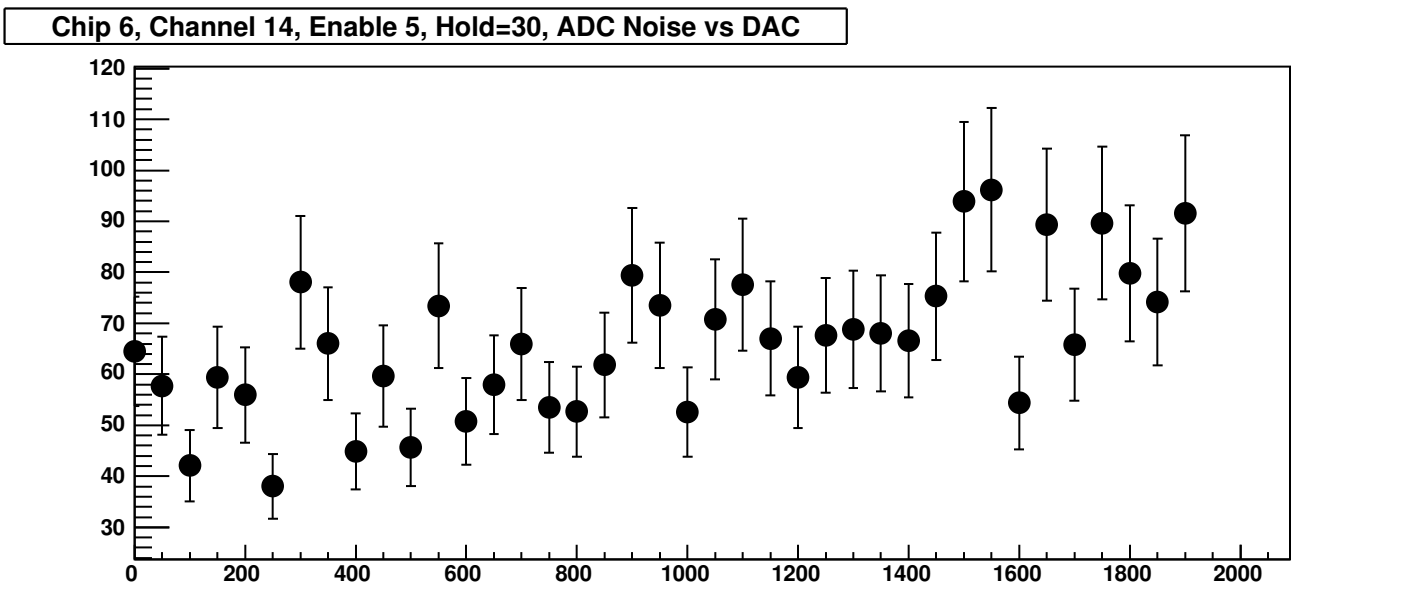

Chip 6, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

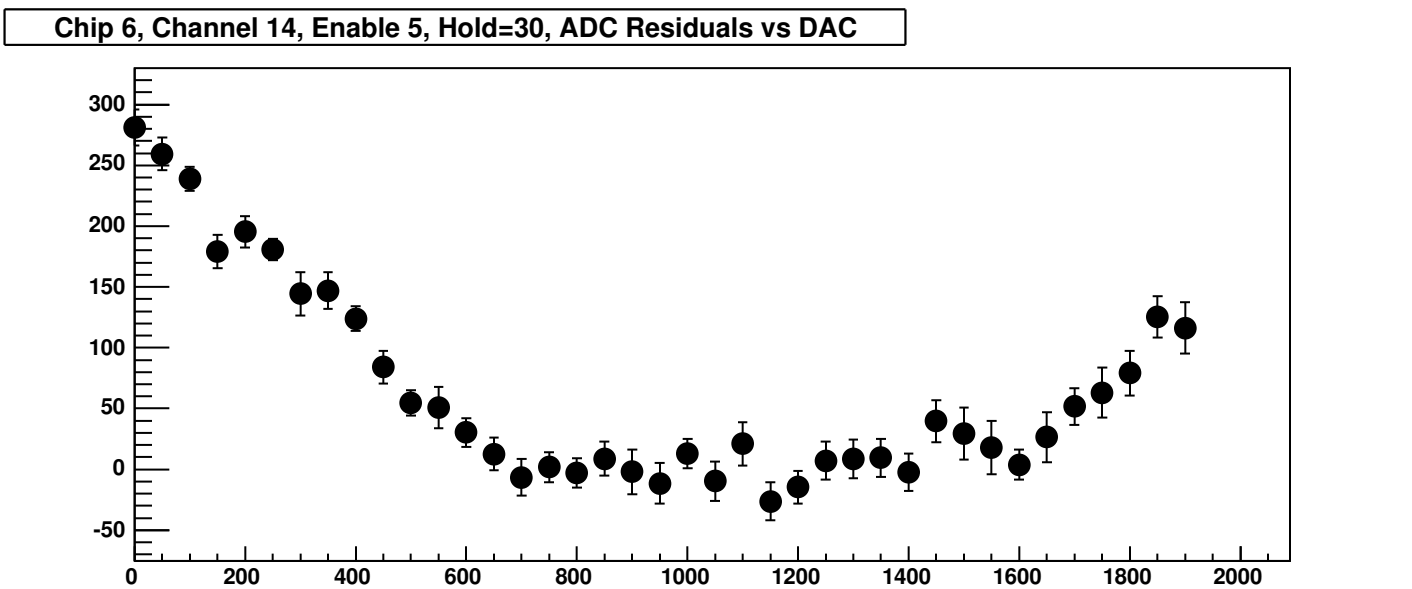

Chip 6, Channel 14, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5.287 / 11
p0 -1535 ± 22.87
p1 0.05595 ± 0.02027
-1400
-1420
-1440
-1460
-1480
-1500
-1520
-1540
0
200
400
600
800
1000
1200
1400
1600
1800
2000

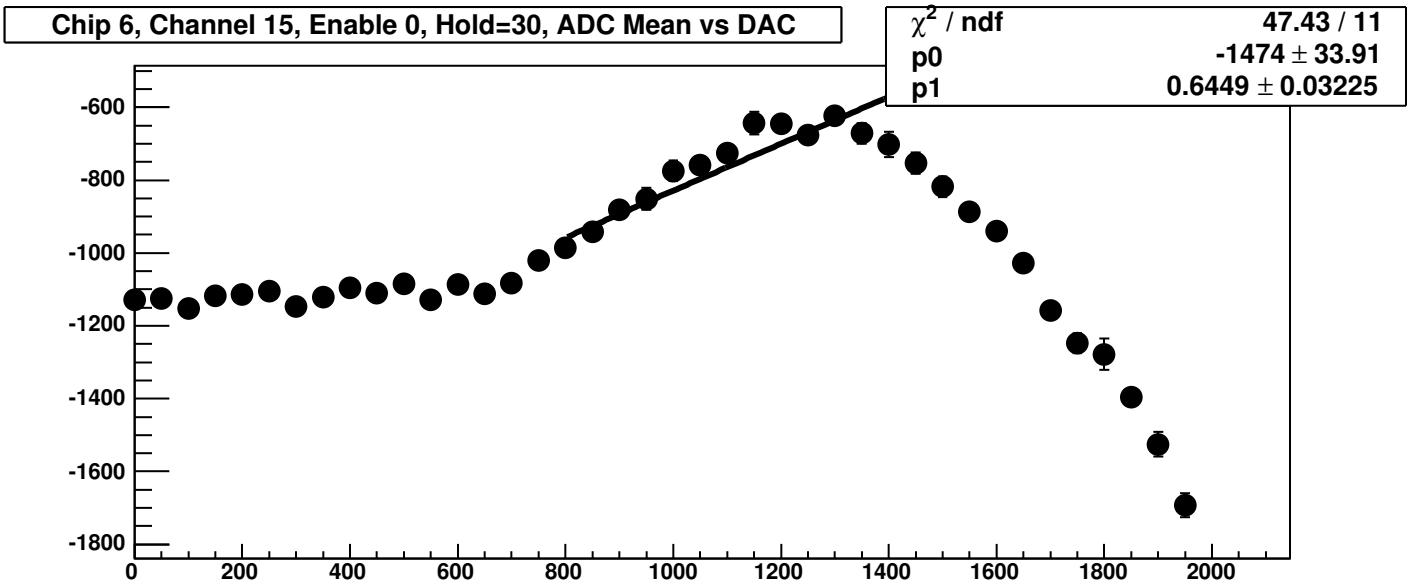

Chip 6, Channel 14, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

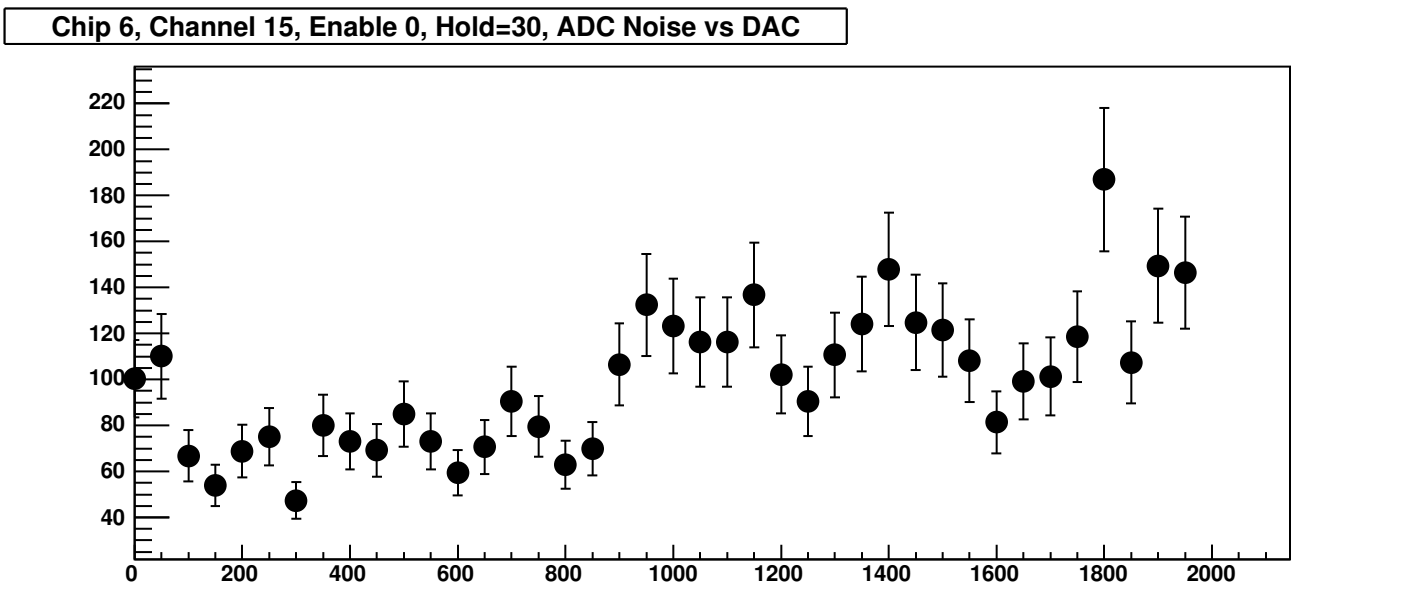

Chip 6, Channel 14, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

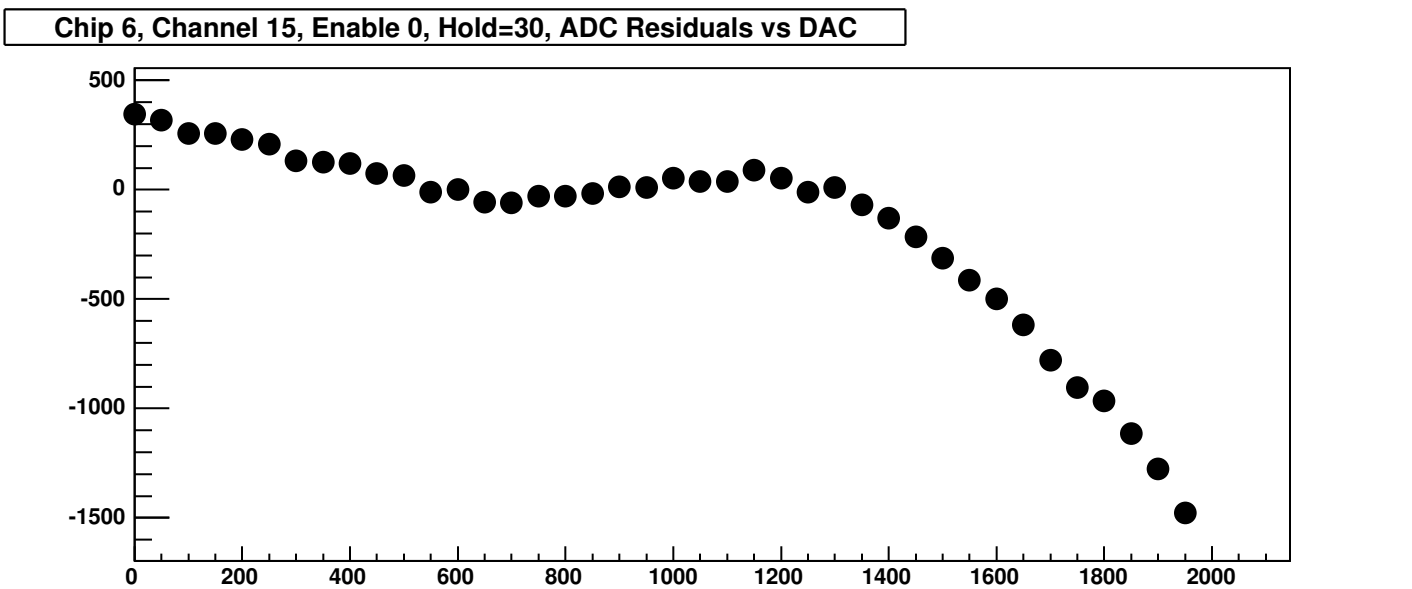

Chip 6, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8.866 / 11
p0 -1586 ± 23.76
p1 0.4073 ± 0.02155
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
120
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

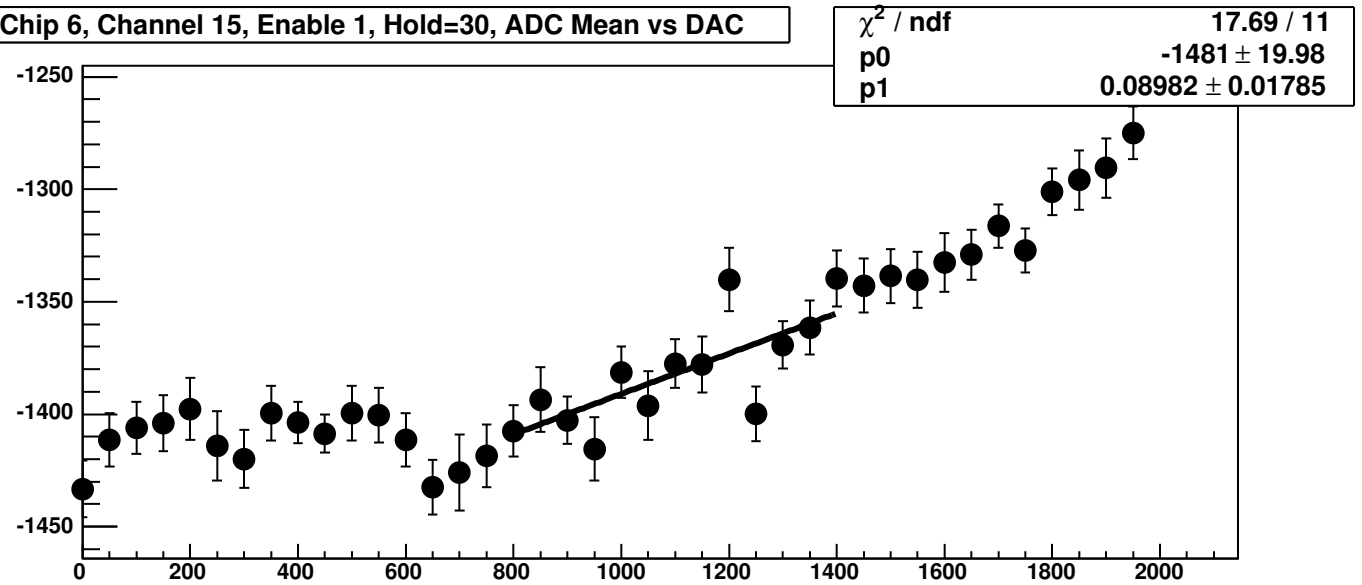

Chip 6, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 47.43 / 11
p0 -1474 ± 33.91
p1 0.6449 ± 0.03225
-600
-800
-1000
-1200
-1400
-1600
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

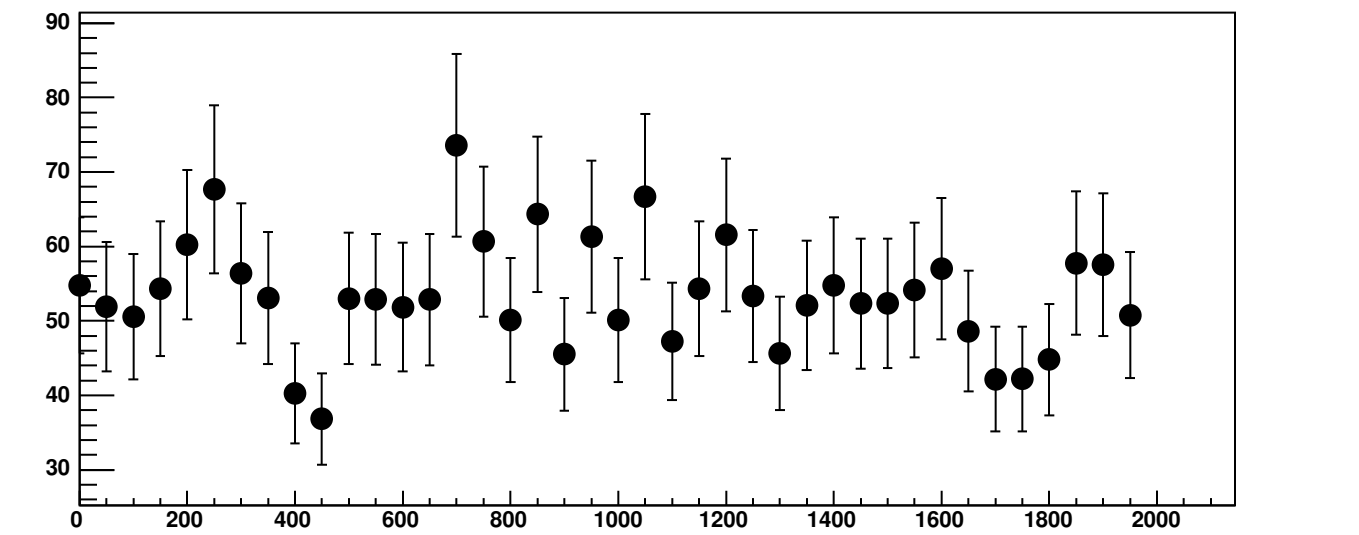

Chip 6, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
500
0
-500
-1000
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

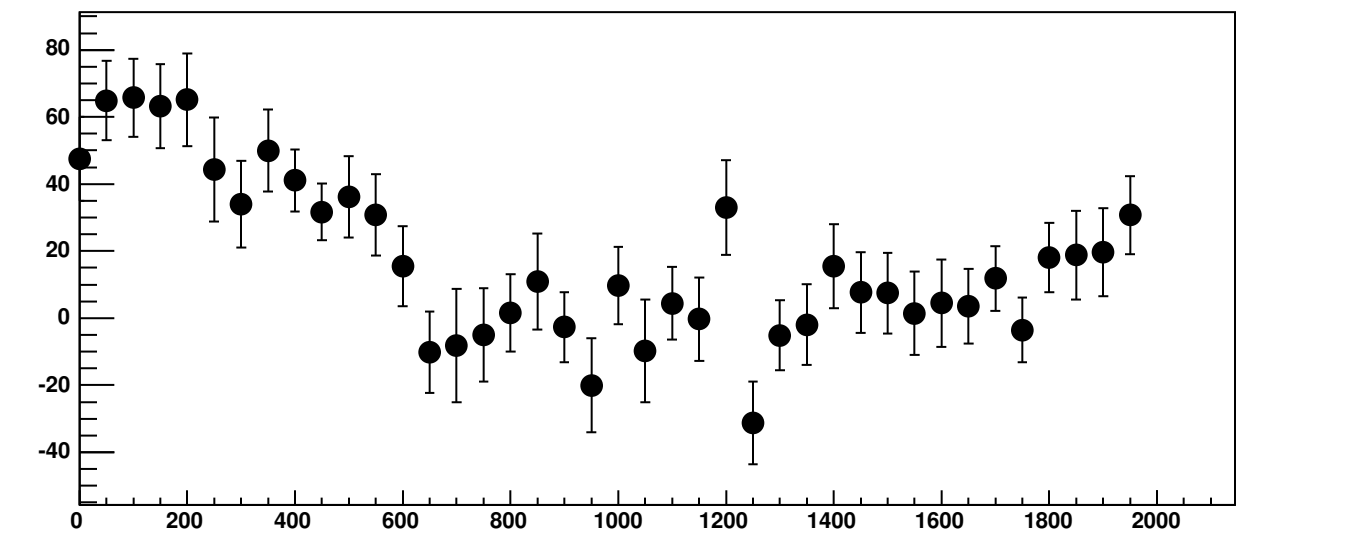
Chip 6, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC



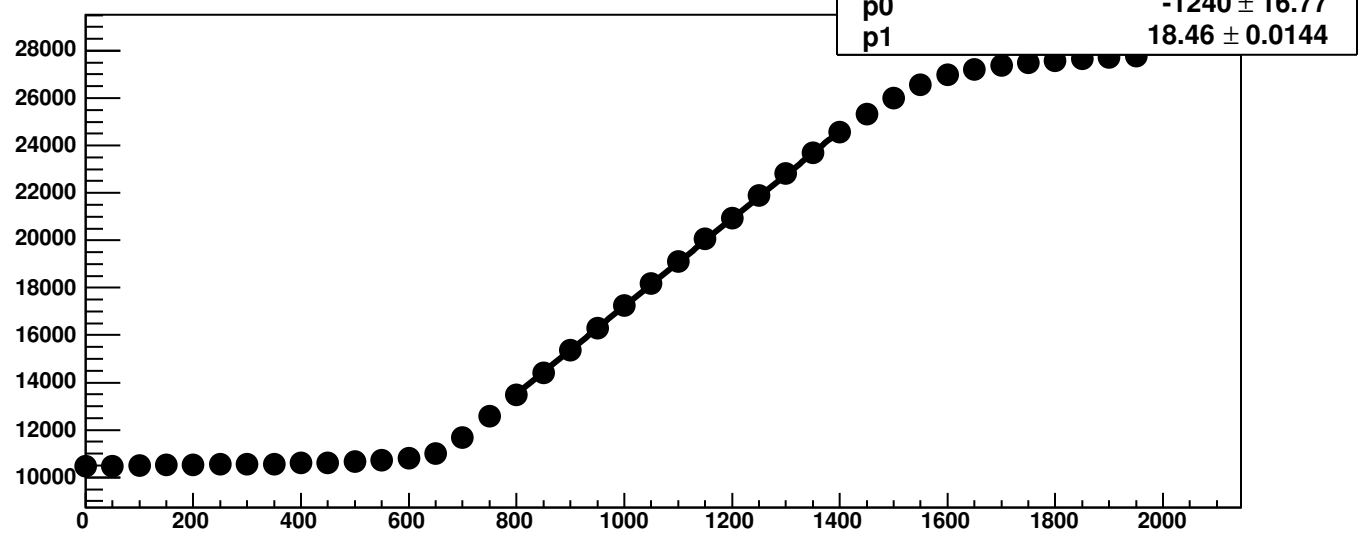
Chip 6, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC



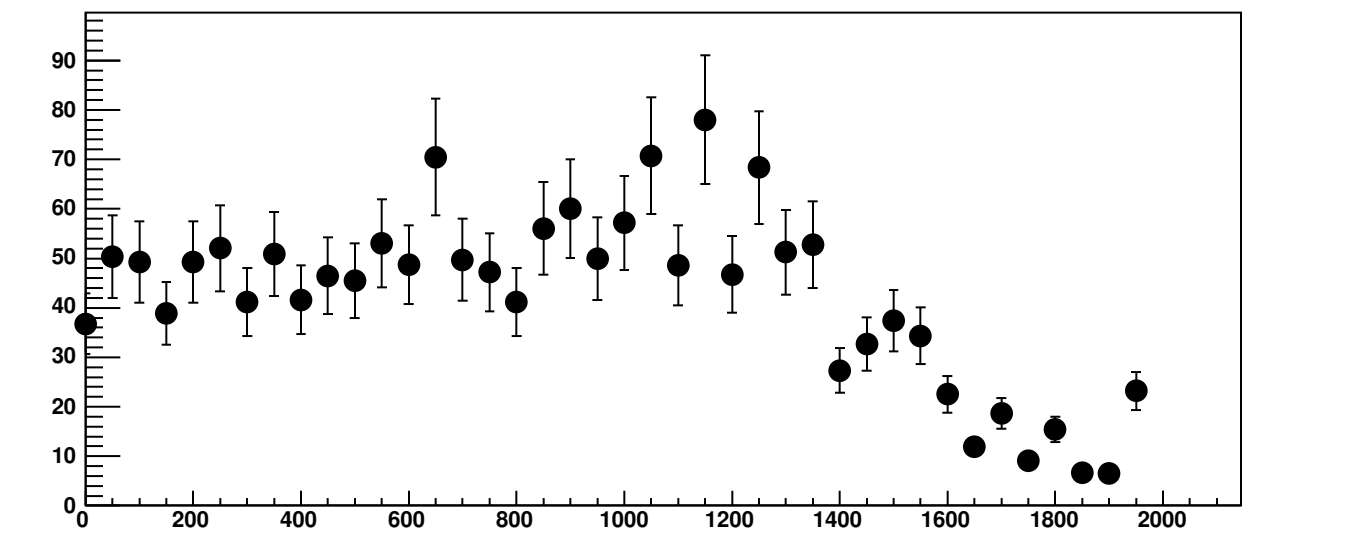
Chip 6, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC



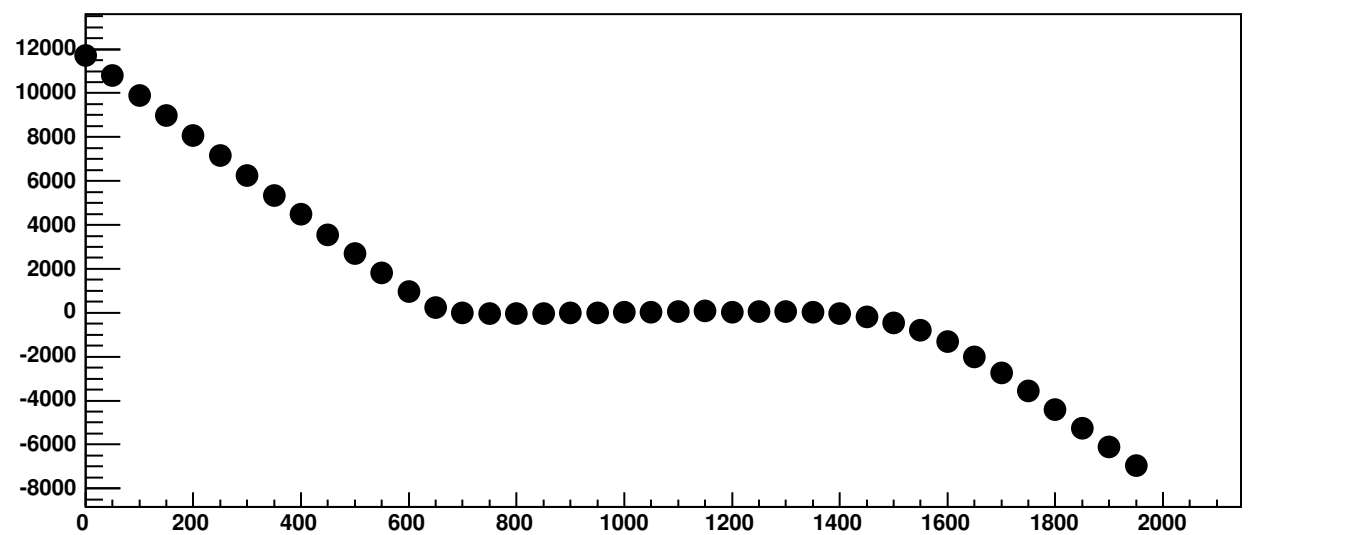
Chip 6, Channel 15, Enable 2!, Hold=30, ADC Mean vs DAC

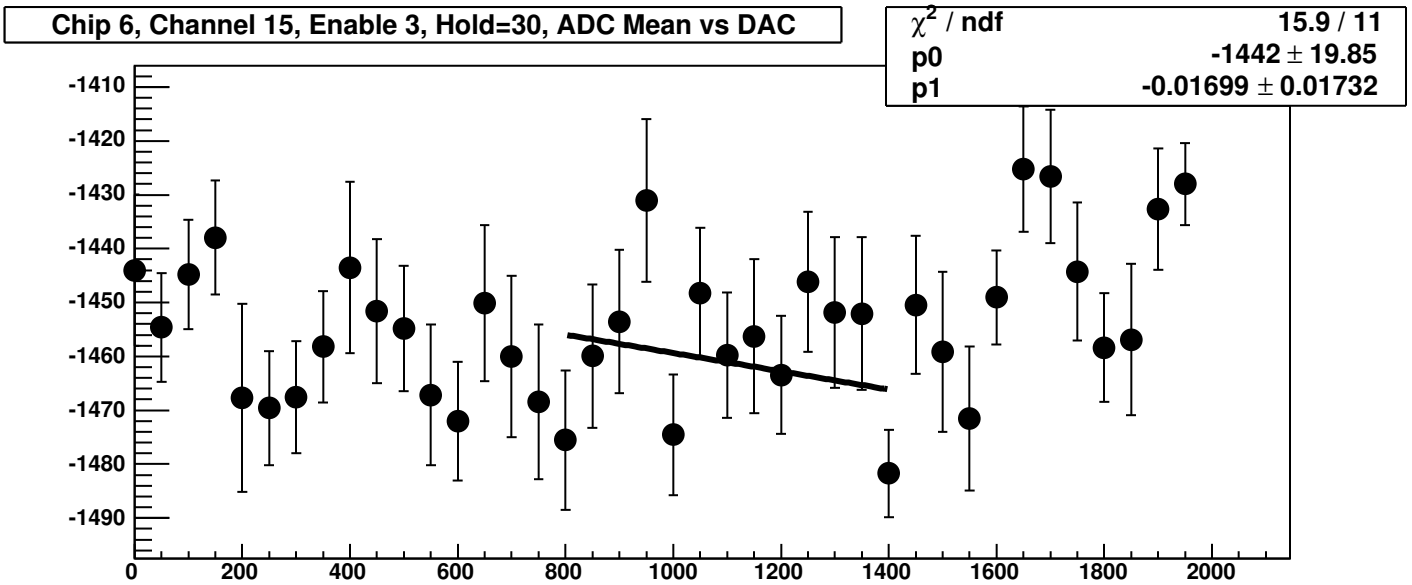


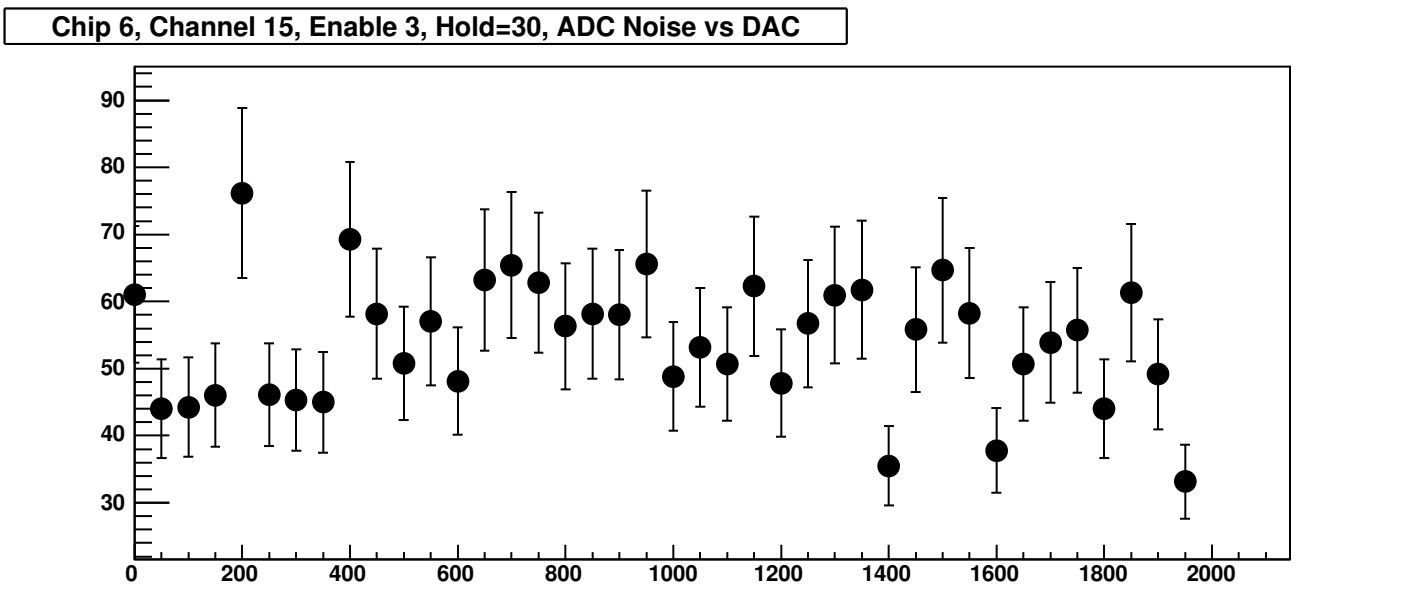
Chip 6, Channel 15, Enable 2!, Hold=30, ADC Noise vs DAC

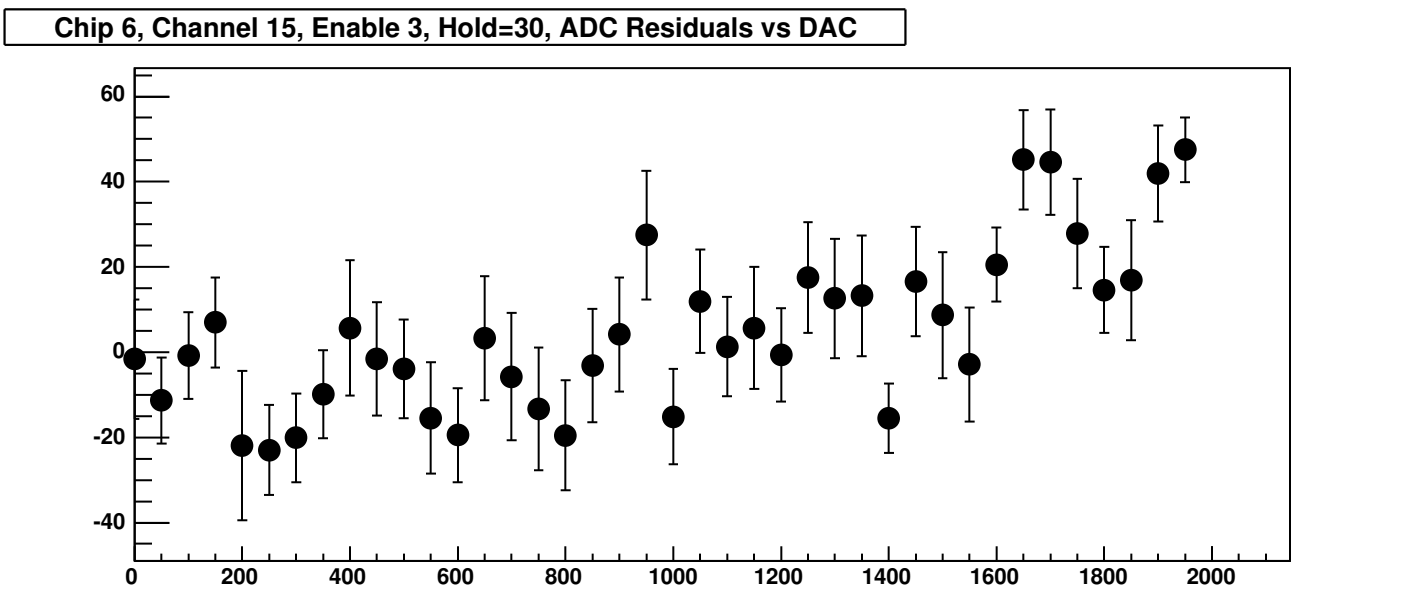


Chip 6, Channel 15, Enable 2!, Hold=30, ADC Residuals vs DAC

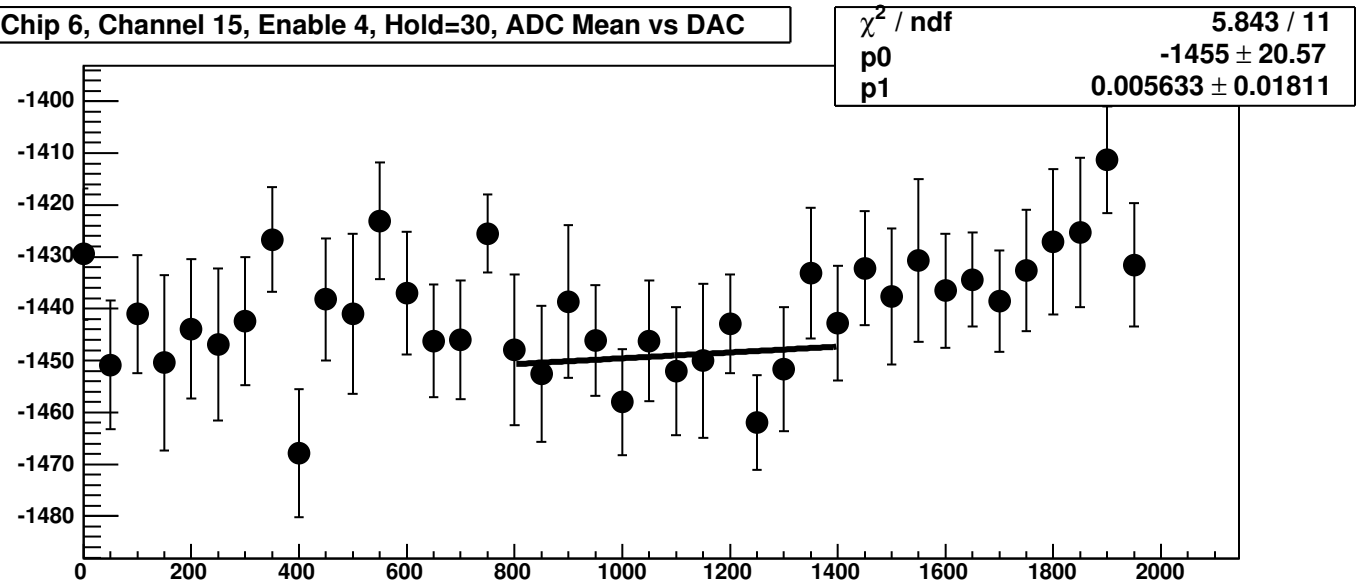



Chip 6, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 15.9 / 11
p0 -1442 ± 19.85
p1 -0.01699 ± 0.01732
-1410
-1420
-1430
-1440
-1450
-1460
-1470
-1480
-1490
0
200
400
600
800
1000
1200
1400
1600
1800
2000

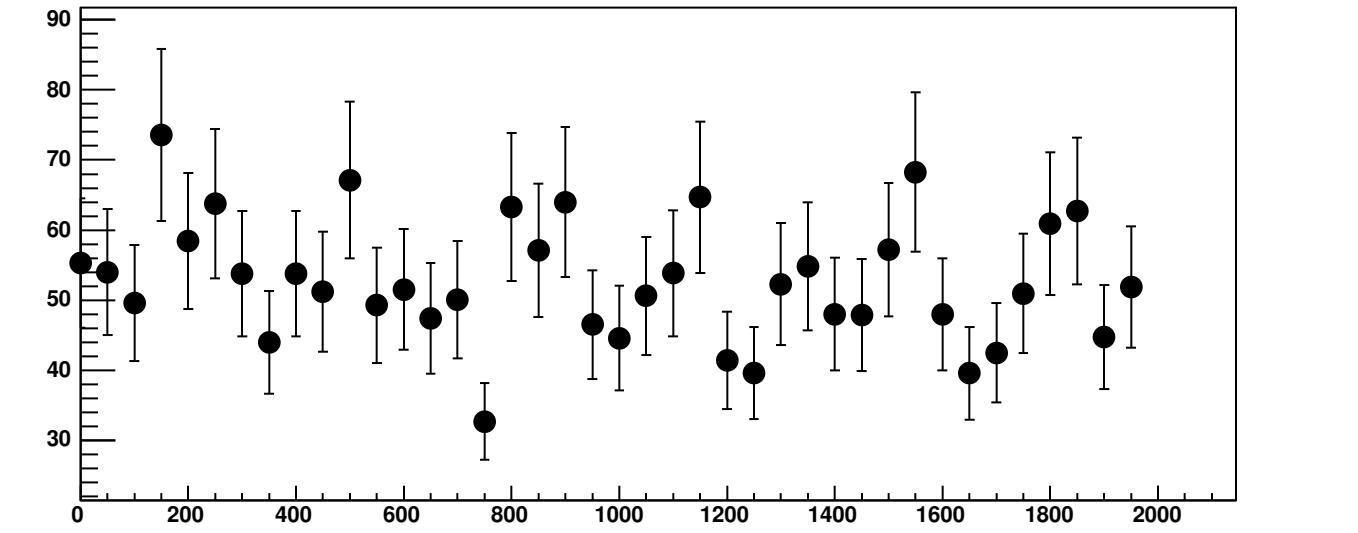

Chip 6, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

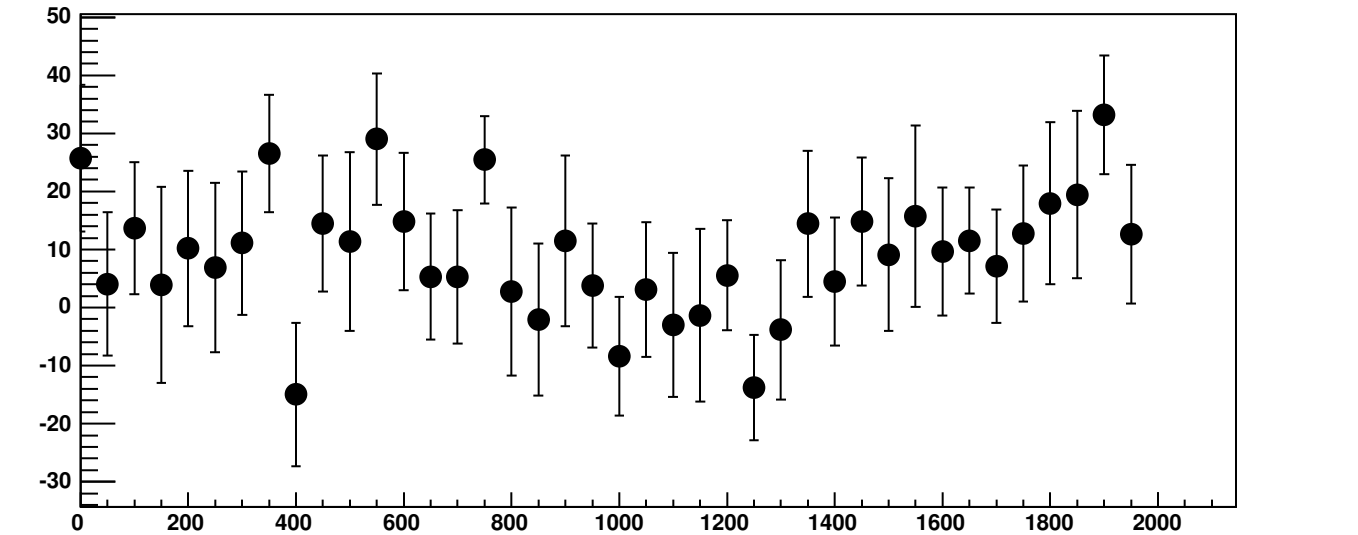
Chip 6, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC

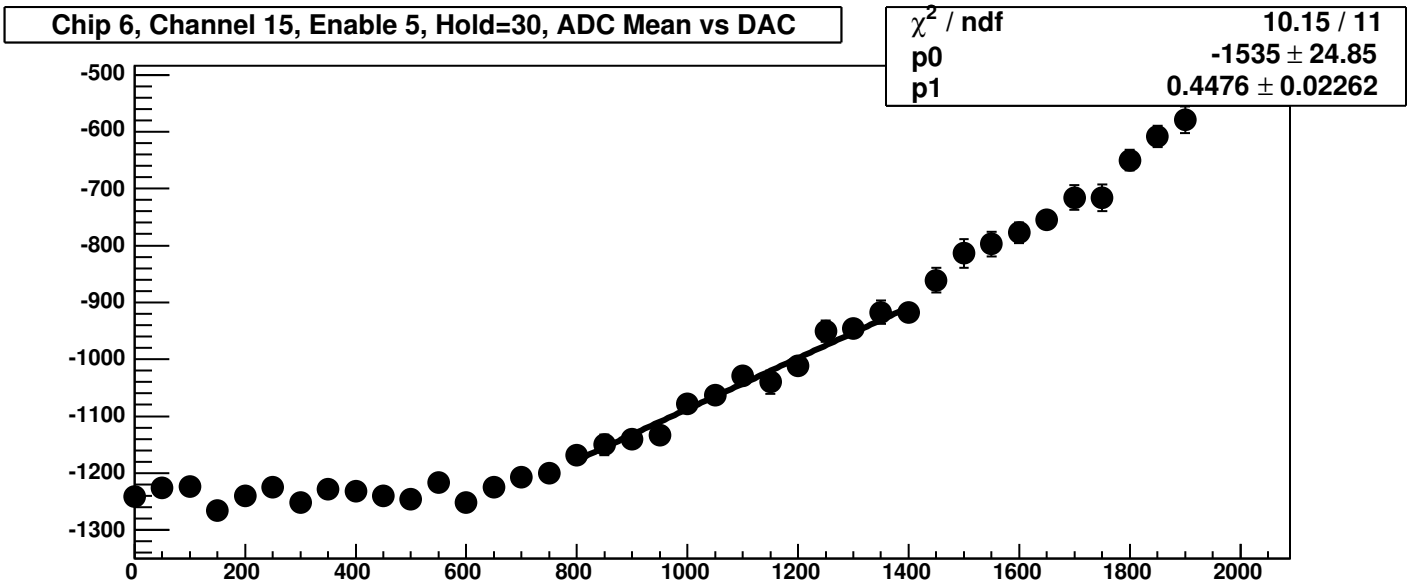


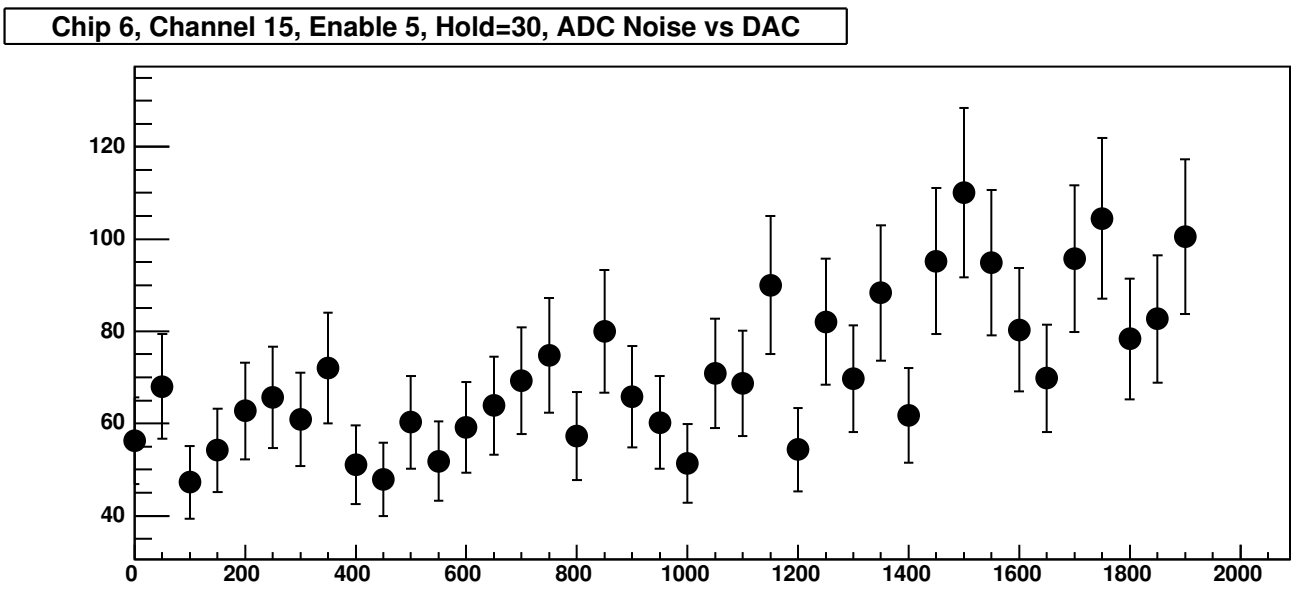
Chip 6, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC

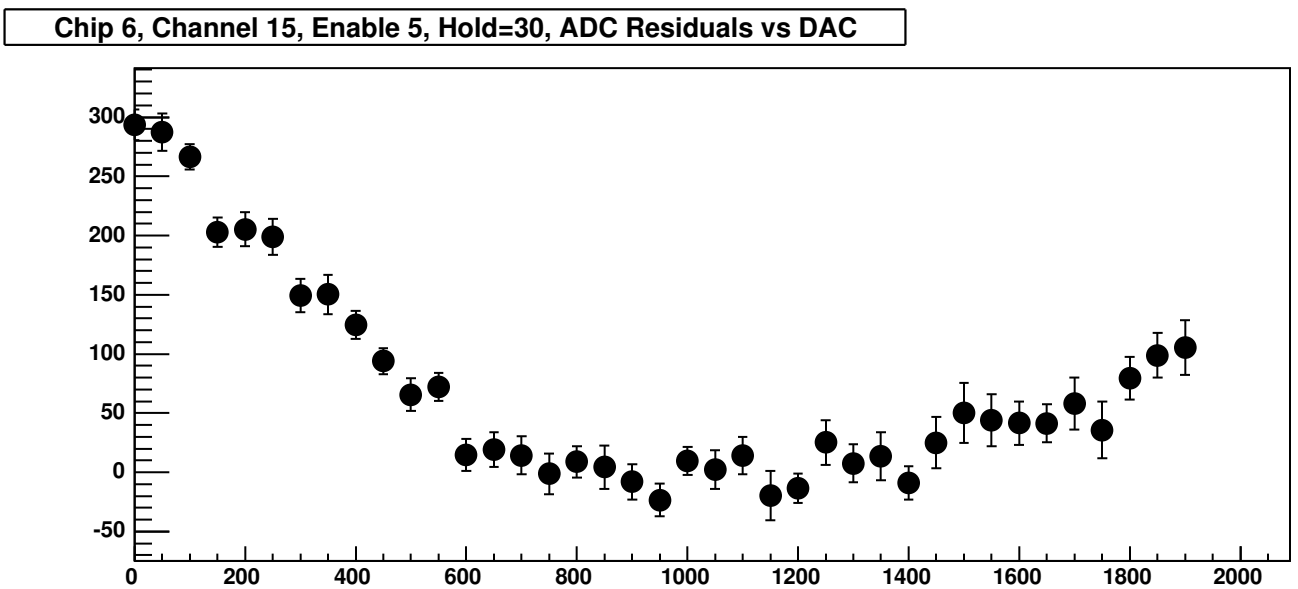


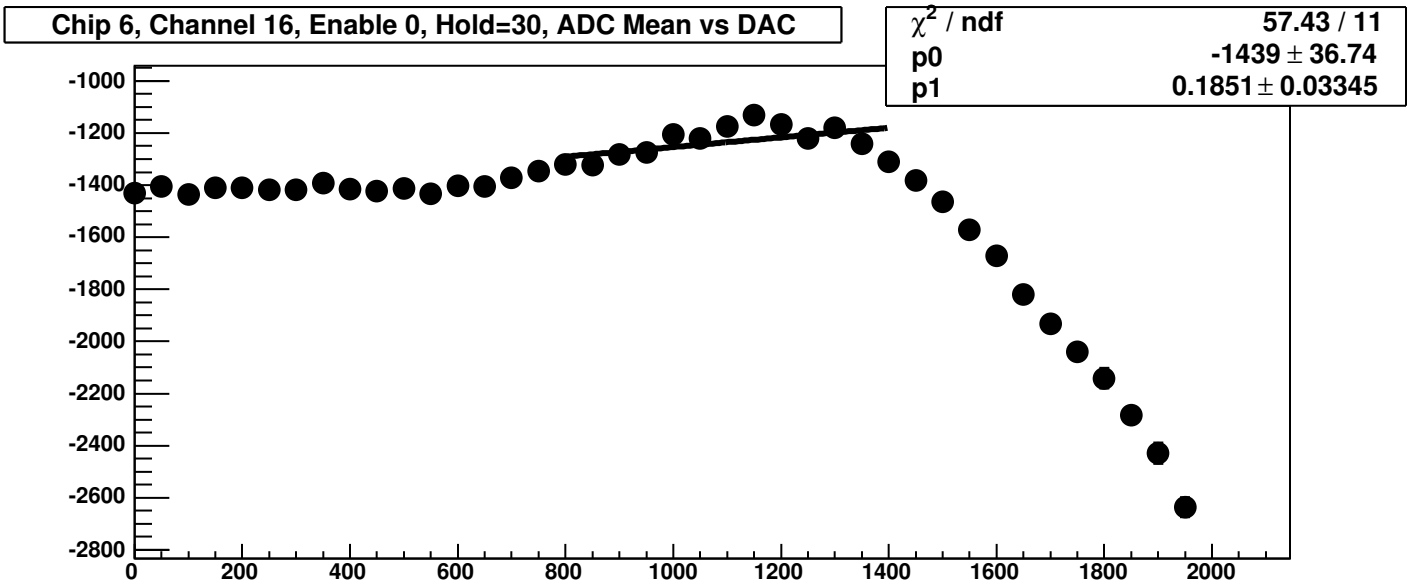
Chip 6, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC

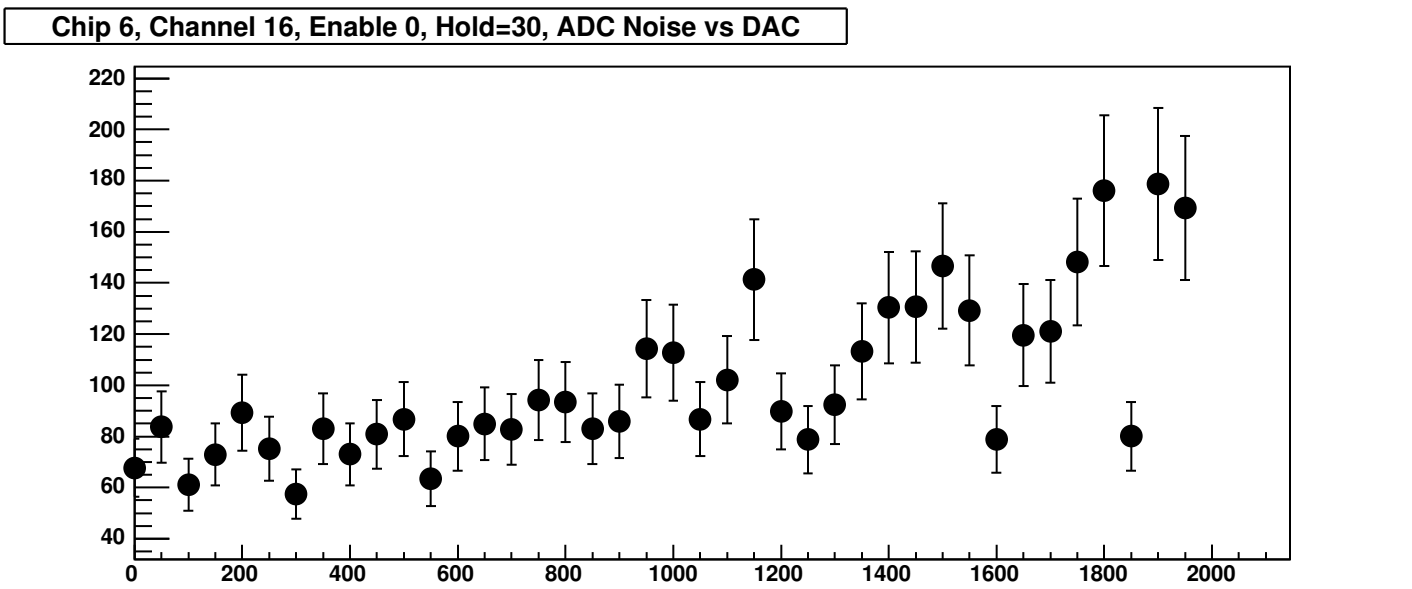


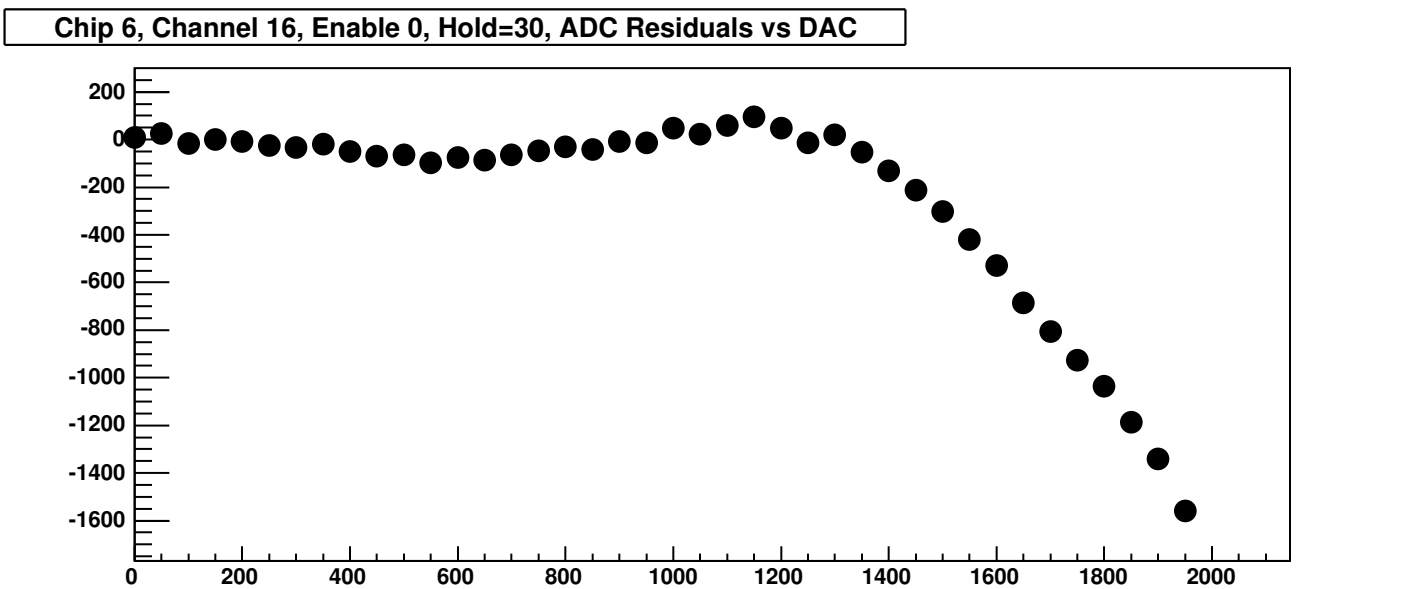

Chip 6, Channel 15, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 10.15 / 11
p0 -1535 ± 24.85
p1 0.4476 ± 0.02262
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

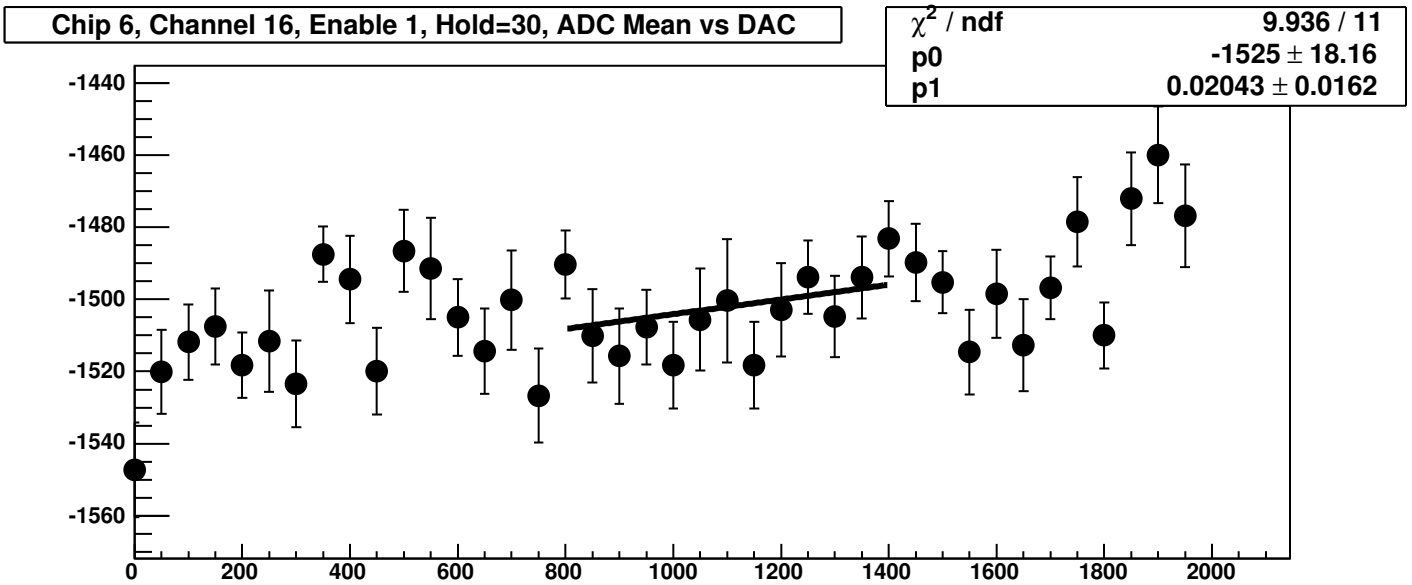

Chip 6, Channel 15, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

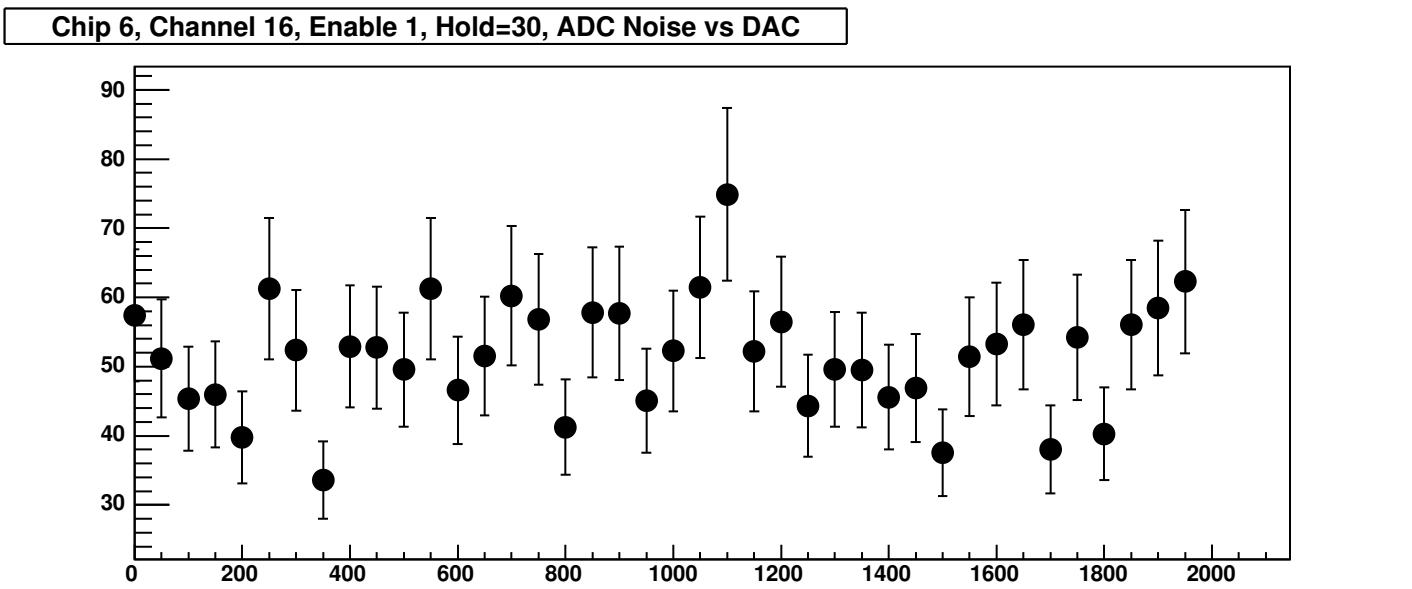

Chip 6, Channel 15, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

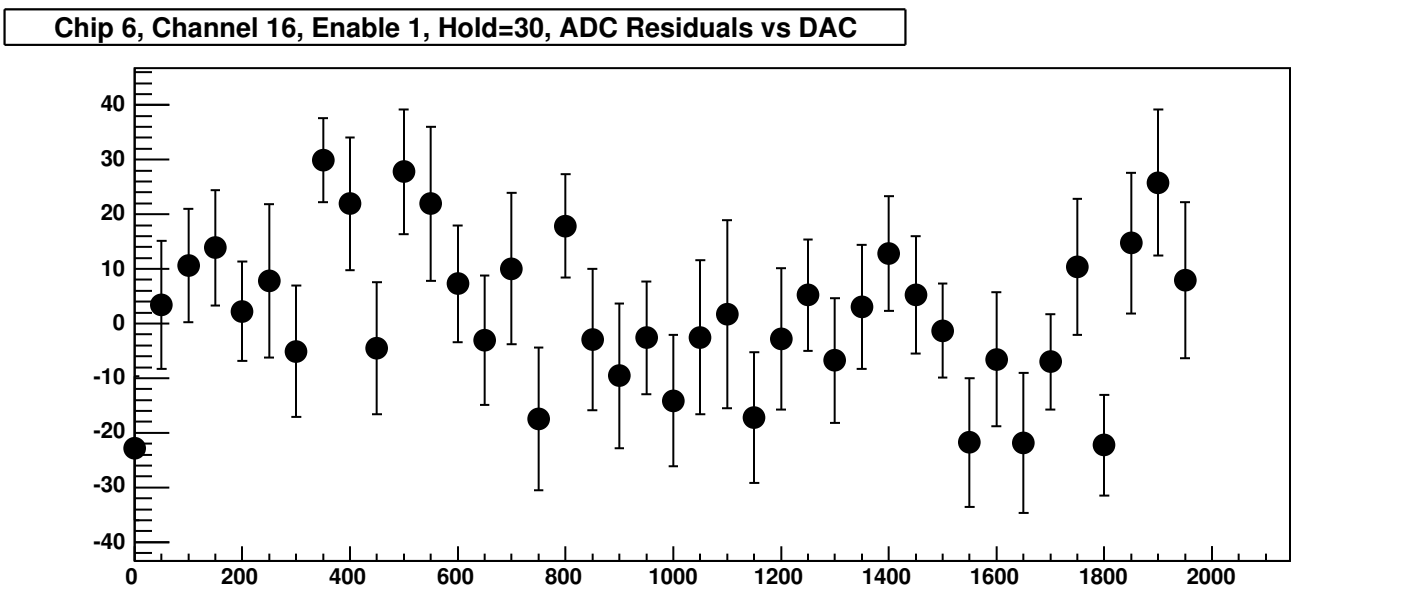

Chip 6, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 57.43 / 11
p0 -1439 ± 36.74
p1 0.1851 ± 0.03345
-1000
-1200
-1400
-1600
-1800
-2000
-2200
-2400
-2600
-2800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

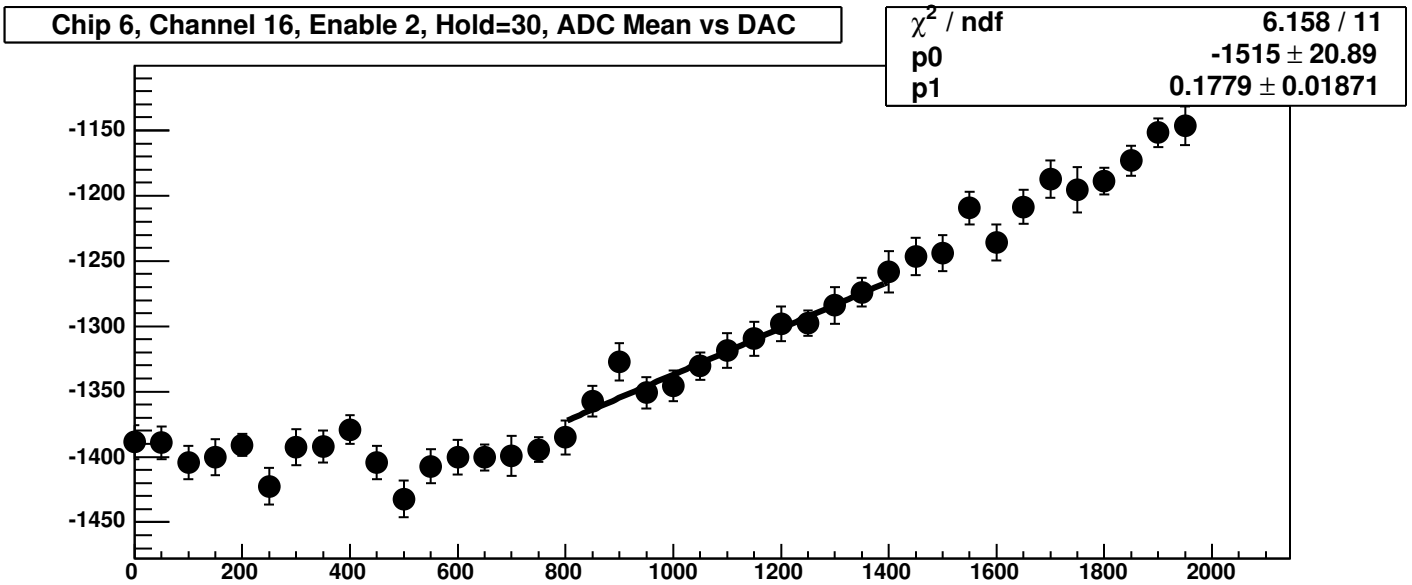

Chip 6, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

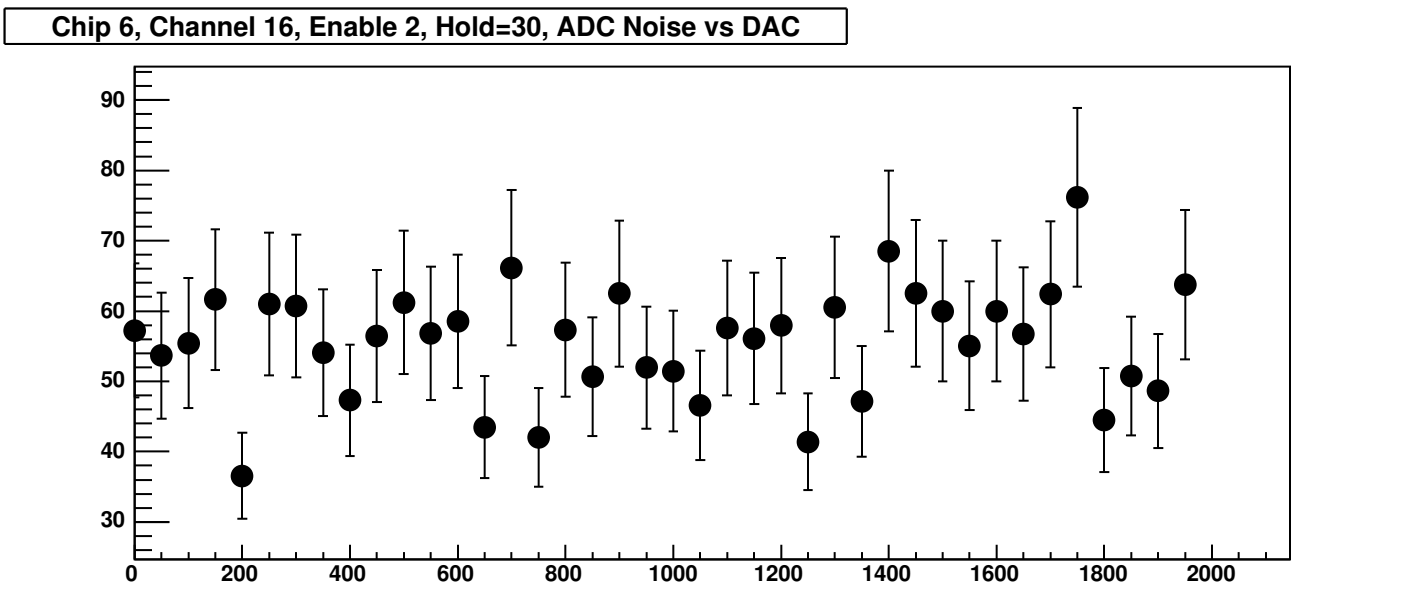

Chip 6, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

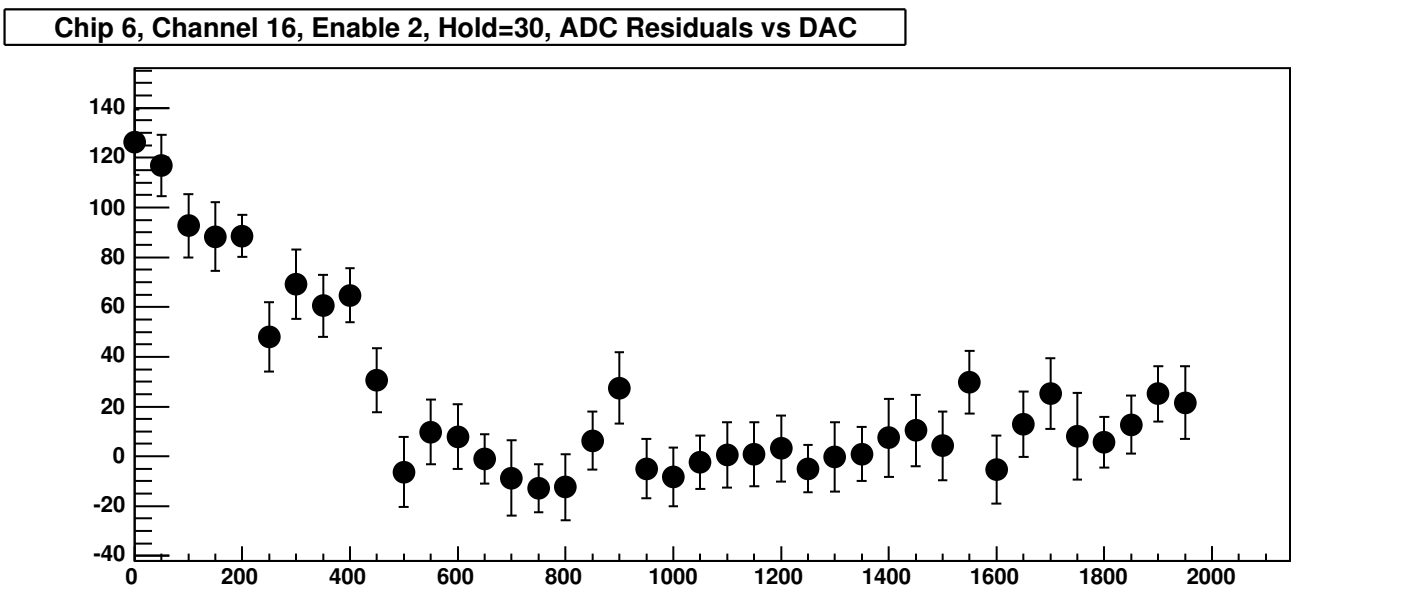

Chip 6, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 9.936 / 11
p0 -1525 ± 18.16
p1 0.02043 ± 0.0162
-1440
-1460
-1480
-1500
-1520
-1540
-1560
0
200
400
600
800
1000
1200
1400
1600
1800
2000

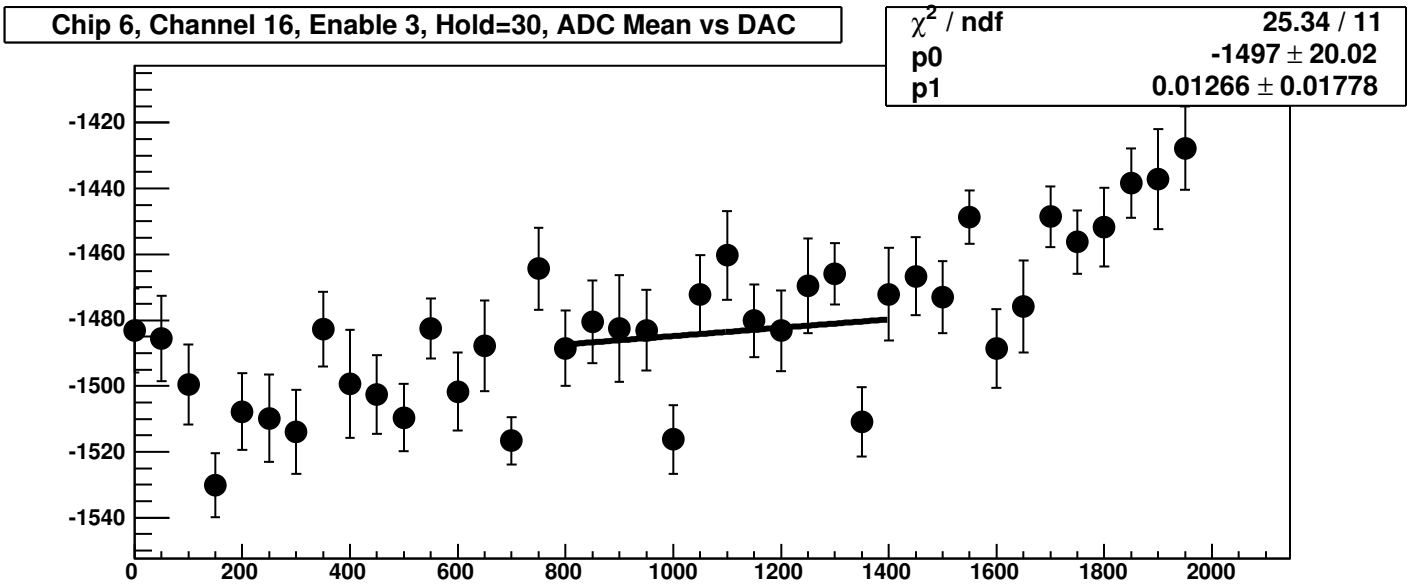

Chip 6, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

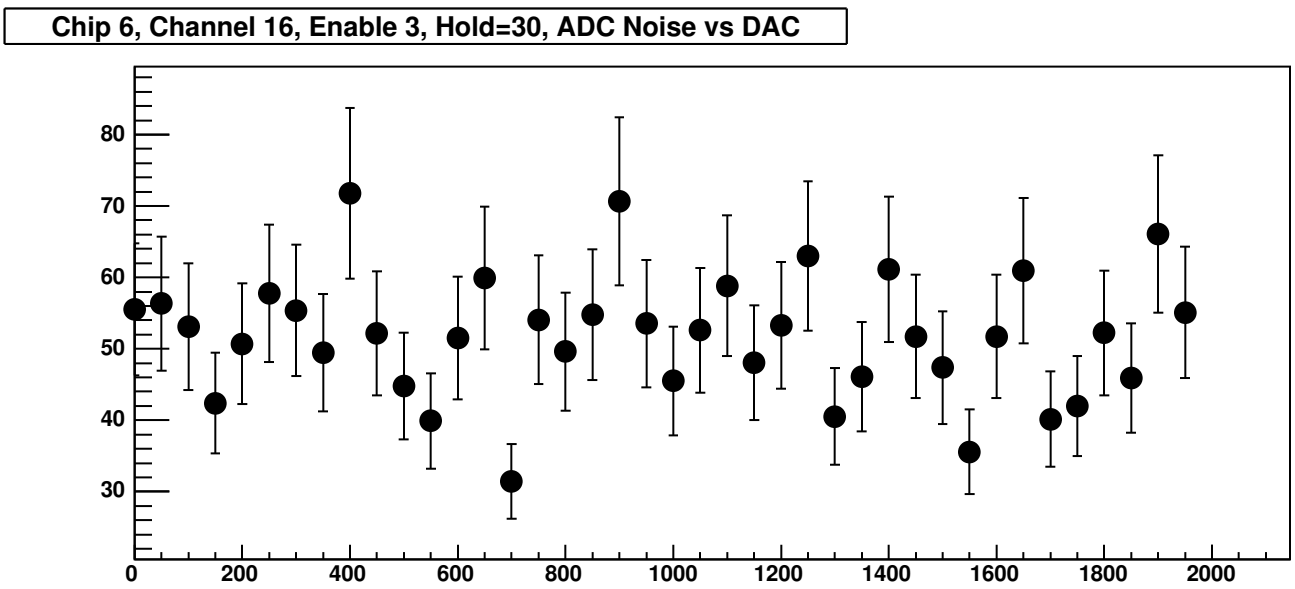

Chip 6, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
40
30
20
10
0
-10
-20
-30
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

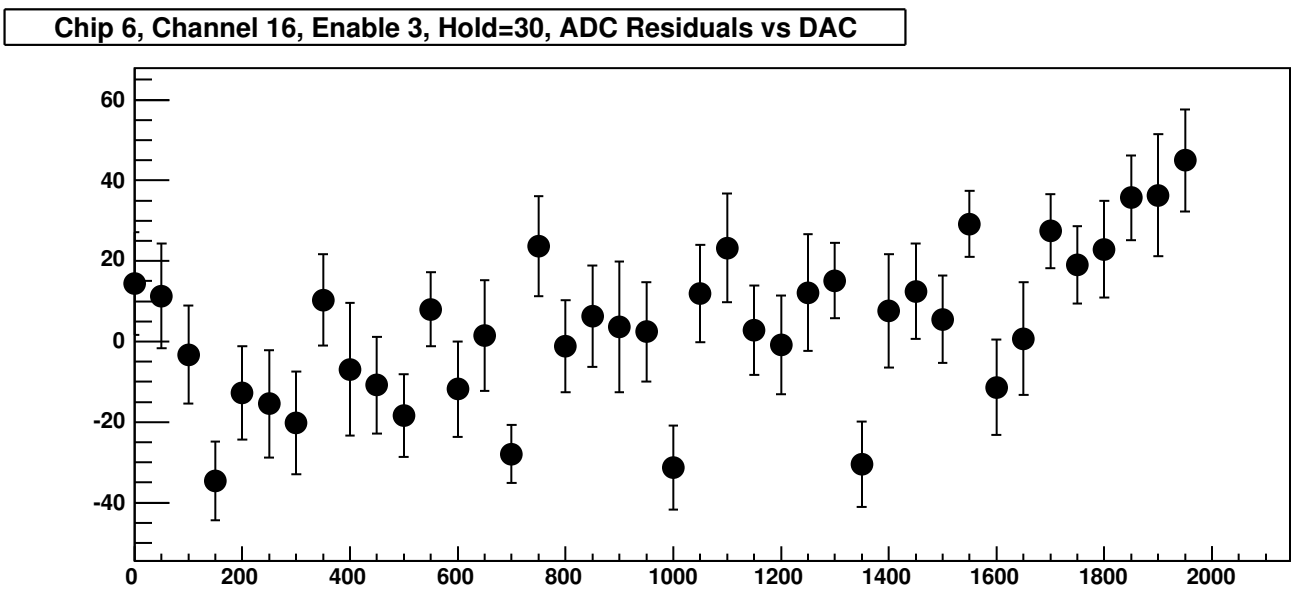

Chip 6, Channel 16, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 6.158 / 11
p0 -1515 ± 20.89
p1 0.1779 ± 0.01871
-1150
-1200
-1250
-1300
-1350
-1400
-1450
0
200
400
600
800
1000
1200
1400
1600
1800
2000

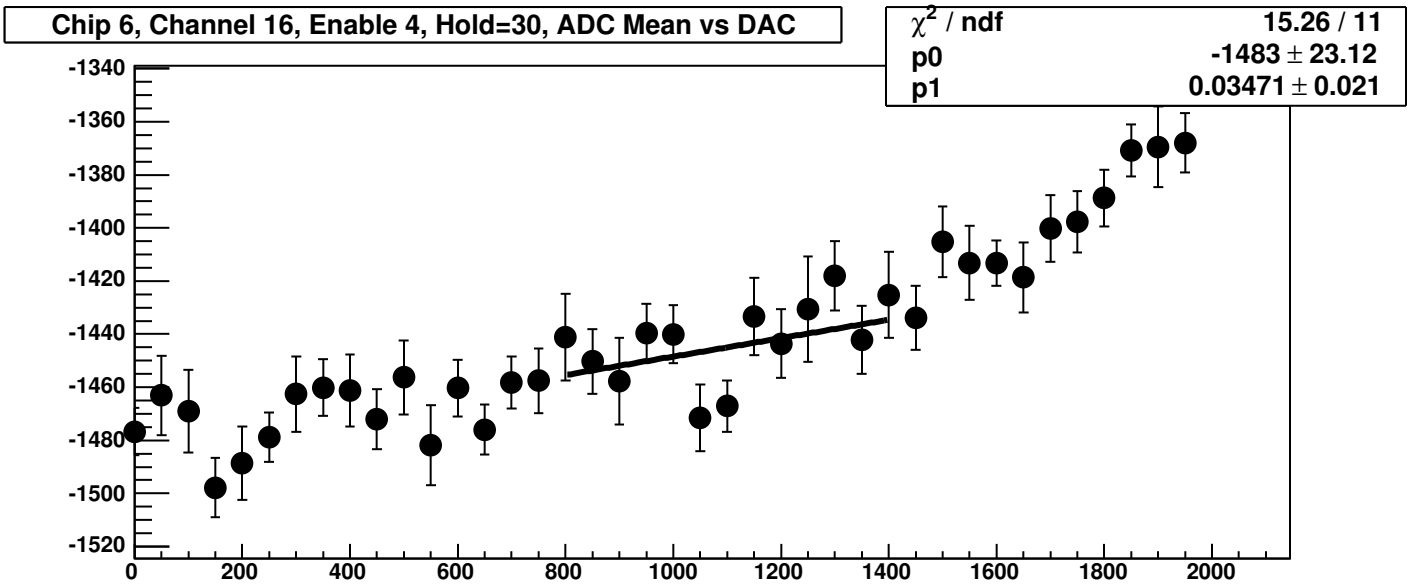

Chip 6, Channel 16, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

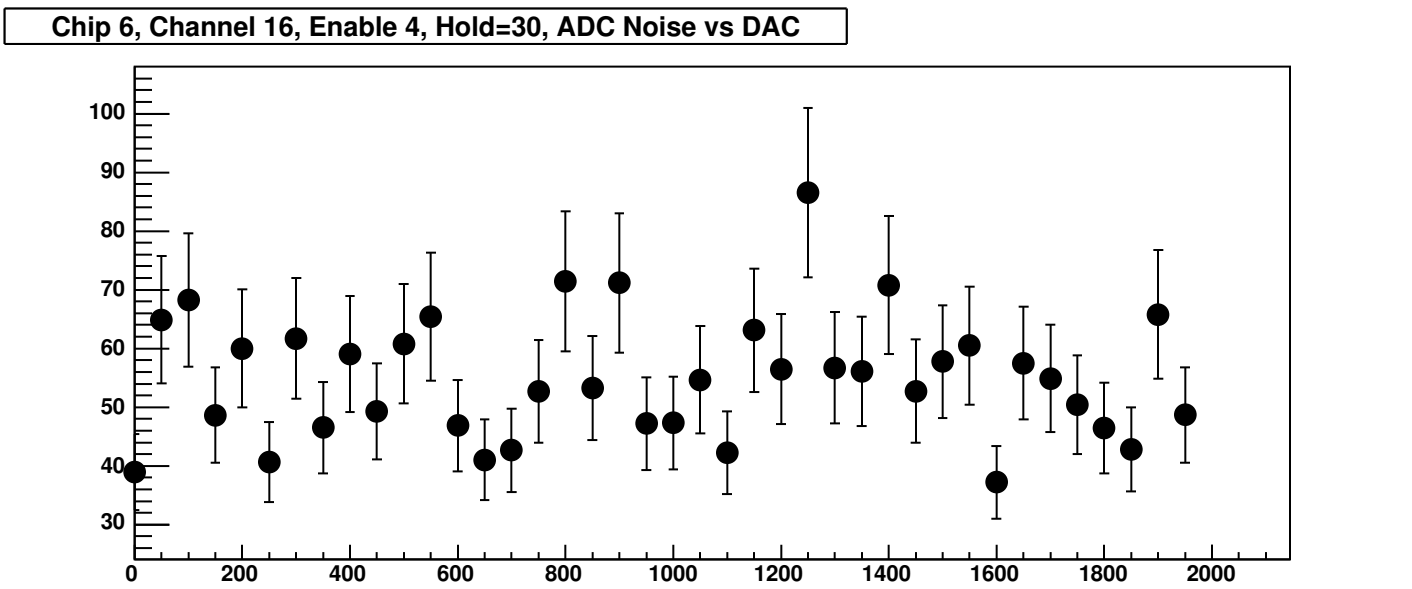

Chip 6, Channel 16, Enable 2, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

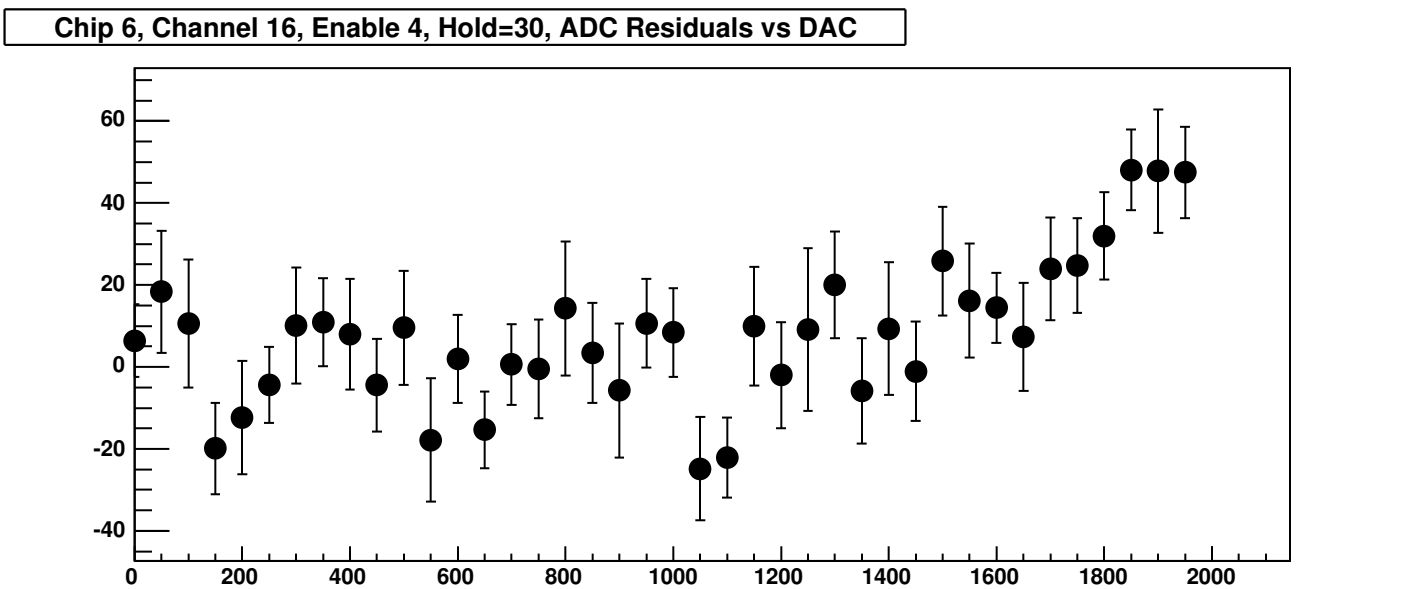

Chip 6, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 25.34 / 11
p0 -1497 ± 20.02
p1 0.01266 ± 0.01778
-1420
-1440
-1460
-1480
-1500
-1520
-1540
0
200
400
600
800
1000
1200
1400
1600
1800
2000

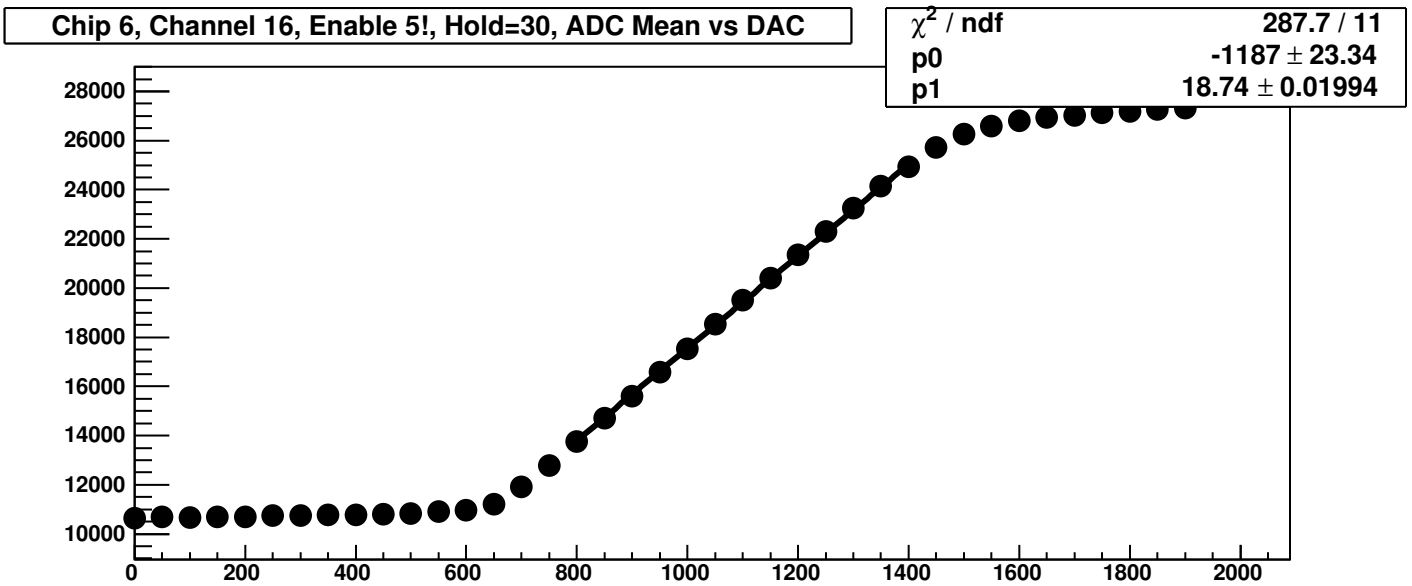

Chip 6, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

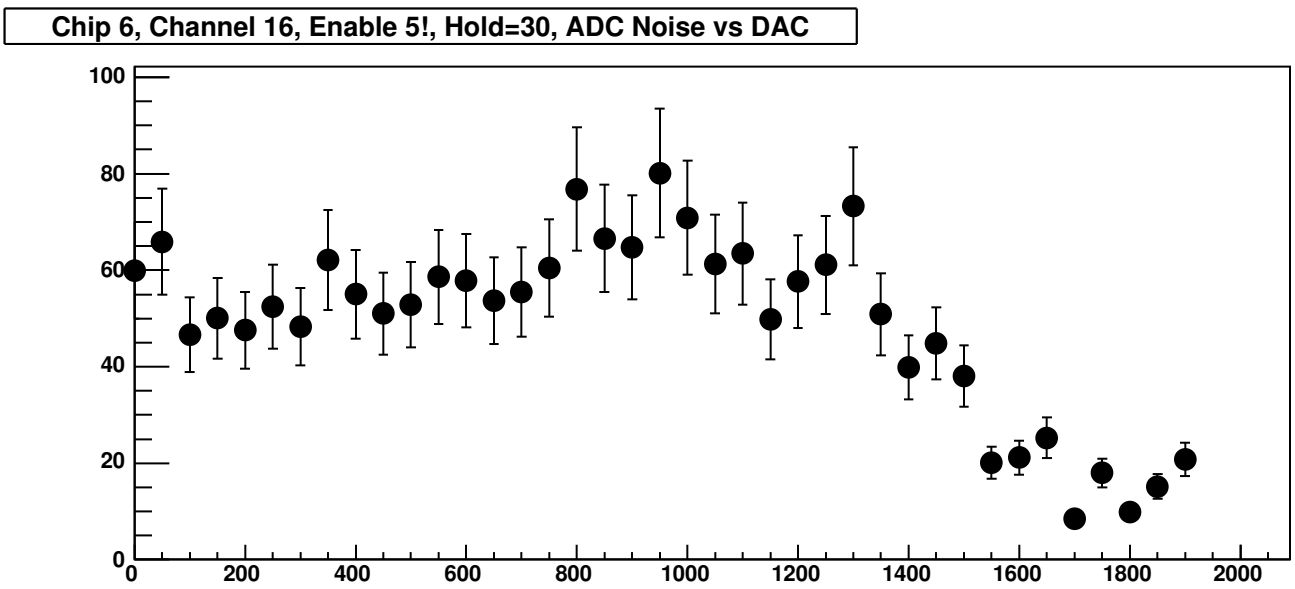

Chip 6, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

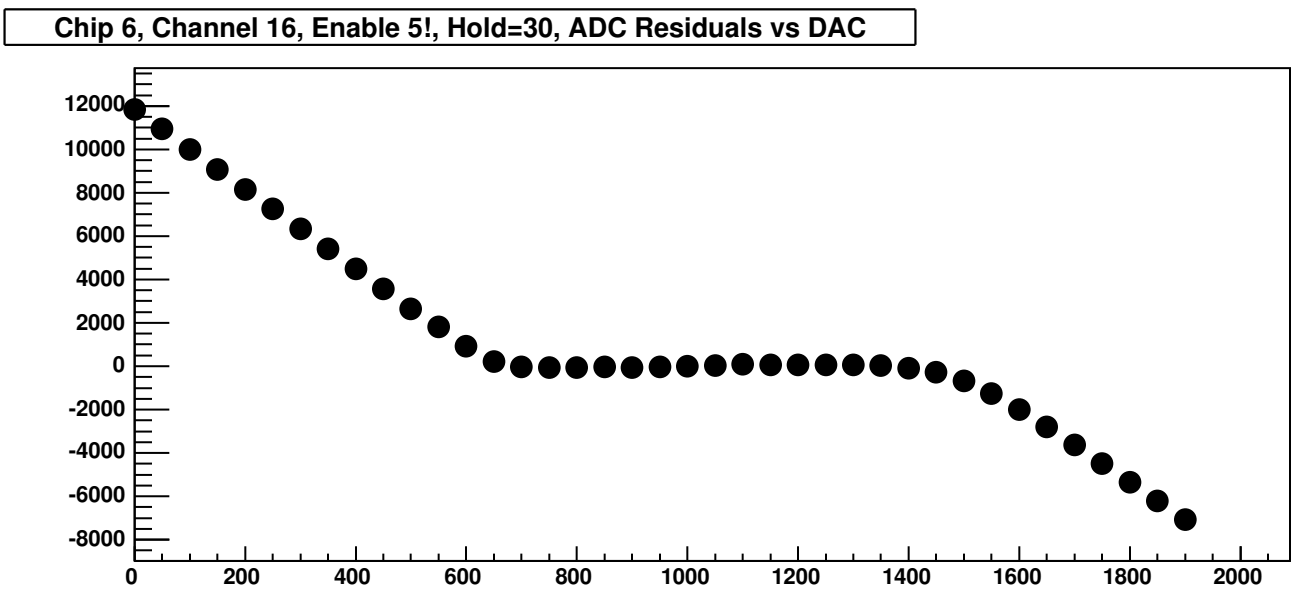

Chip 6, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 15.26 / 11
p0 -1483 ± 23.12
p1 0.03471 ± 0.021
-1340
-1360
-1380
-1400
-1420
-1440
-1460
-1480
-1500
-1520
0
200
400
600
800
1000
1200
1400
1600
1800
2000

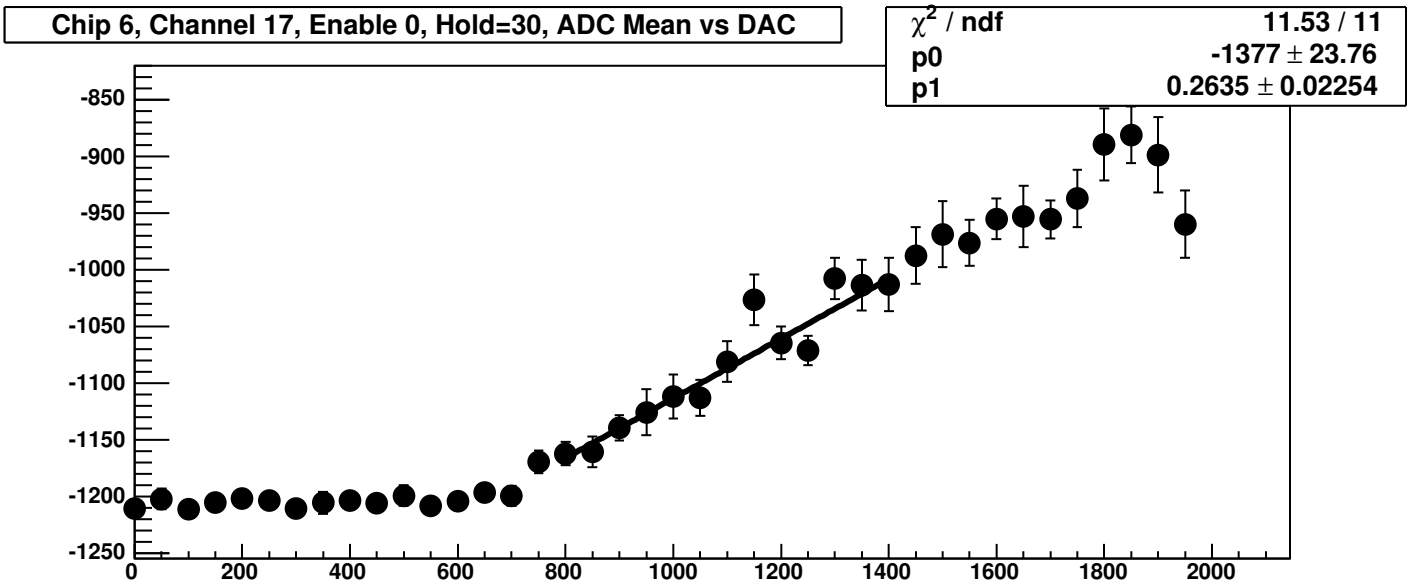

Chip 6, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

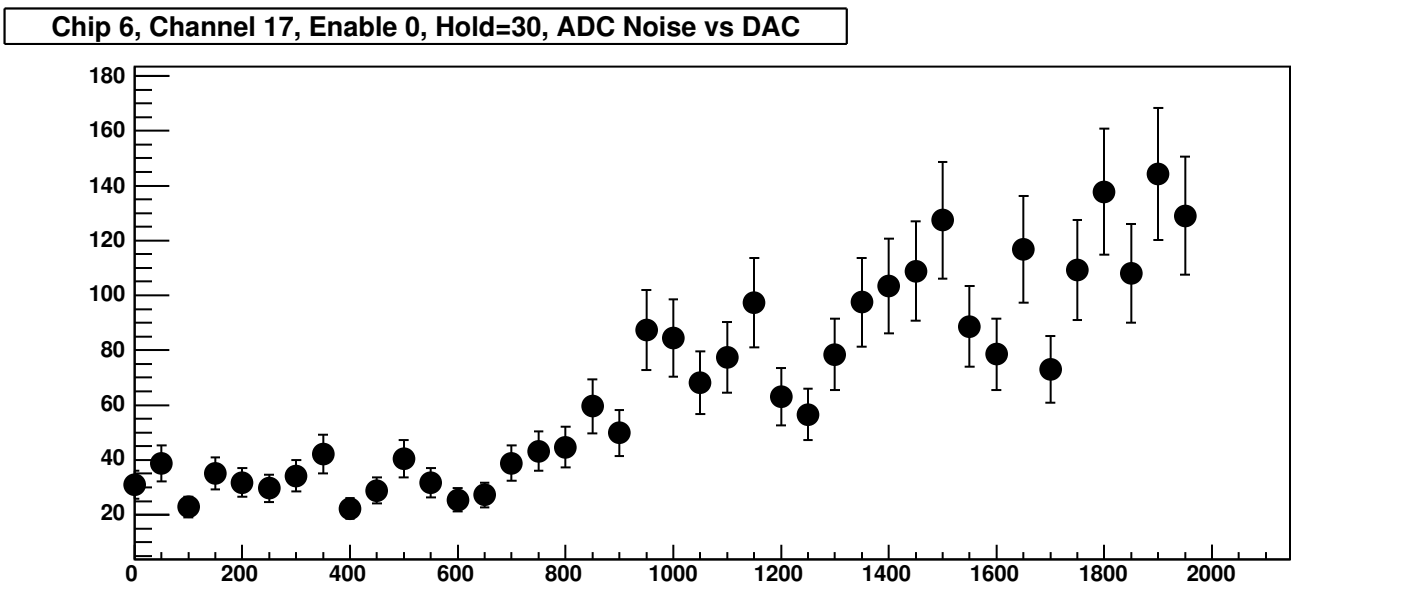

Chip 6, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

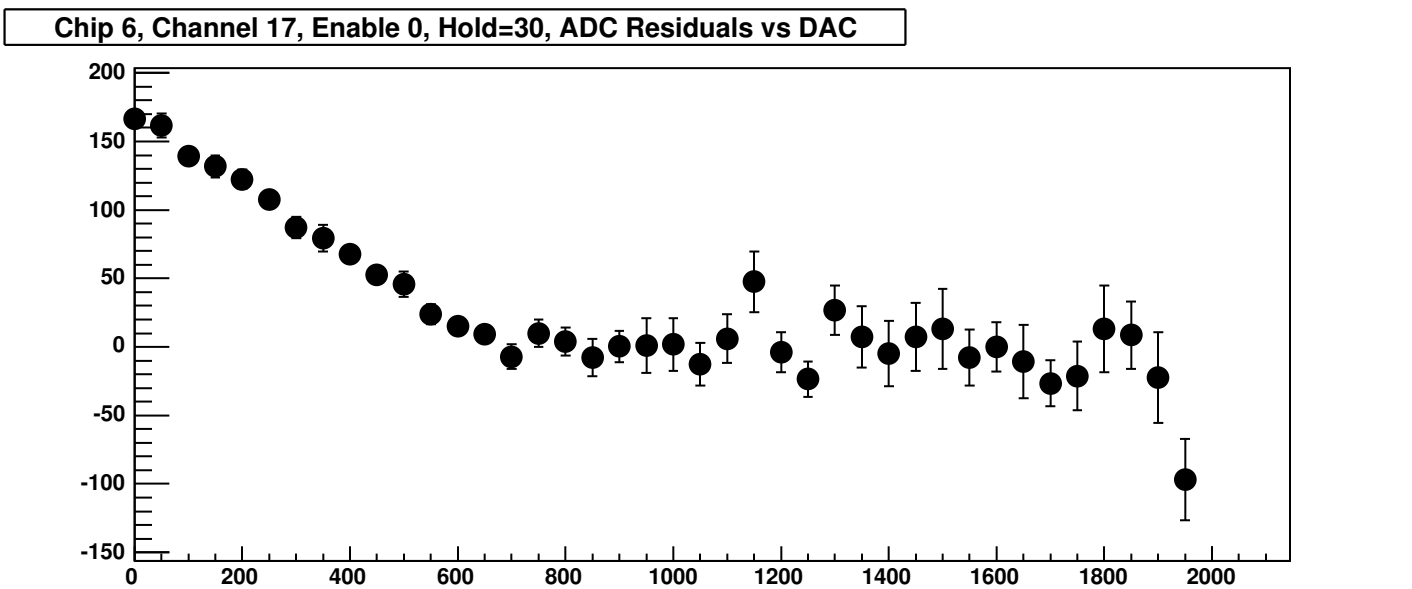

Chip 6, Channel 16, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 287.7 / 11
p0 -1187 ± 23.34
p1 18.74 ± 0.01994
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 16, Enable 5!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

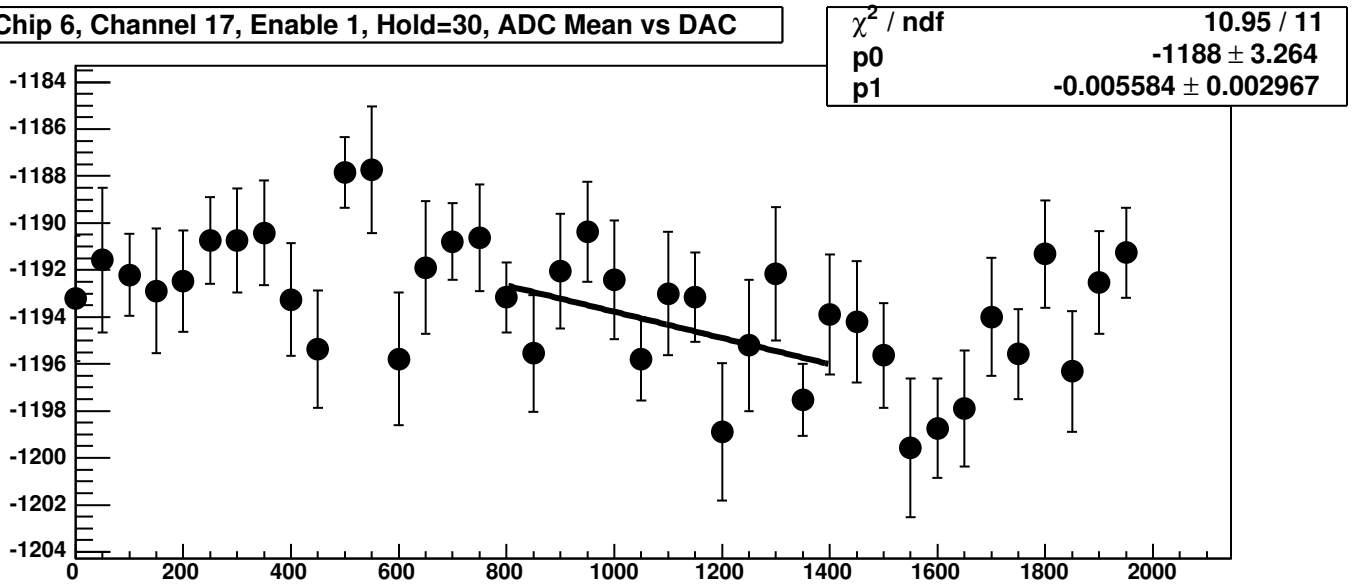

Chip 6, Channel 16, Enable 5!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 11.53 / 11
p0 -1377 ± 23.76
p1 0.2635 ± 0.02254
-850
-900
-950
-1000
-1050
-1100
-1150
-1200
-1250
0
200
400
600
800
1000
1200
1400
1600
1800
2000

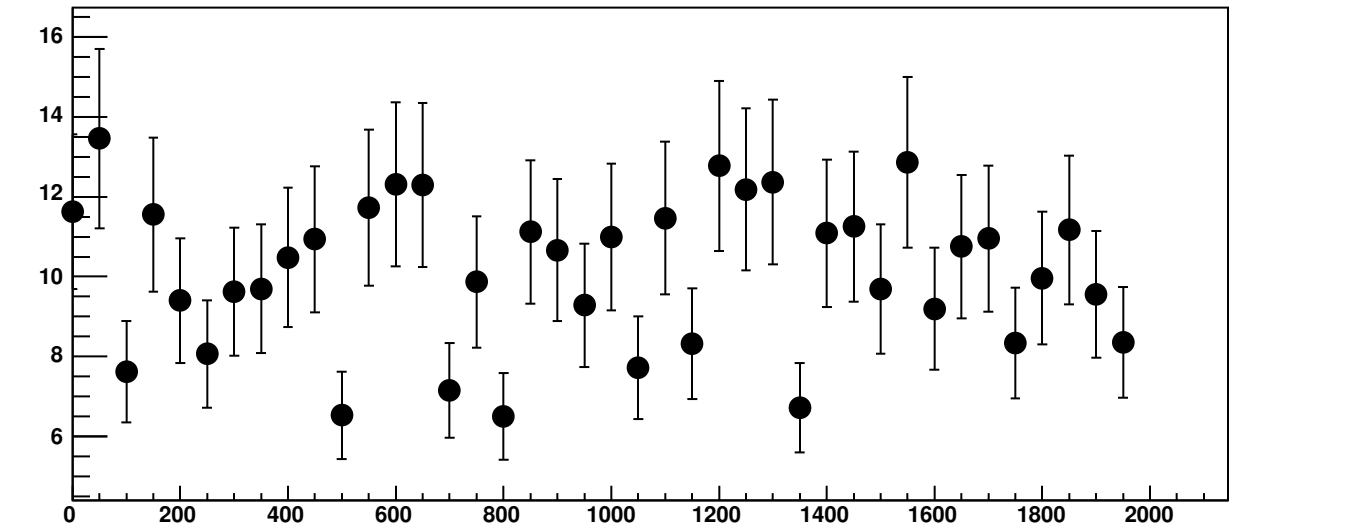

Chip 6, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
0
200
400
600
800
1000
1200
1400
1600
1800
2000

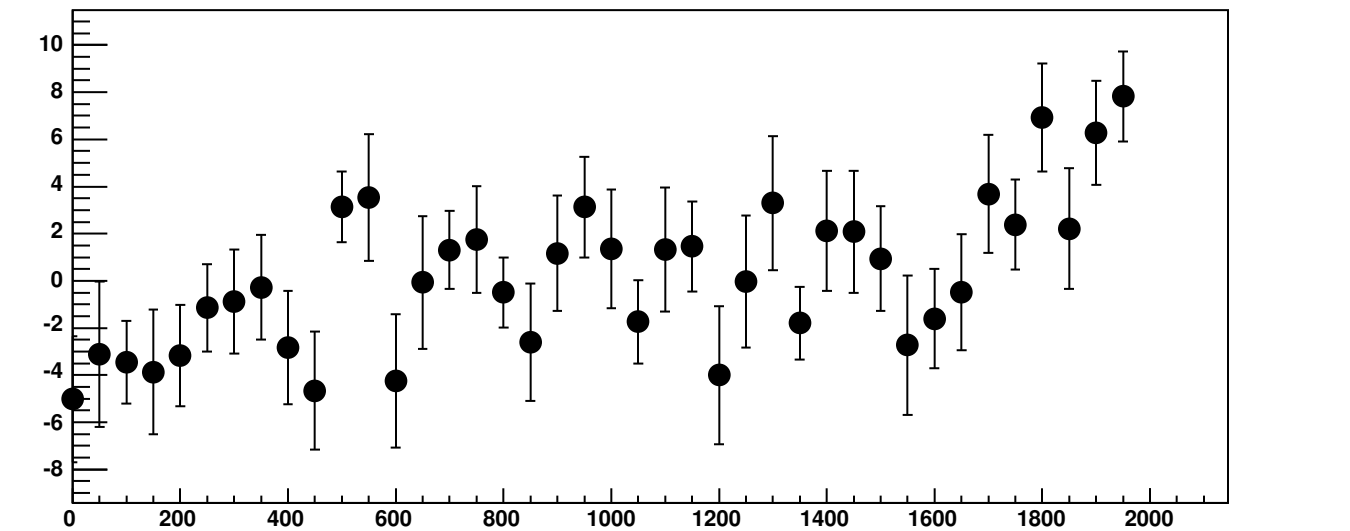
Chip 6, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC



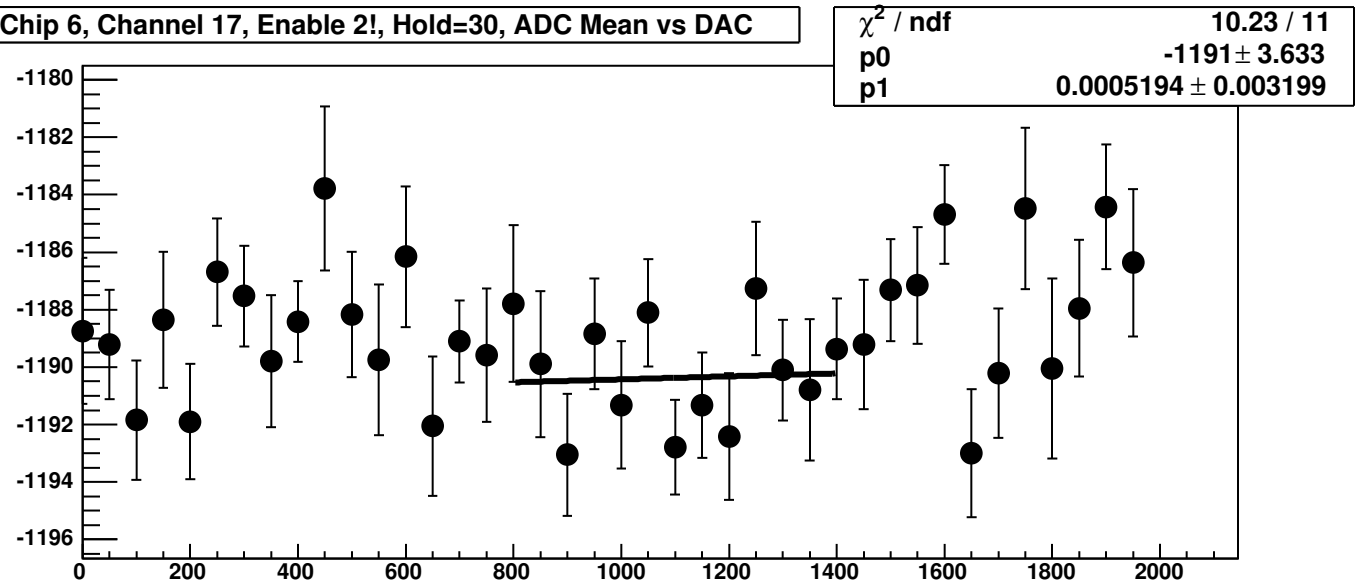
Chip 6, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC



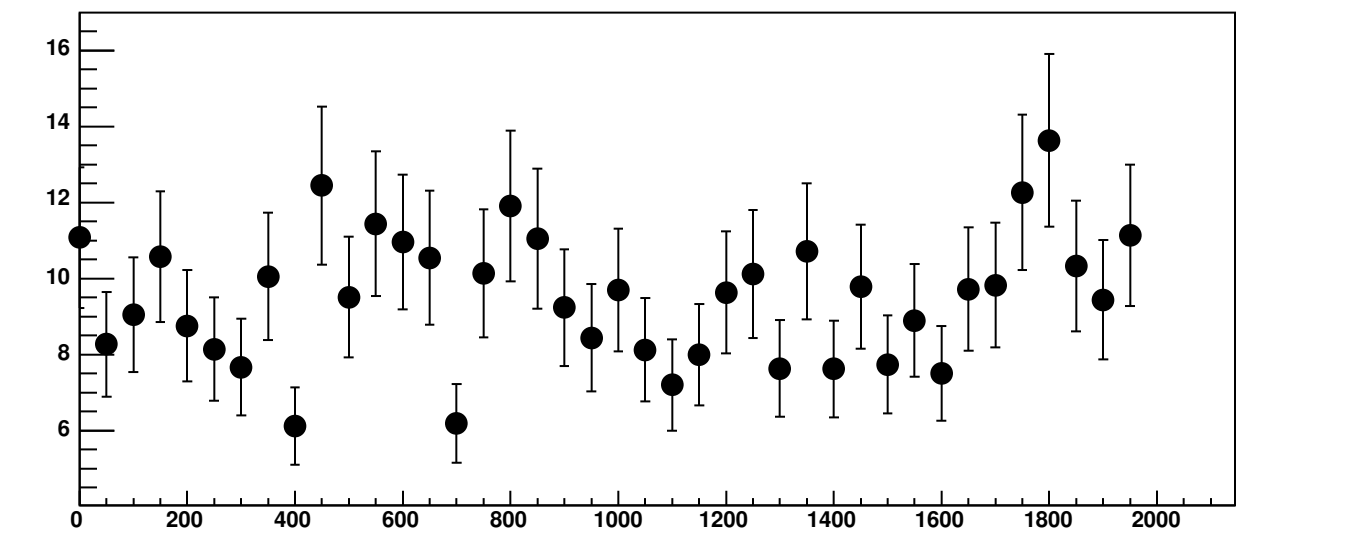
Chip 6, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC



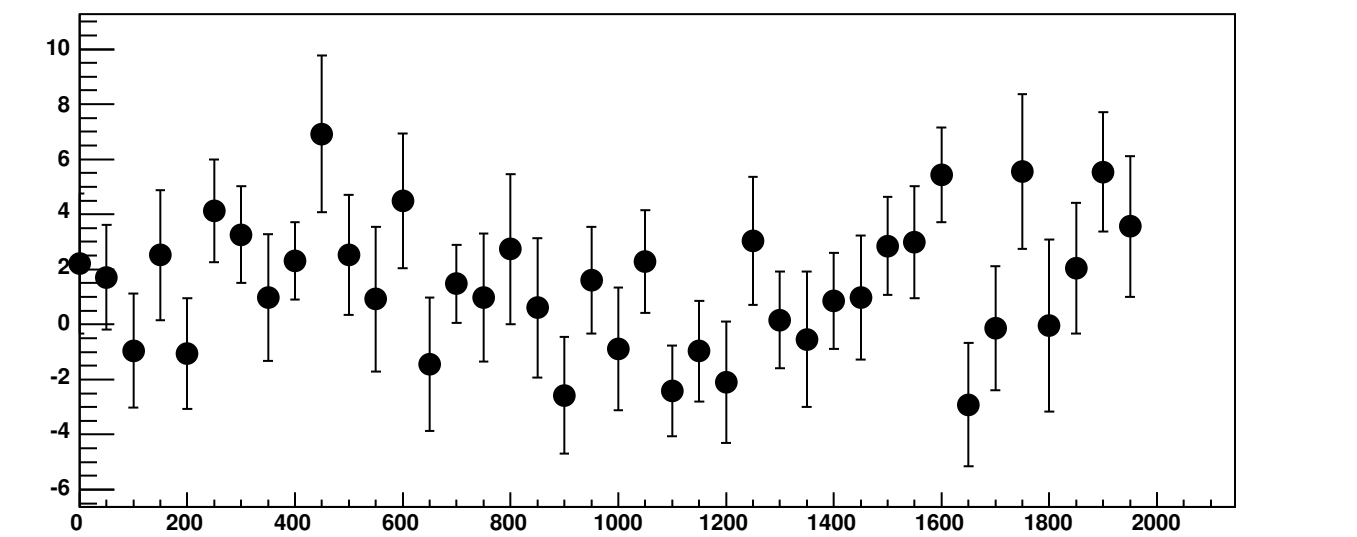
Chip 6, Channel 17, Enable 2!, Hold=30, ADC Mean vs DAC

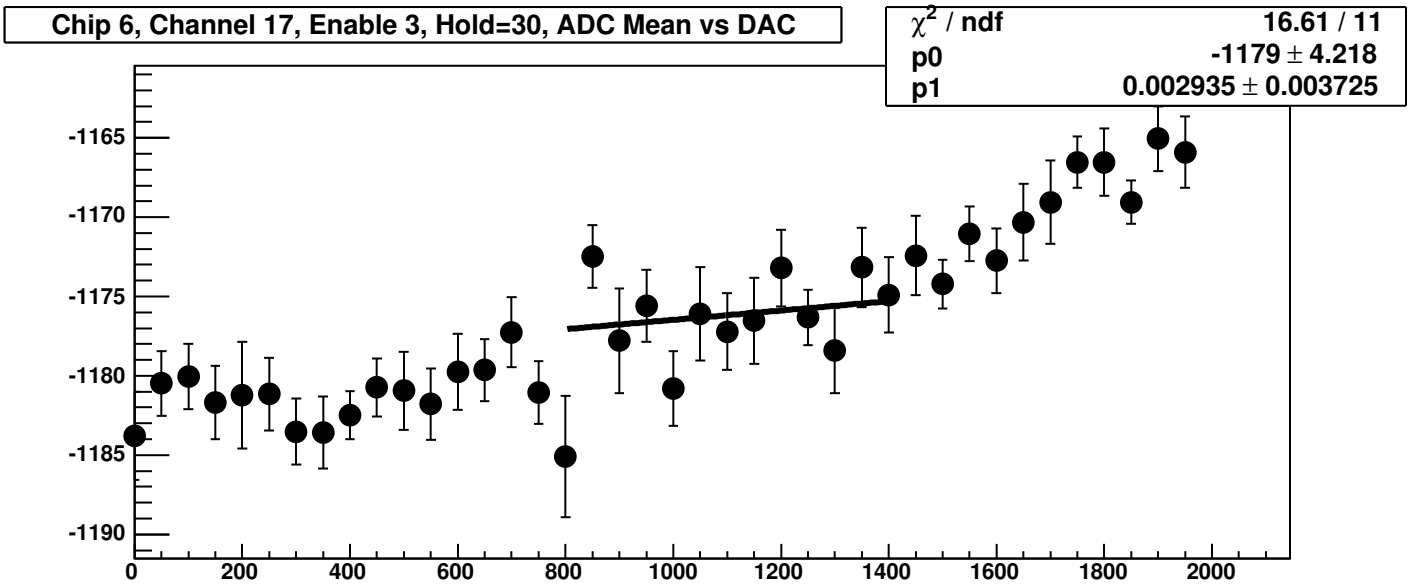


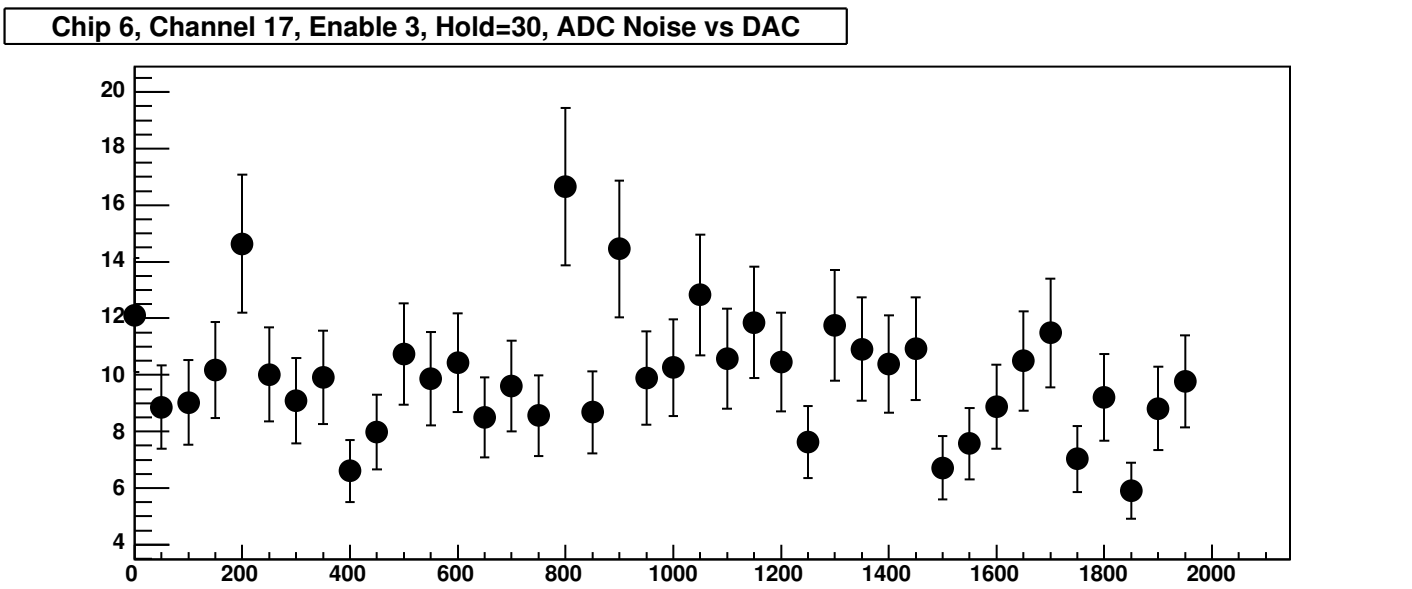
Chip 6, Channel 17, Enable 2!, Hold=30, ADC Noise vs DAC

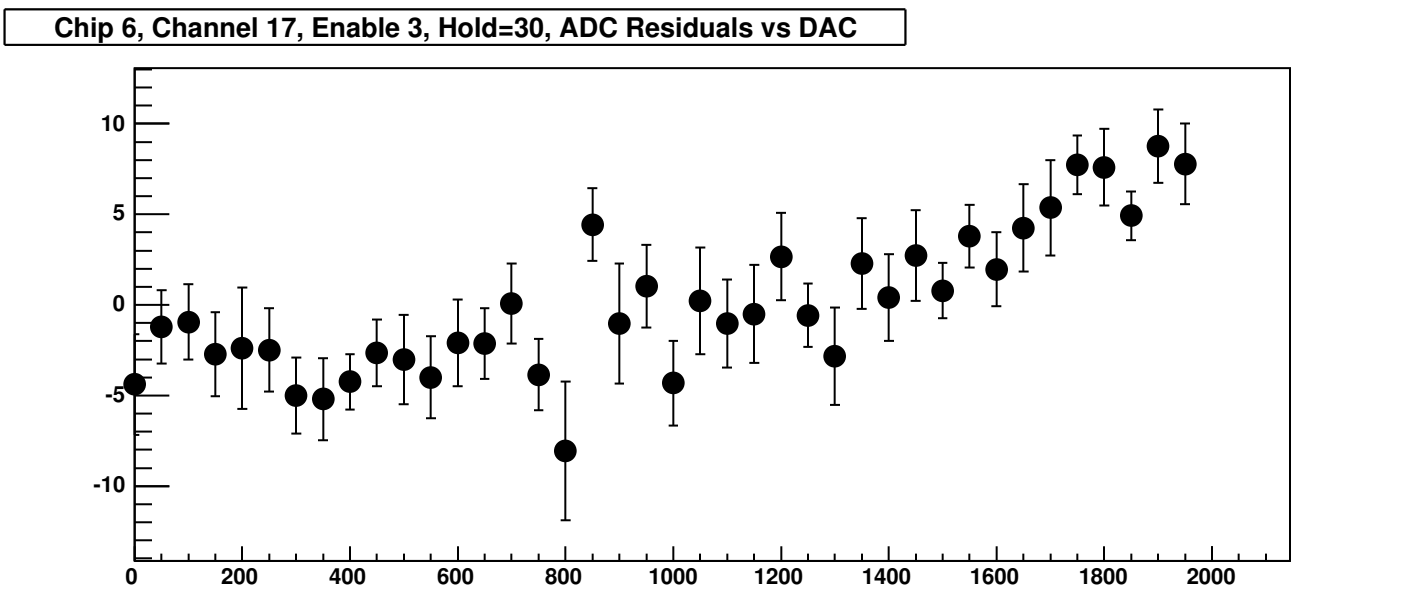


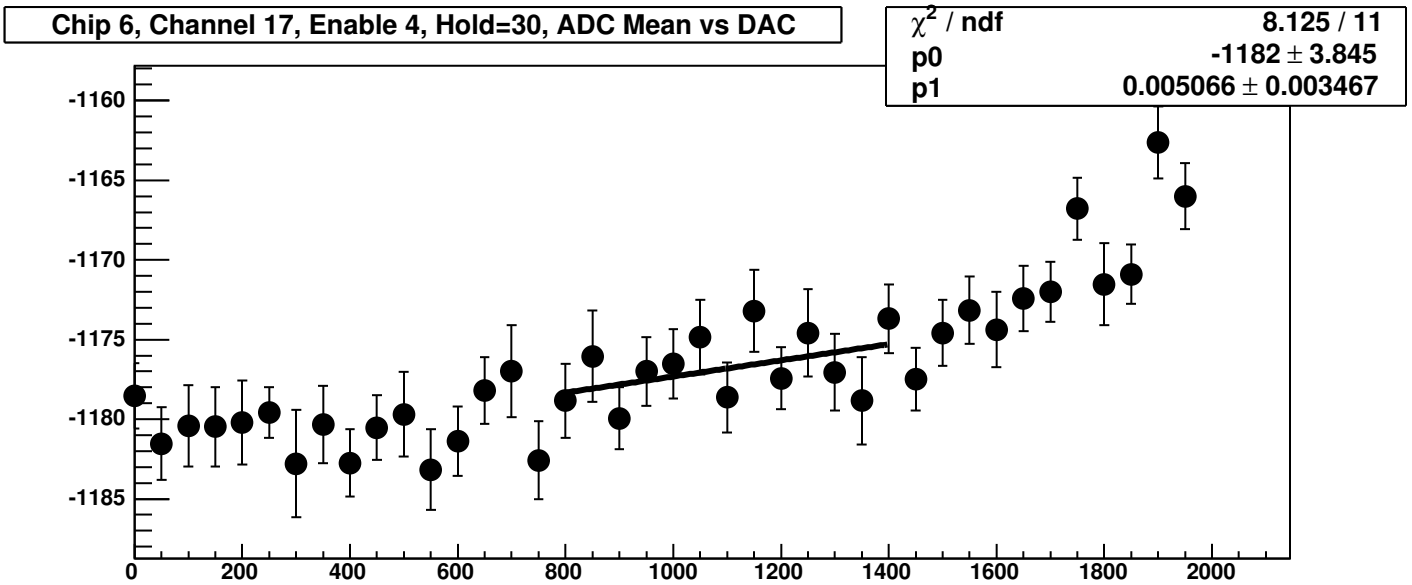
Chip 6, Channel 17, Enable 2!, Hold=30, ADC Residuals vs DAC

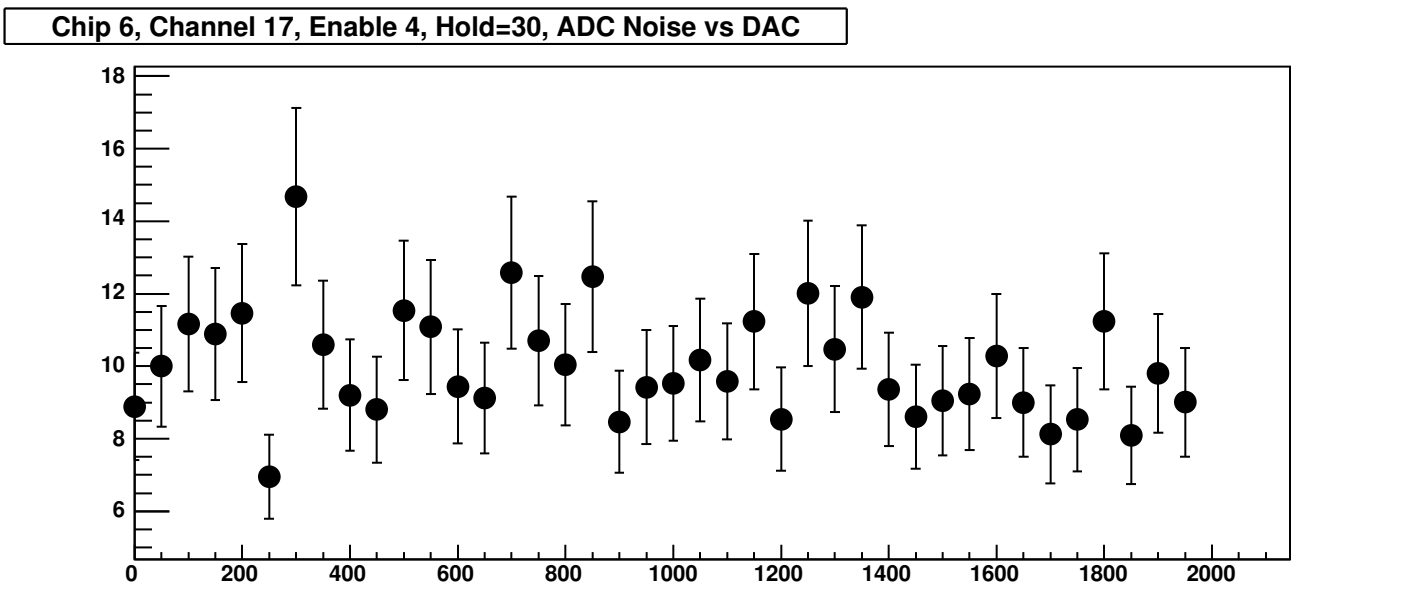


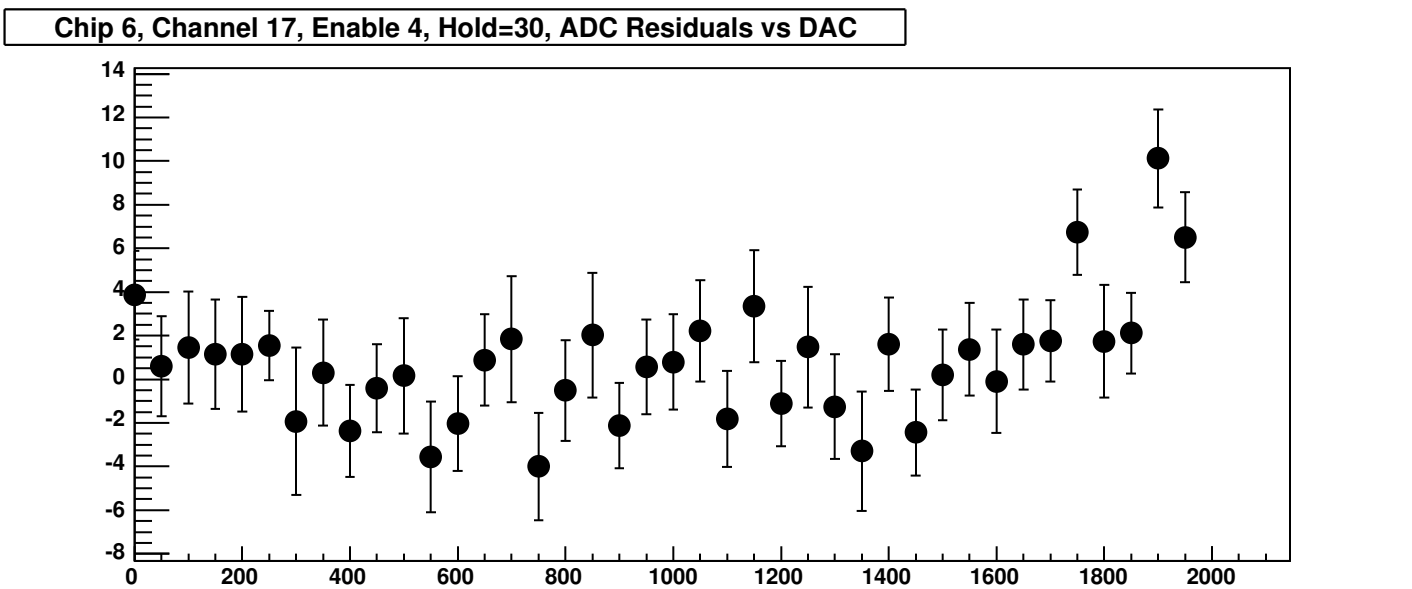

Chip 6, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 16.61 / 11
p0 -1179 ± 4.218
p1 0.002935 ± 0.003725
-1165
-1170
-1175
-1180
-1185
-1190
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
20
18
16
14
12
10
8
6
4
0
200
400
600
800
1000
1200
1400
1600
1800
2000

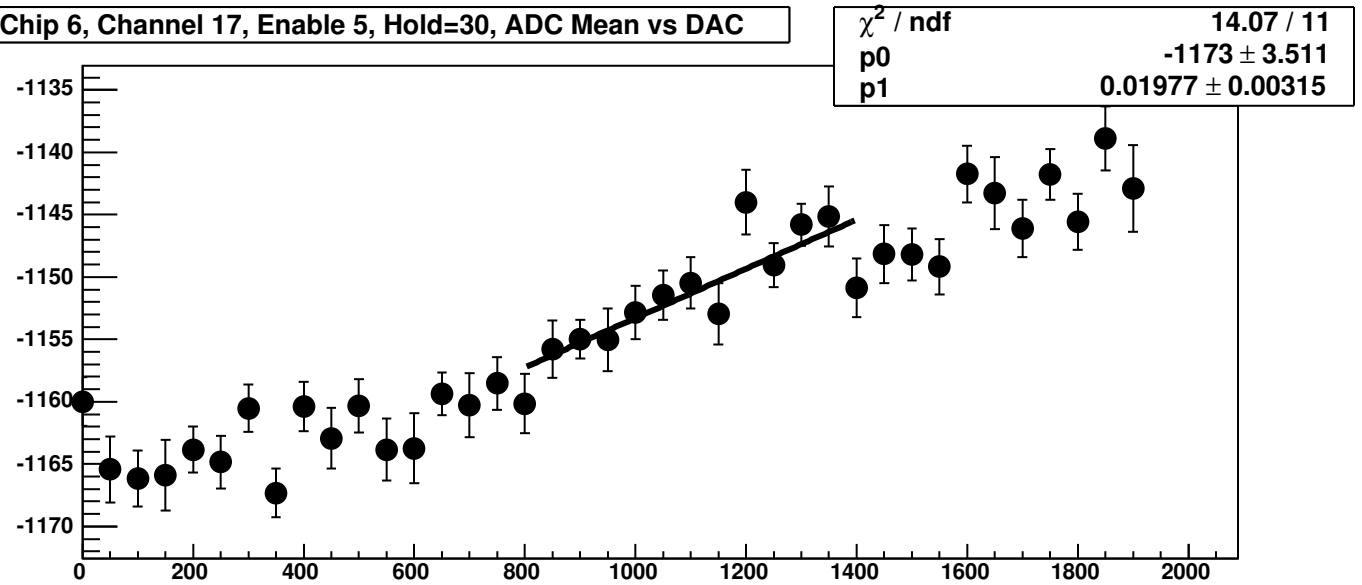

Chip 6, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 8.125 / 11
p0 -1182 ± 3.845
p1 0.005066 ± 0.003467
-1160
-1165
-1170
-1175
-1180
-1185
0
200
400
600
800
1000
1200
1400
1600
1800
2000

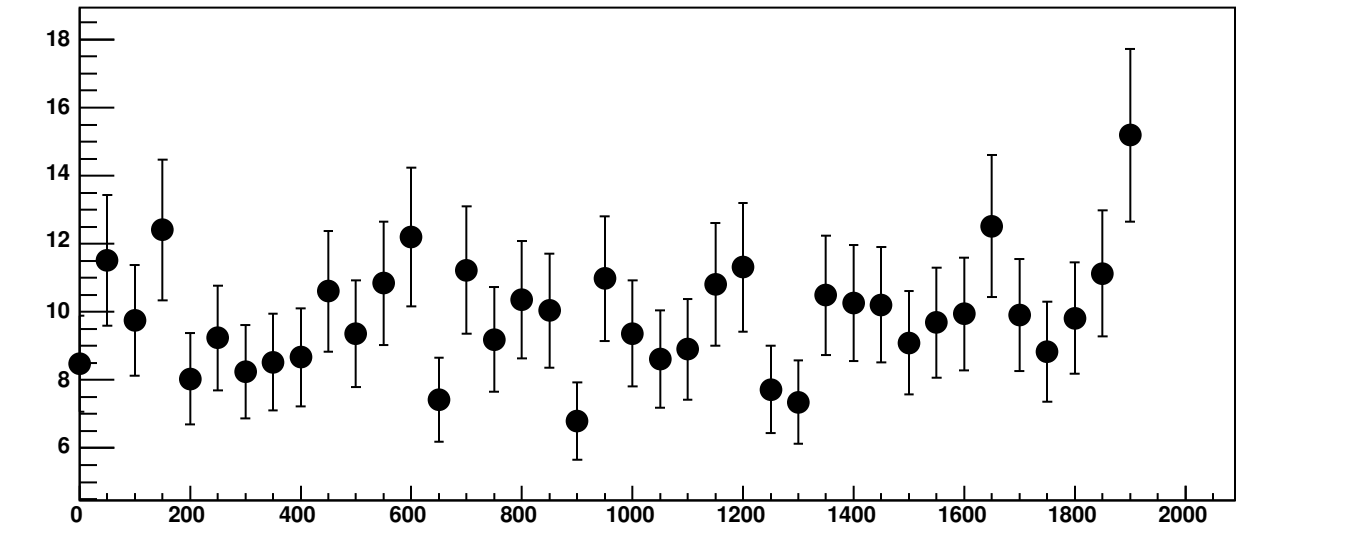

Chip 6, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC
18
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 6, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
0
200
400
600
800
1000
1200
1400
1600
1800
2000

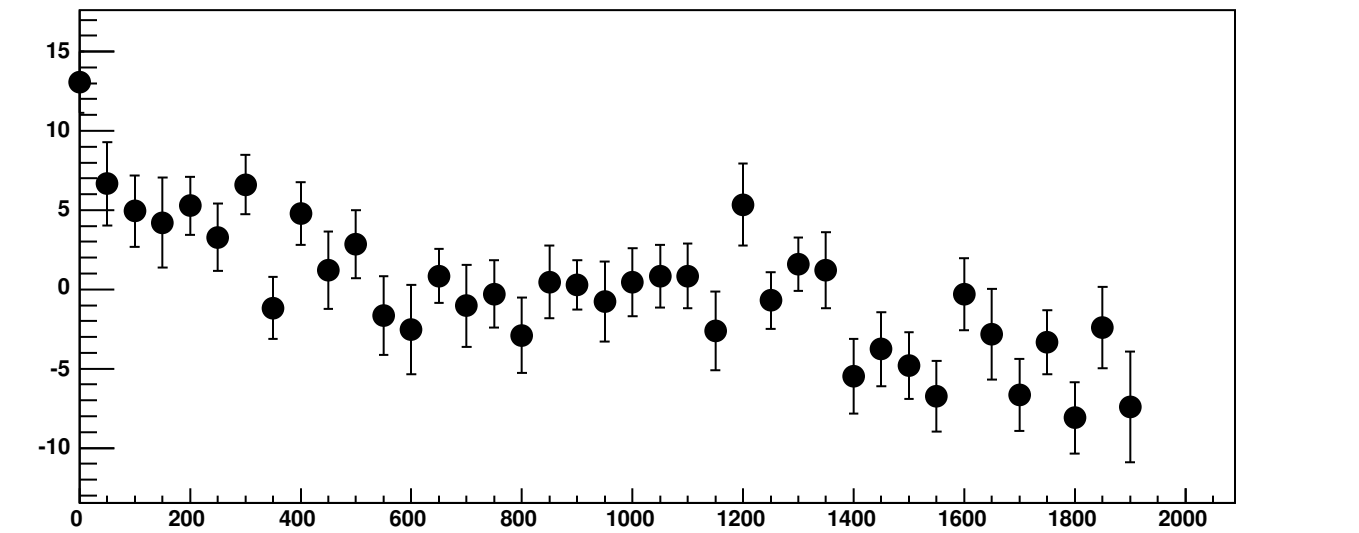
Chip 6, Channel 17, Enable 5, Hold=30, ADC Mean vs DAC

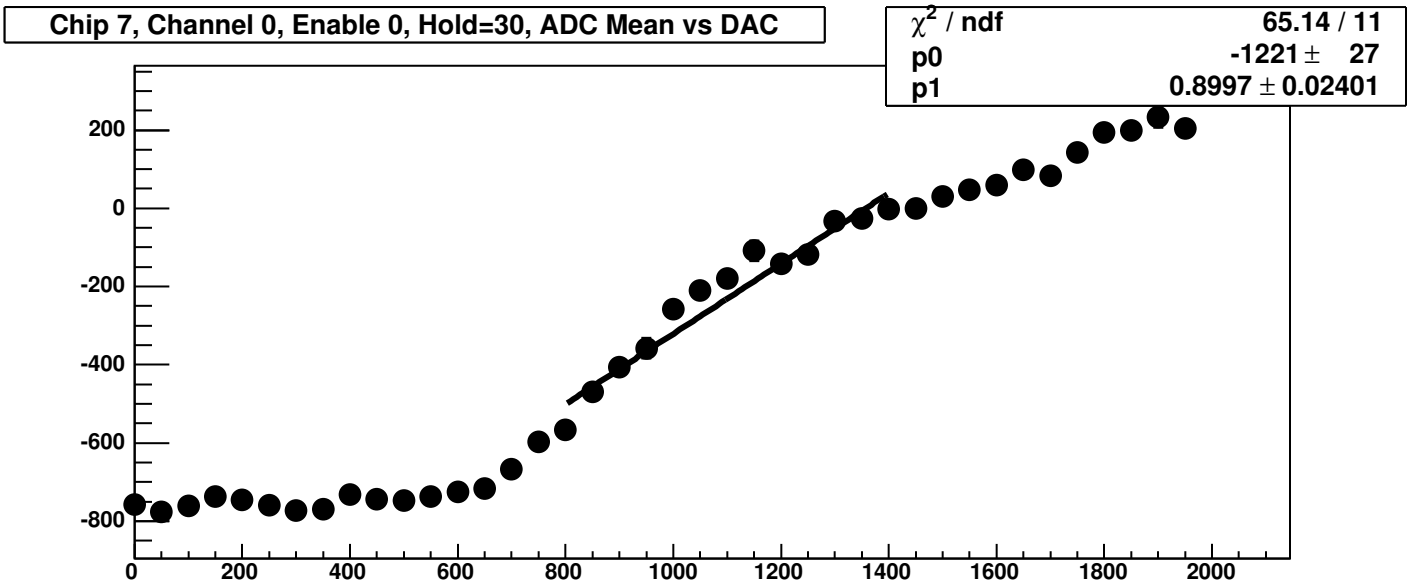


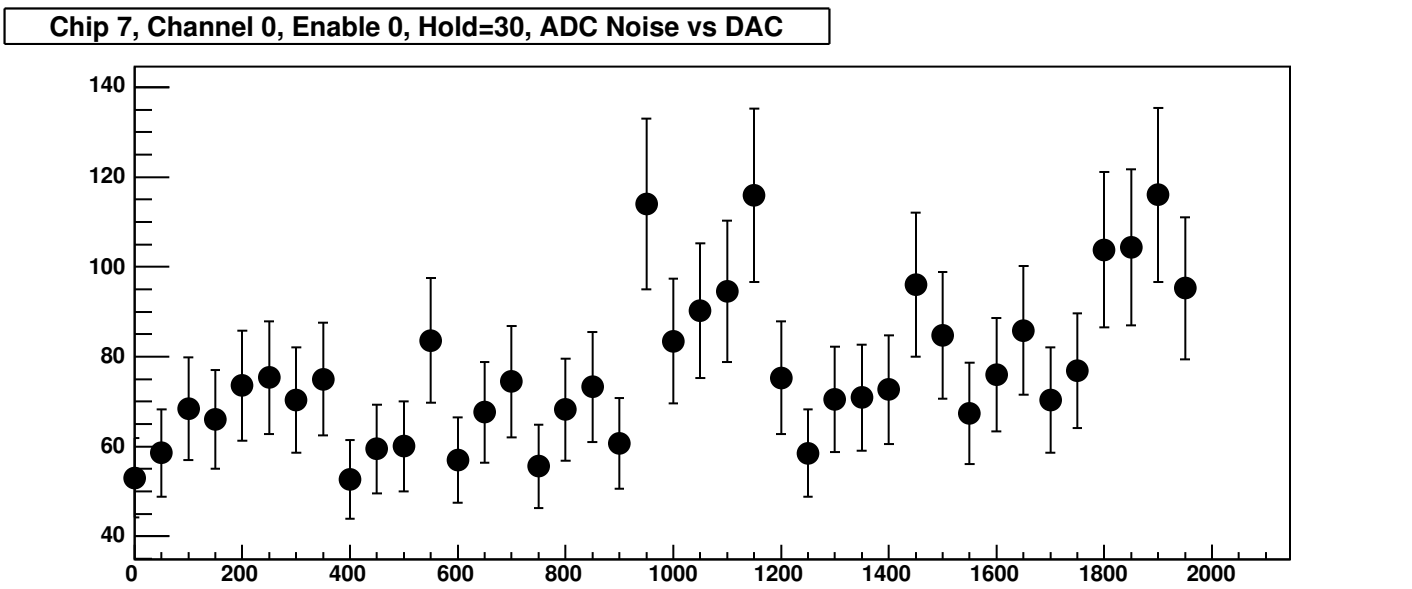
Chip 6, Channel 17, Enable 5, Hold=30, ADC Noise vs DAC

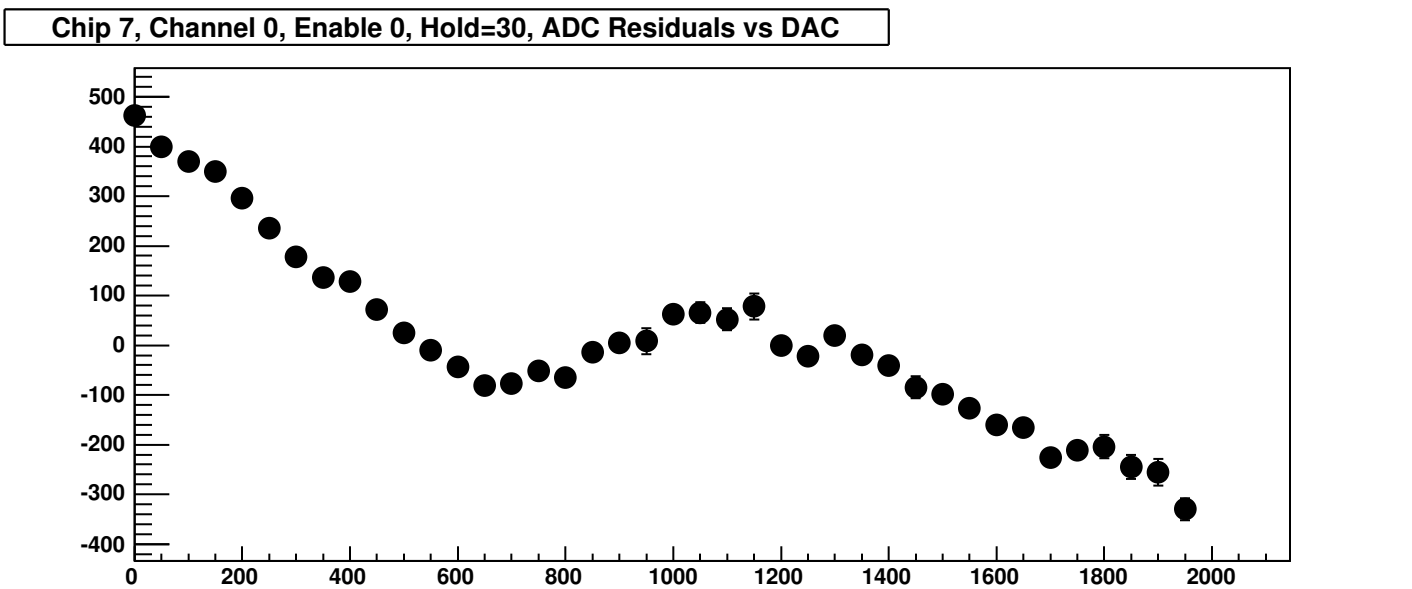


Chip 6, Channel 17, Enable 5, Hold=30, ADC Residuals vs DAC

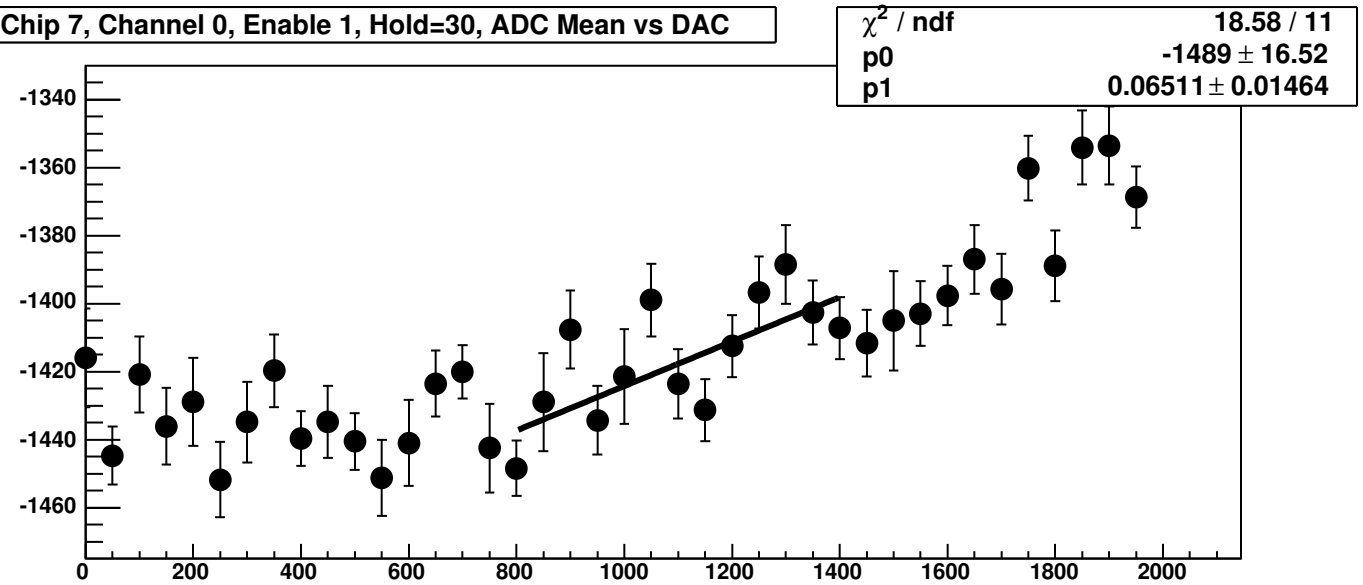



Chip 7, Channel 0, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 65.14 / 11
p0 -1221 ± 27
p1 0.8997 ± 0.02401
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

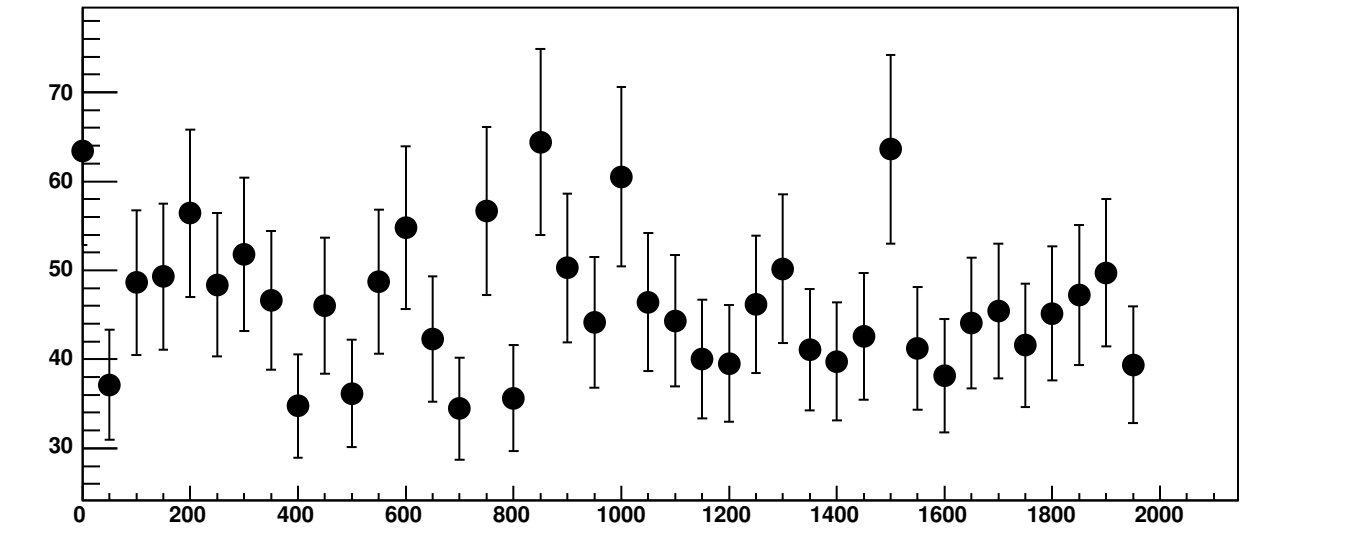

Chip 7, Channel 0, Enable 0, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 0, Enable 0, Hold=30, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
-300
-400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

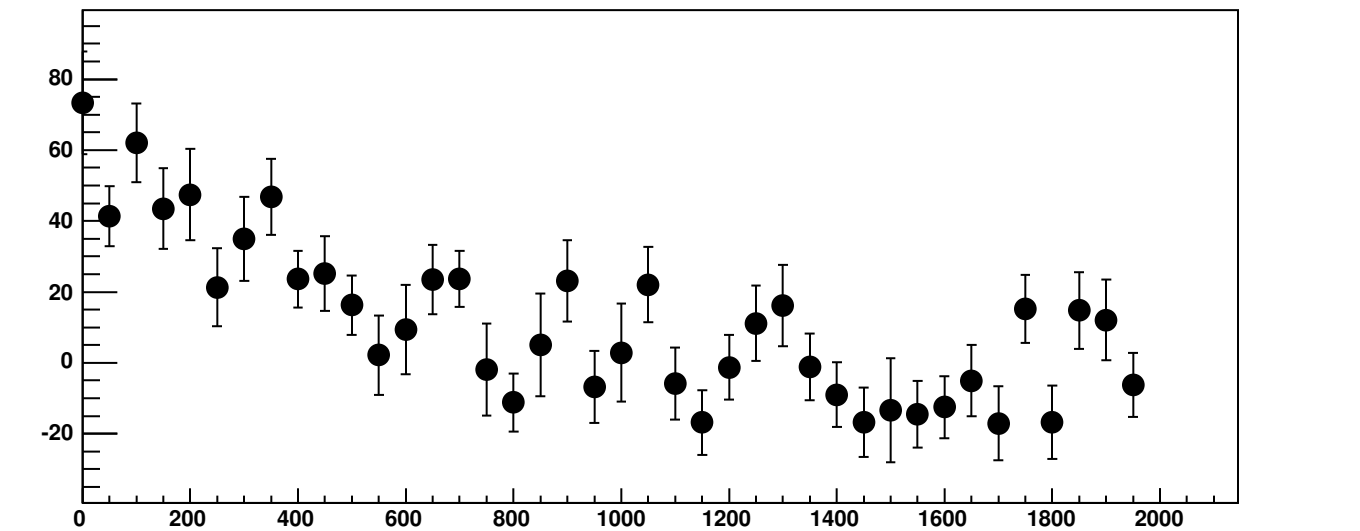
Chip 7, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC

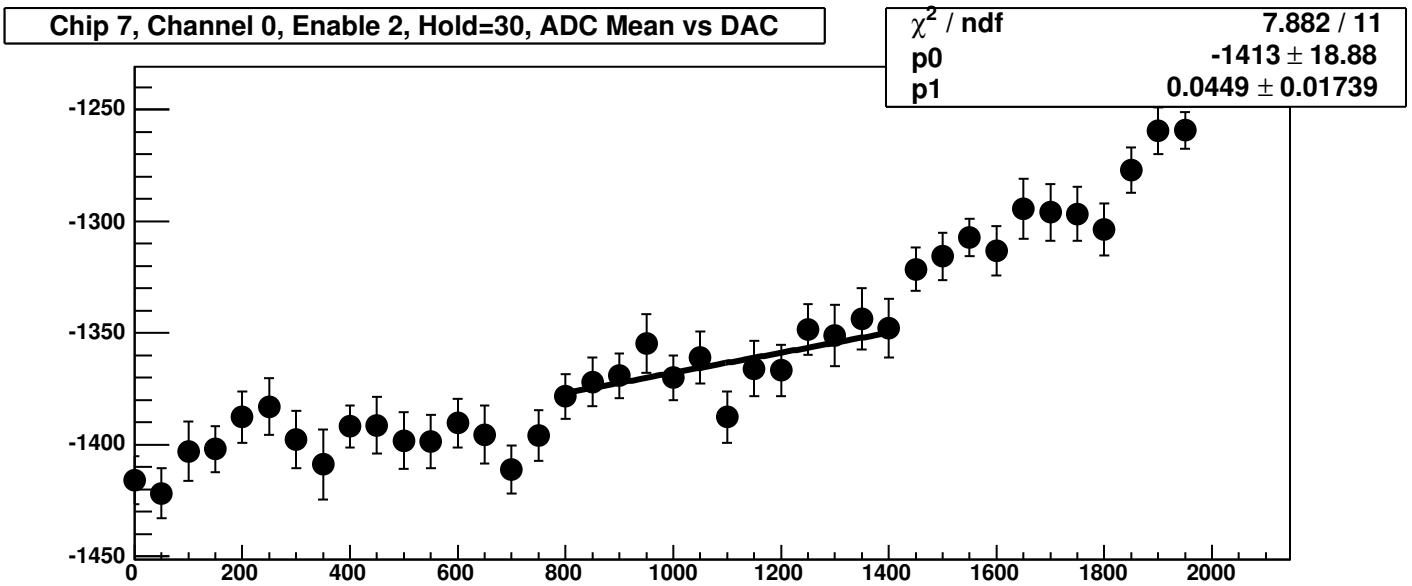


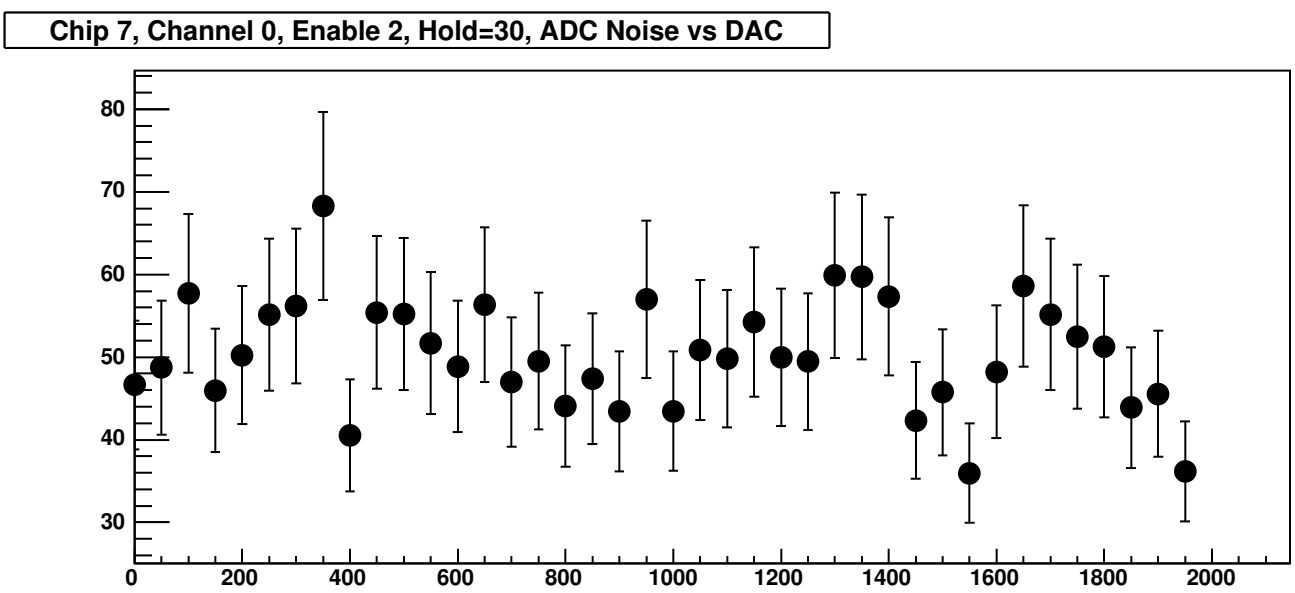
Chip 7, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC

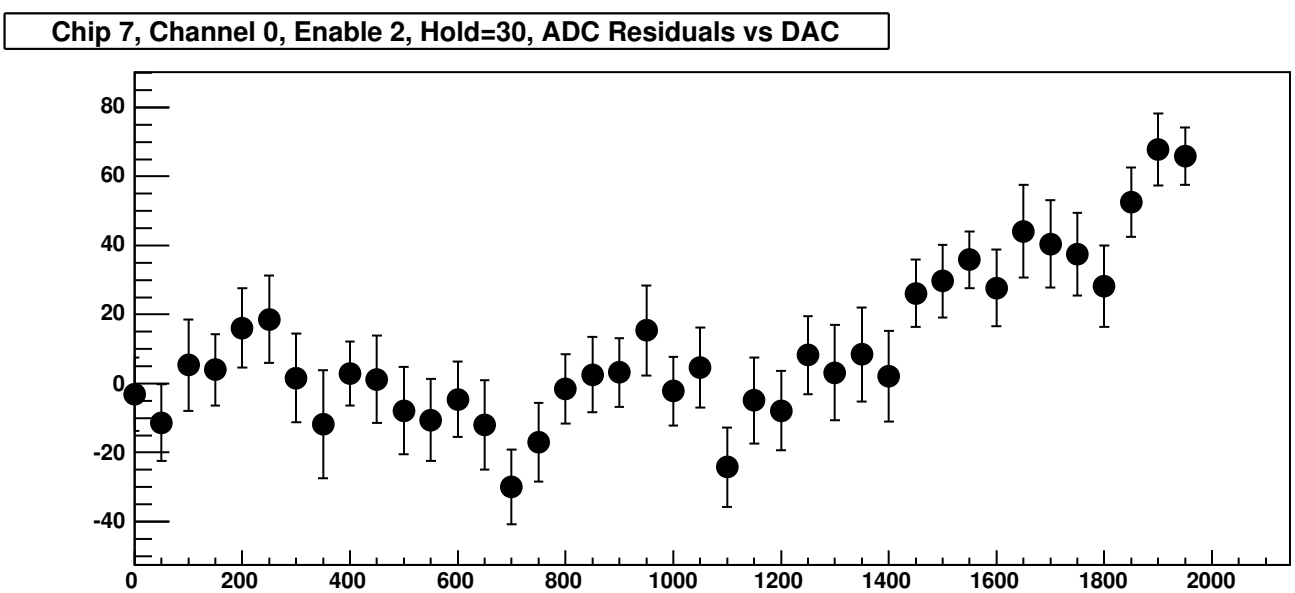


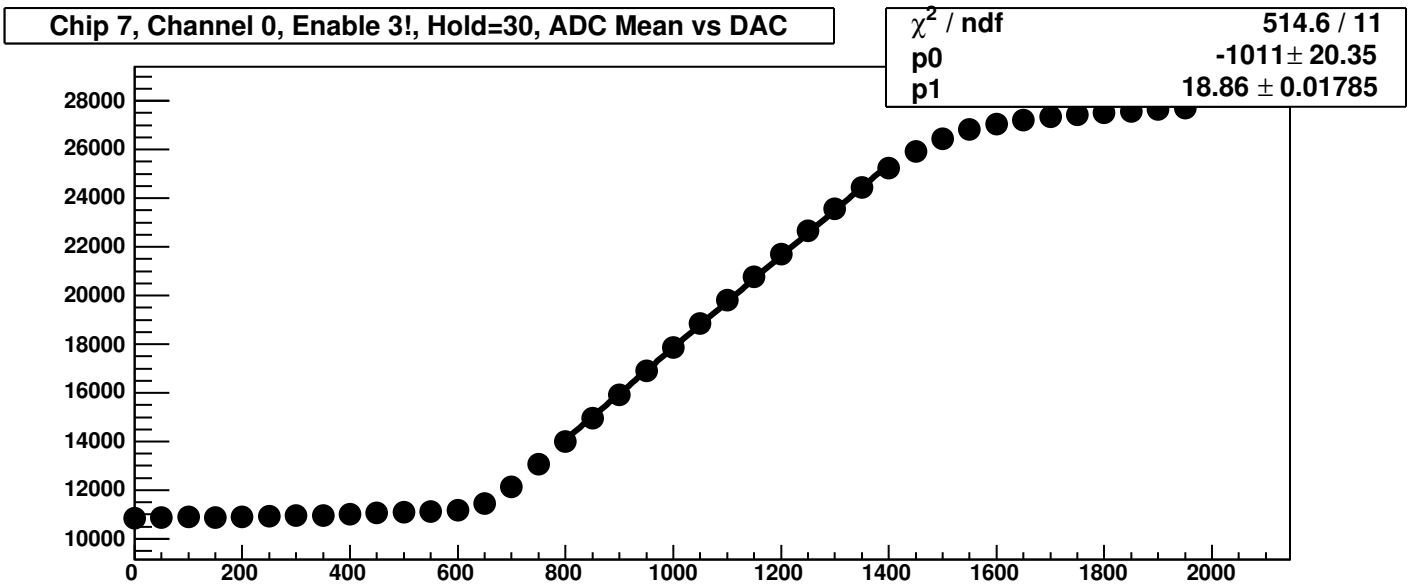
Chip 7, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC

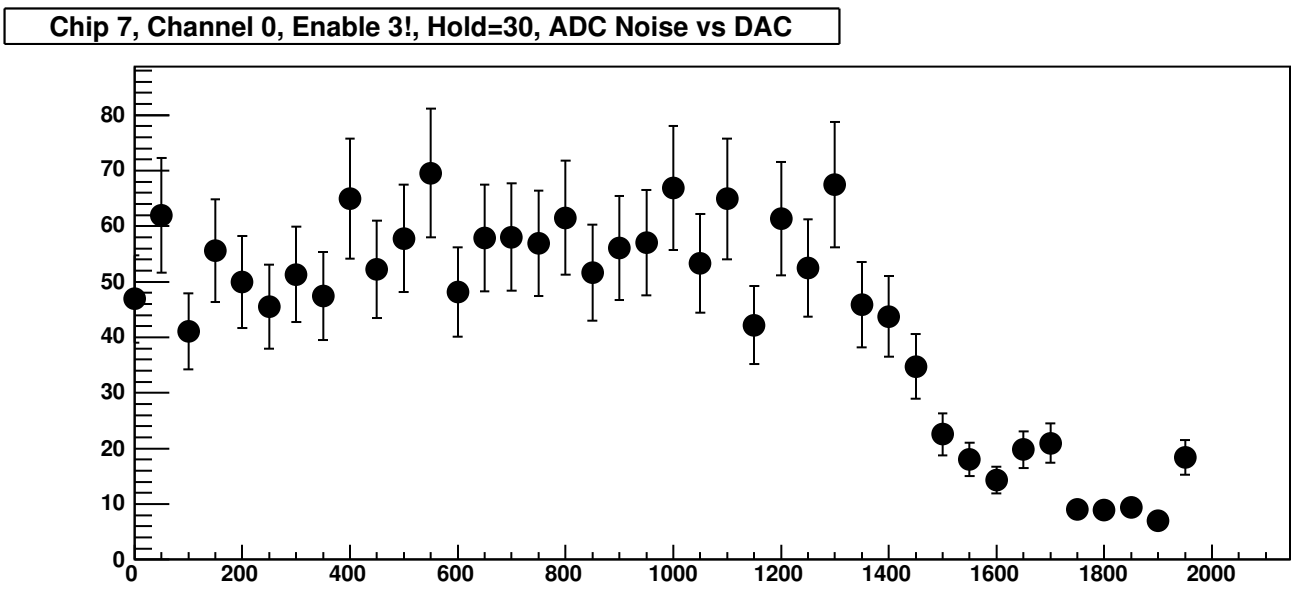


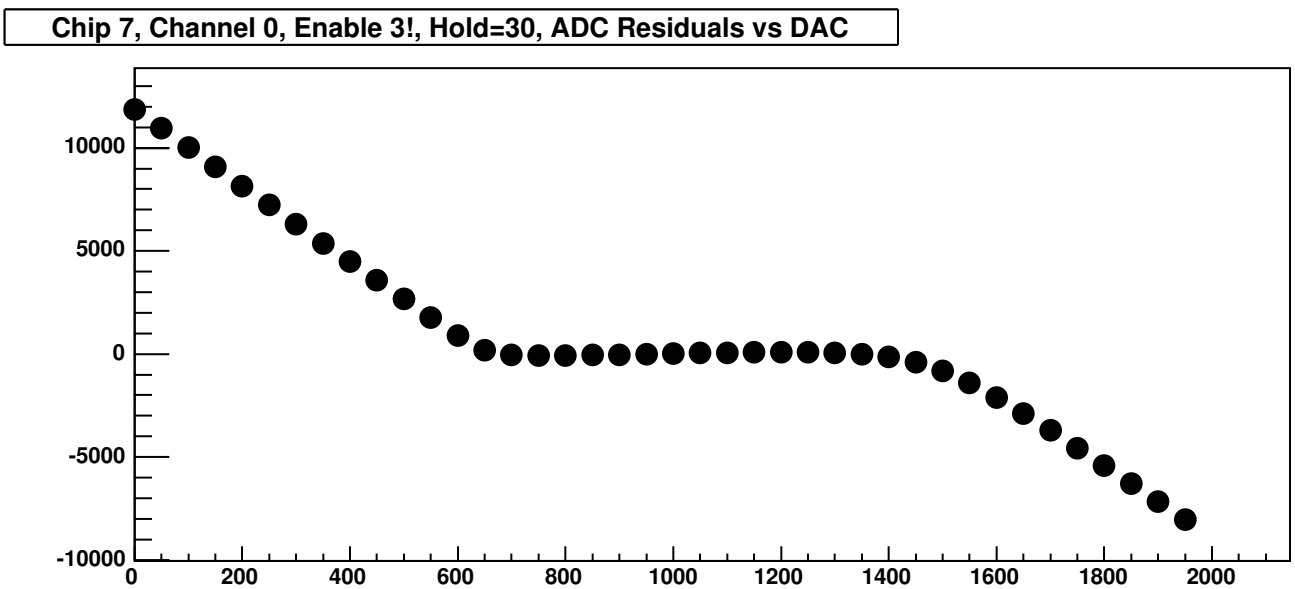

Chip 7, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 7.882 / 11
p0 -1413 ± 18.88
p1 0.0449 ± 0.01739
-1250
-1300
-1350
-1400
-1450
0
200
400
600
800
1000
1200
1400
1600
1800
2000

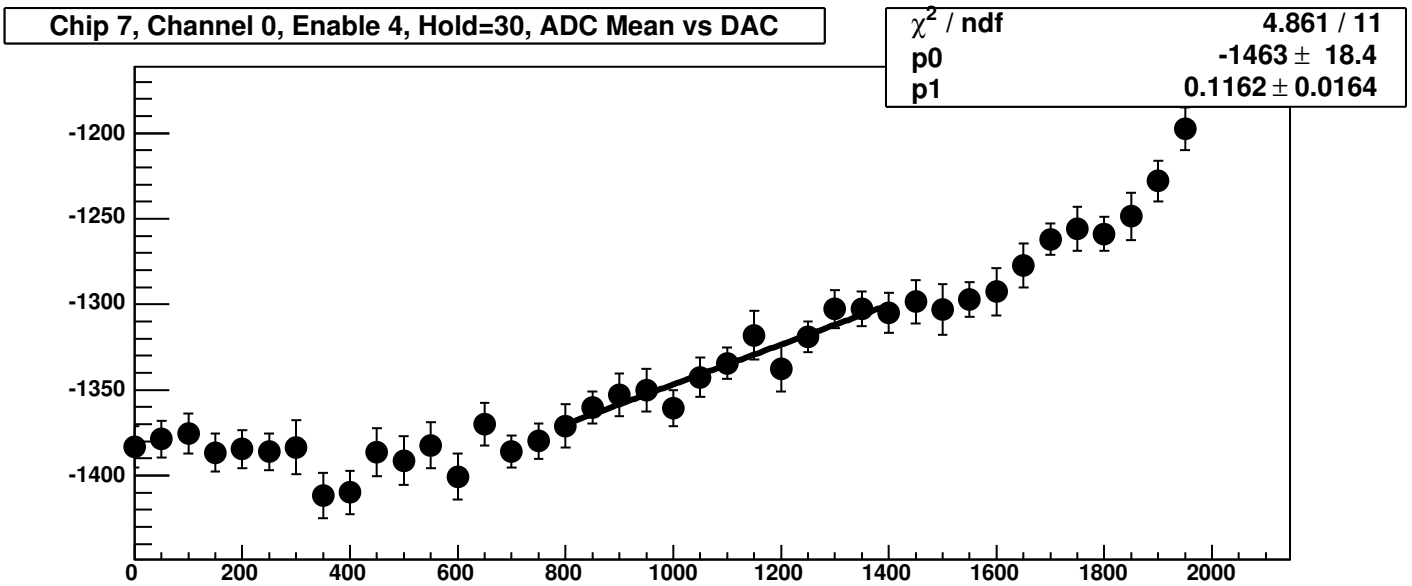

Chip 7, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

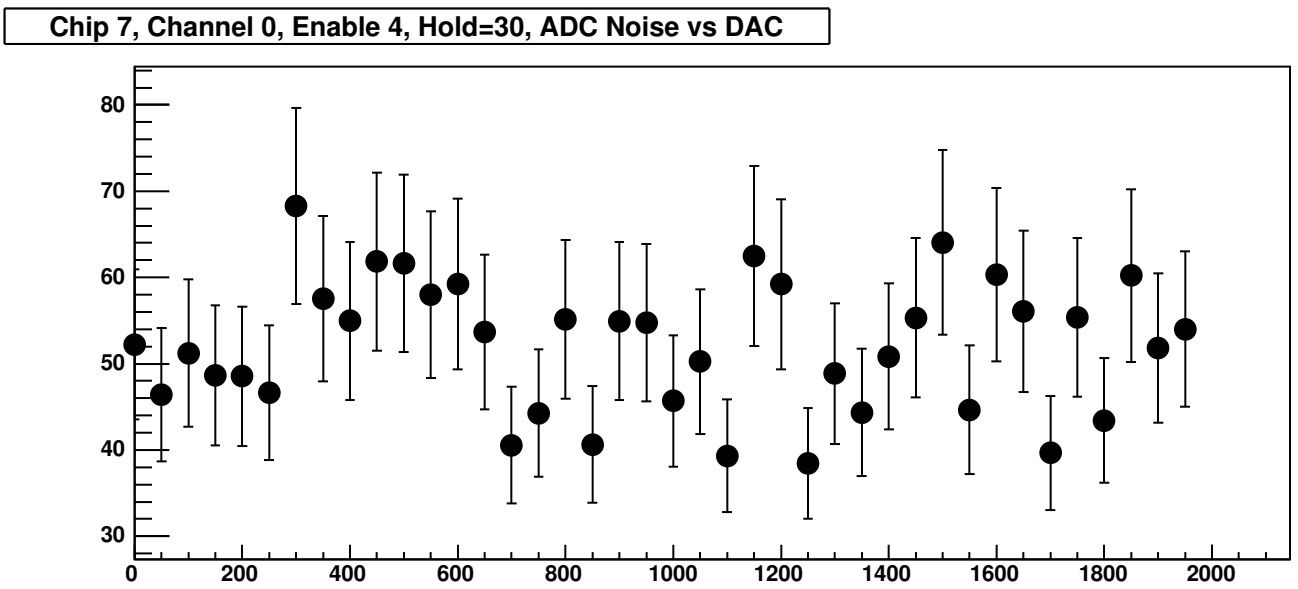

Chip 7, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

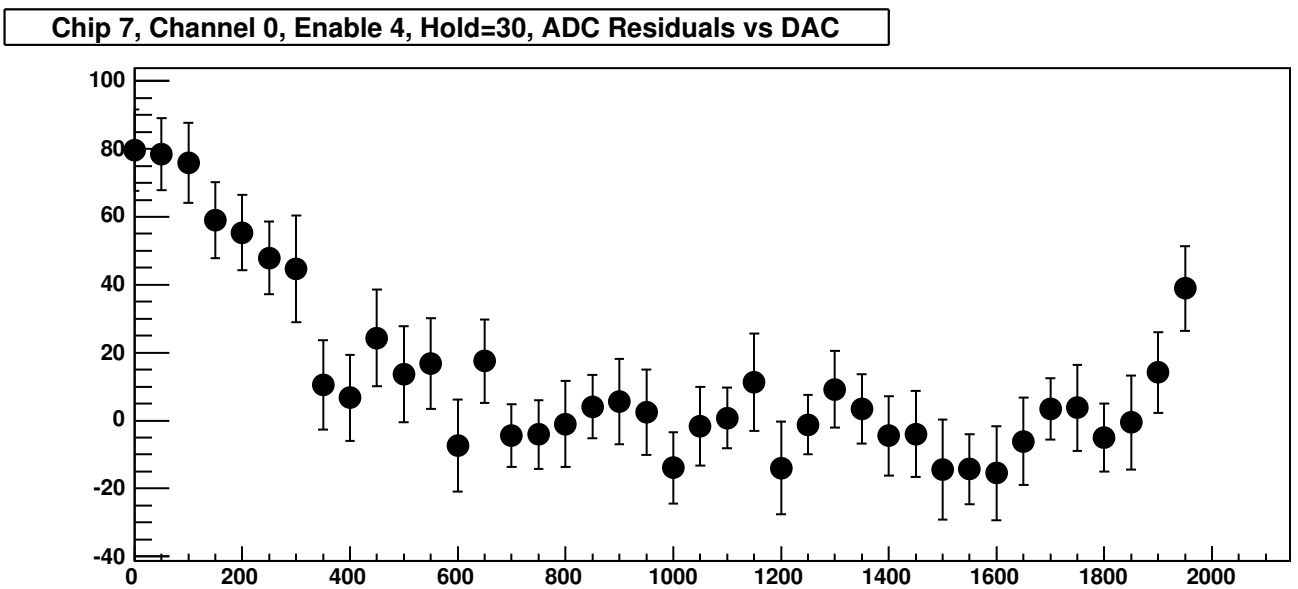

Chip 7, Channel 0, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 514.6 / 11
p0 -1011 ± 20.35
p1 18.86 ± 0.01785
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

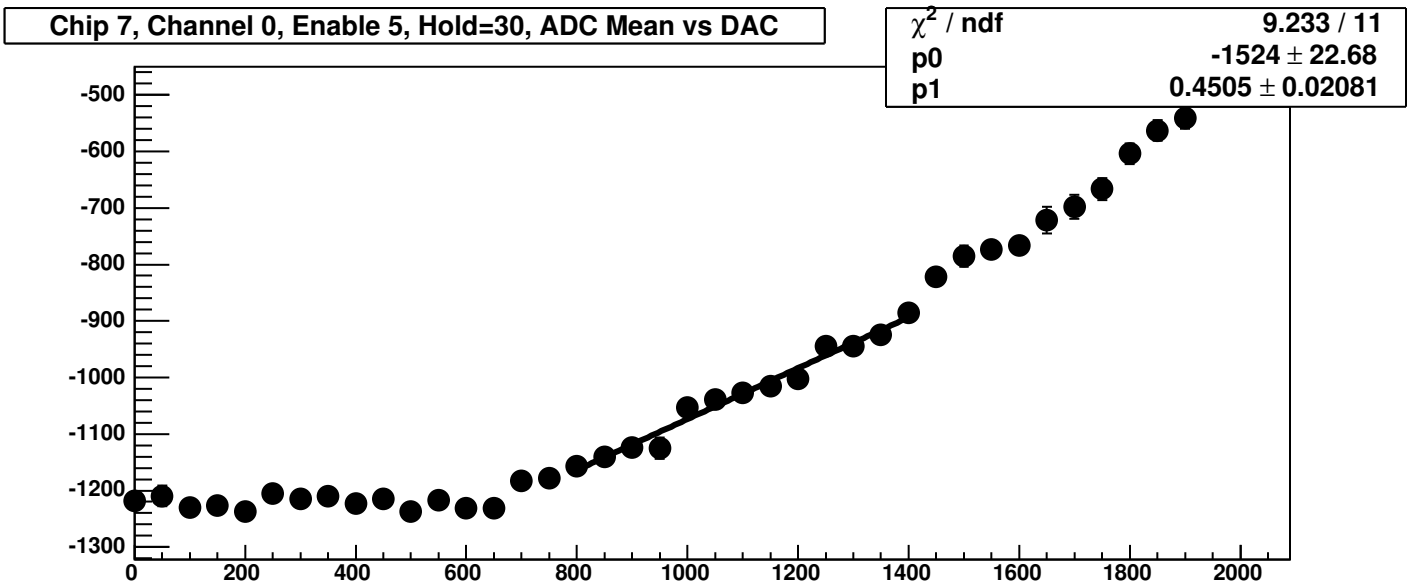

Chip 7, Channel 0, Enable 3!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

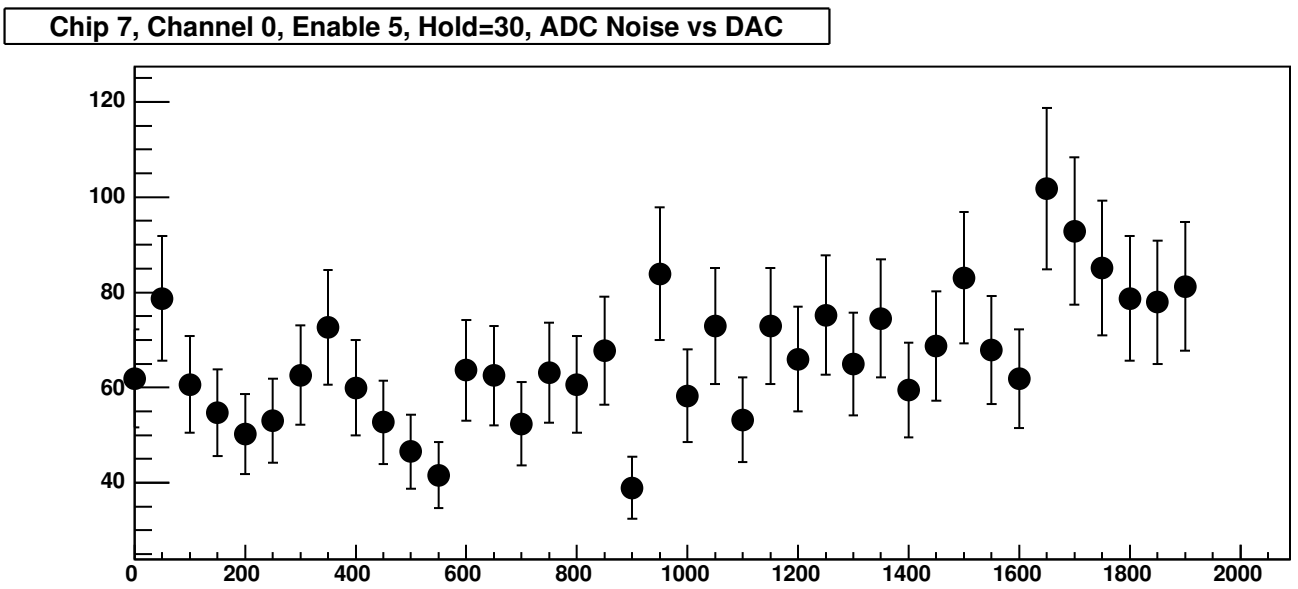

Chip 7, Channel 0, Enable 3!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

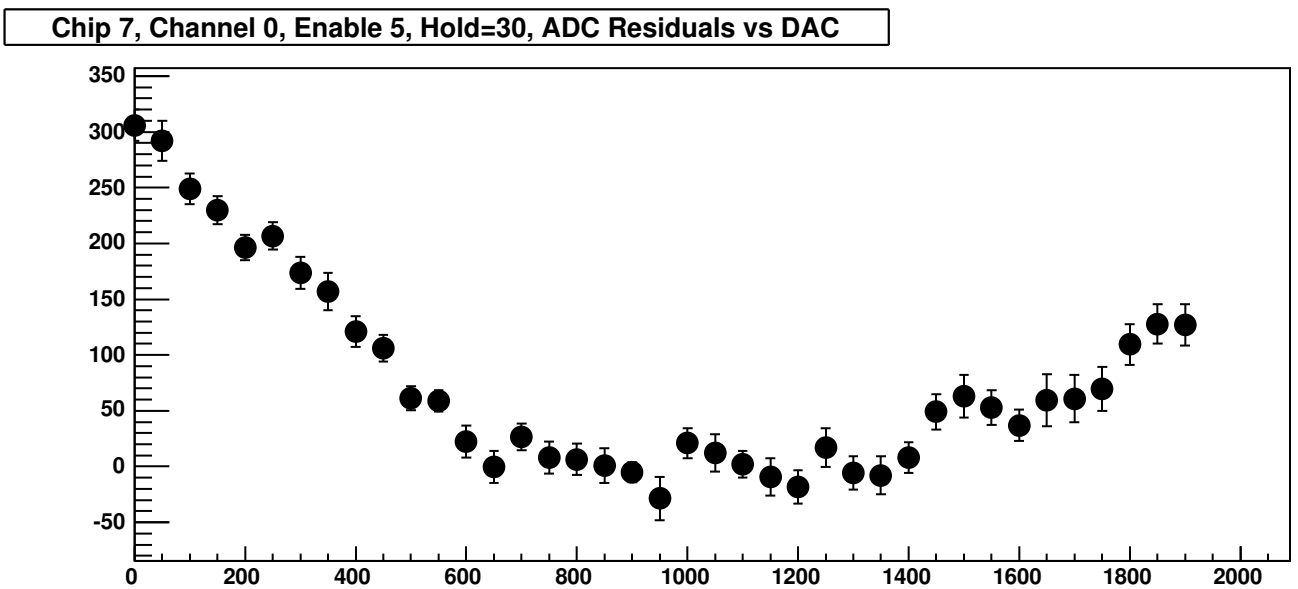

Chip 7, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4.861 / 11
p0 -1463 ± 18.4
p1 0.1162 ± 0.0164
-1200
-1250
-1300
-1350
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

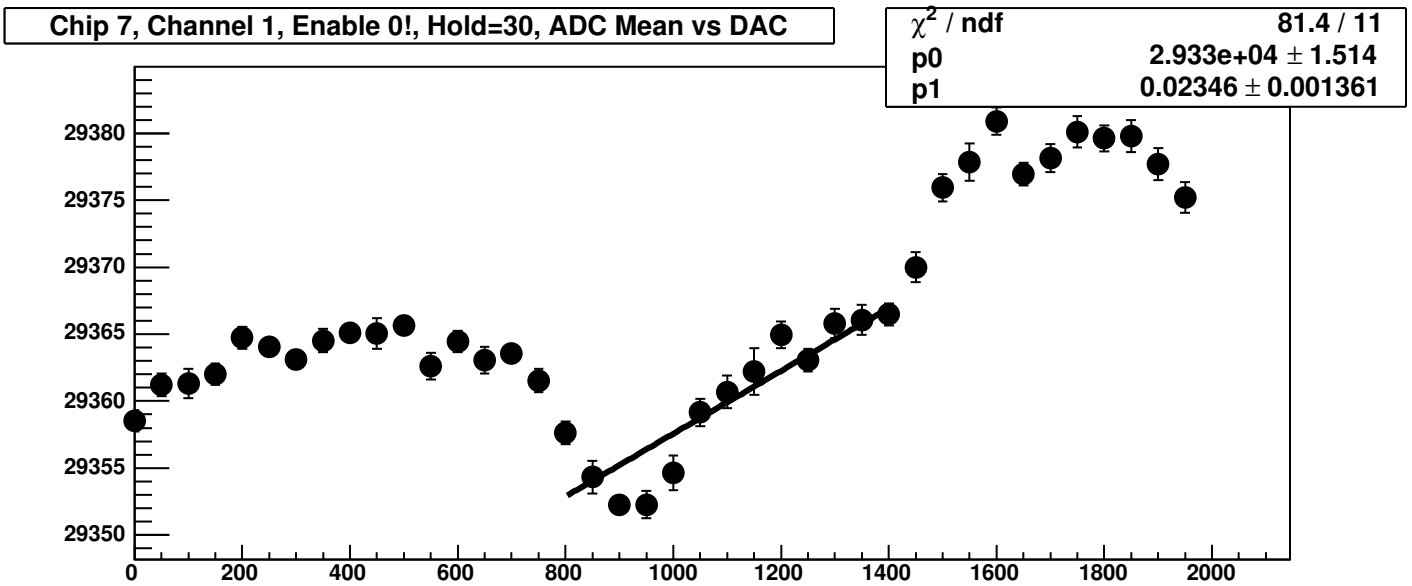

Chip 7, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

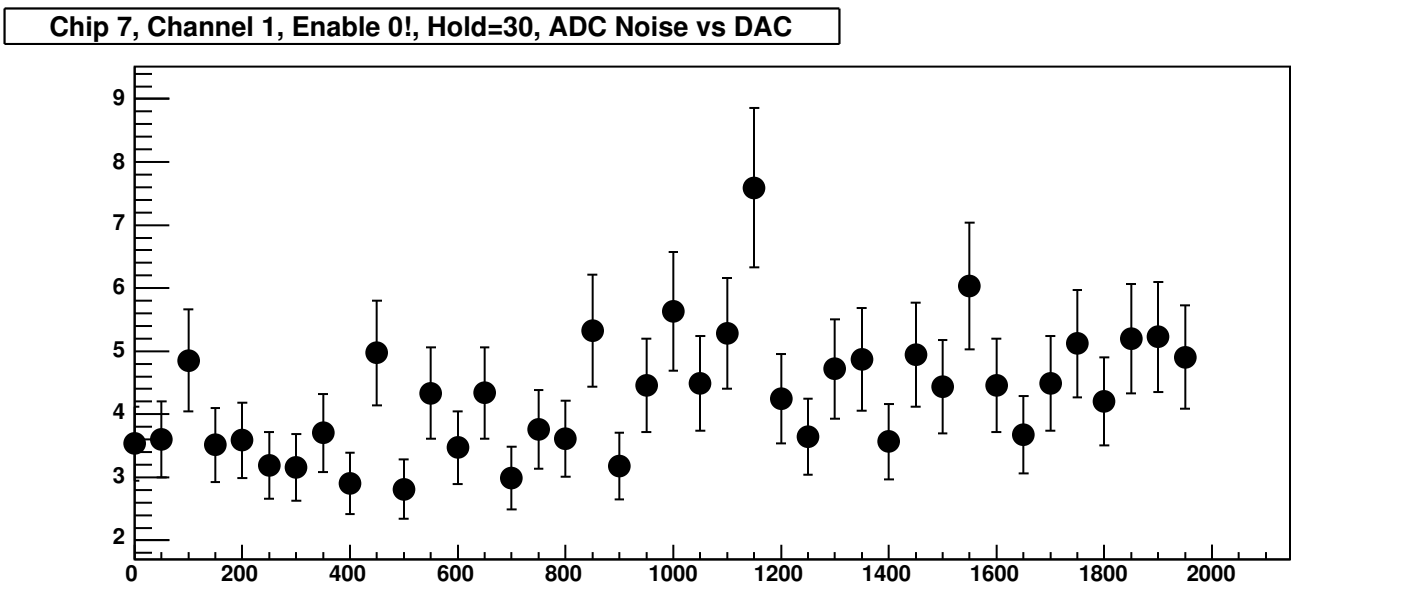

Chip 7, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

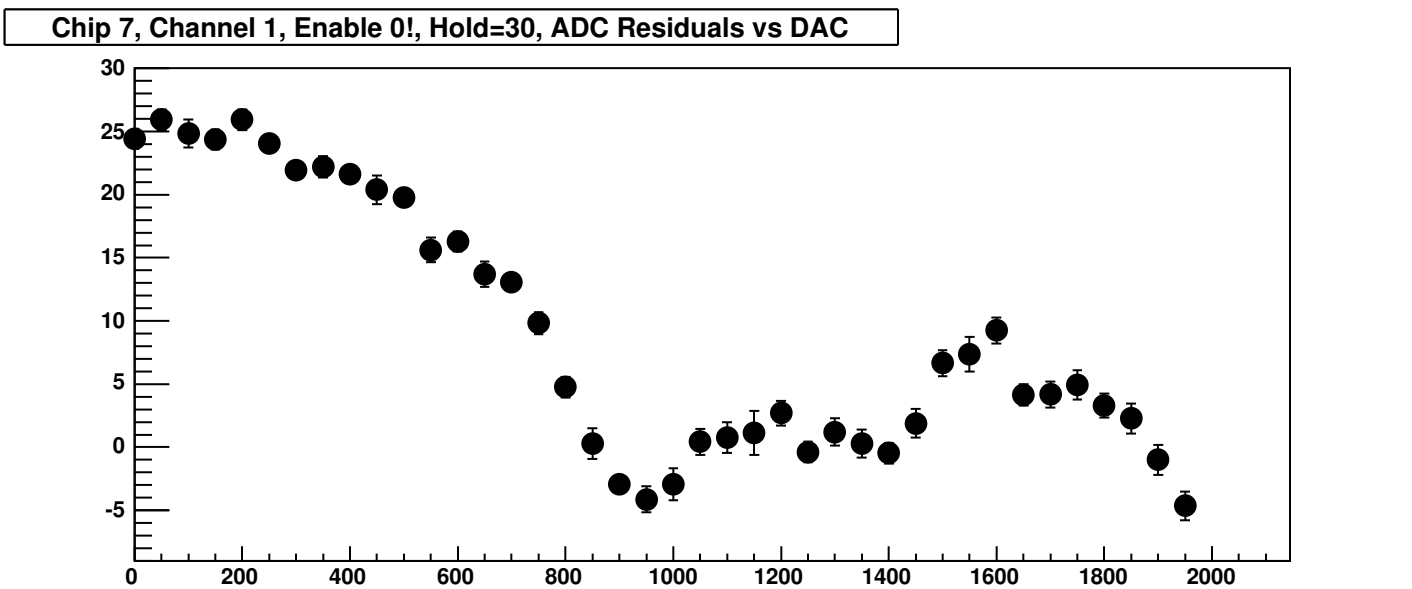

Chip 7, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9.233 / 11
p0 -1524 ± 22.68
p1 0.4505 ± 0.02081
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

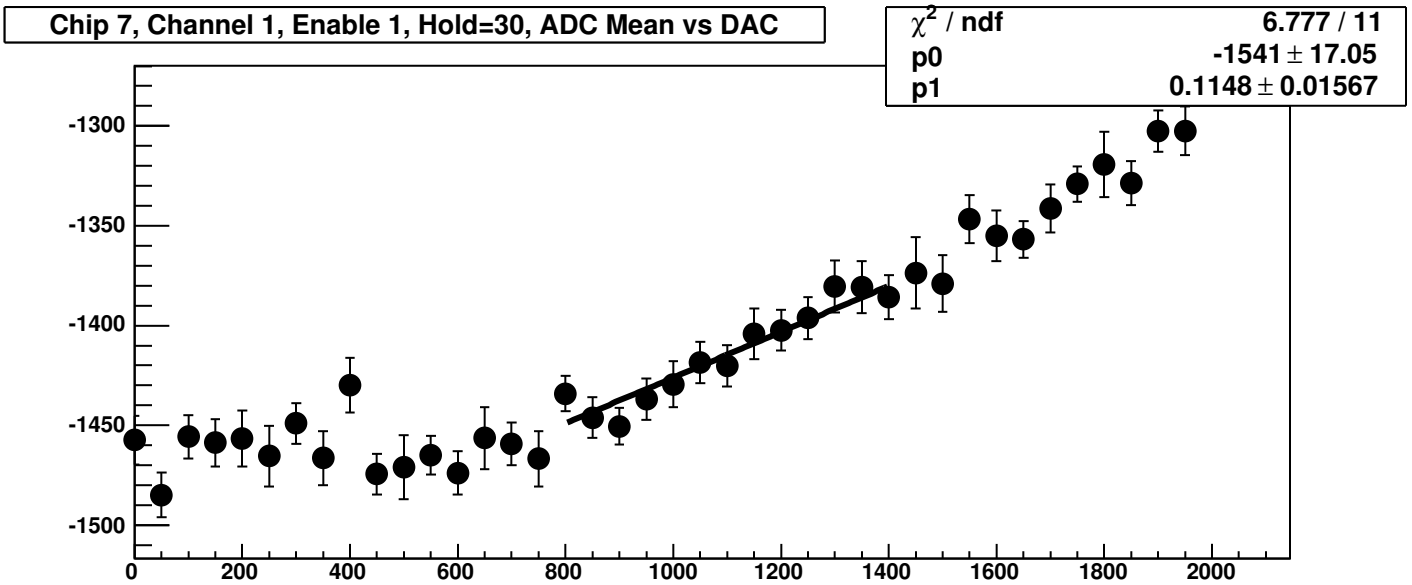

Chip 7, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

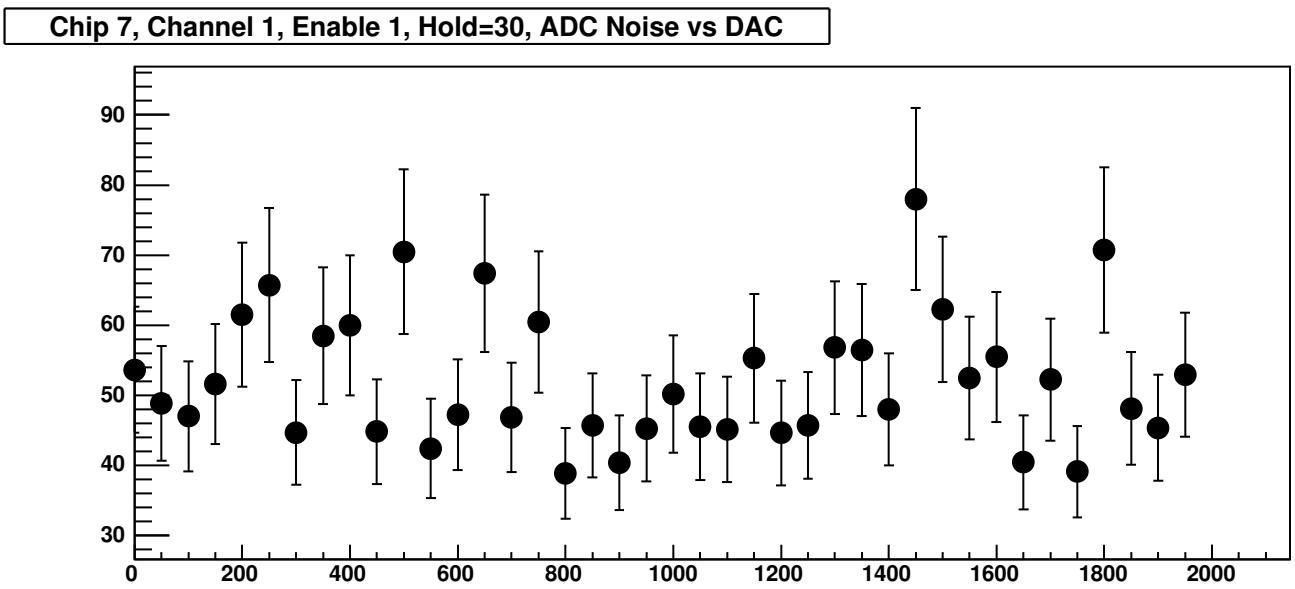

Chip 7, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

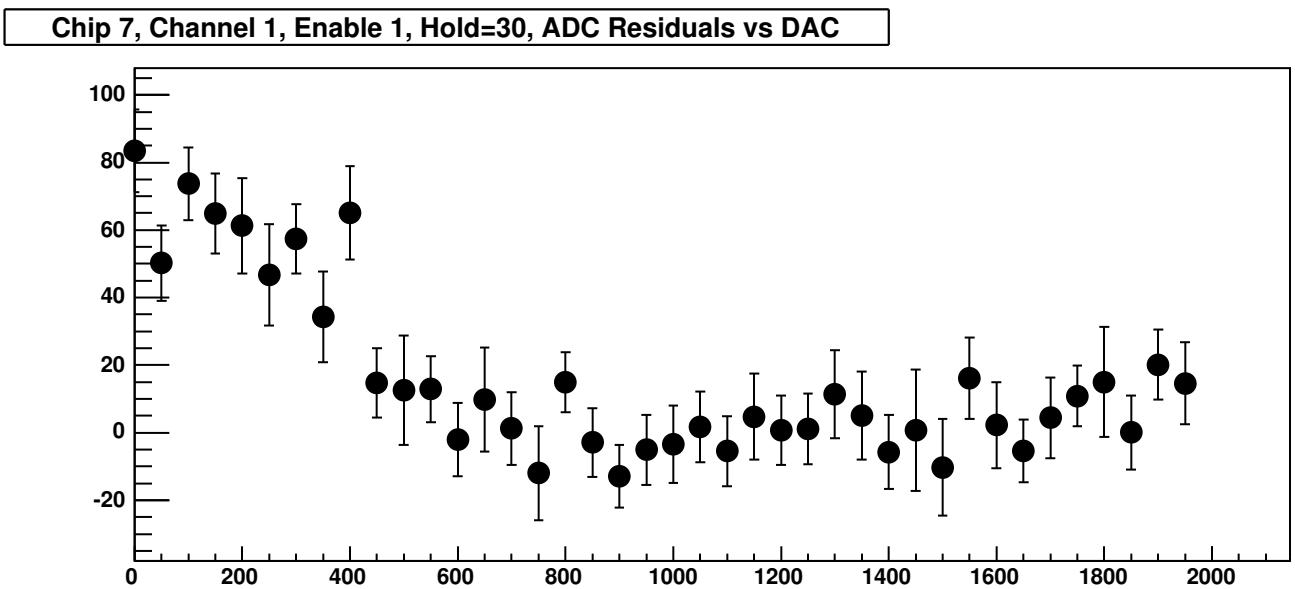

Chip 7, Channel 1, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 81.4 / 11
p0 2.933e+04 ± 1.514
p1 0.02346 ± 0.001361
29380
29375
29370
29365
29360
29355
29350
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 1, Enable 0!, Hold=30, ADC Noise vs DAC
9
8
7
6
5
4
3
2
0
200
400
600
800
1000
1200
1400
1600
1800
2000

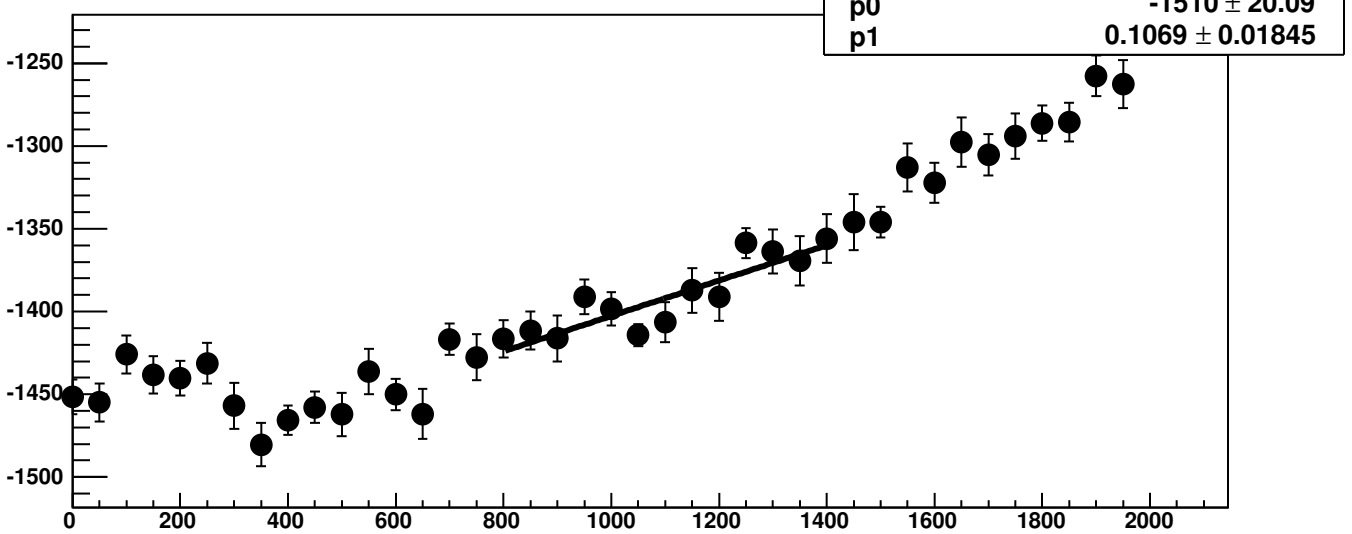

Chip 7, Channel 1, Enable 0!, Hold=30, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 6.777 / 11
p0 -1541 ± 17.05
p1 0.1148 ± 0.01567
-1300
-1350
-1400
-1450
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

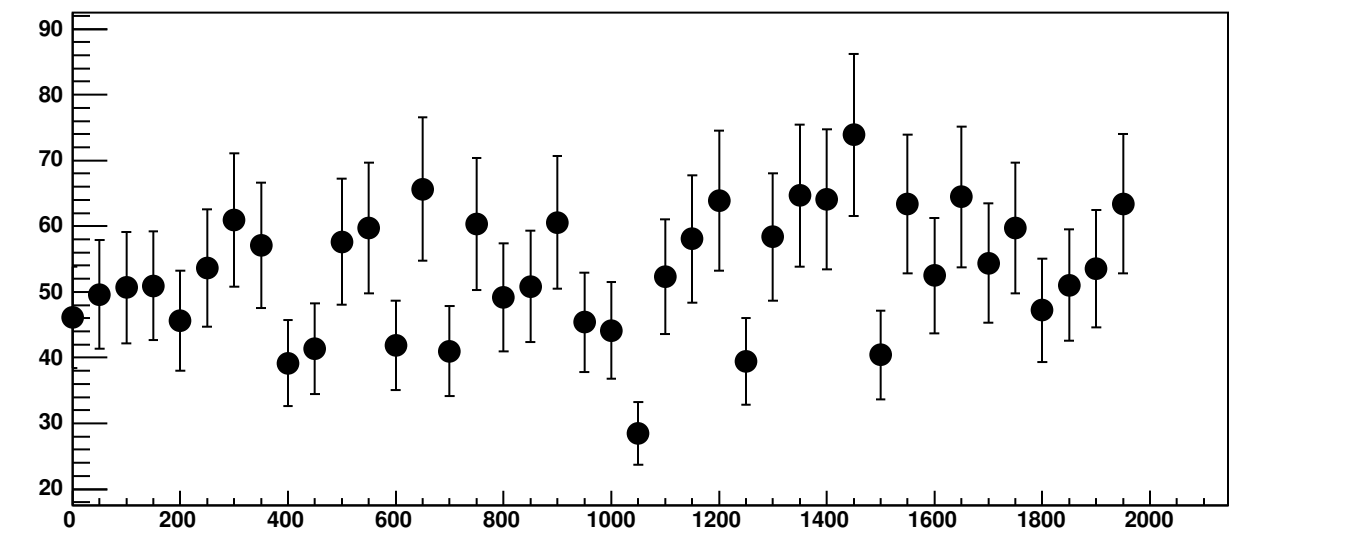

Chip 7, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

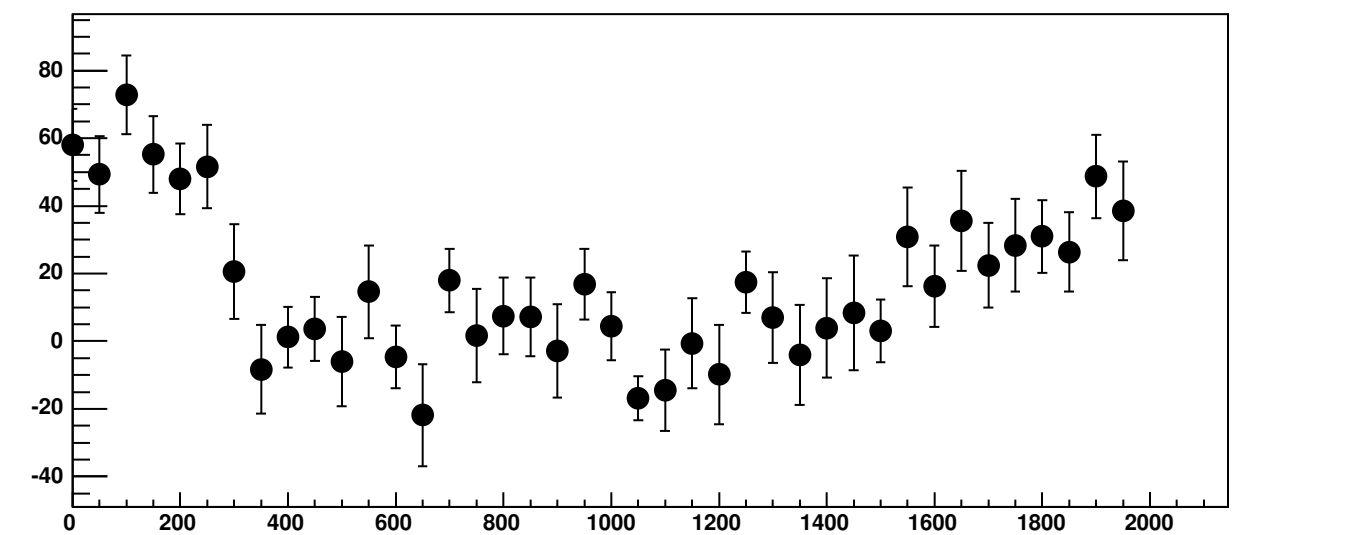
Chip 7, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC



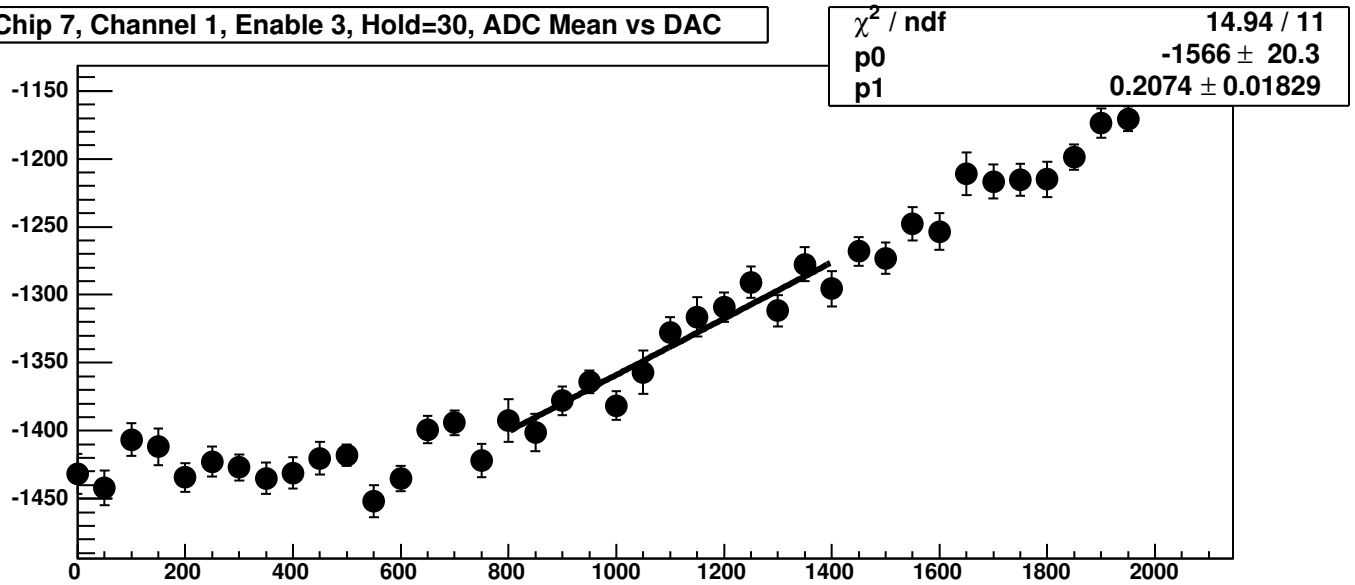
Chip 7, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC



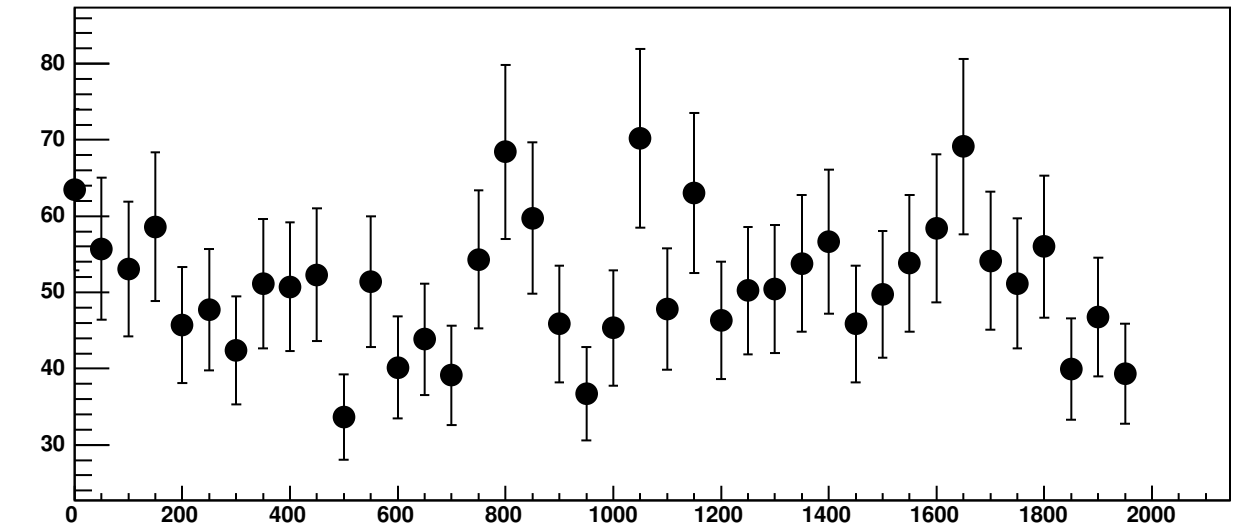
Chip 7, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC



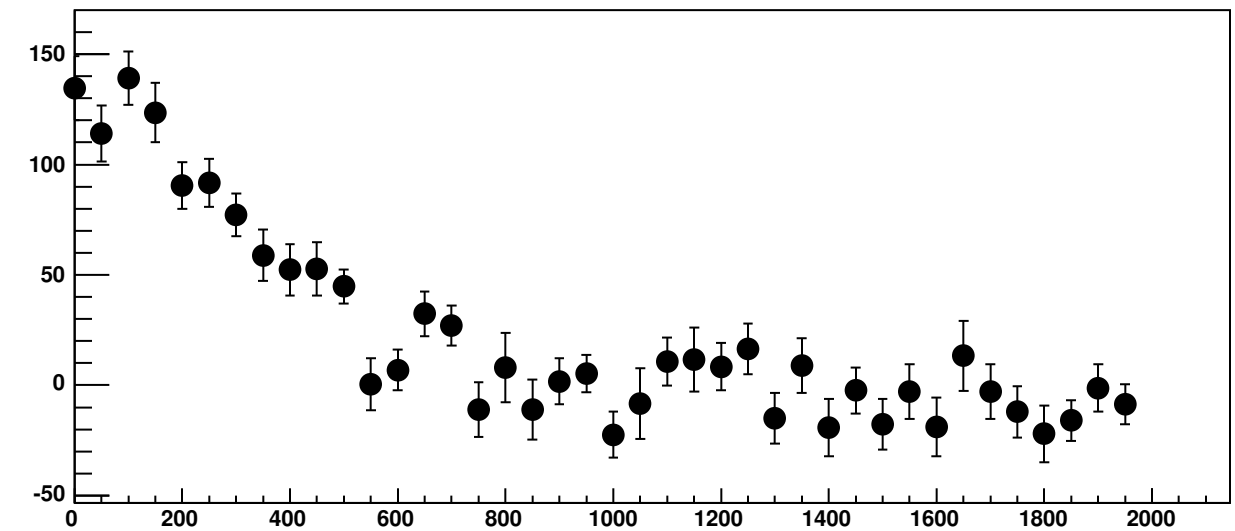
Chip 7, Channel 1, Enable 3, Hold=30, ADC Mean vs DAC

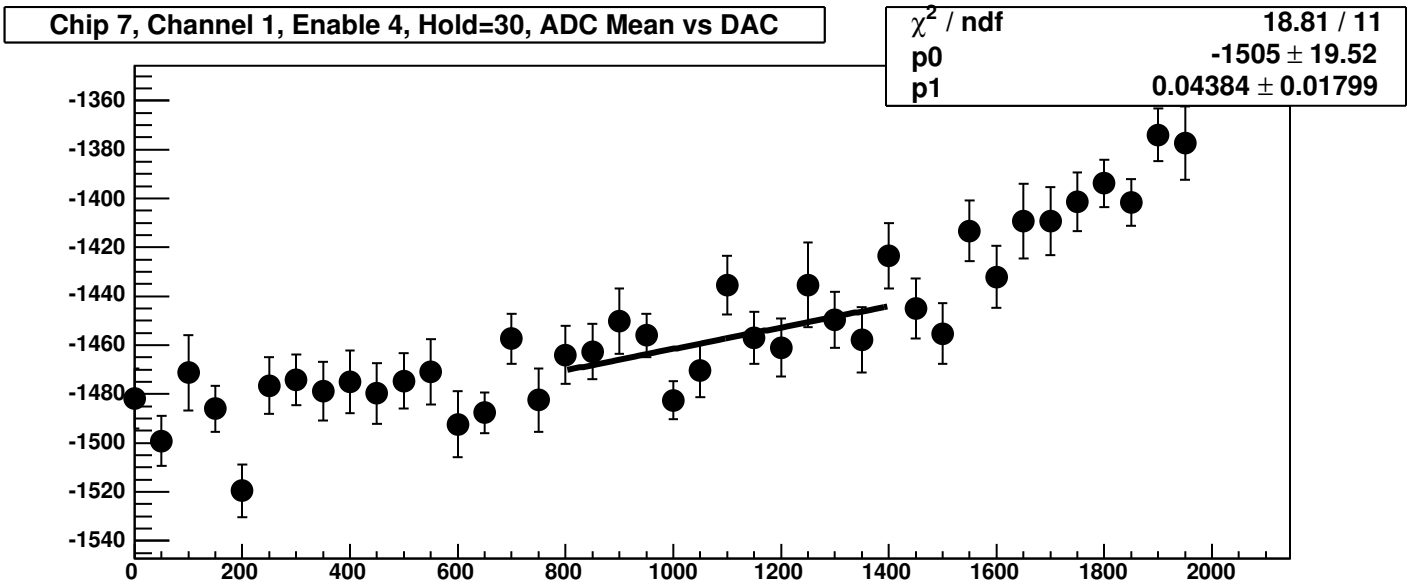


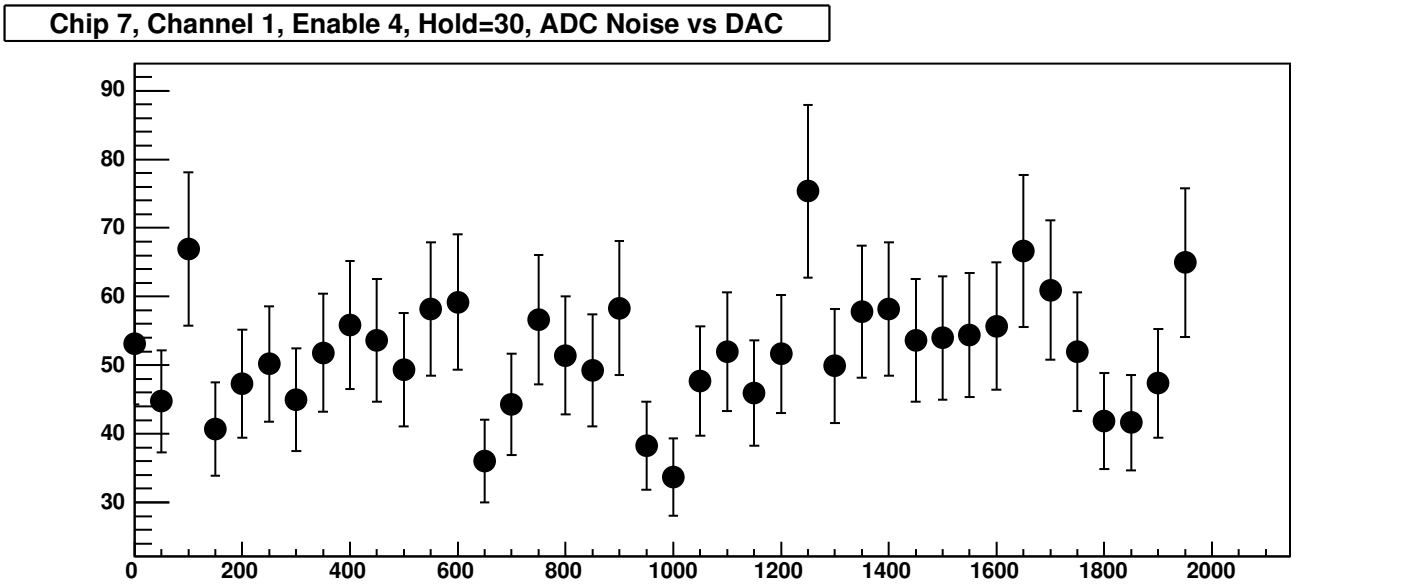
Chip 7, Channel 1, Enable 3, Hold=30, ADC Noise vs DAC

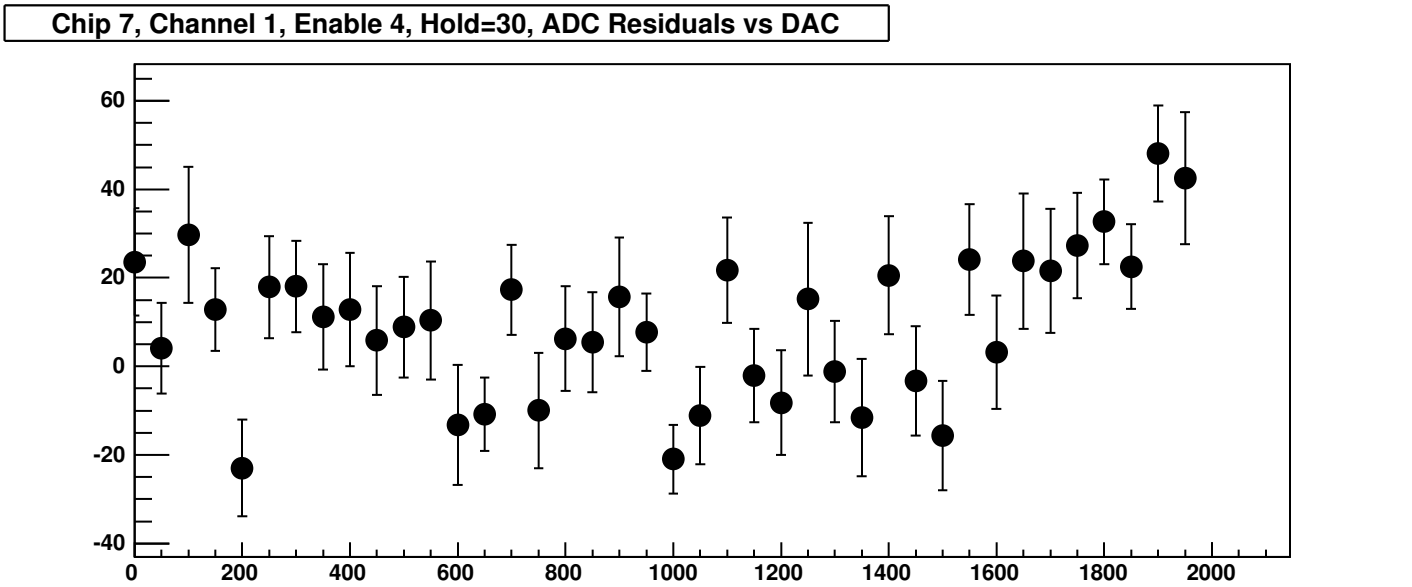


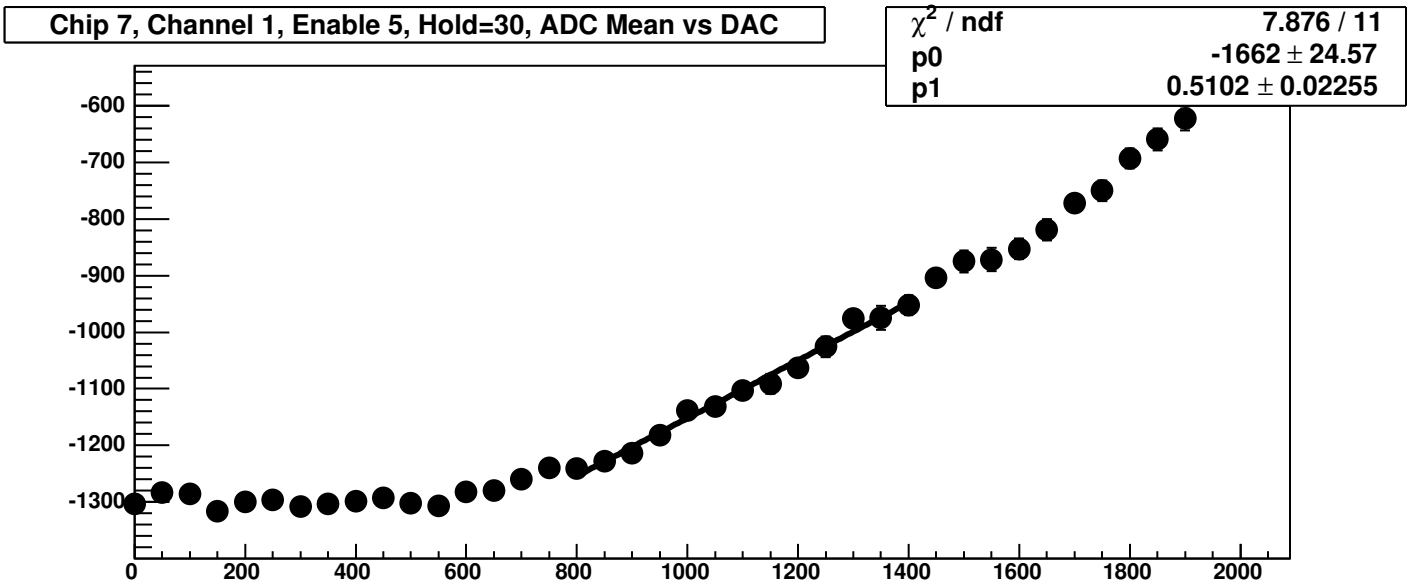
Chip 7, Channel 1, Enable 3, Hold=30, ADC Residuals vs DAC

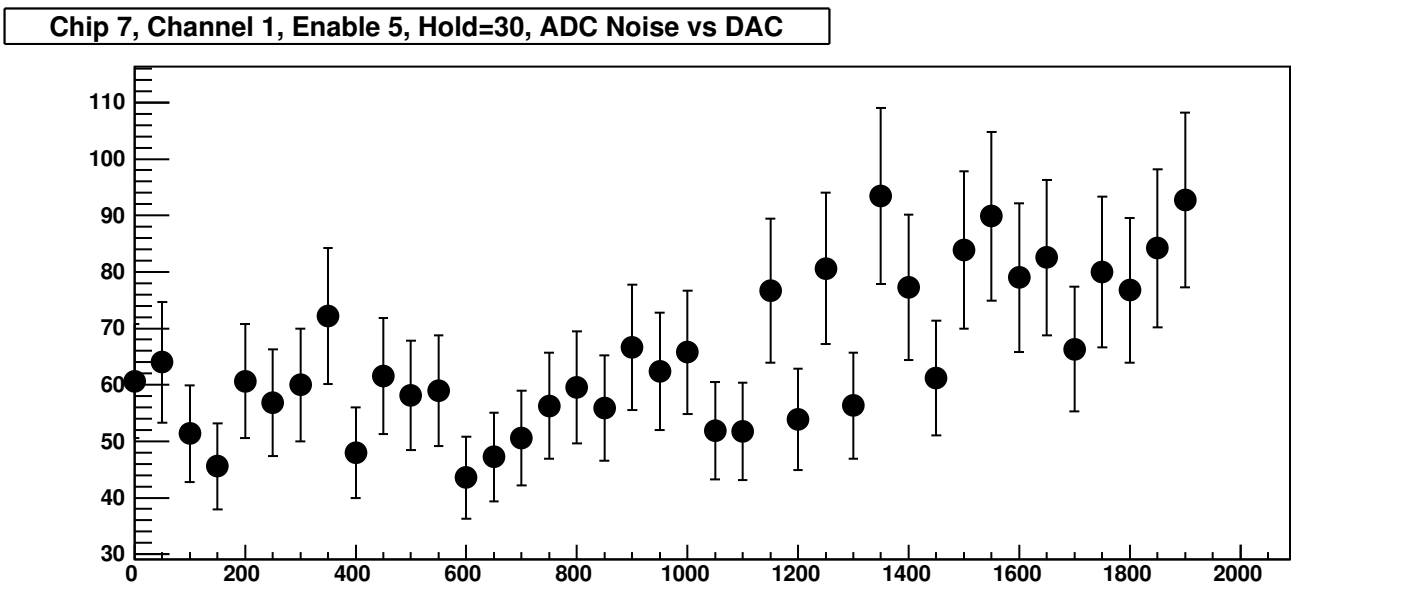


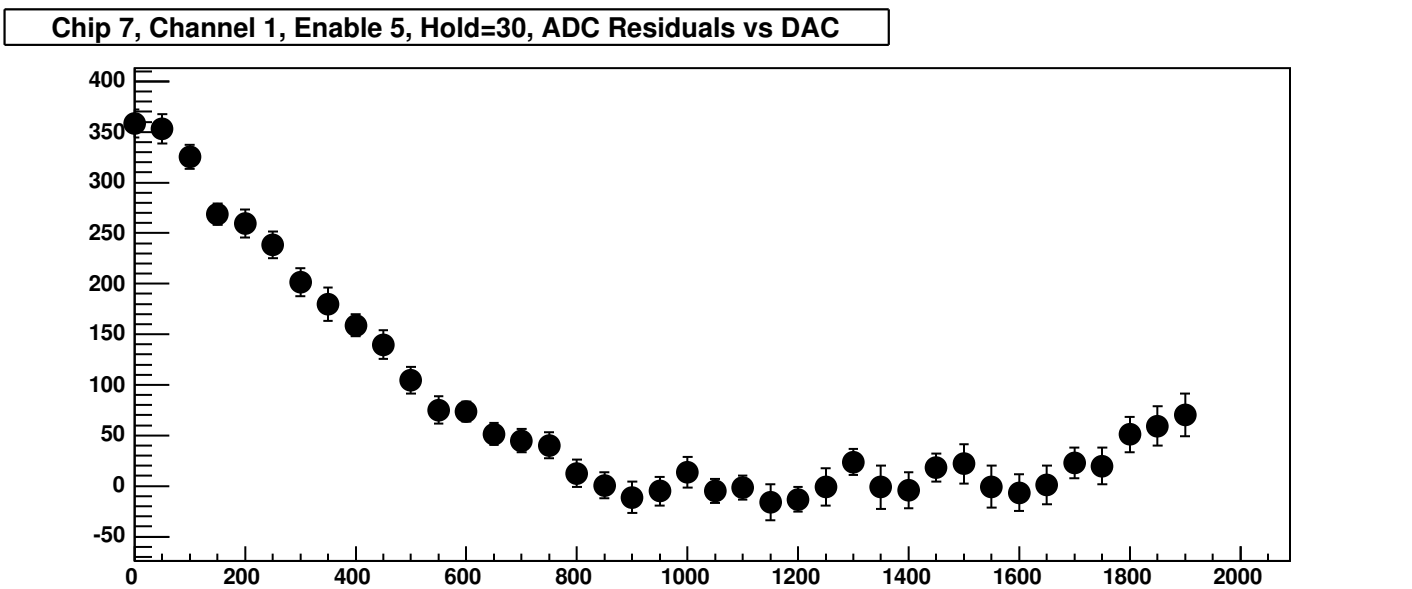

Chip 7, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 18.81 / 11
p0 -1505 ± 19.52
p1 0.04384 ± 0.01799
-1360
-1380
-1400
-1420
-1440
-1460
-1480
-1500
-1520
-1540
0
200
400
600
800
1000
1200
1400
1600
1800
2000

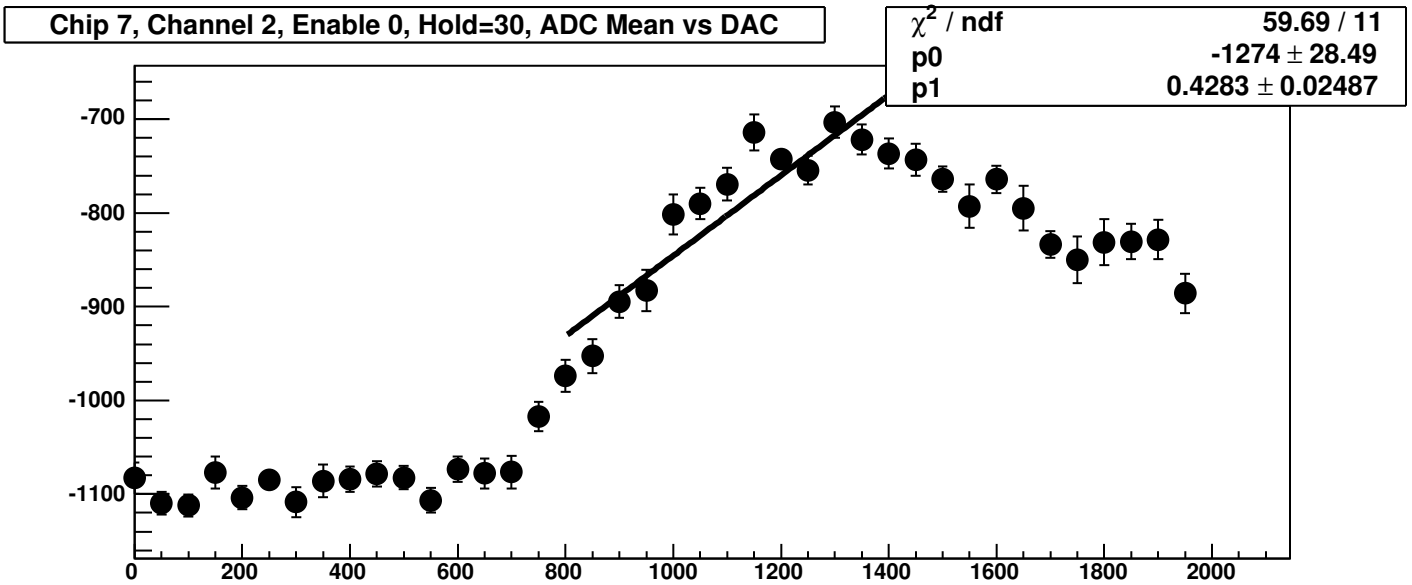

Chip 7, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

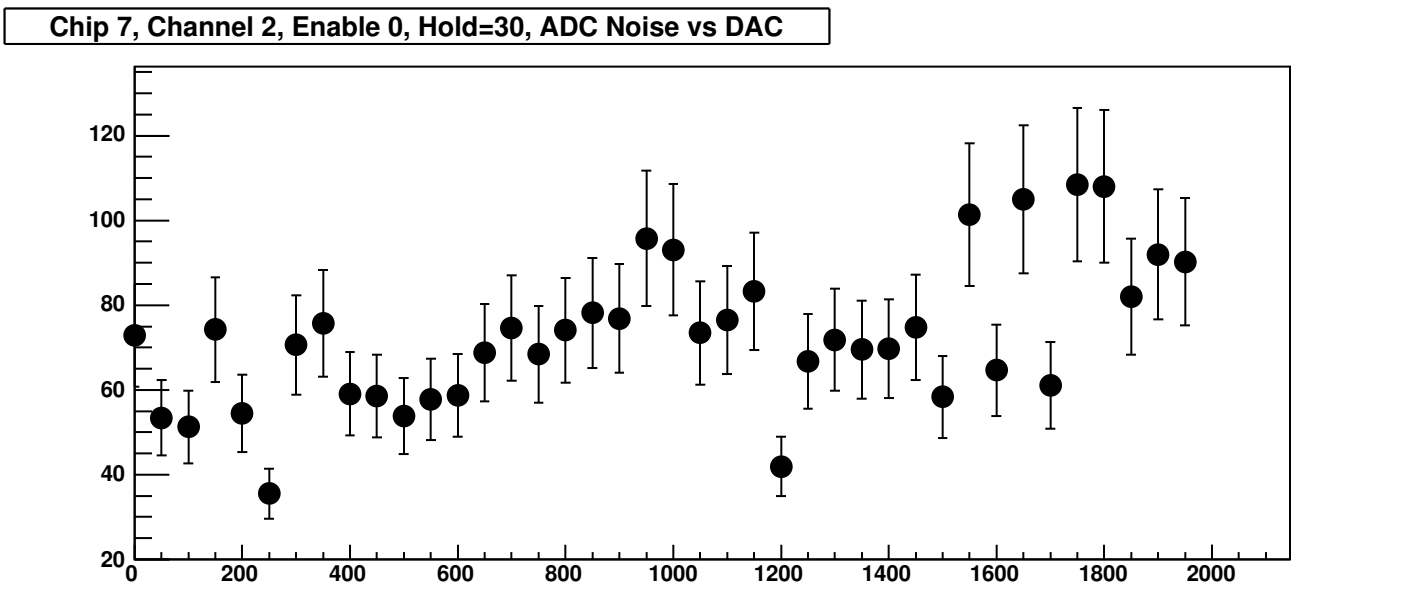

Chip 7, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

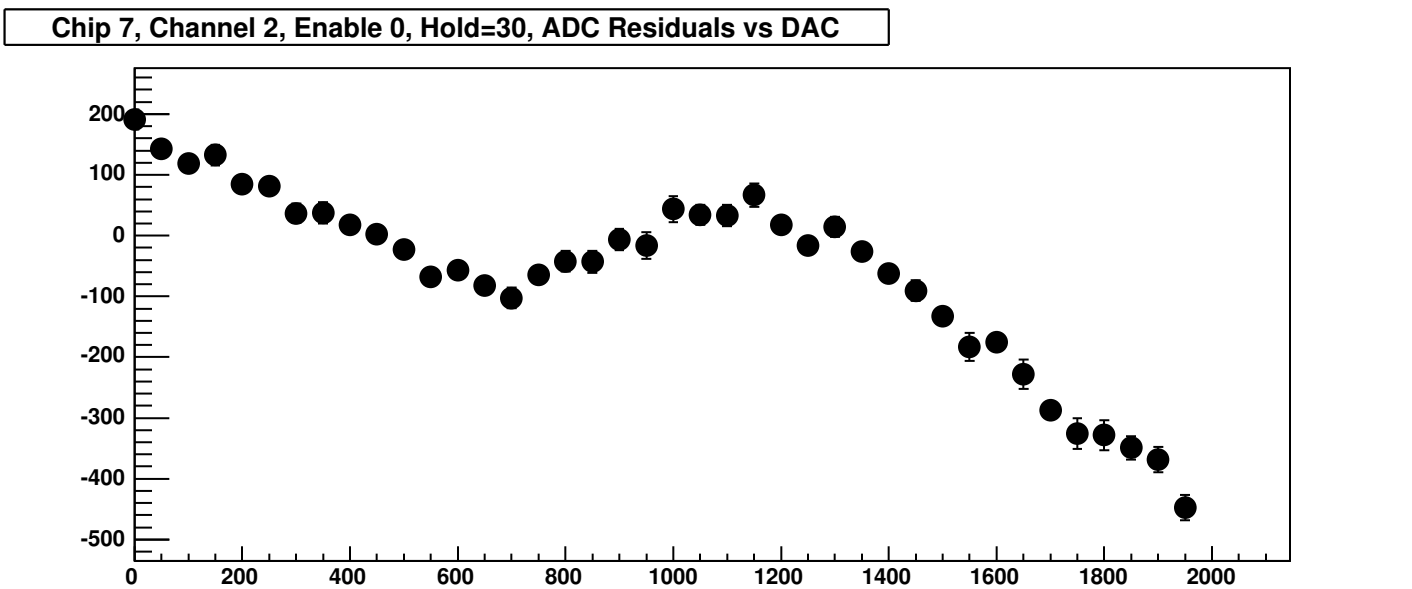

Chip 7, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7.876 / 11
p0 -1662 ± 24.57
p1 0.5102 ± 0.02255
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

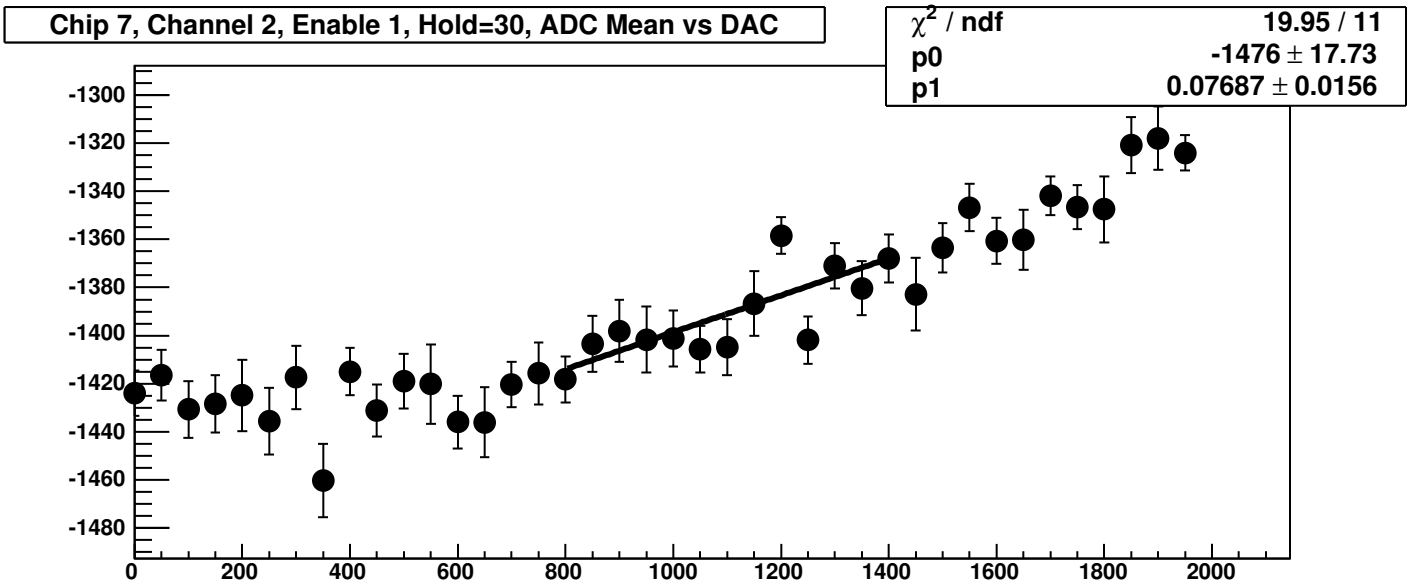

Chip 7, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

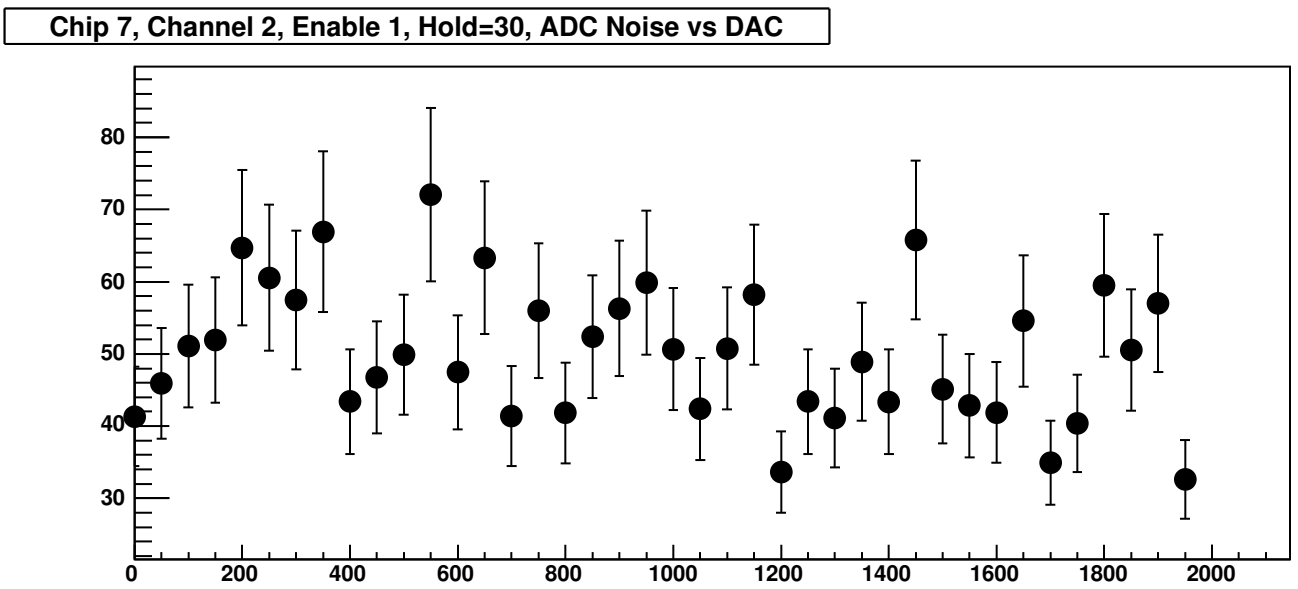

Chip 7, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
400
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

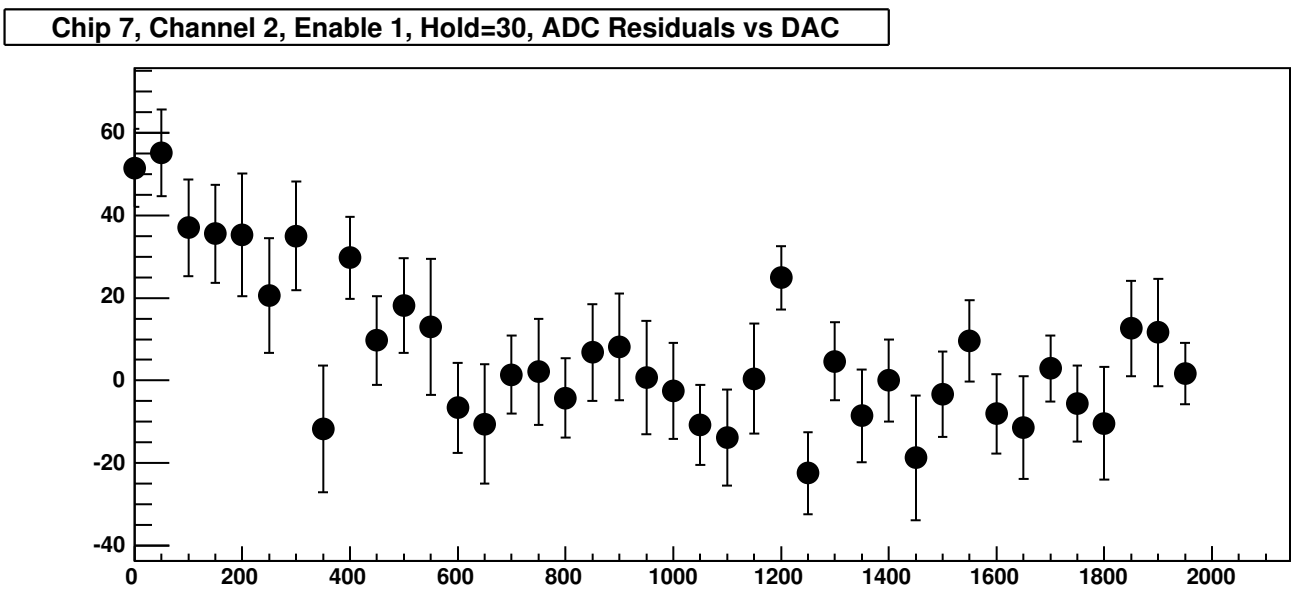

Chip 7, Channel 2, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 59.69 / 11
p0 -1274 ± 28.49
p1 0.4283 ± 0.02487
-700
-800
-900
-1000
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

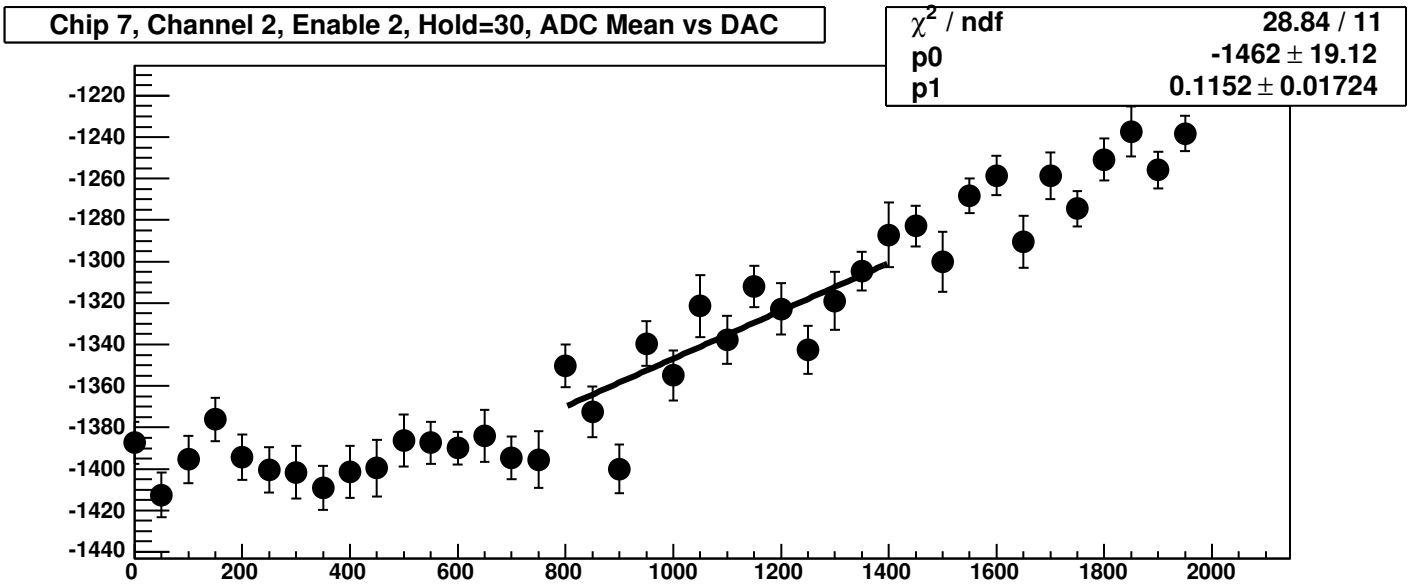

Chip 7, Channel 2, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

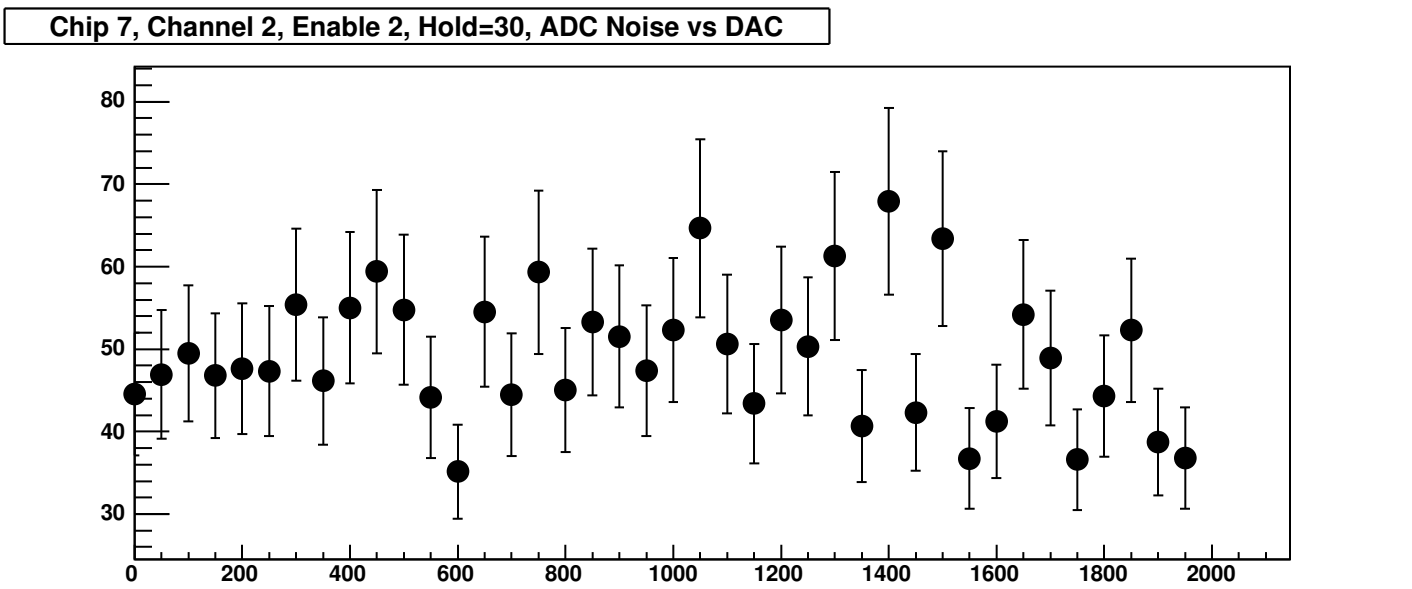

Chip 7, Channel 2, Enable 0, Hold=30, ADC Residuals vs DAC
200
100
0
-100
-200
-300
-400
-500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

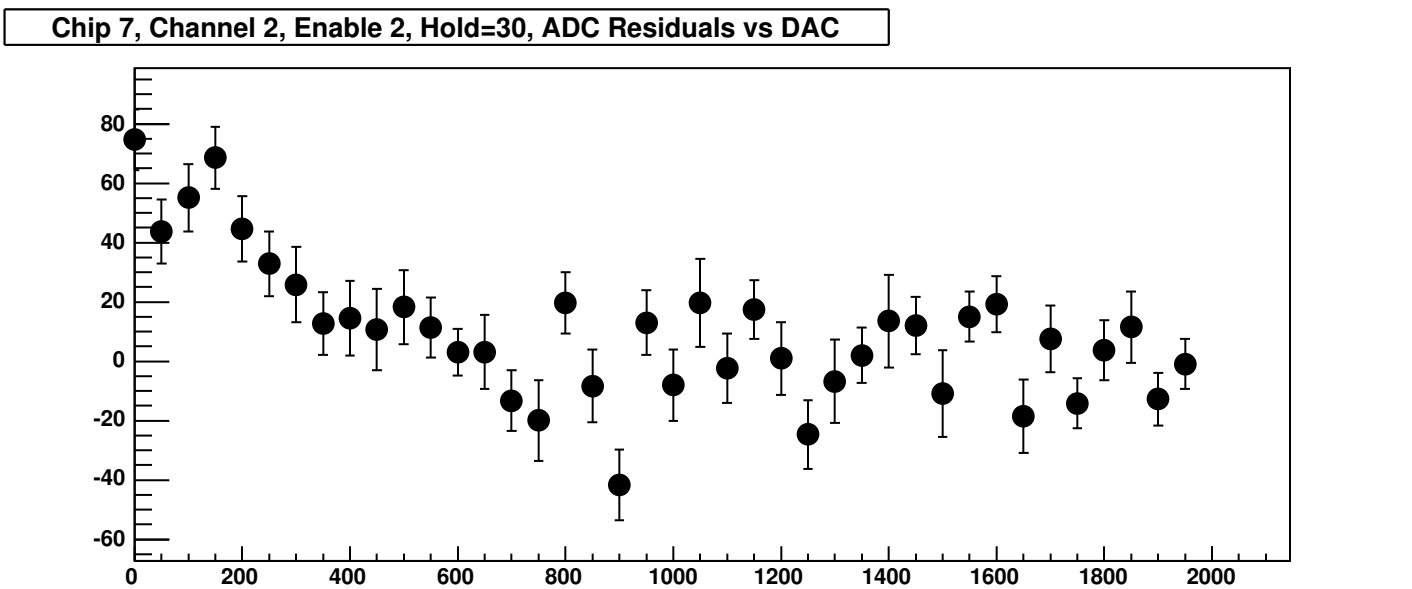

Chip 7, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 19.95 / 11
p0 -1476 ± 17.73
p1 0.07687 ± 0.0156
-1300
-1320
-1340
-1360
-1380
-1400
-1420
-1440
-1460
-1480
0
200
400
600
800
1000
1200
1400
1600
1800
2000

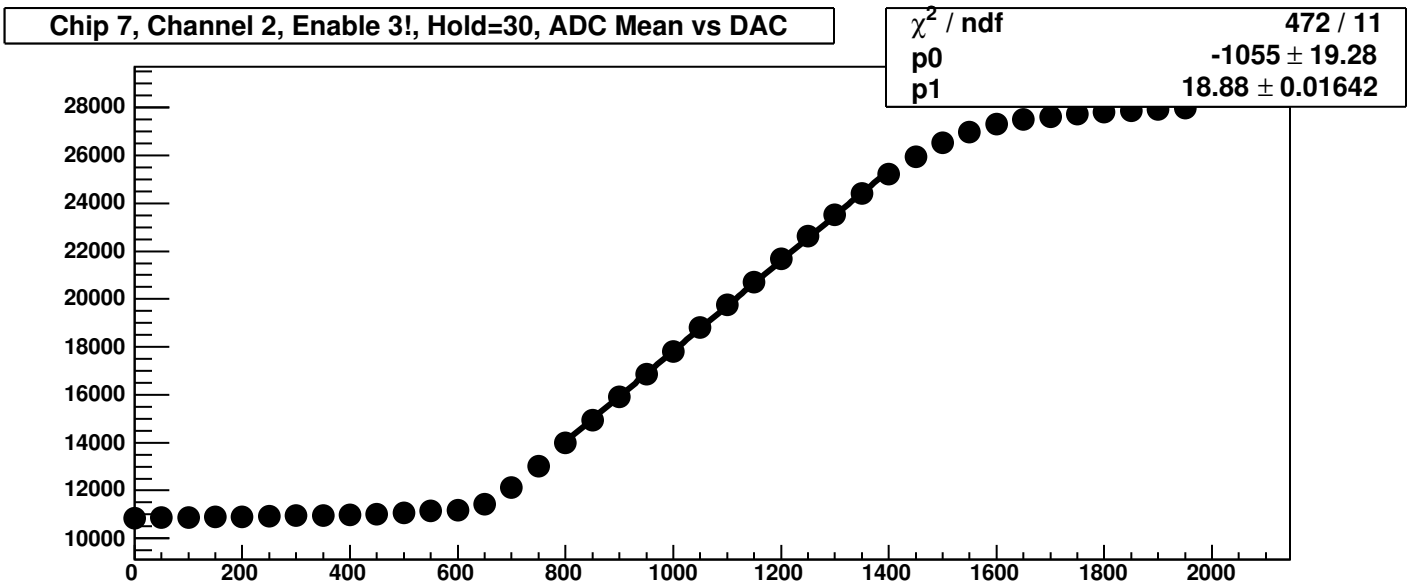

Chip 7, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

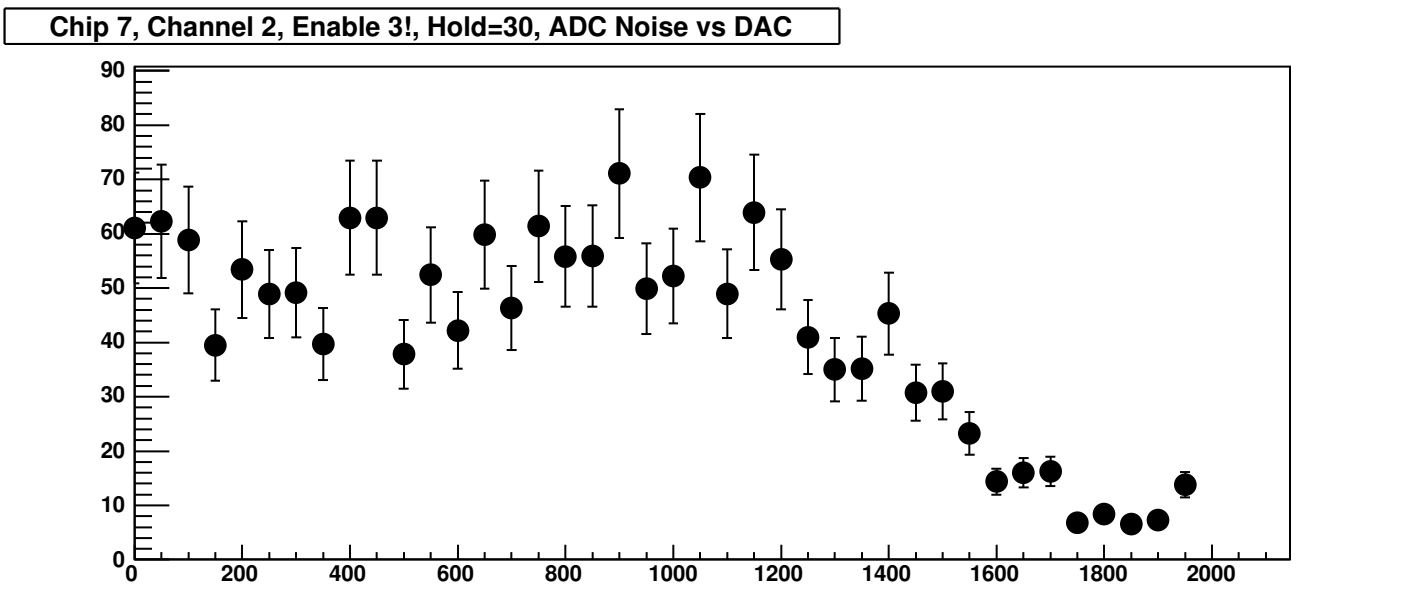

Chip 7, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

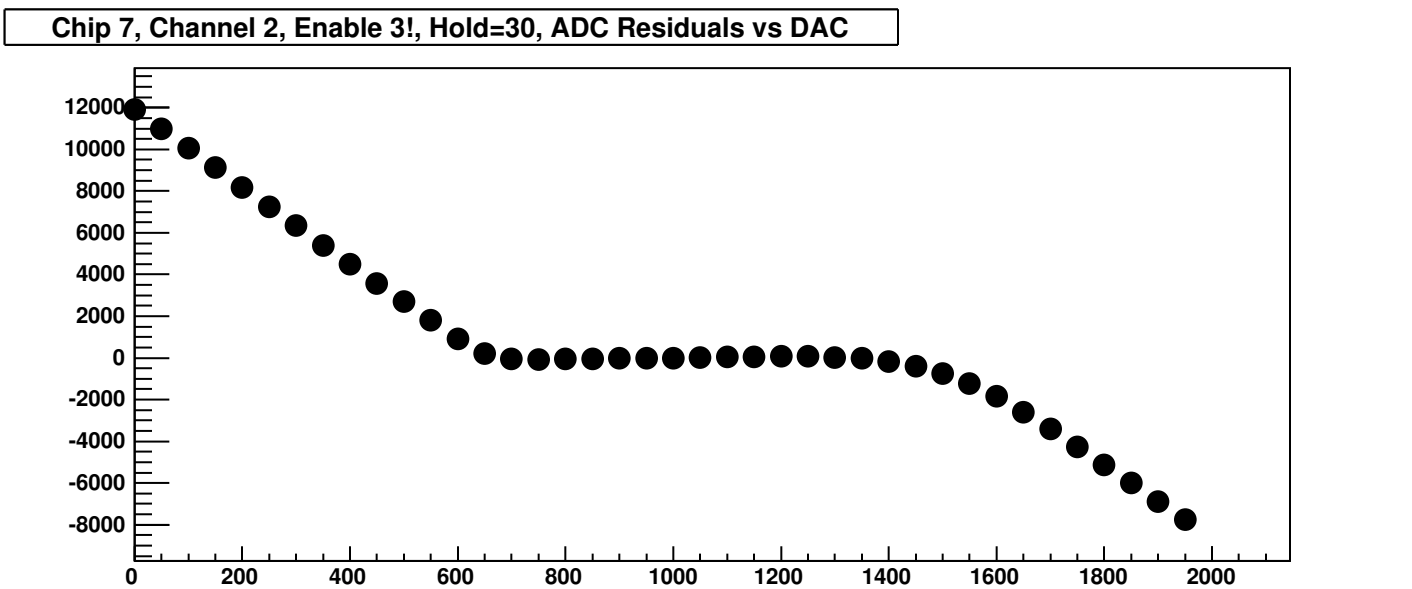

Chip 7, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 28.84 / 11
p0 -1462 ± 19.12
p1 0.1152 ± 0.01724
-1220
-1240
-1260
-1280
-1300
-1320
-1340
-1360
-1380
-1400
-1420
-1440
0
200
400
600
800
1000
1200
1400
1600
1800
2000

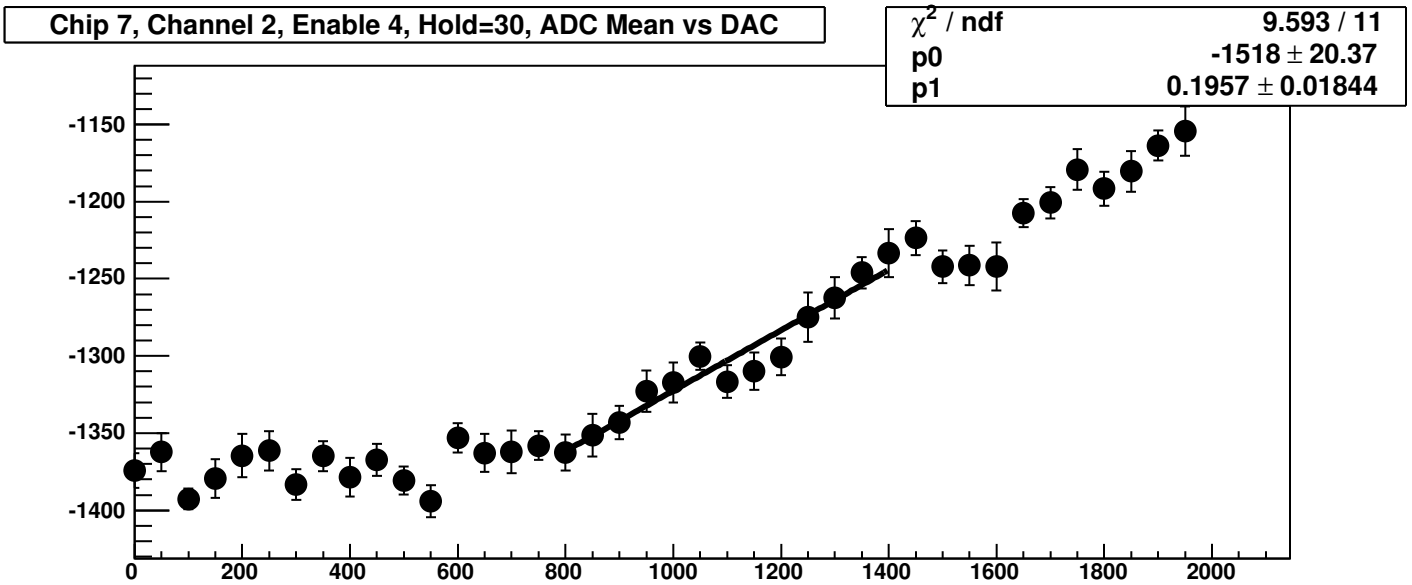

Chip 7, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

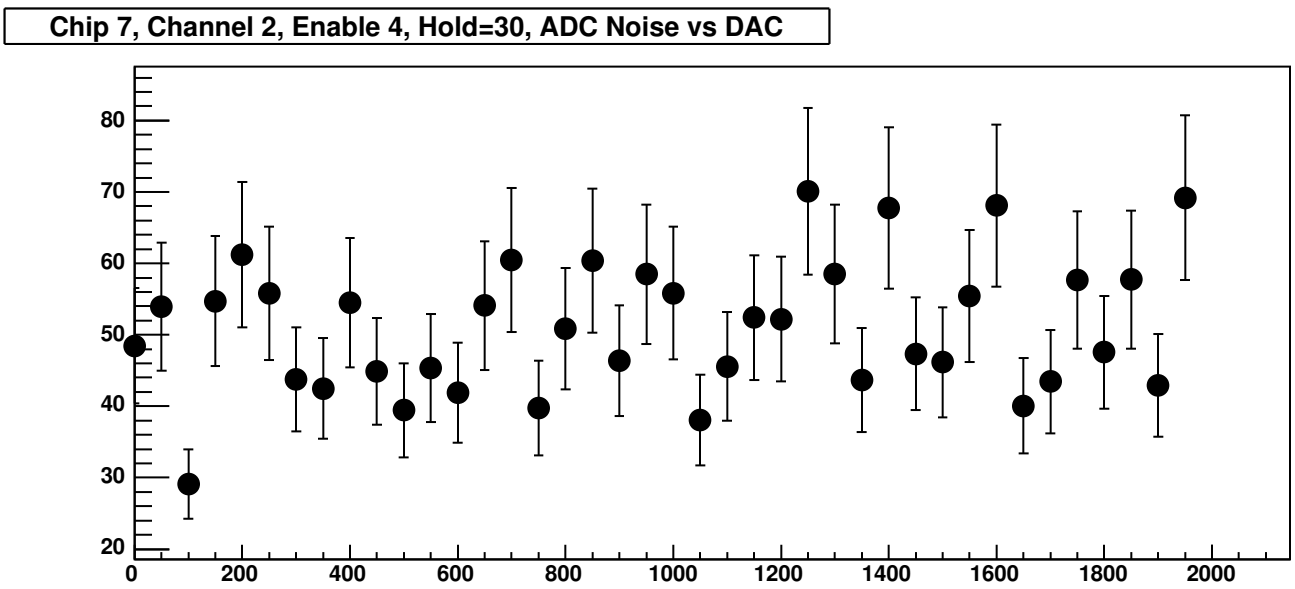

Chip 7, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

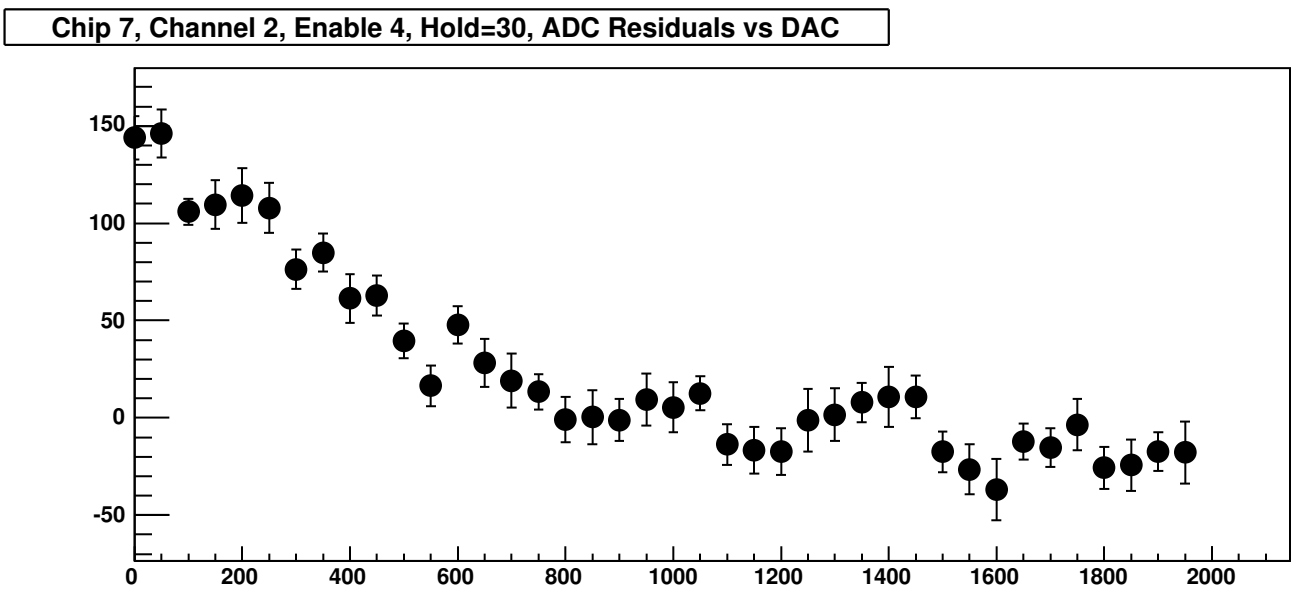

Chip 7, Channel 2, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 472 / 11
p0 -1055 ± 19.28
p1 18.88 ± 0.01642
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

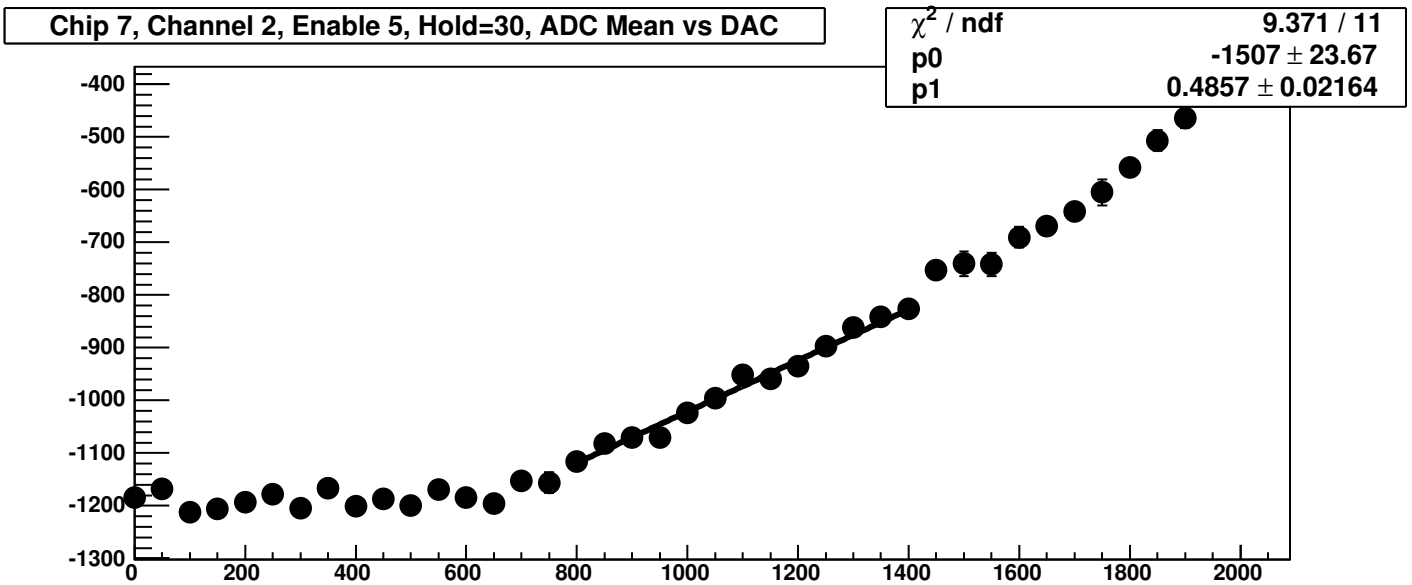

Chip 7, Channel 2, Enable 3!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

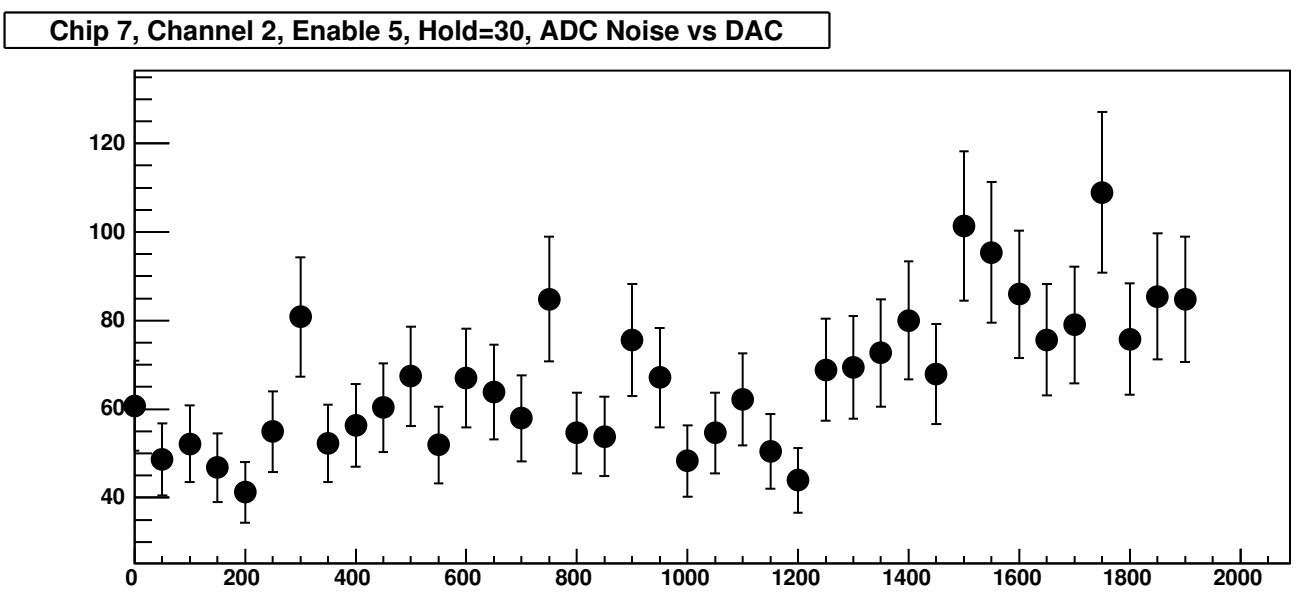

Chip 7, Channel 2, Enable 3!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

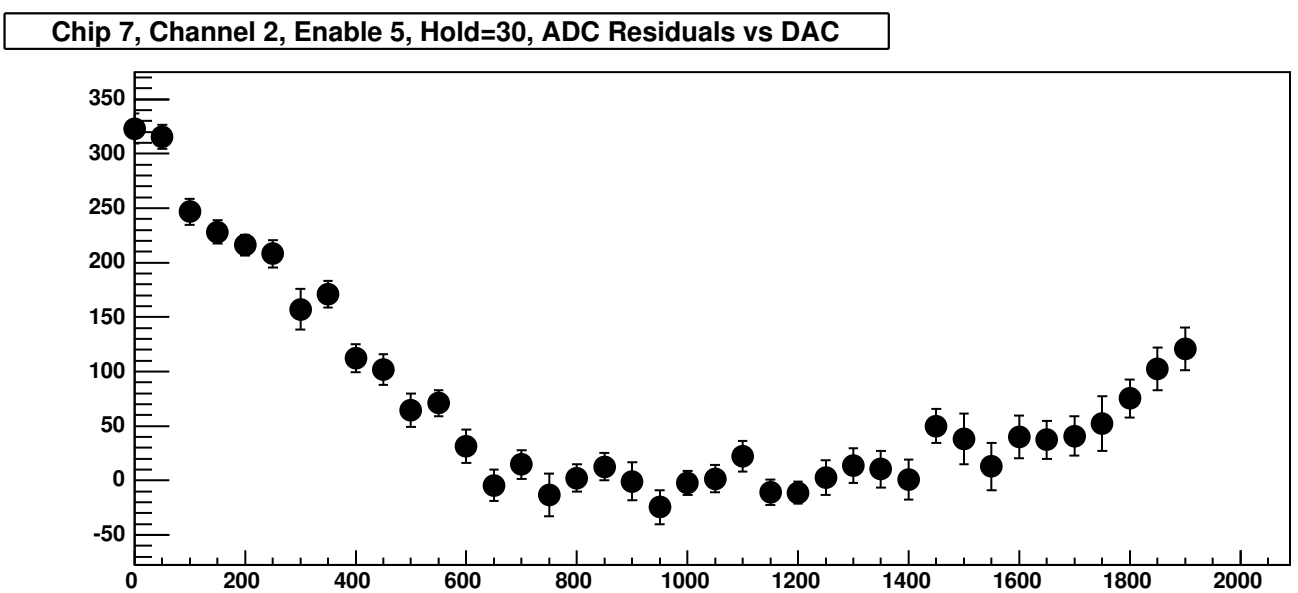

Chip 7, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 9.593 / 11
p0 -1518 ± 20.37
p1 0.1957 ± 0.01844
-1150
-1200
-1250
-1300
-1350
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

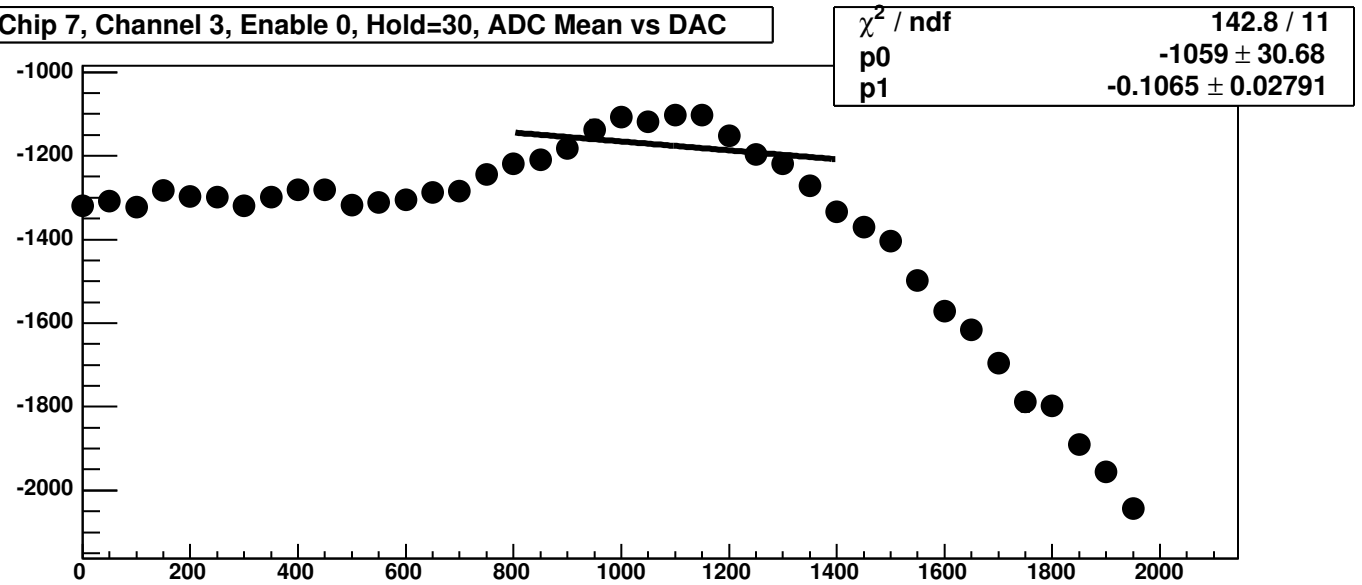

Chip 7, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9.371 / 11
p0 -1507 ± 23.67
p1 0.4857 ± 0.02164
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

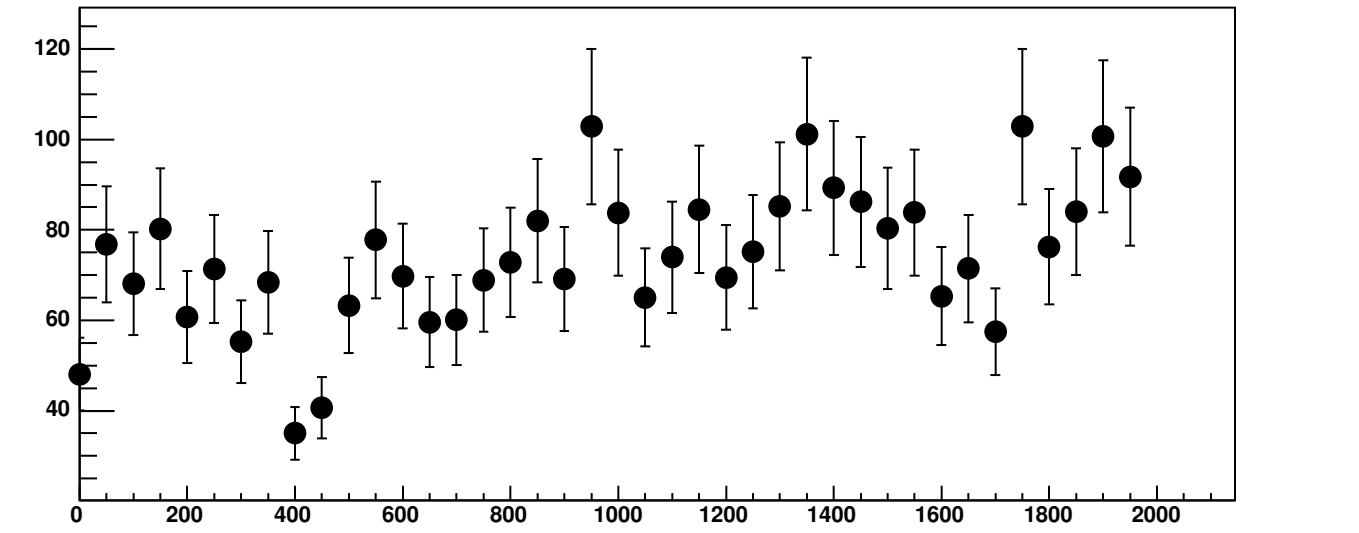

Chip 7, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

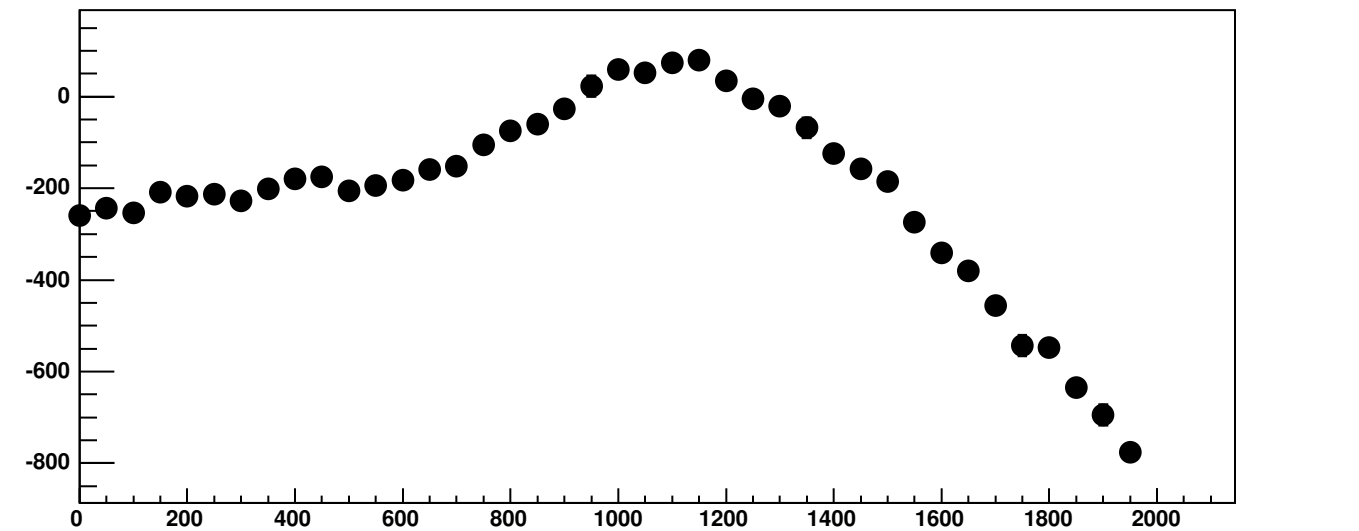
Chip 7, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC

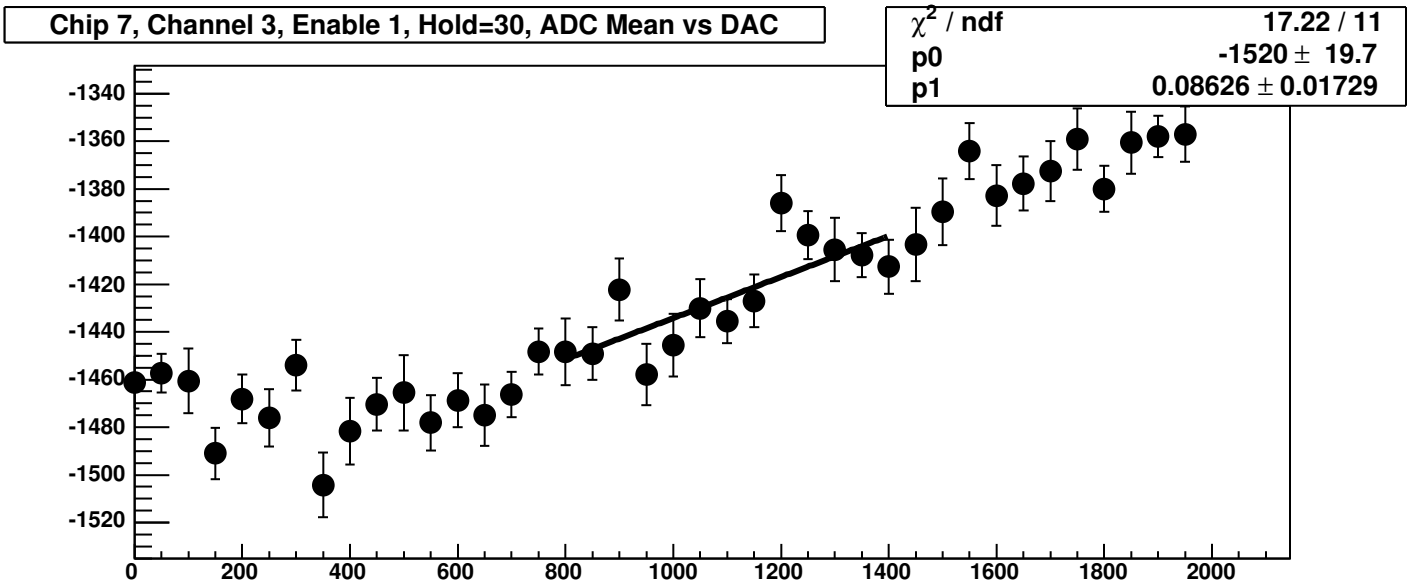


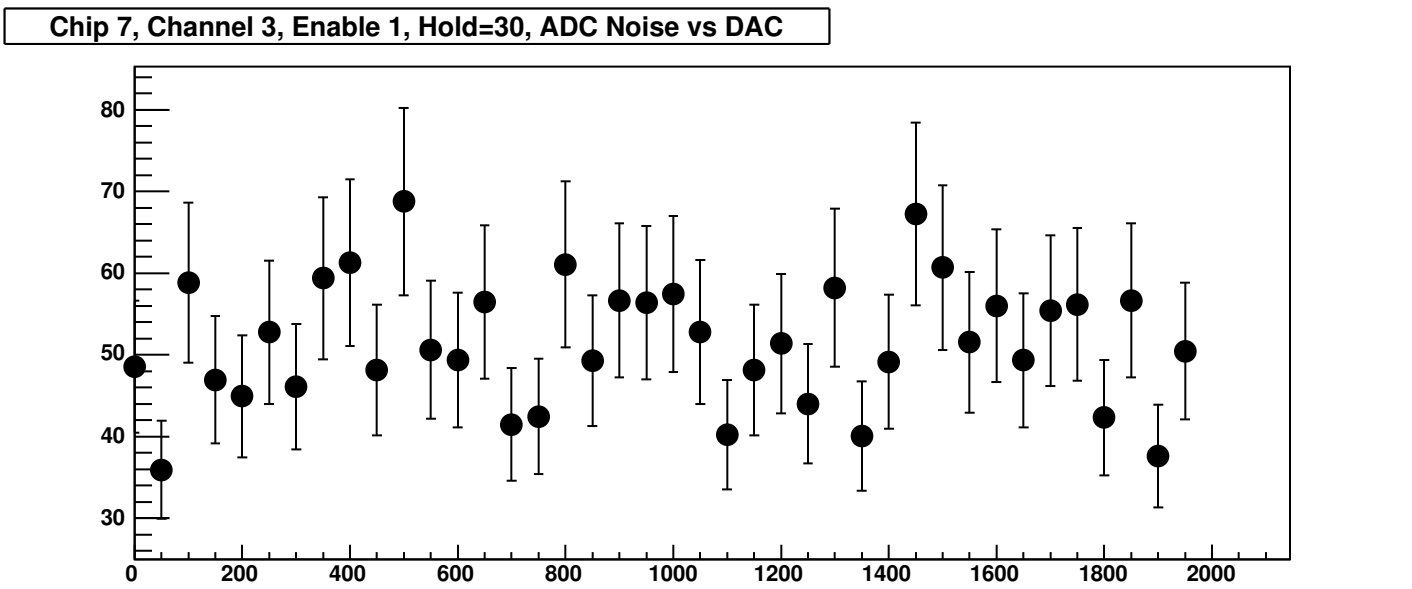
Chip 7, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC

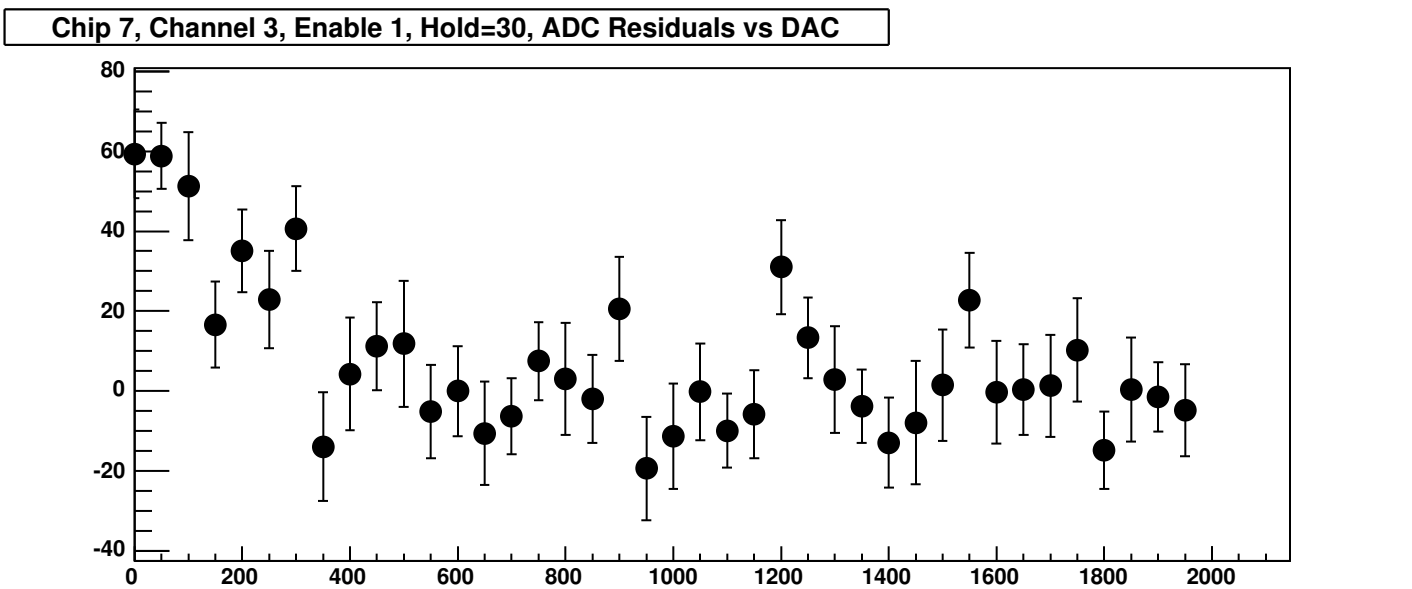


Chip 7, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC

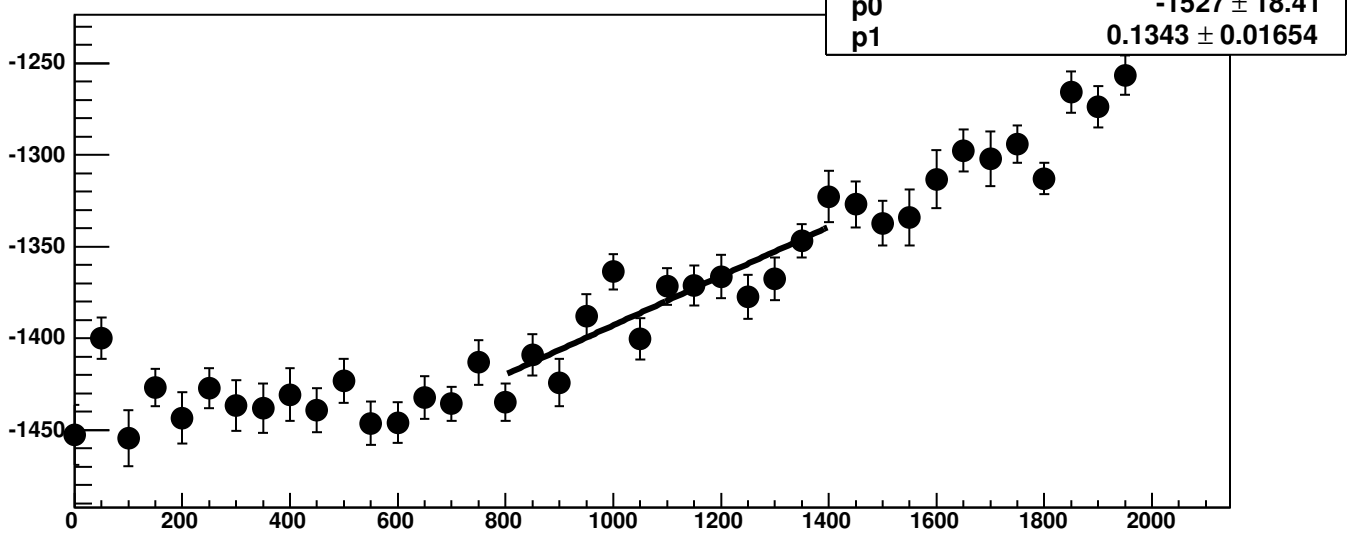



Chip 7, Channel 3, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 17.22 / 11
p0 -1520 ± 19.7
p1 0.08626 ± 0.01729
-1340
-1360
-1380
-1400
-1420
-1440
-1460
-1480
-1500
-1520
0
200
400
600
800
1000
1200
1400
1600
1800
2000

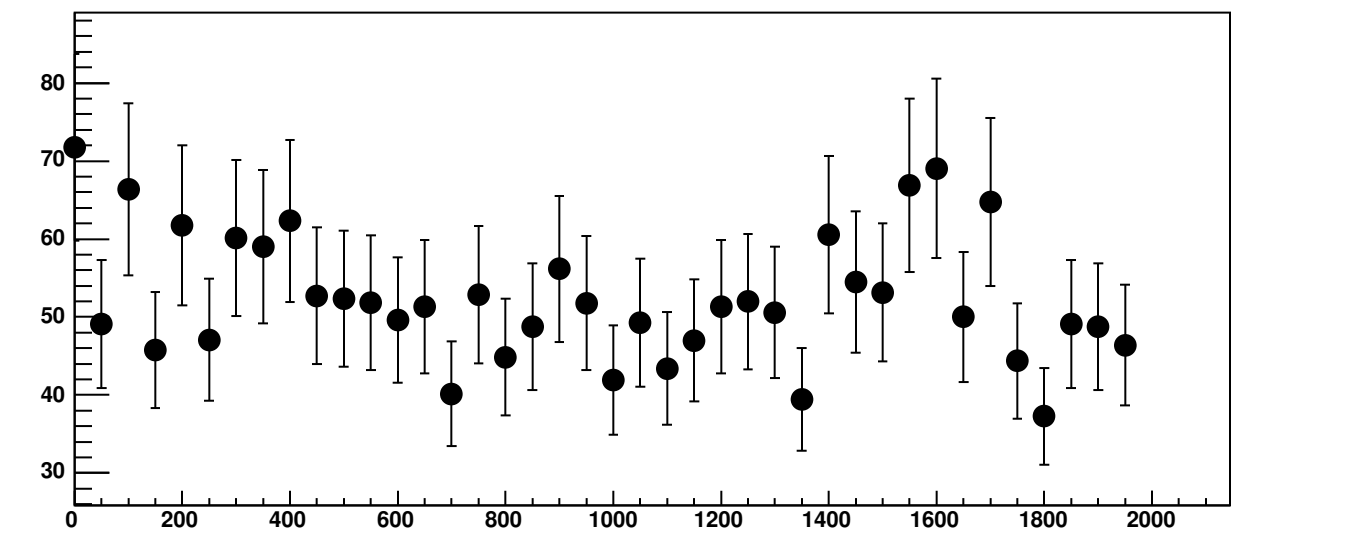

Chip 7, Channel 3, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 3, Enable 1, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

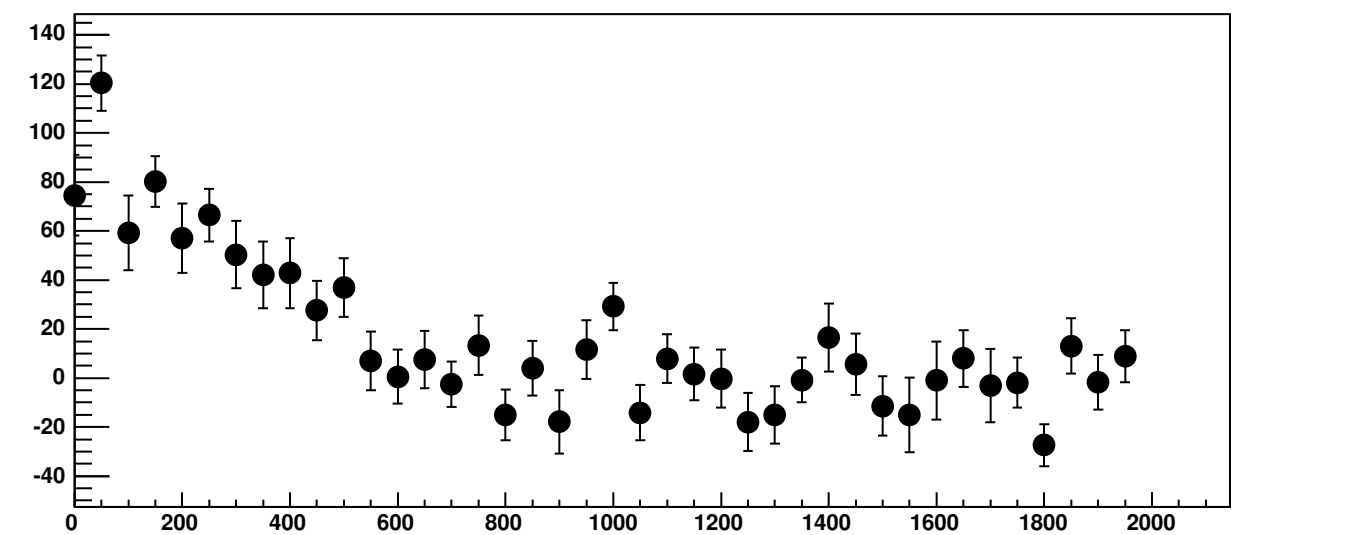
Chip 7, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC

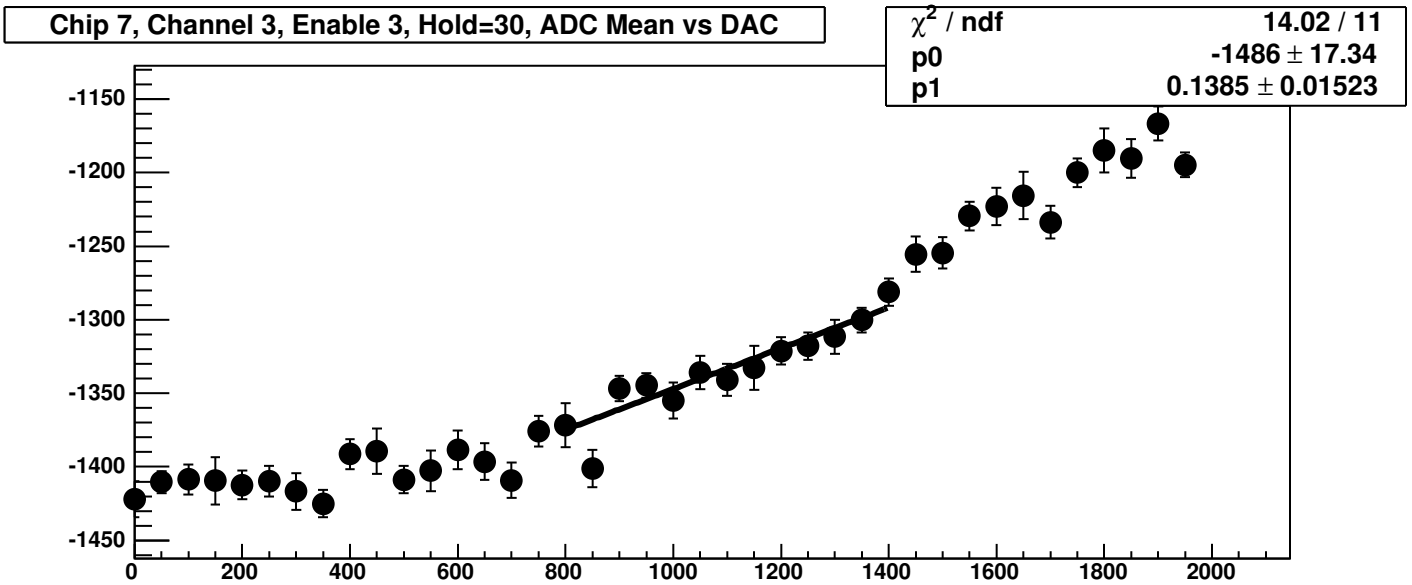


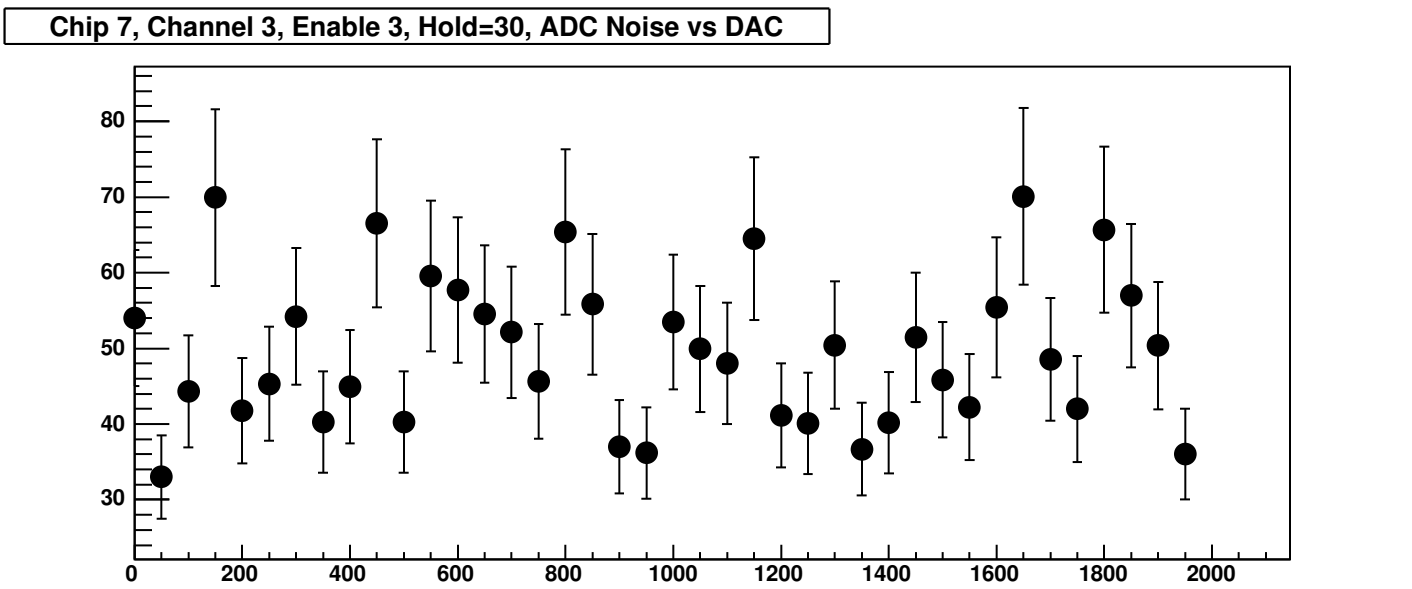
Chip 7, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC

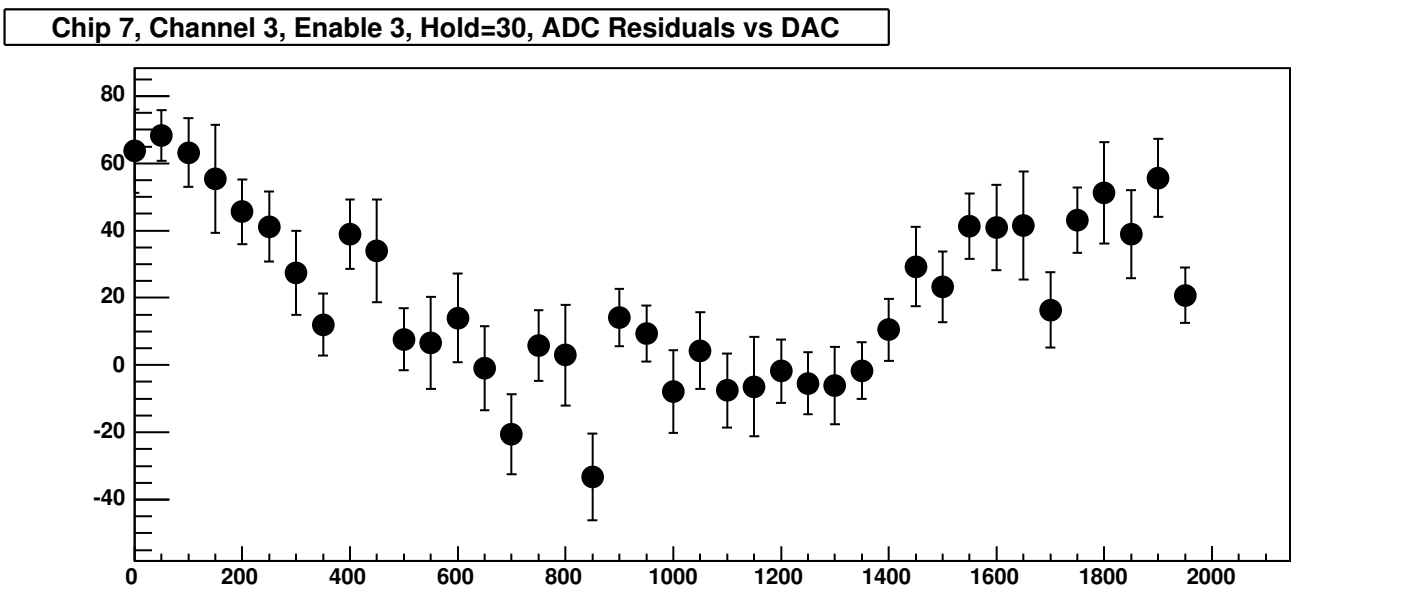


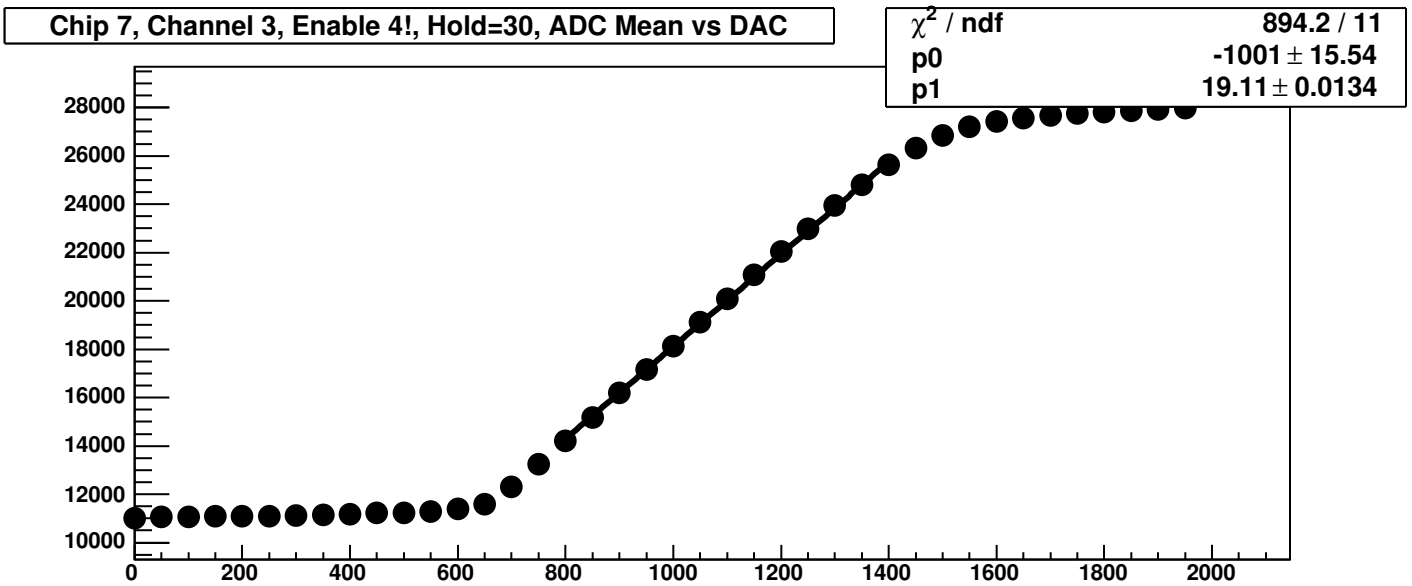
Chip 7, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC

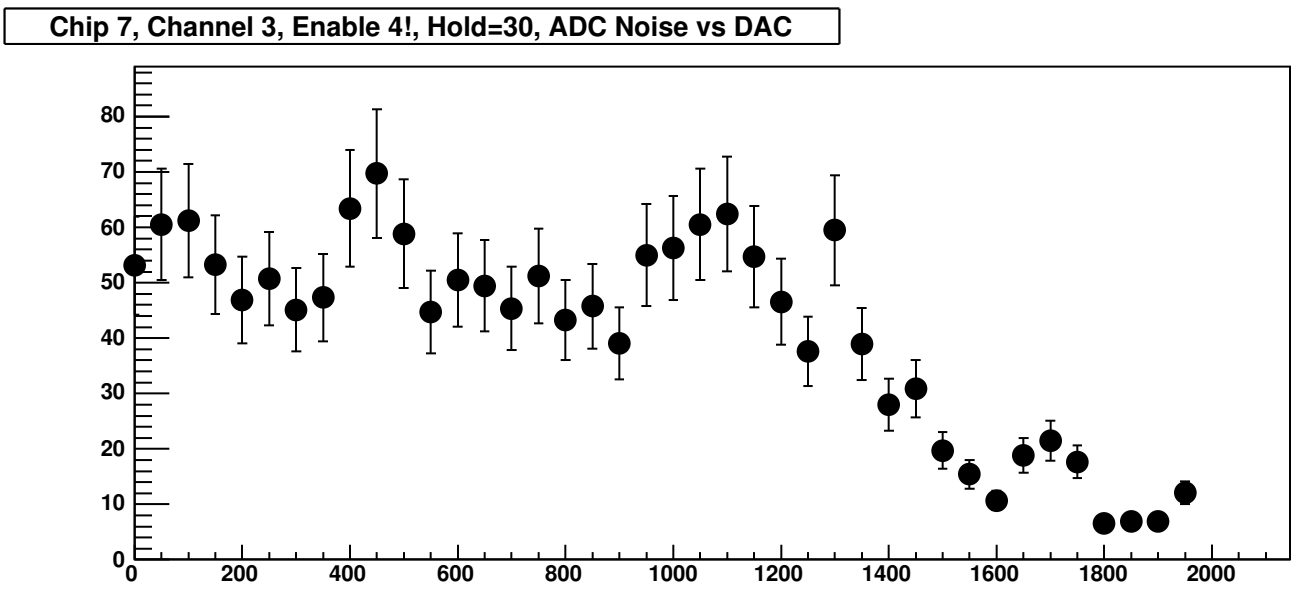


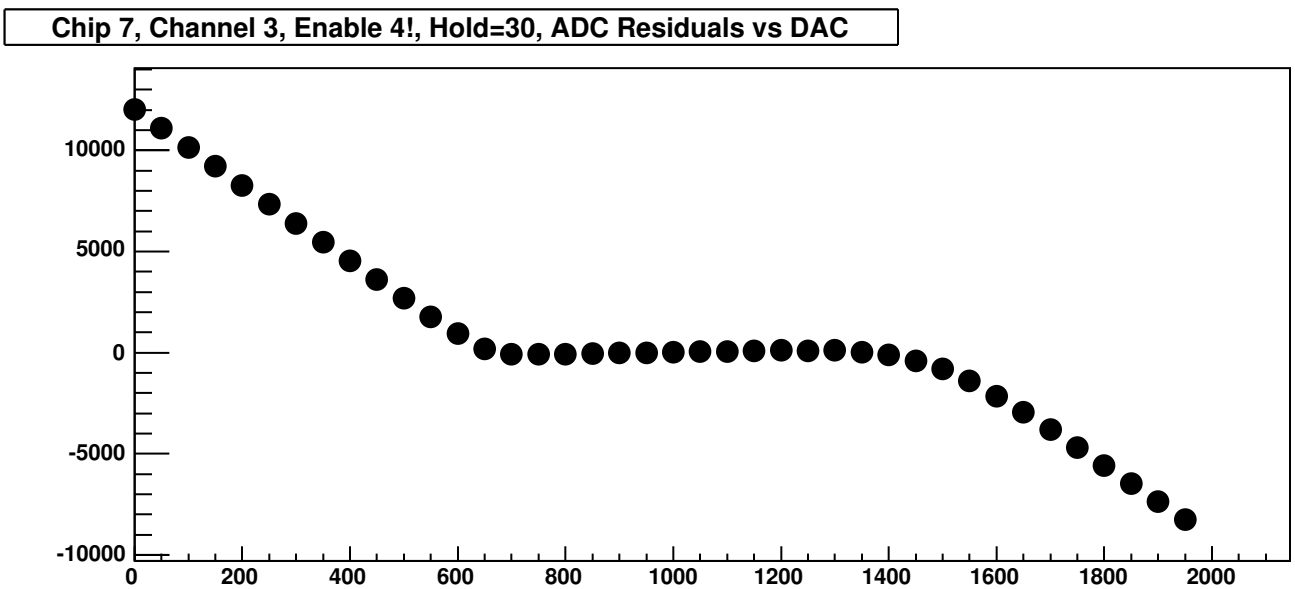

Chip 7, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 14.02 / 11
p0 -1486 ± 17.34
p1 0.1385 ± 0.01523
-1150
-1200
-1250
-1300
-1350
-1400
-1450
0
200
400
600
800
1000
1200
1400
1600
1800
2000

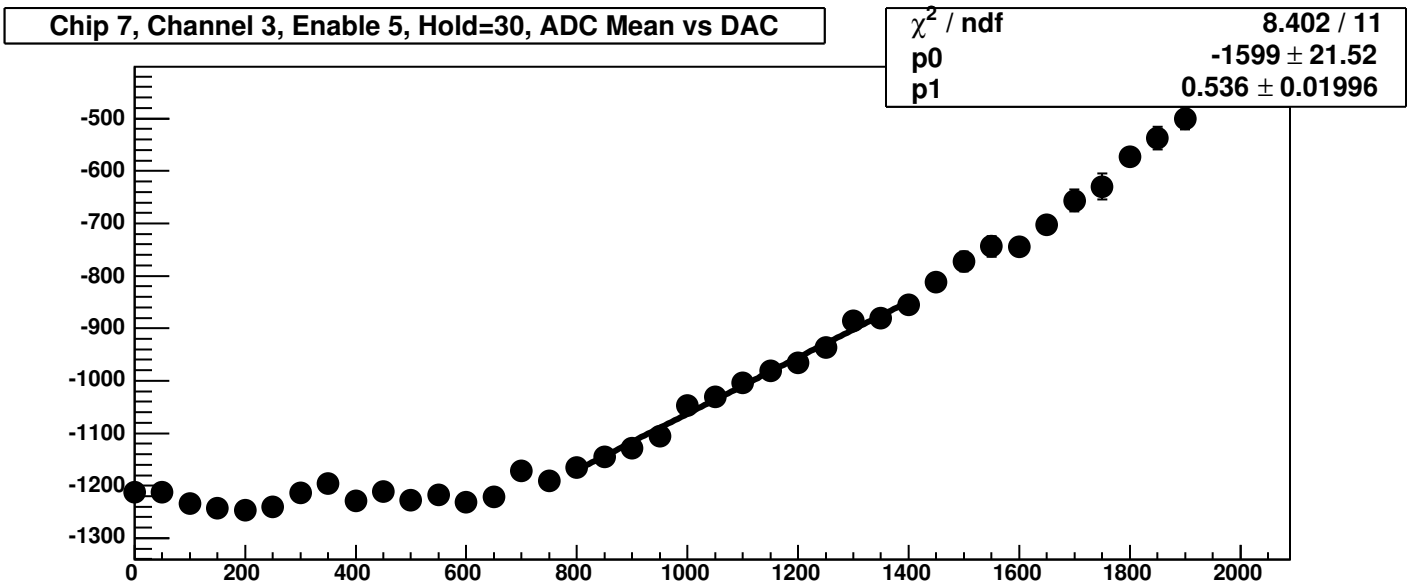

Chip 7, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

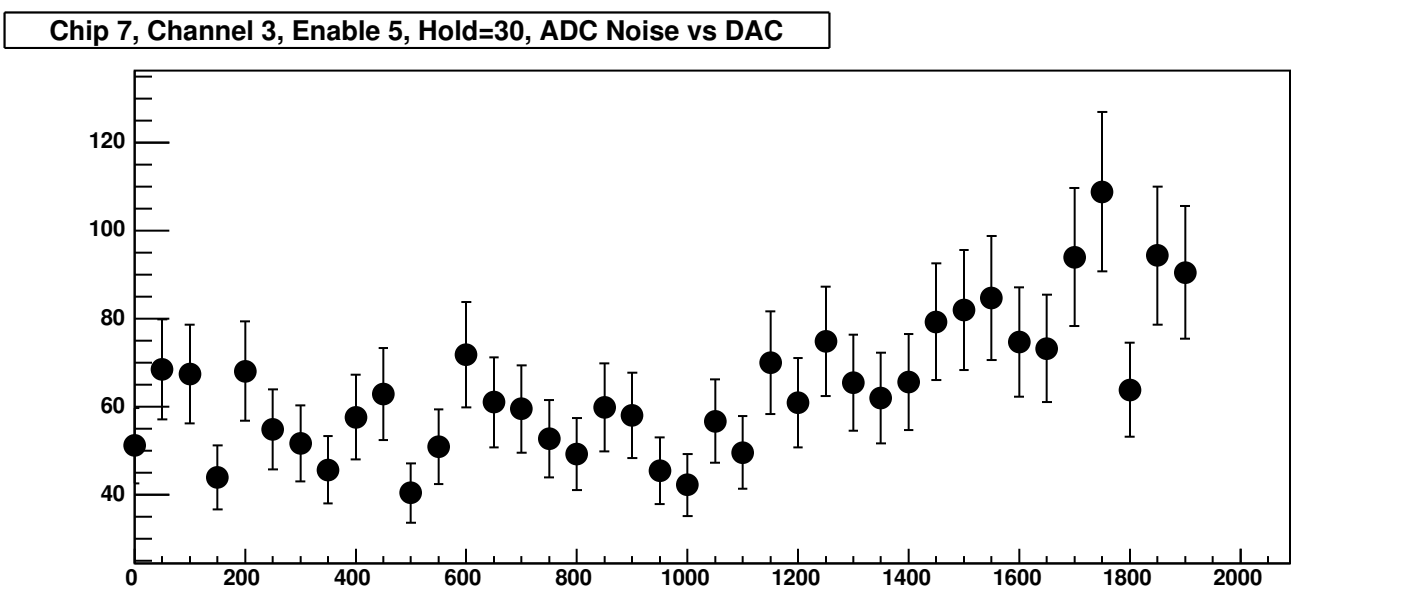

Chip 7, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

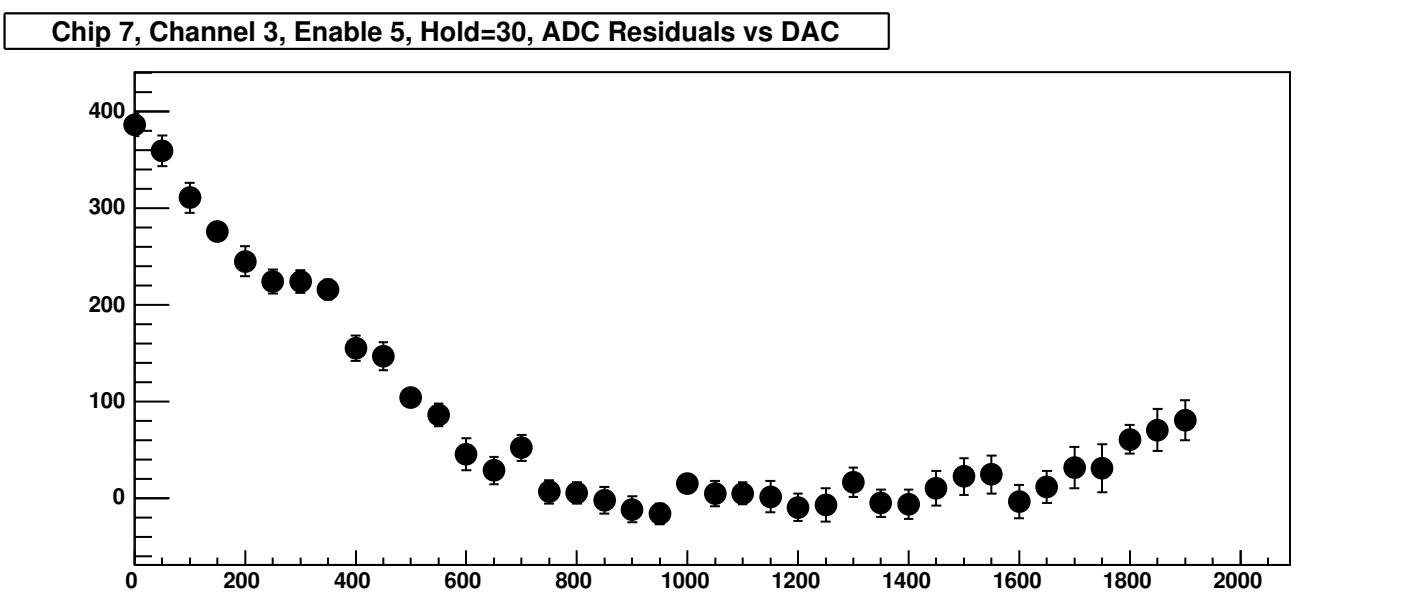

Chip 7, Channel 3, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 894.2 / 11
p0 -1001 ± 15.54
p1 19.11 ± 0.0134
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

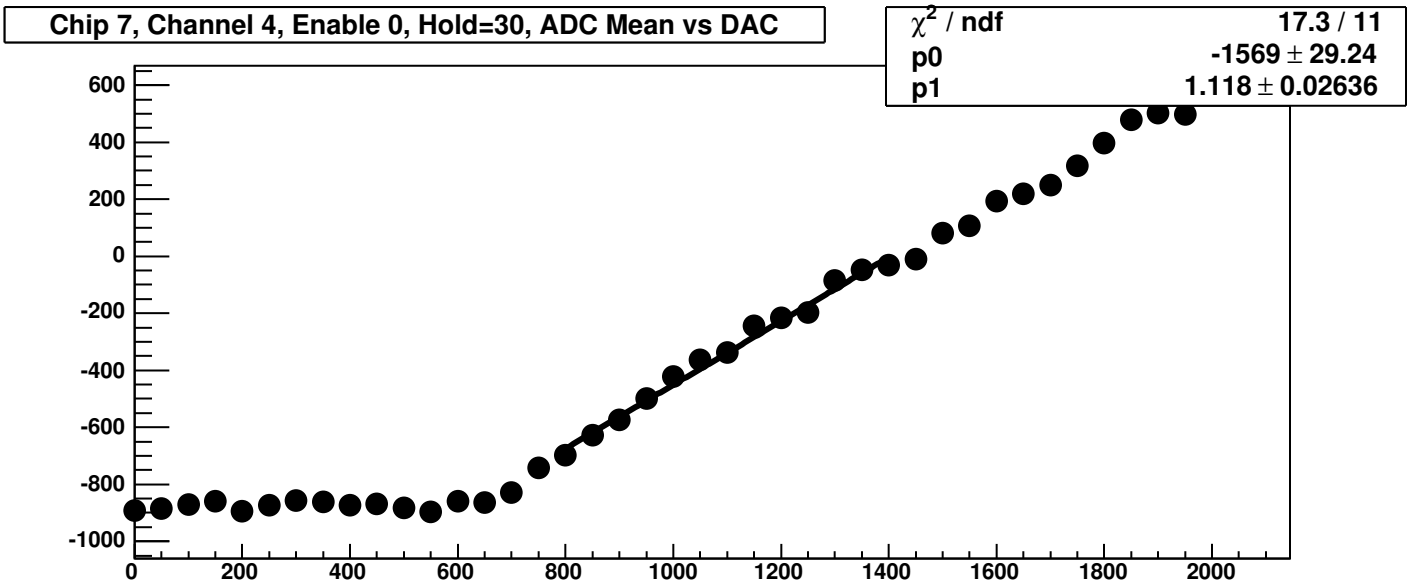

Chip 7, Channel 3, Enable 4!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

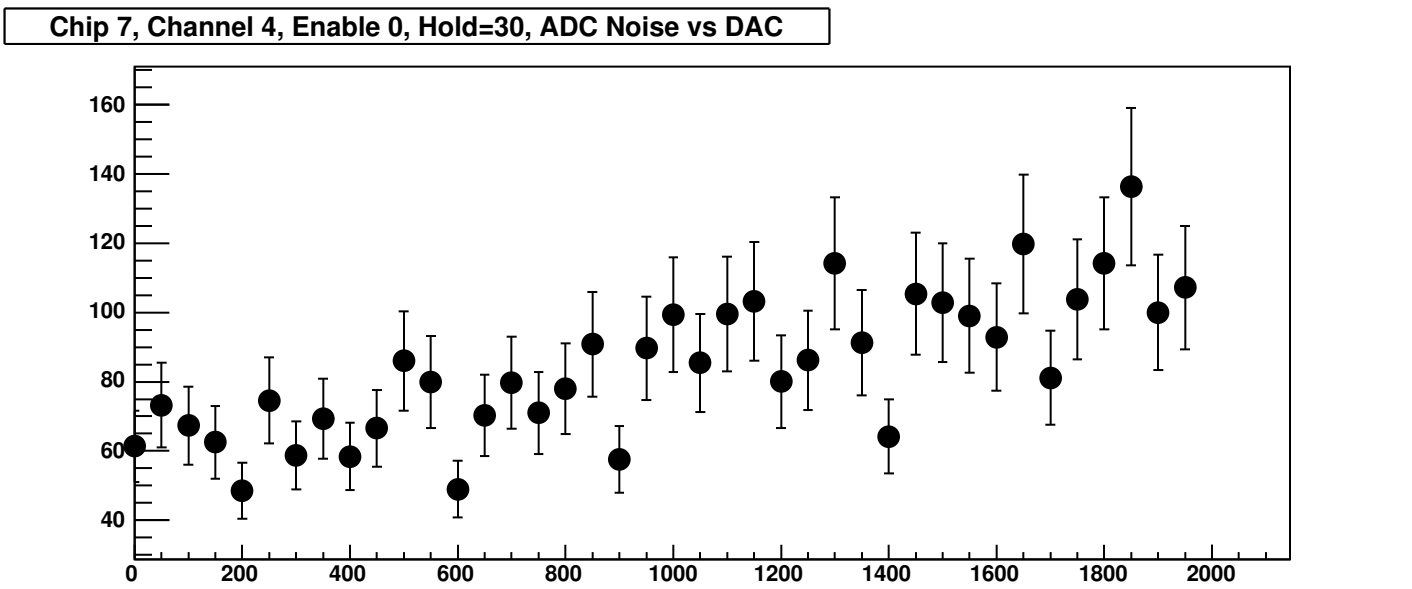

Chip 7, Channel 3, Enable 4!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

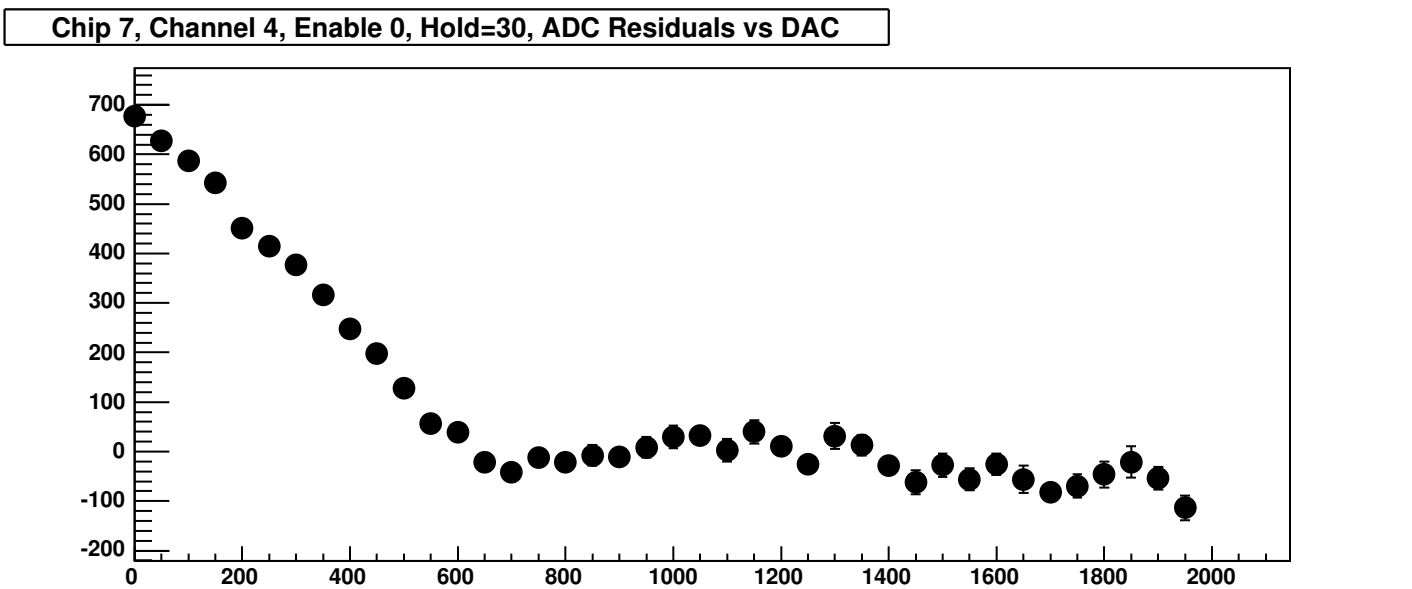

Chip 7, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8.402 / 11
p0 -1599 ± 21.52
p1 0.536 ± 0.01996
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

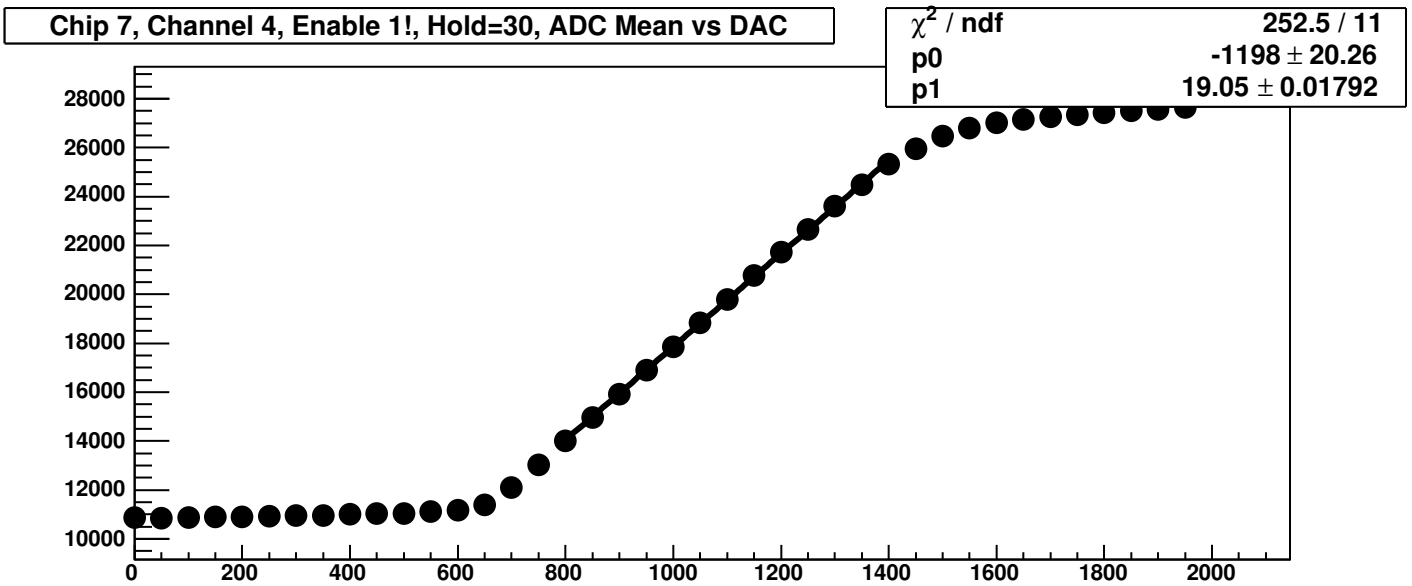

Chip 7, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

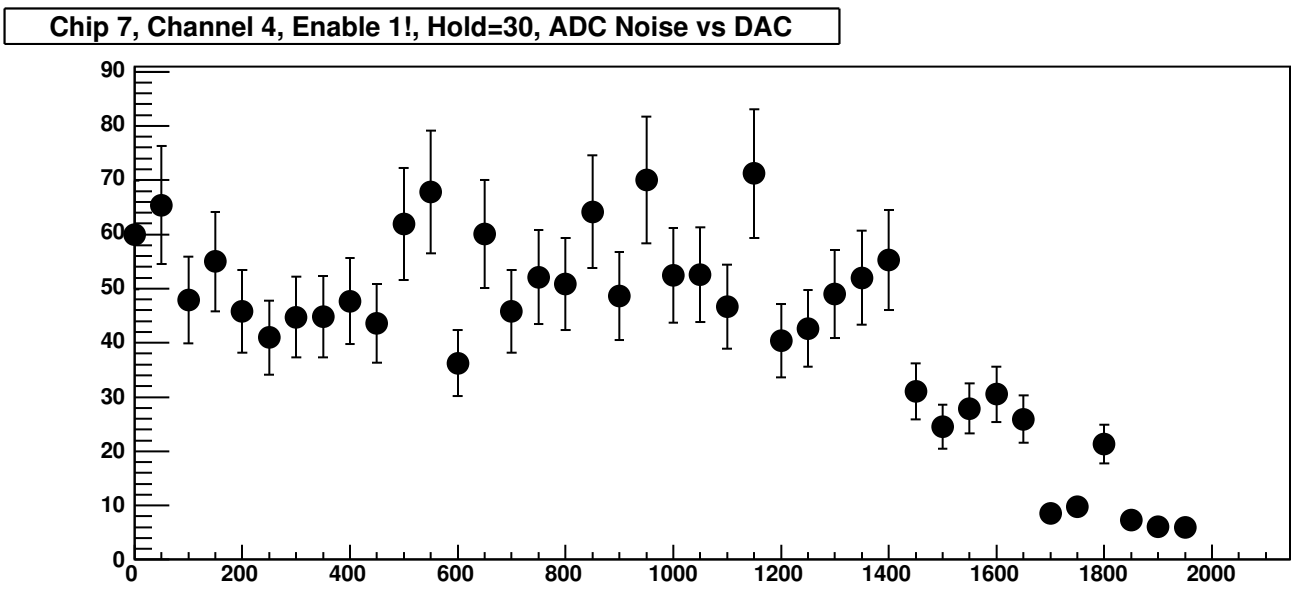

Chip 7, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
400
300
200
100
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

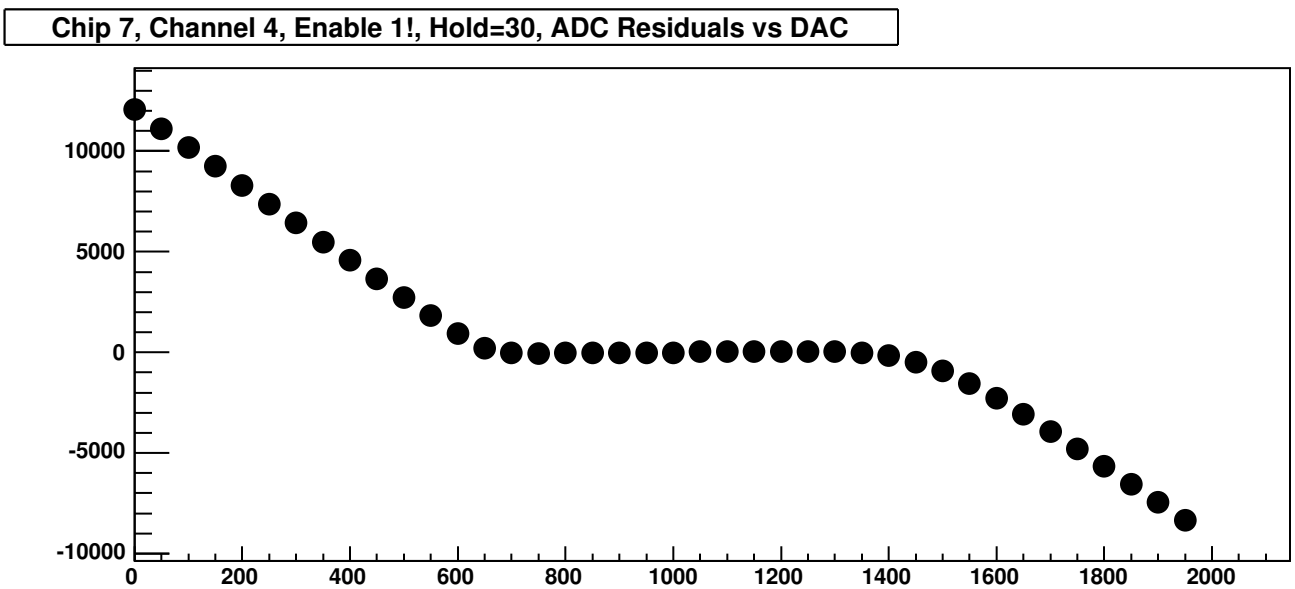

Chip 7, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 17.3 / 11
p0 -1569 ± 29.24
p1 1.118 ± 0.02636
600
400
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

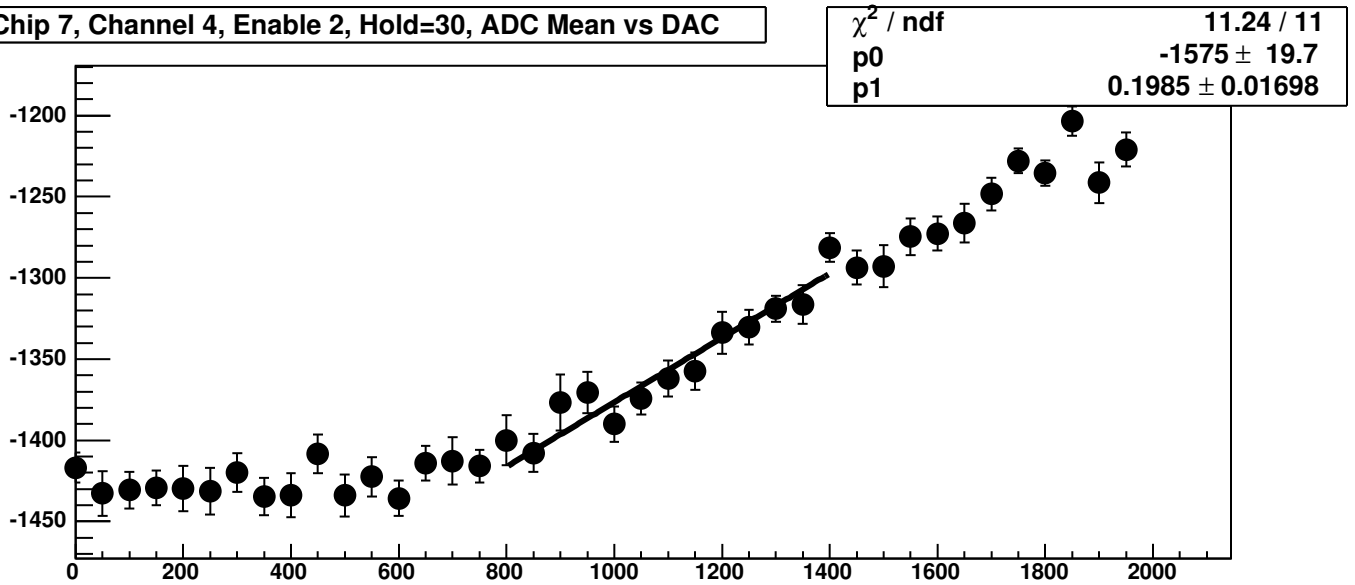

Chip 7, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC
700
600
500
400
300
200
100
0
-100
-200
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 4, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 252.5 / 11
p0 -1198 ± 20.26
p1 19.05 ± 0.01792
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

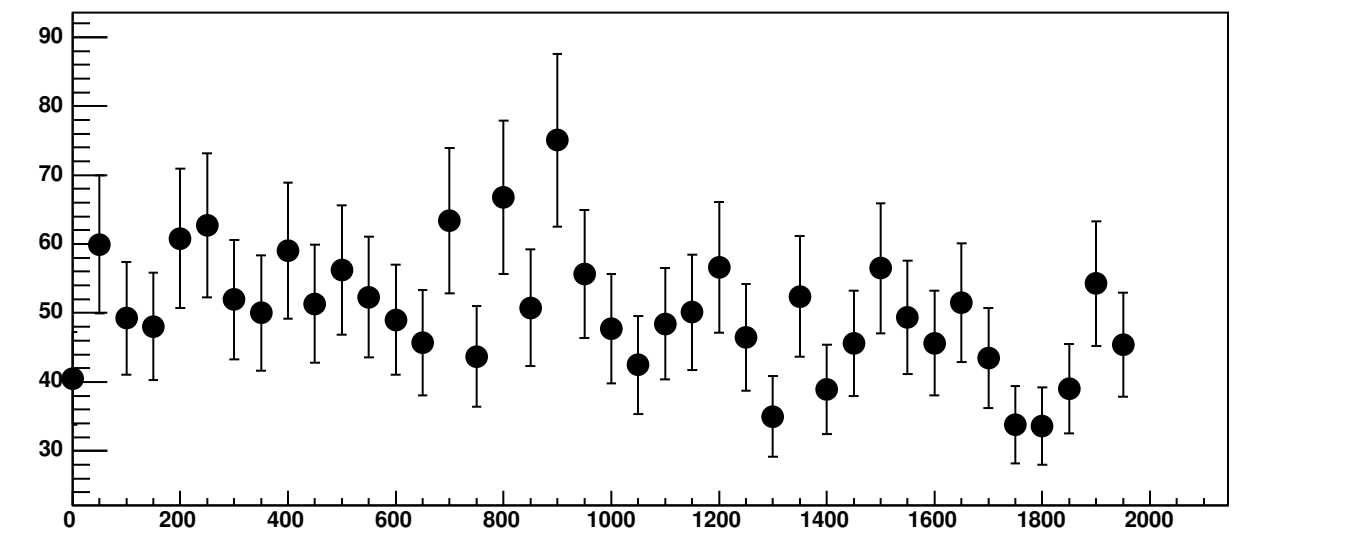

Chip 7, Channel 4, Enable 1!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 4, Enable 1!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

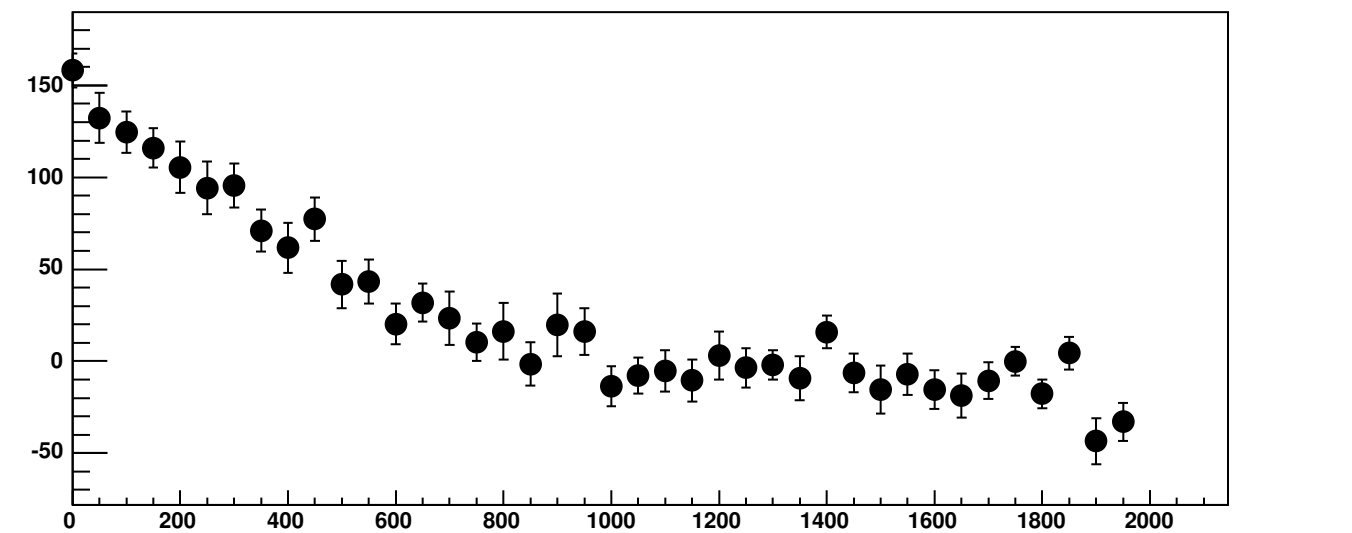
Chip 7, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC



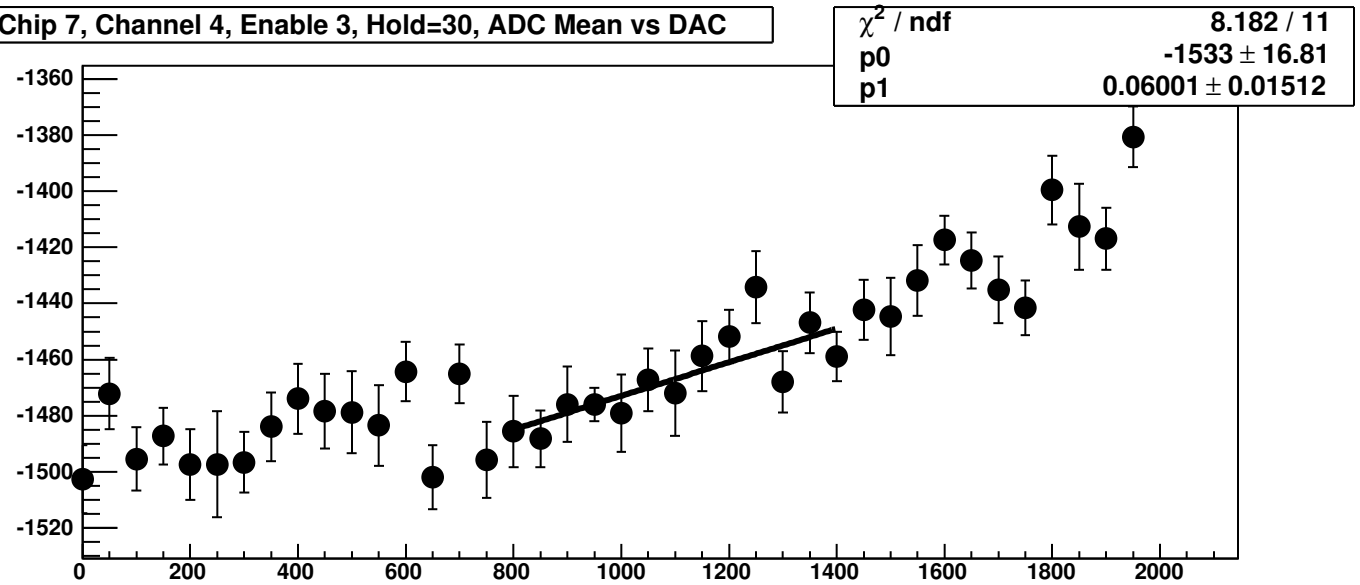
Chip 7, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC



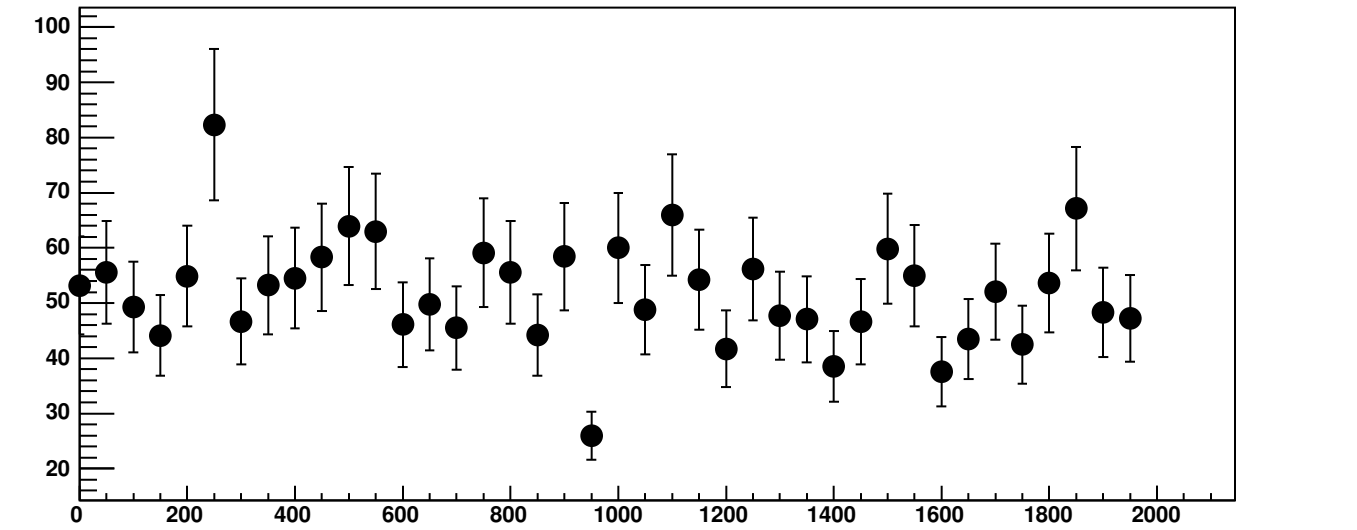
Chip 7, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC



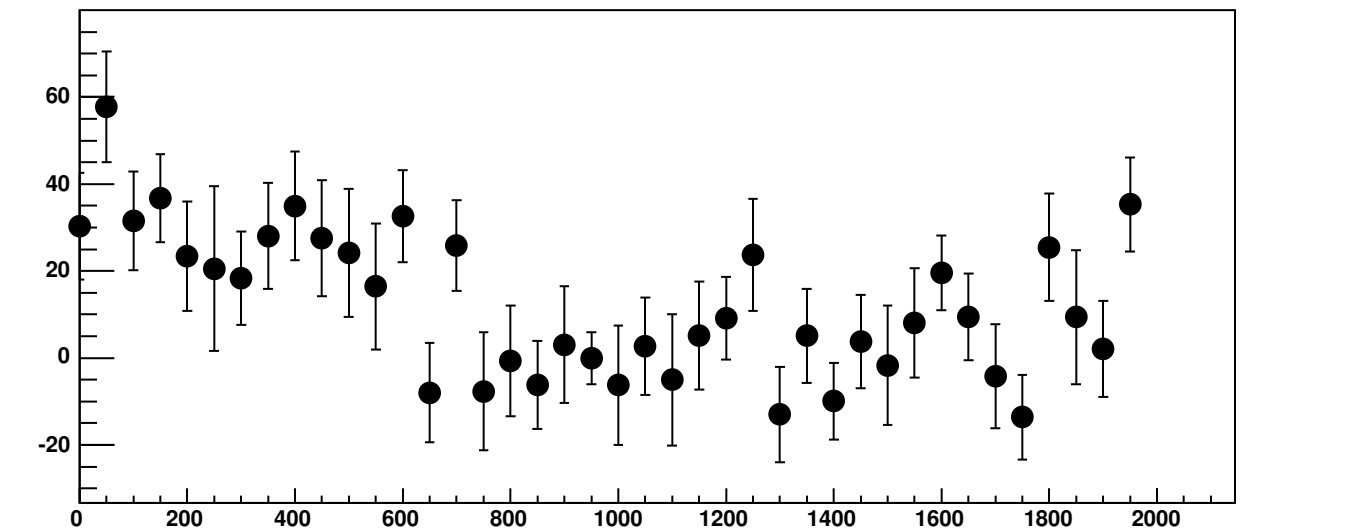
Chip 7, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC

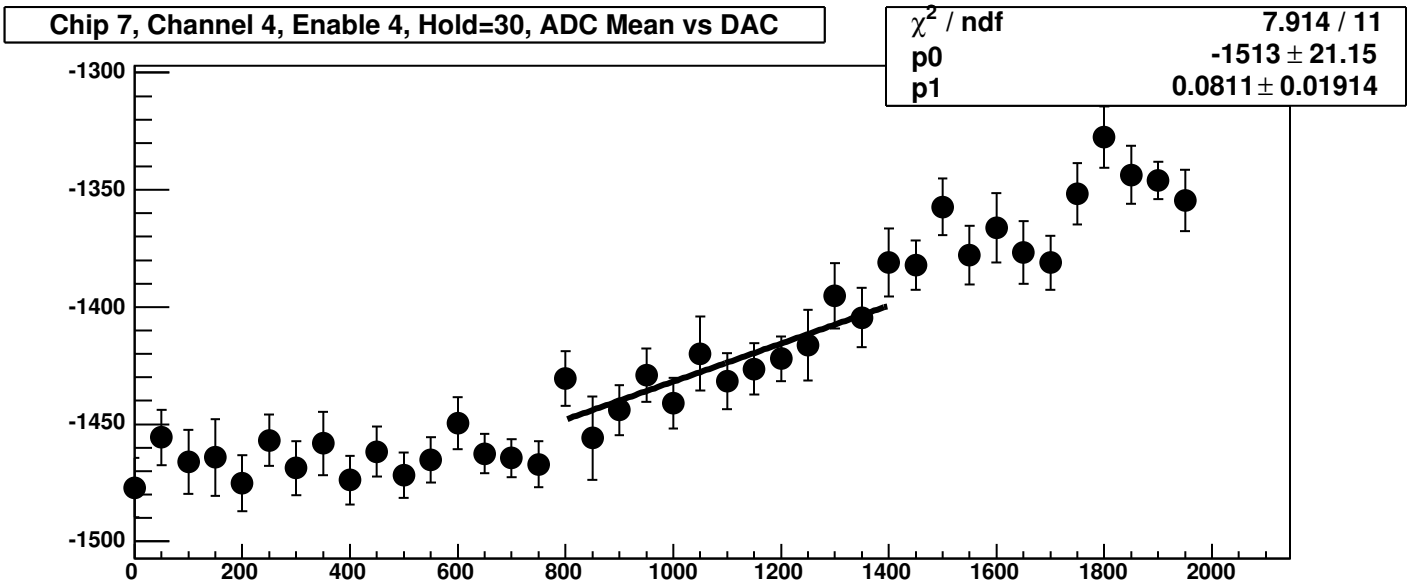


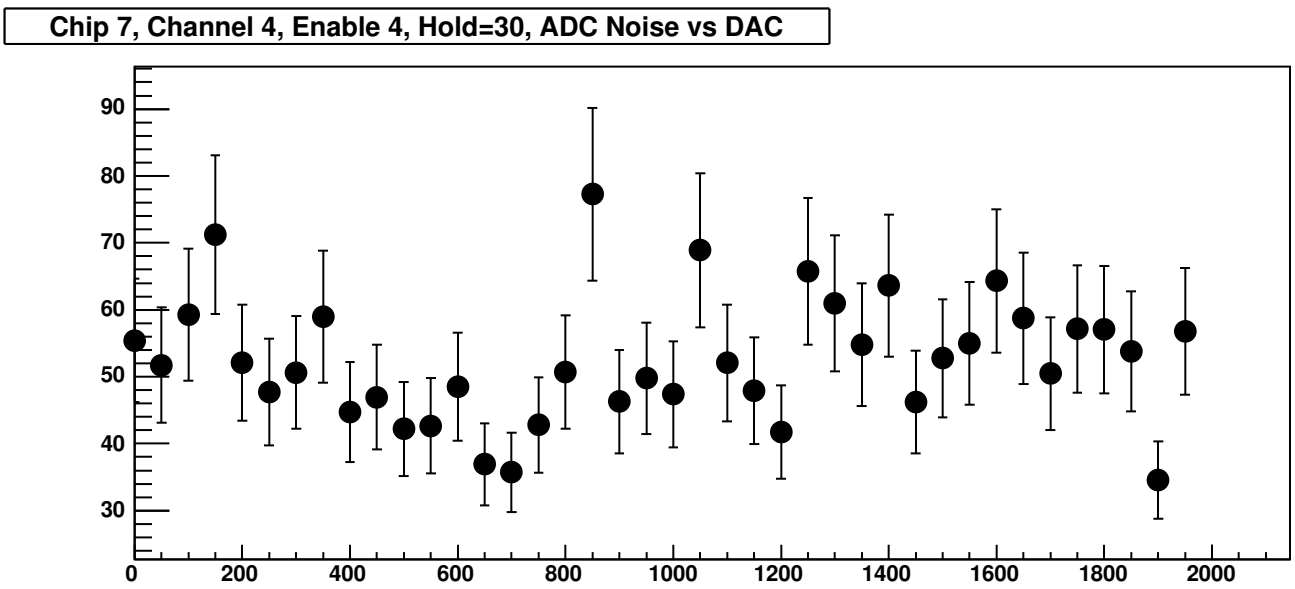
Chip 7, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC

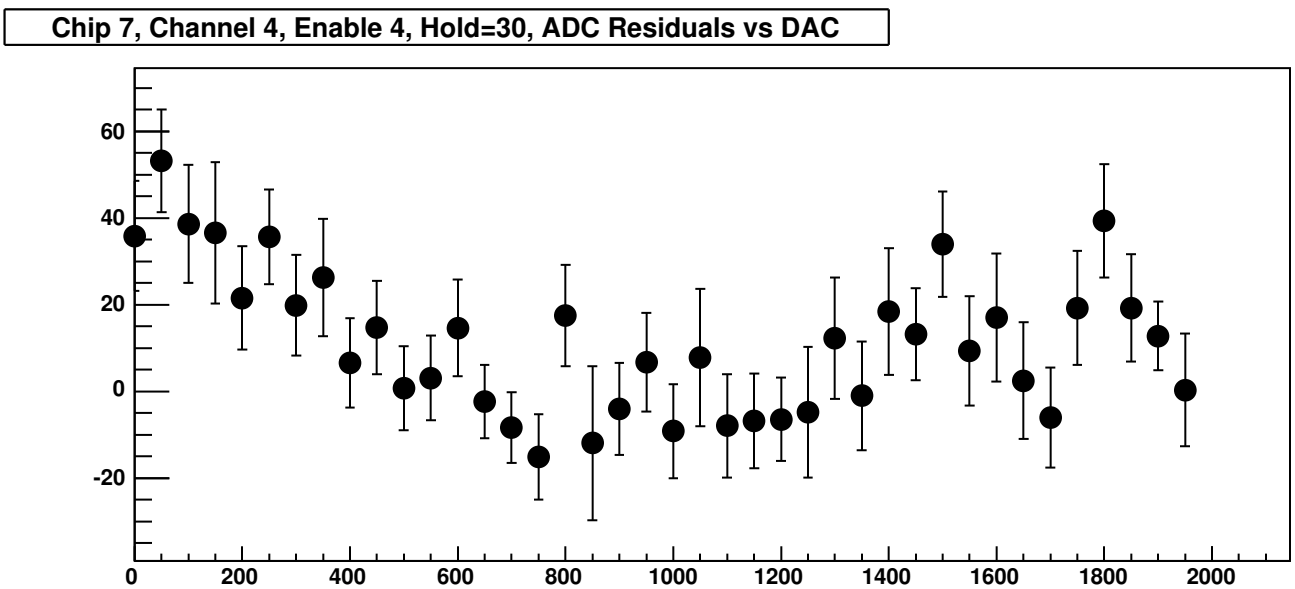


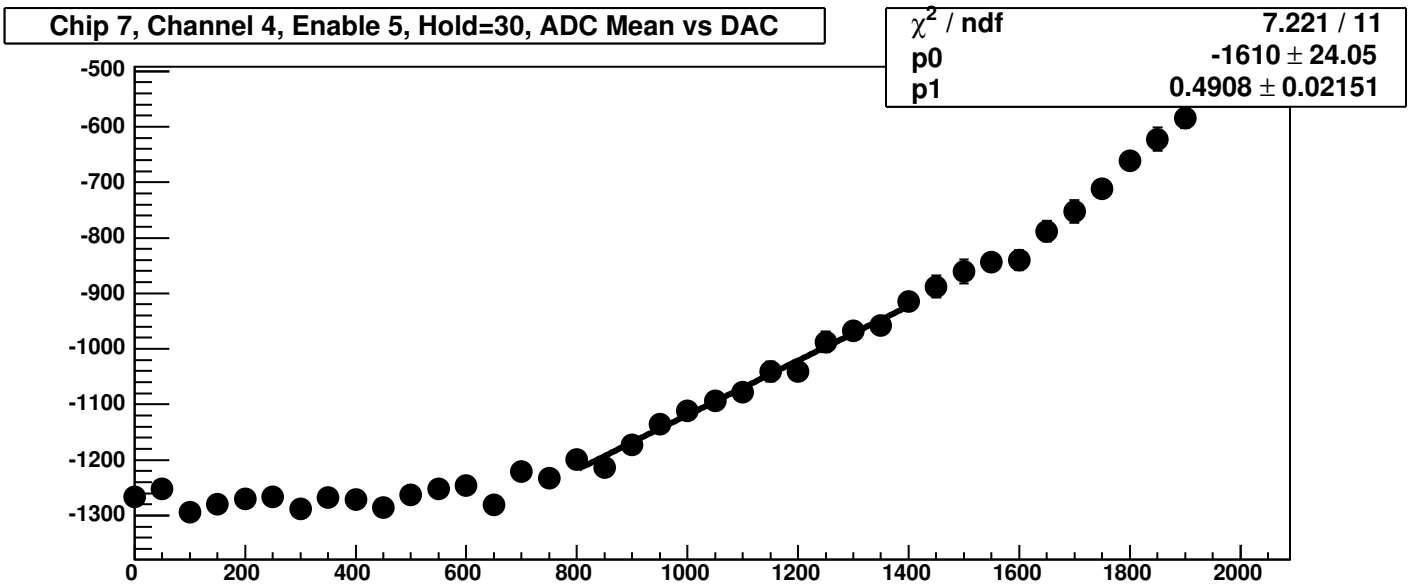
Chip 7, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC

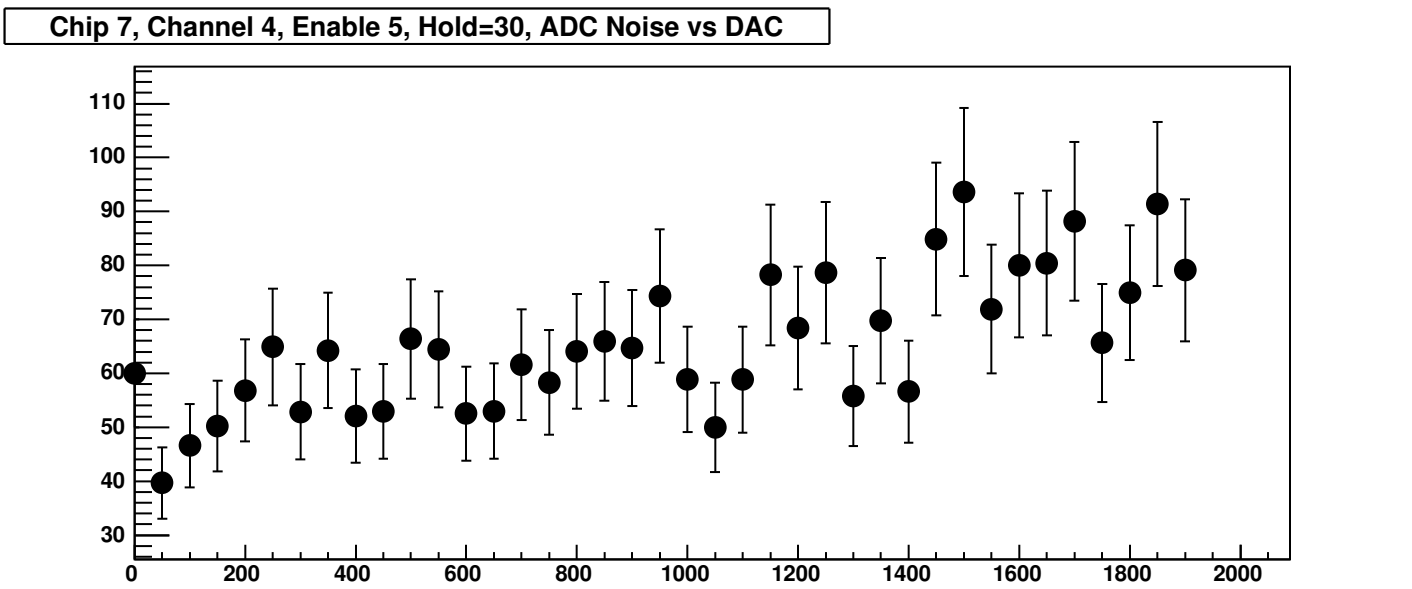


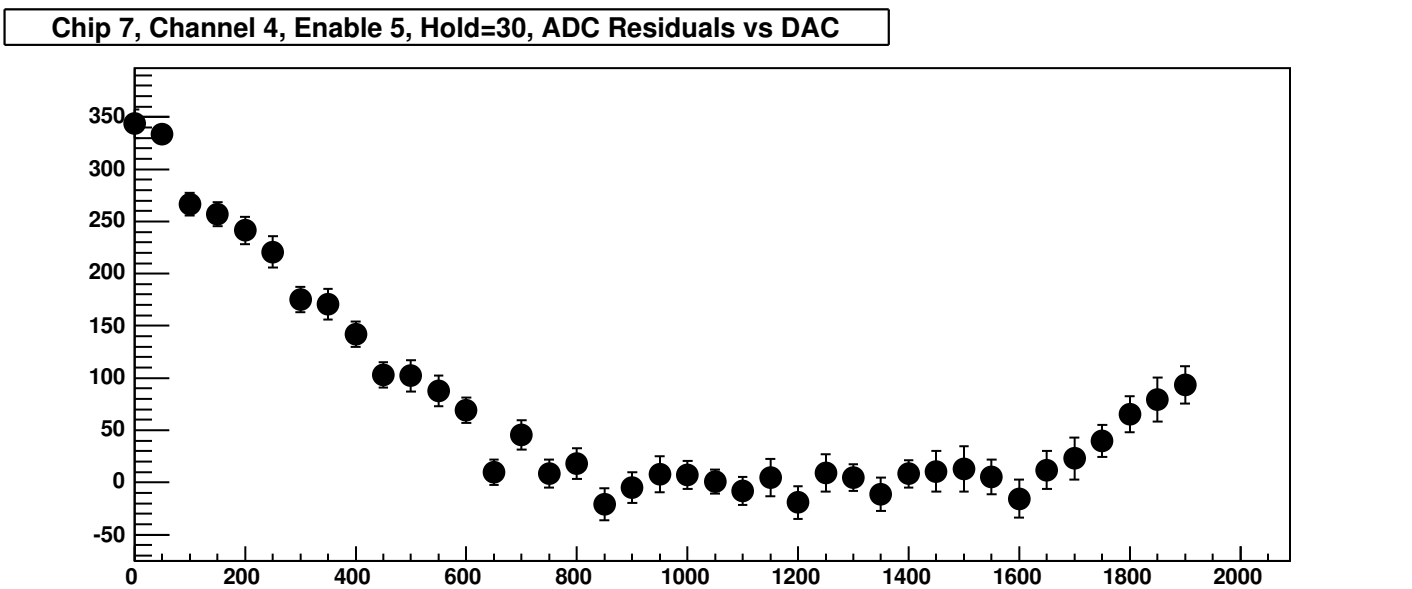

Chip 7, Channel 4, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7.914 / 11
p0 -1513 ± 21.15
p1 0.0811 ± 0.01914
-1300
-1350
-1400
-1450
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

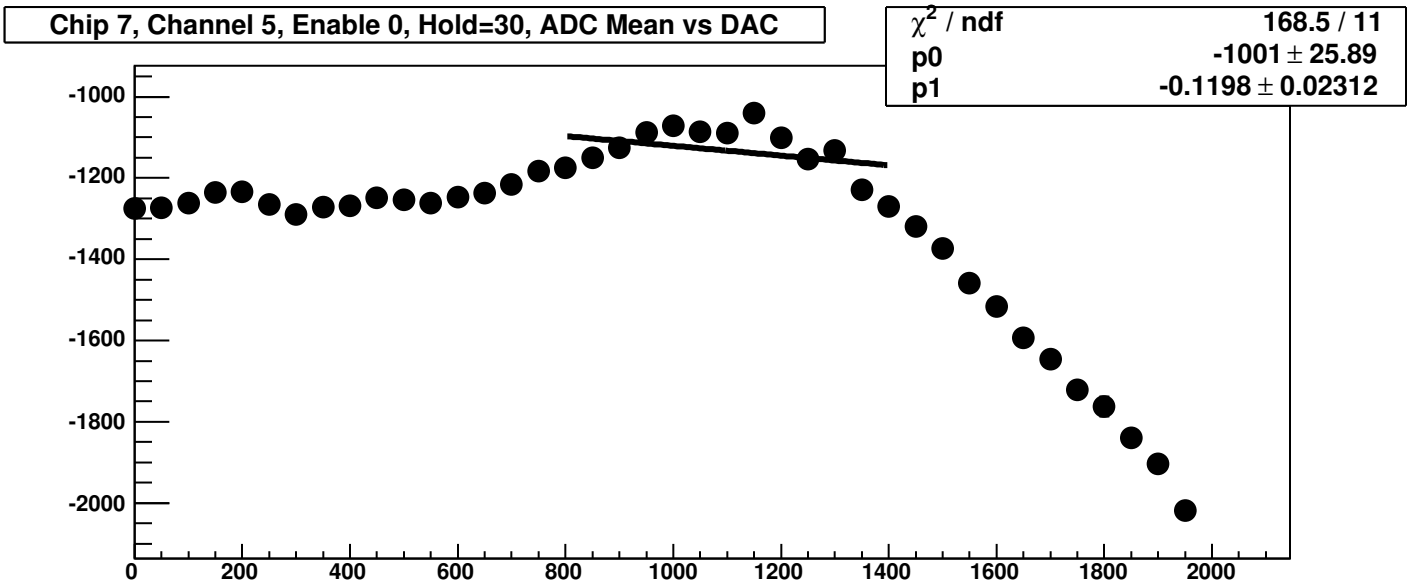

Chip 7, Channel 4, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

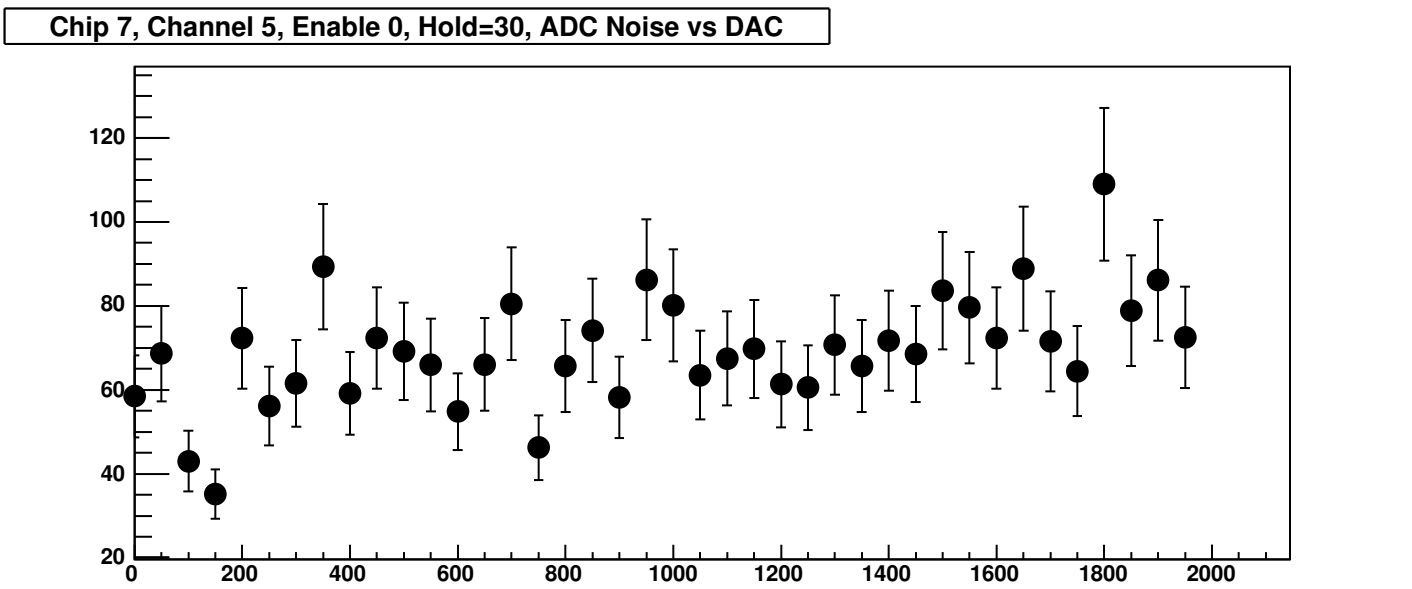

Chip 7, Channel 4, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

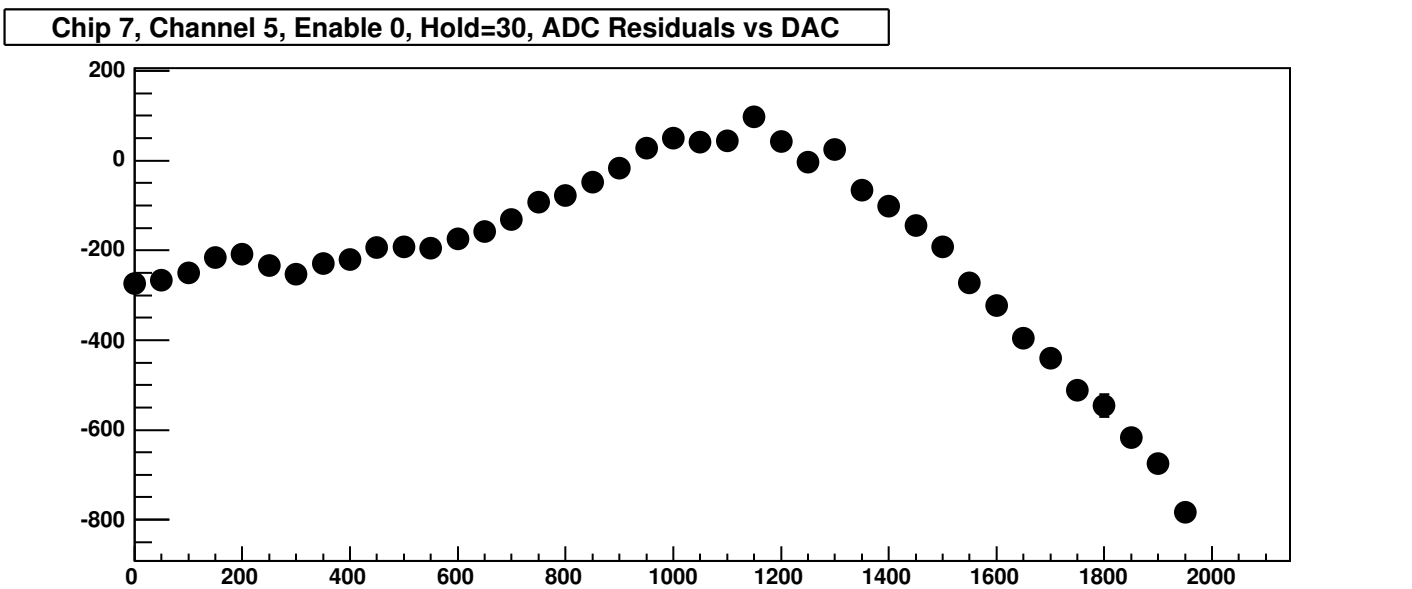

Chip 7, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7.221 / 11
p0 -1610 ± 24.05
p1 0.4908 ± 0.02151
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

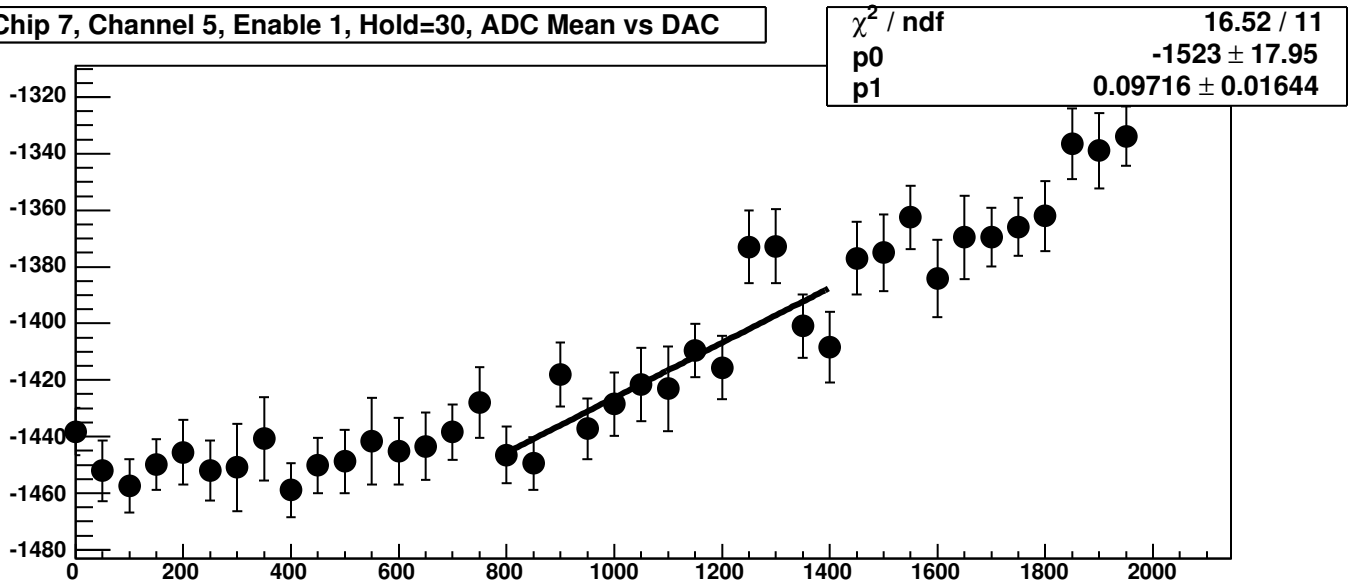

Chip 7, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC
χ^2 / ndf 168.5 / 11
p0 -1001 ± 25.89
p1 -0.1198 ± 0.02312
-1000
-1200
-1400
-1600
-1800
-2000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

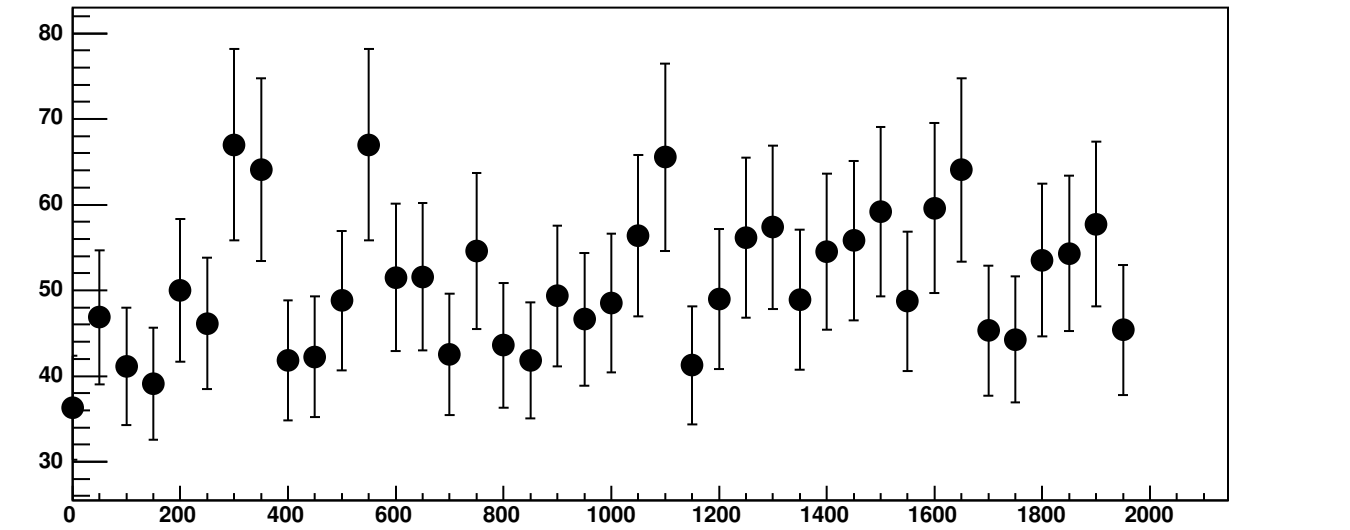

Chip 7, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

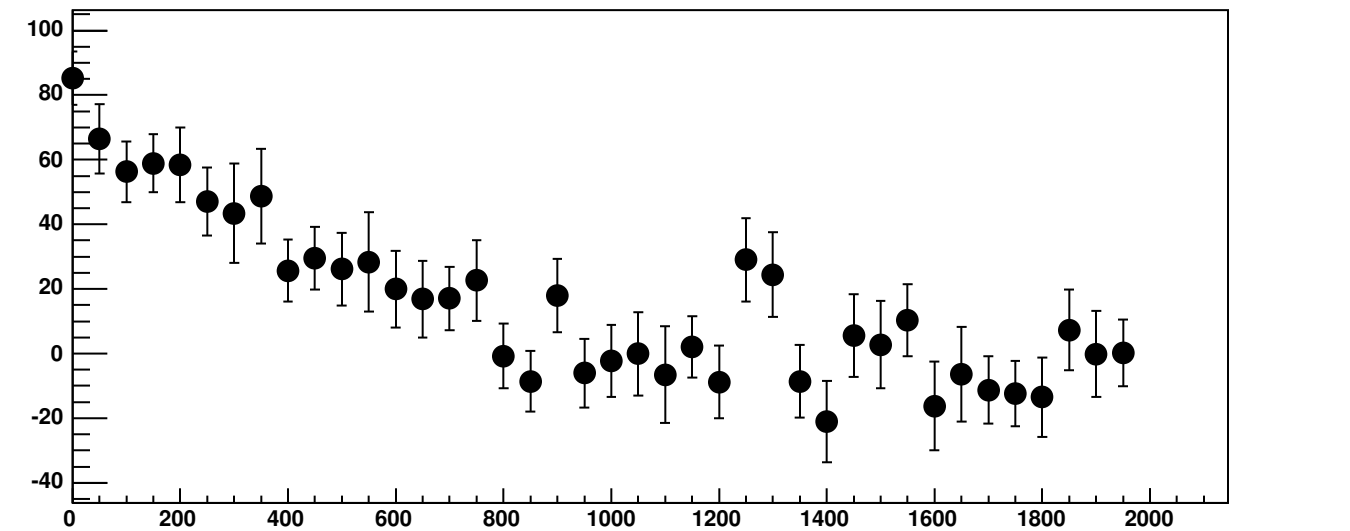
Chip 7, Channel 5, Enable 1, Hold=30, ADC Mean vs DAC



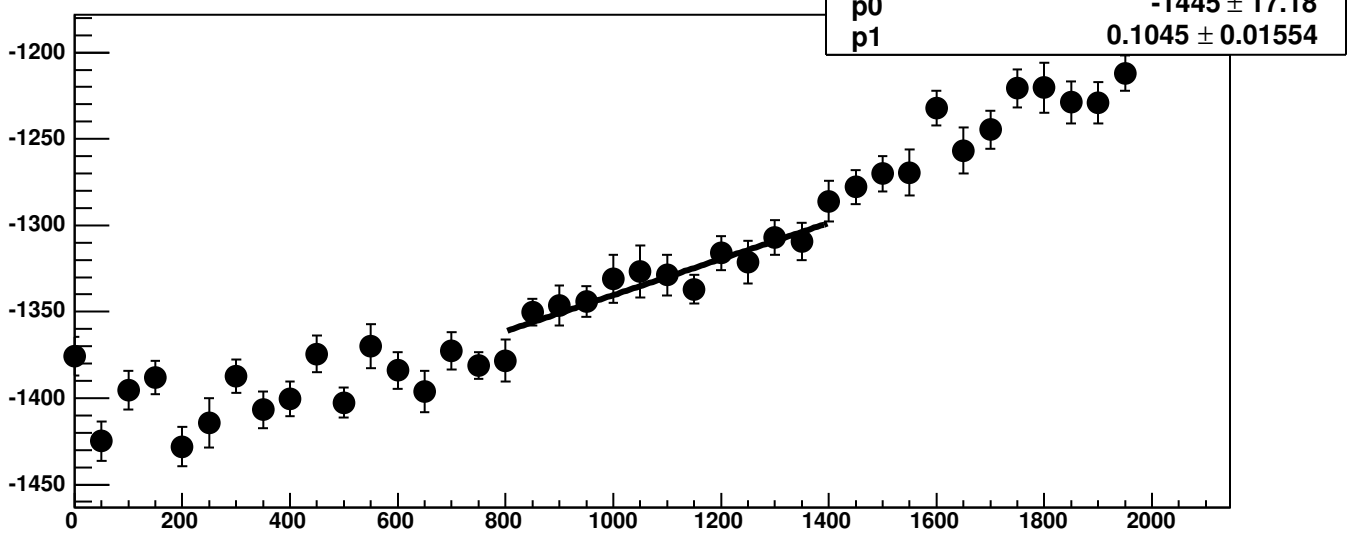
Chip 7, Channel 5, Enable 1, Hold=30, ADC Noise vs DAC



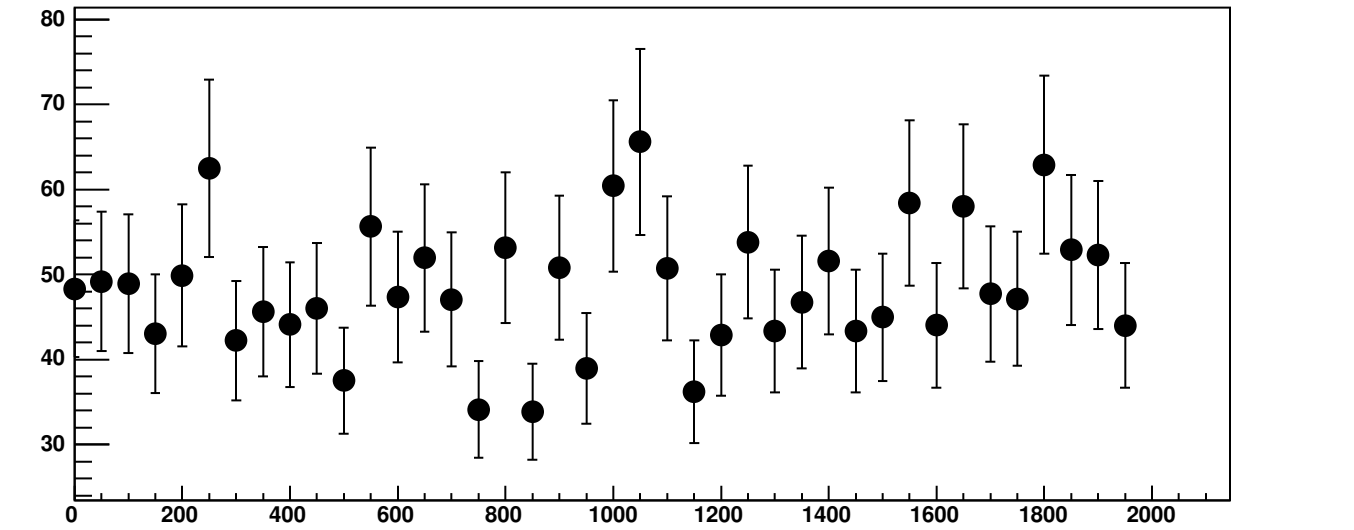
Chip 7, Channel 5, Enable 1, Hold=30, ADC Residuals vs DAC



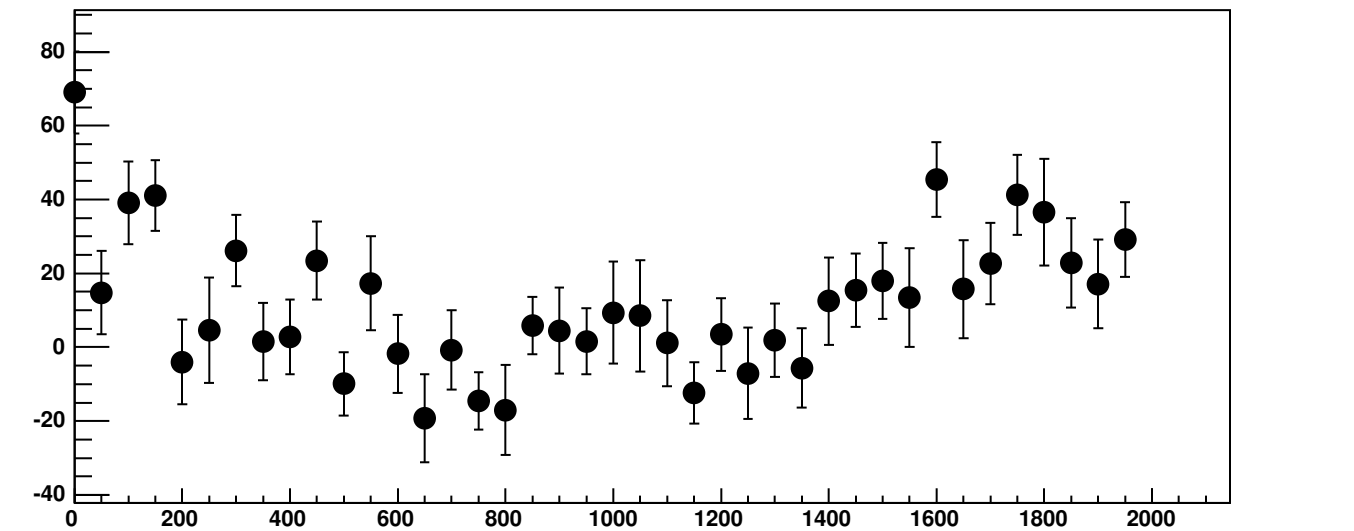
Chip 7, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC



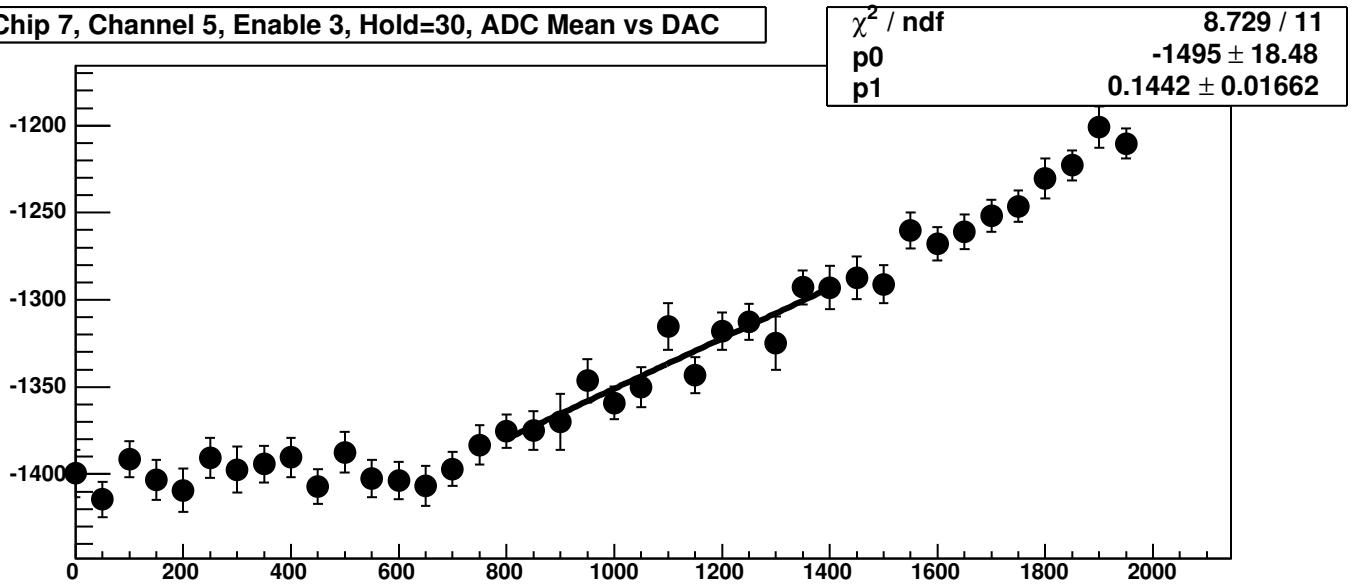
Chip 7, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC



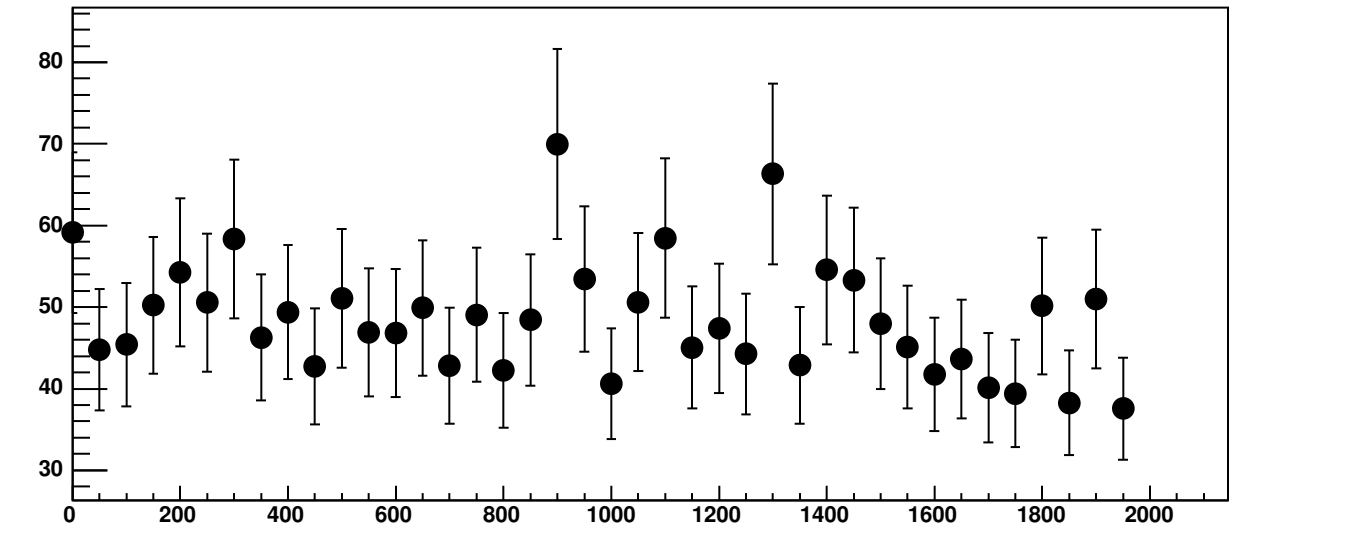
Chip 7, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC



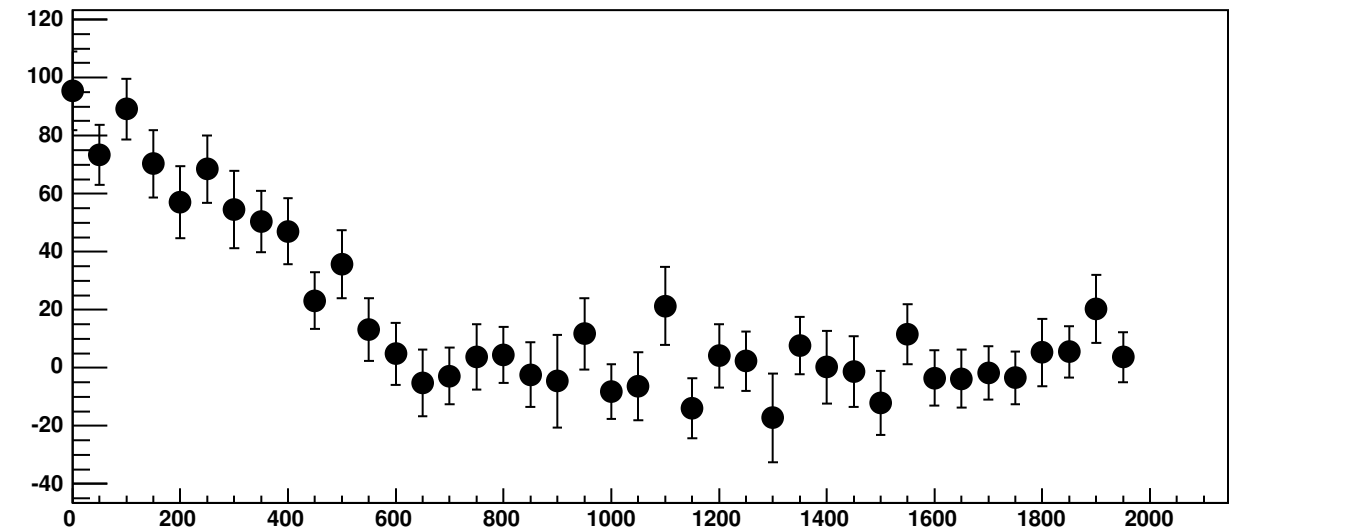
Chip 7, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC



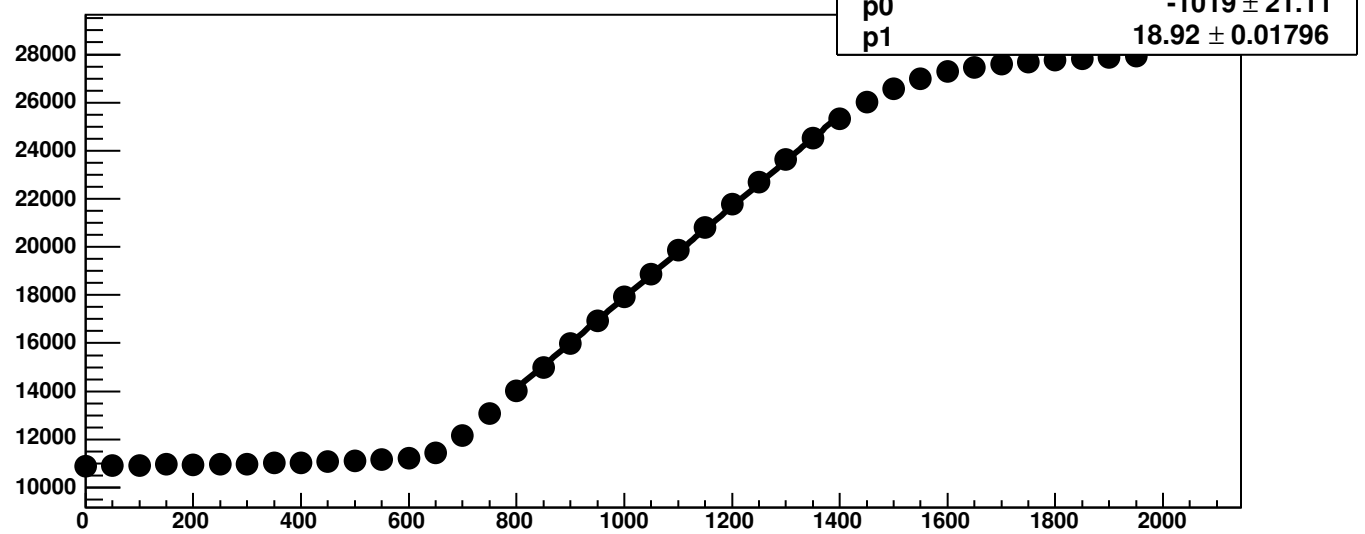
Chip 7, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC



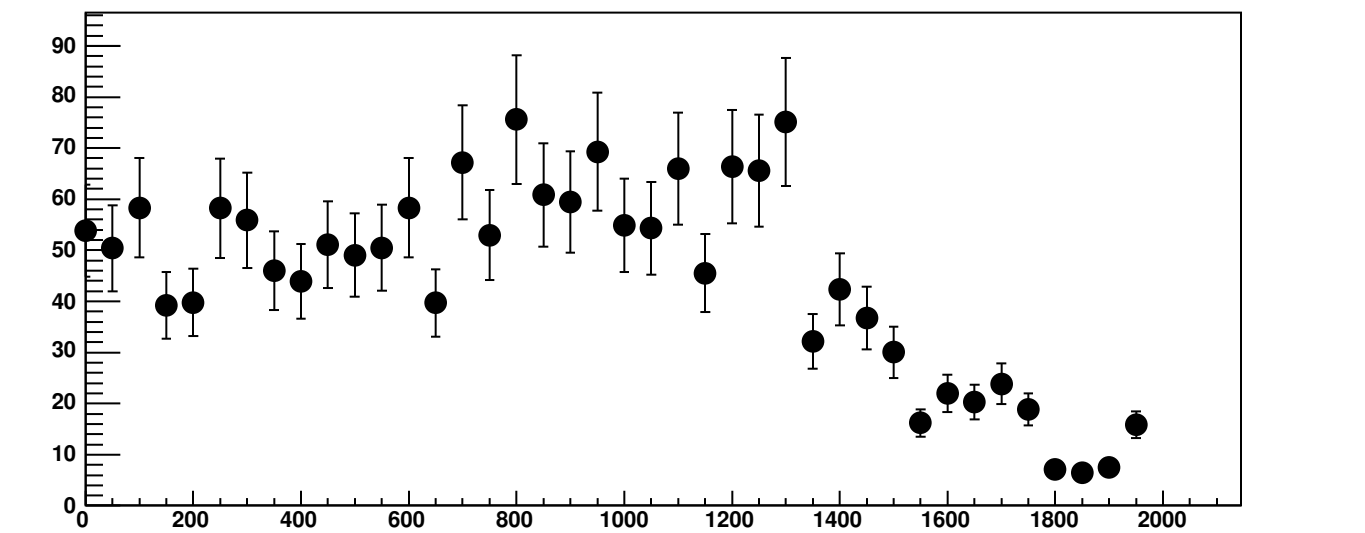
Chip 7, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC



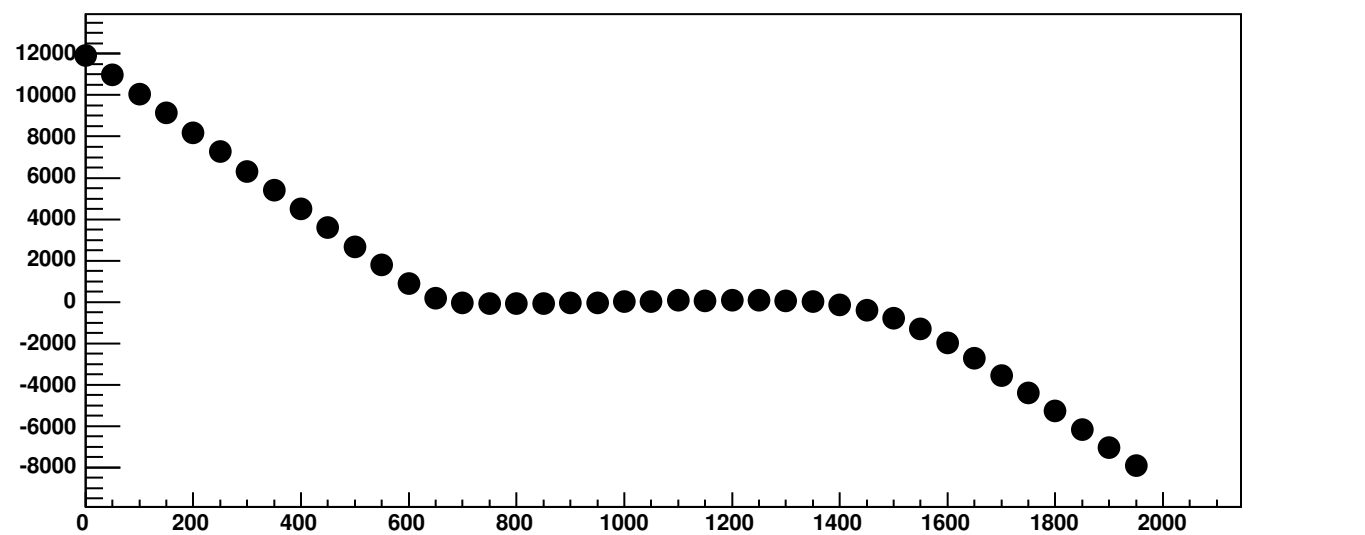
Chip 7, Channel 5, Enable 4!, Hold=30, ADC Mean vs DAC

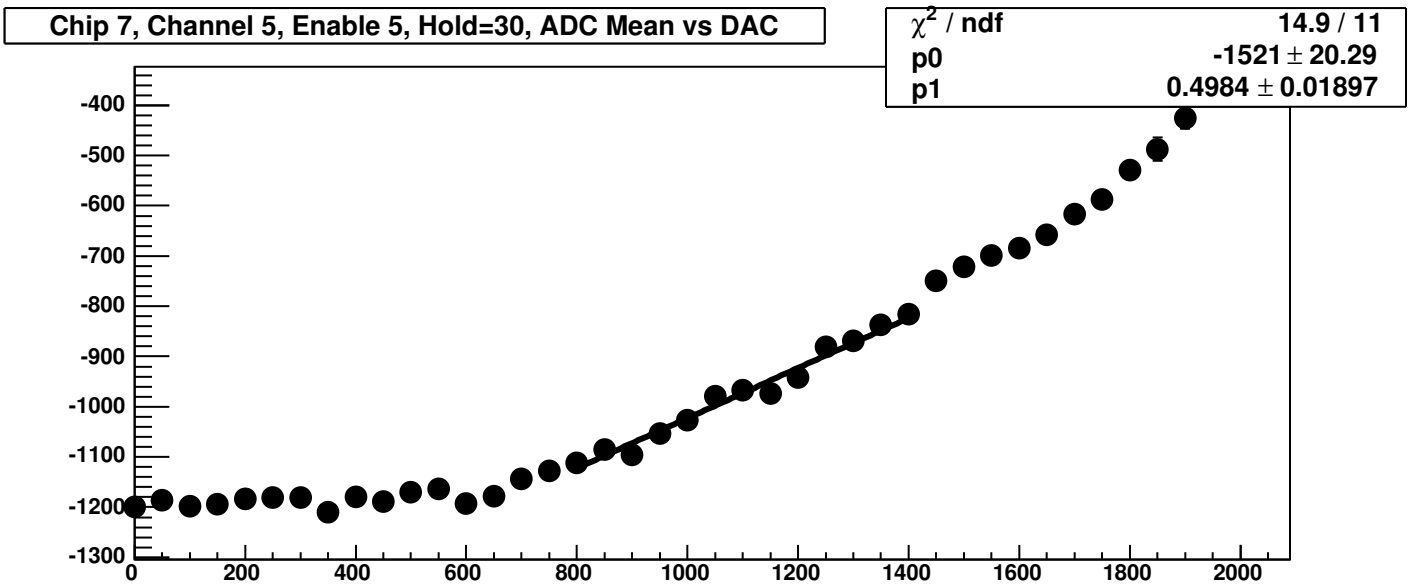


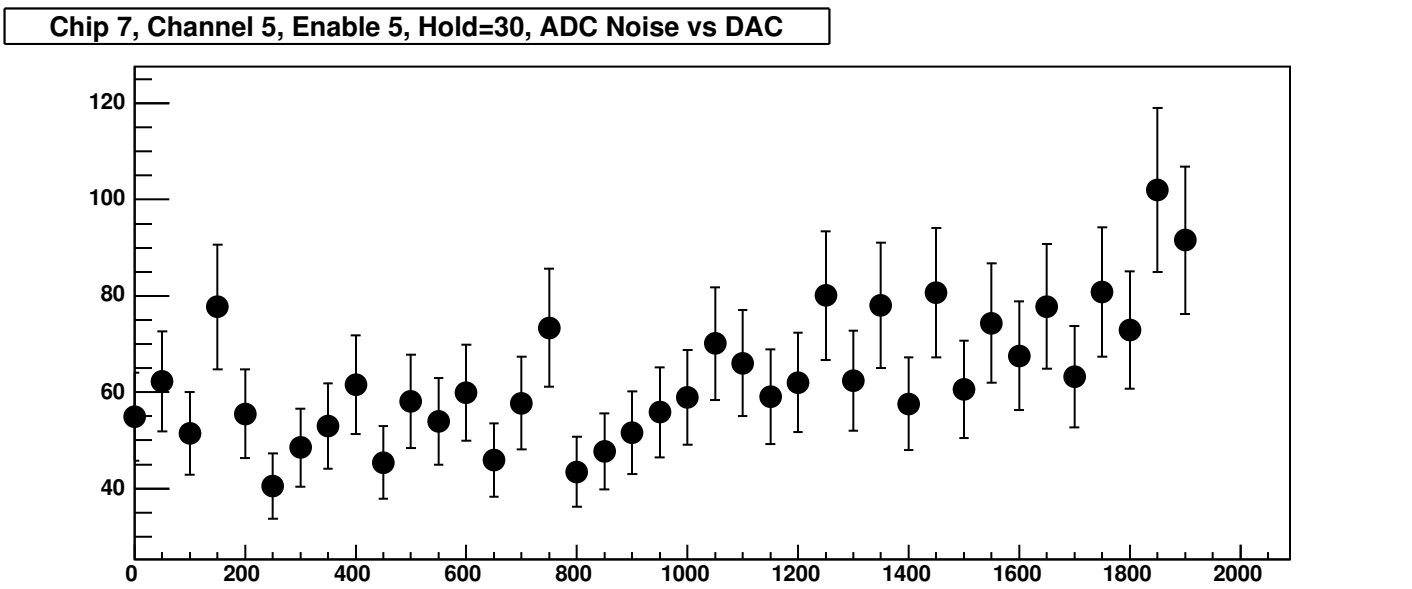
Chip 7, Channel 5, Enable 4!, Hold=30, ADC Noise vs DAC

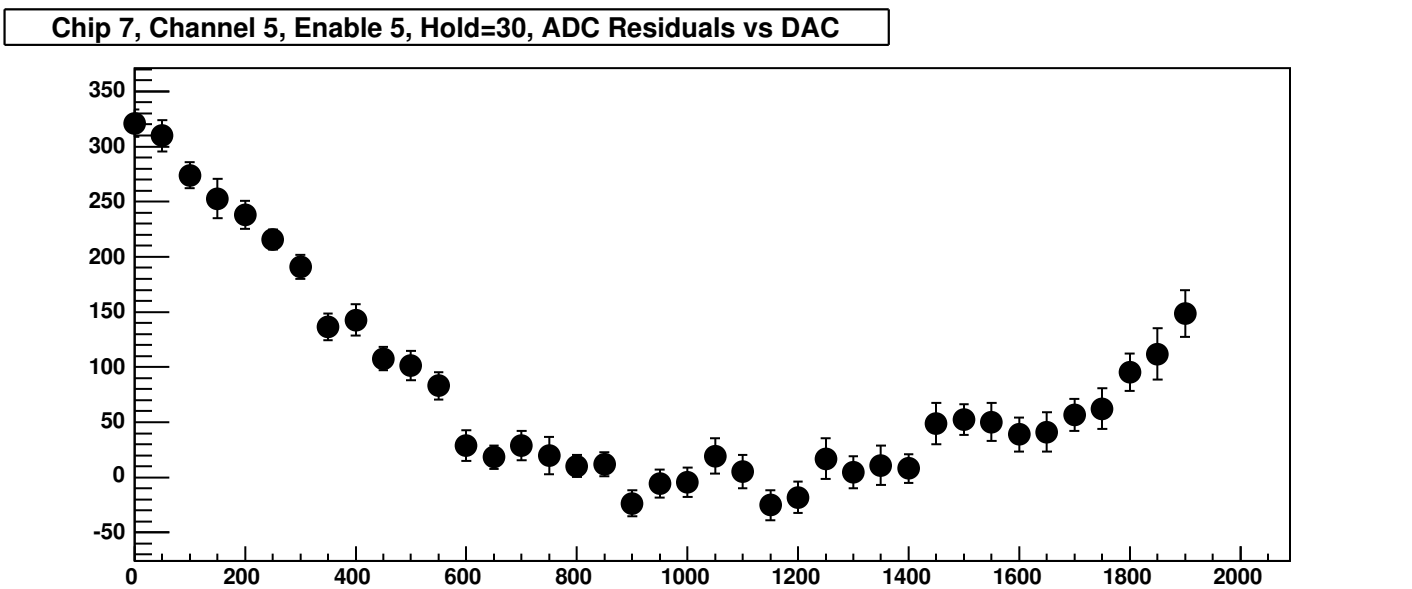


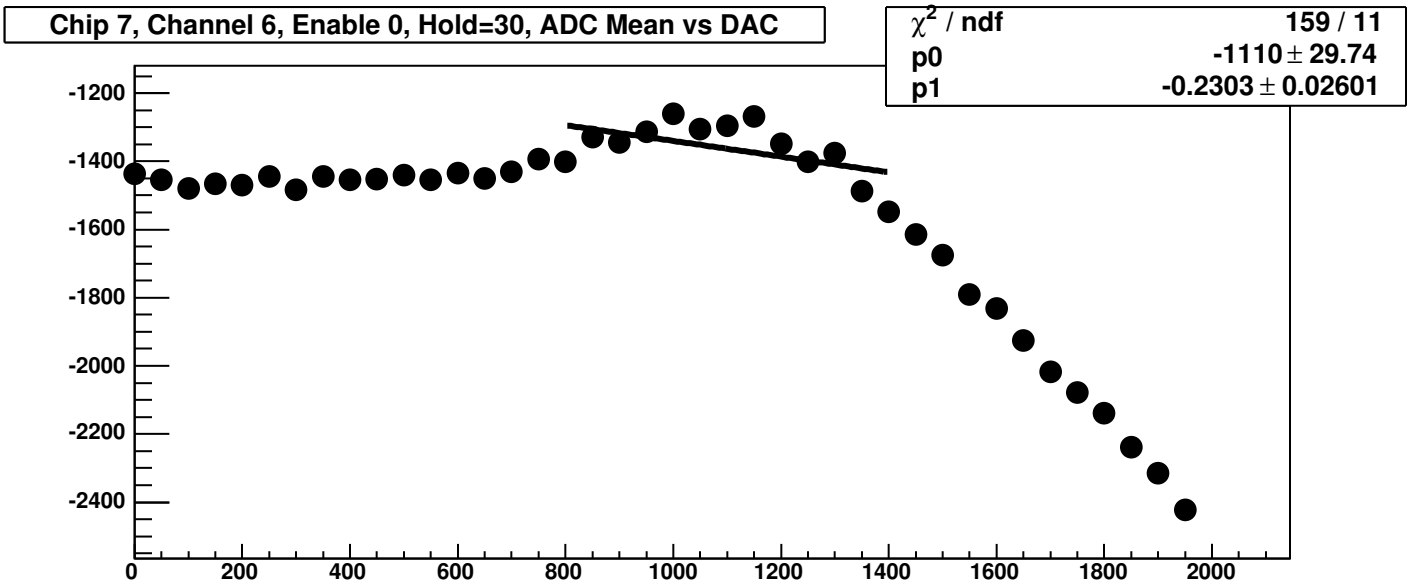
Chip 7, Channel 5, Enable 4!, Hold=30, ADC Residuals vs DAC

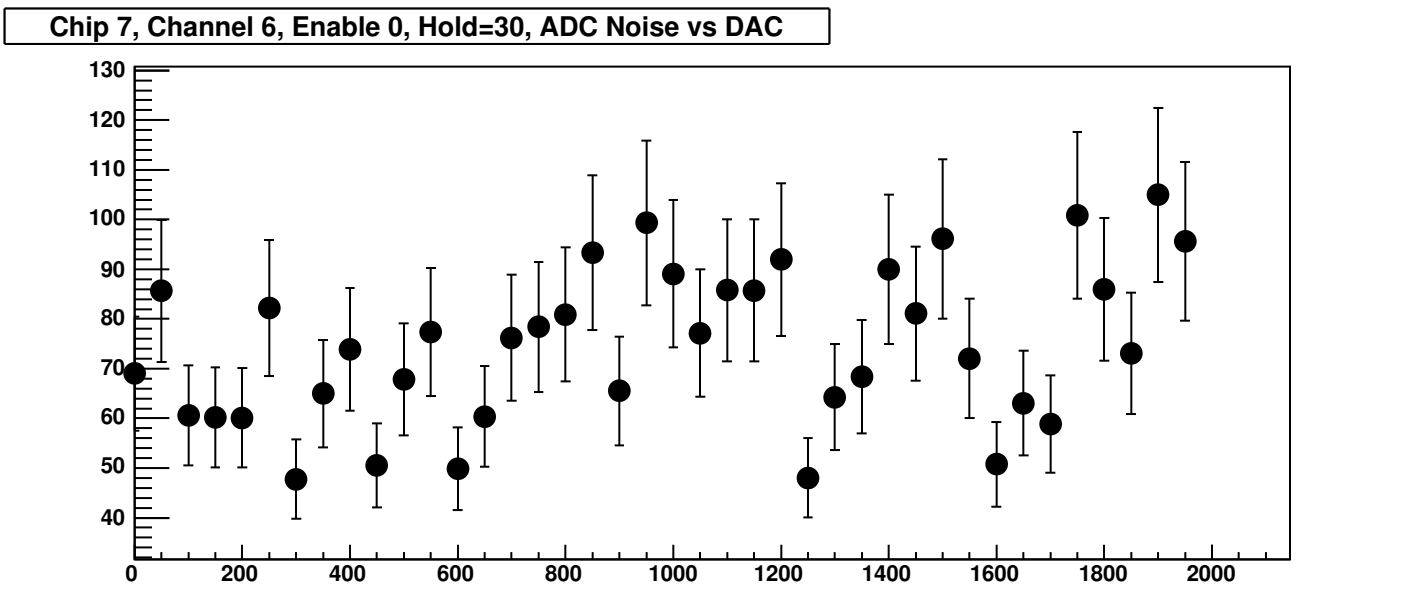


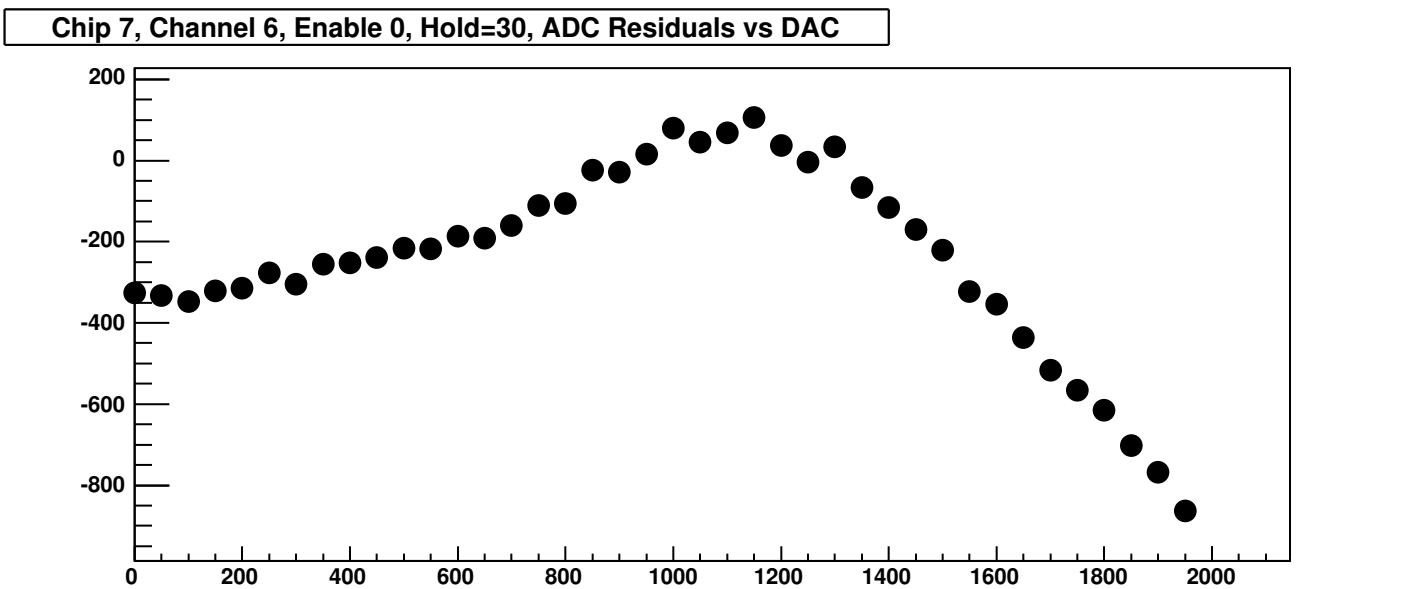

Chip 7, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 14.9 / 11
p0 -1521 ± 20.29
p1 0.4984 ± 0.01897
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

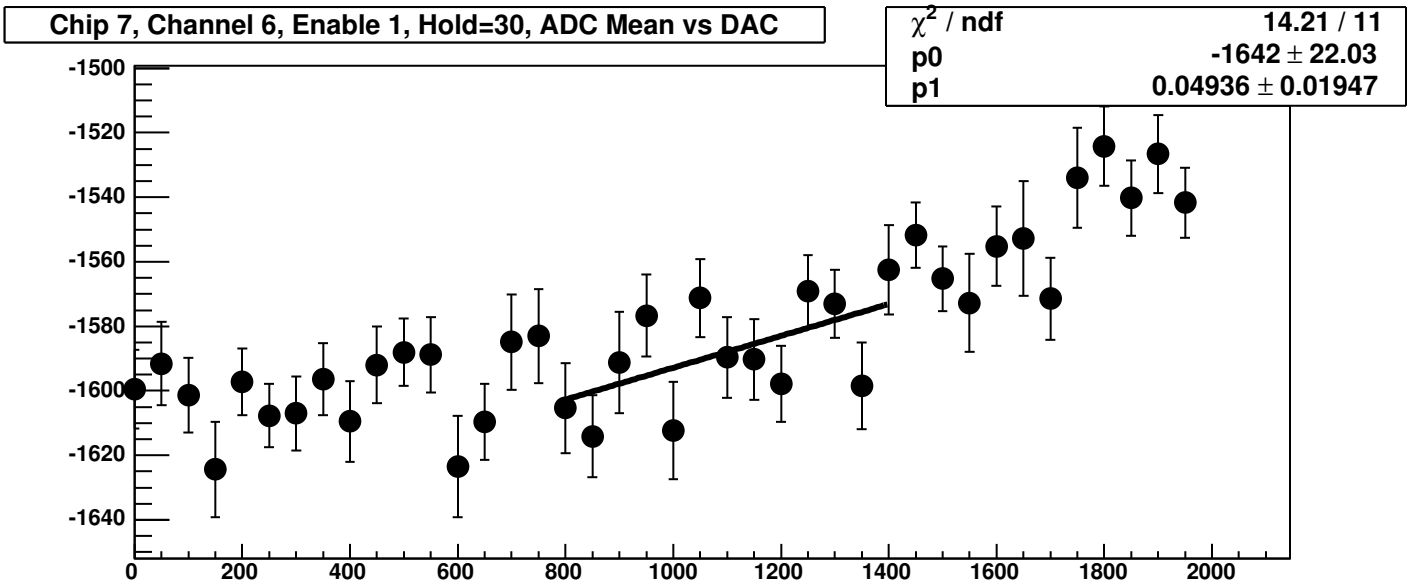

Chip 7, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

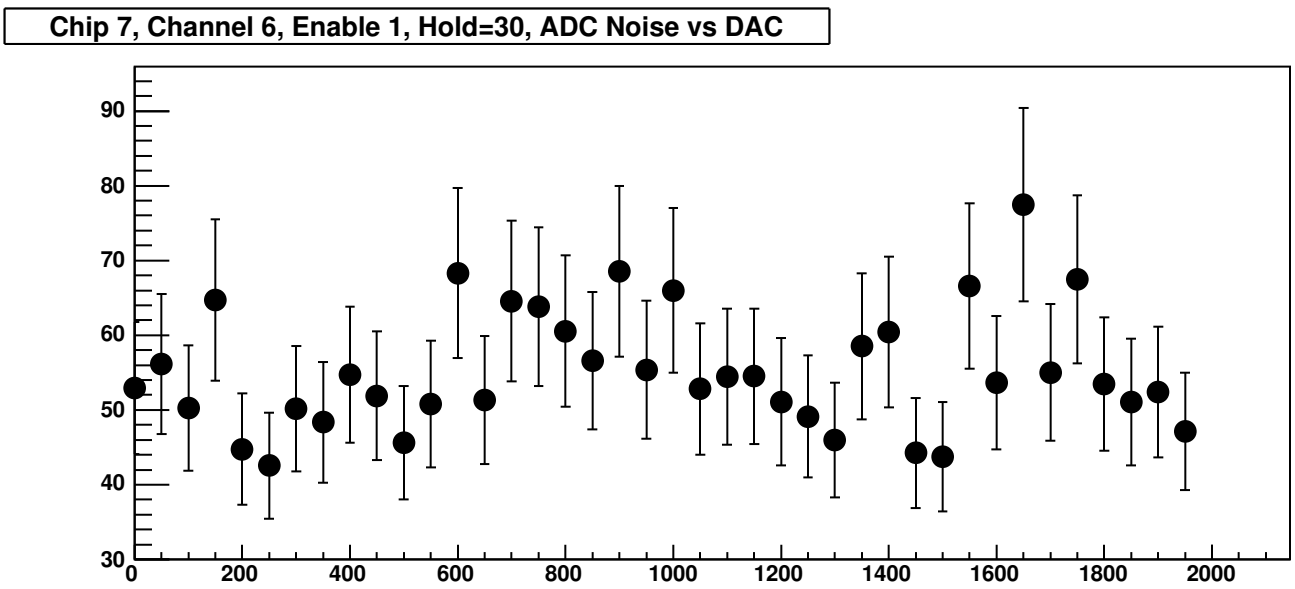

Chip 7, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

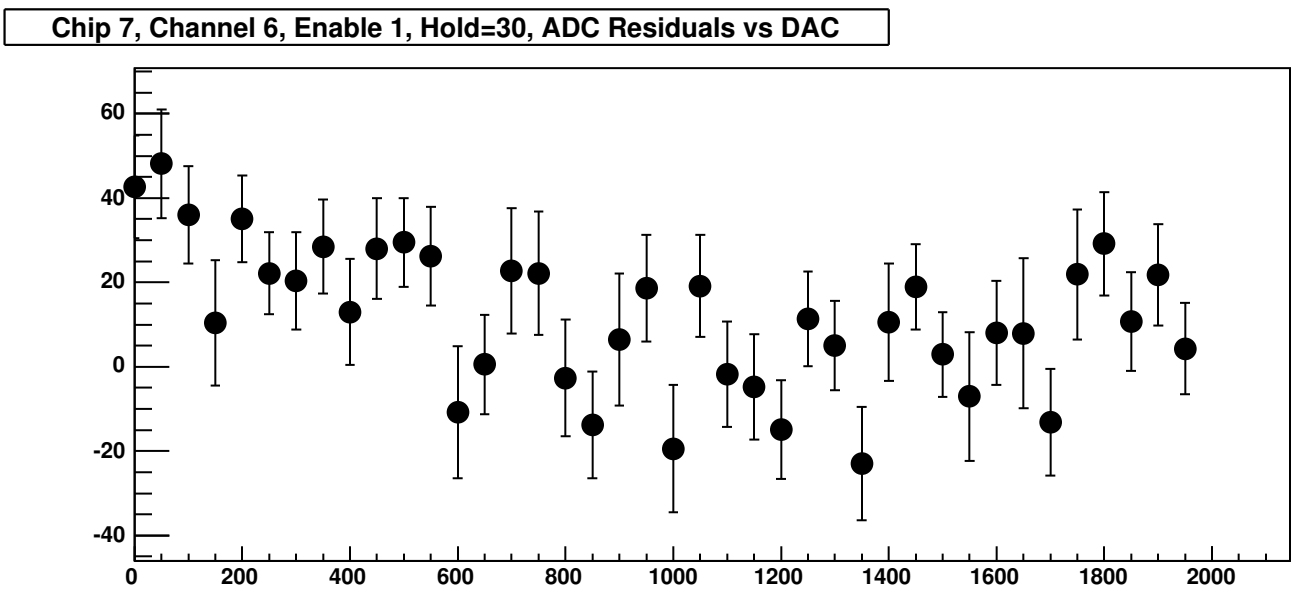

Chip 7, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 159 / 11
p0 -1110 ± 29.74
p1 -0.2303 ± 0.02601
-1200
-1400
-1600
-1800
-2000
-2200
-2400
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC
130
120
110
100
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

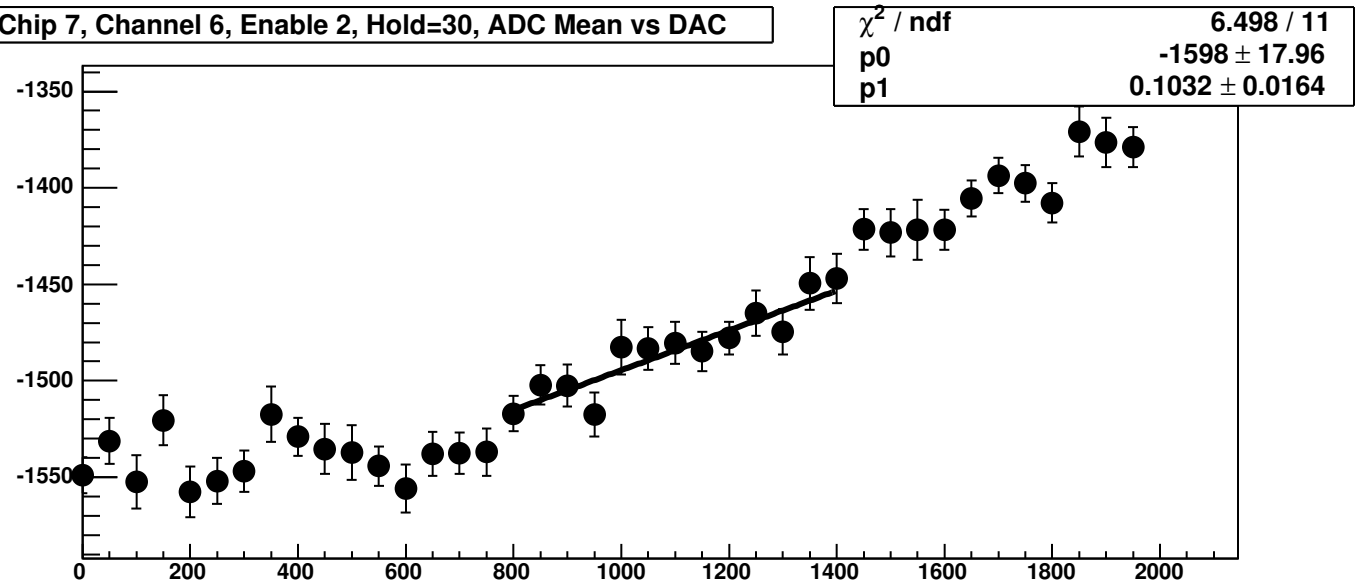

Chip 7, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 14.21 / 11
p0 -1642 ± 22.03
p1 0.04936 ± 0.01947
-1500
-1520
-1540
-1560
-1580
-1600
-1620
-1640
0
200
400
600
800
1000
1200
1400
1600
1800
2000

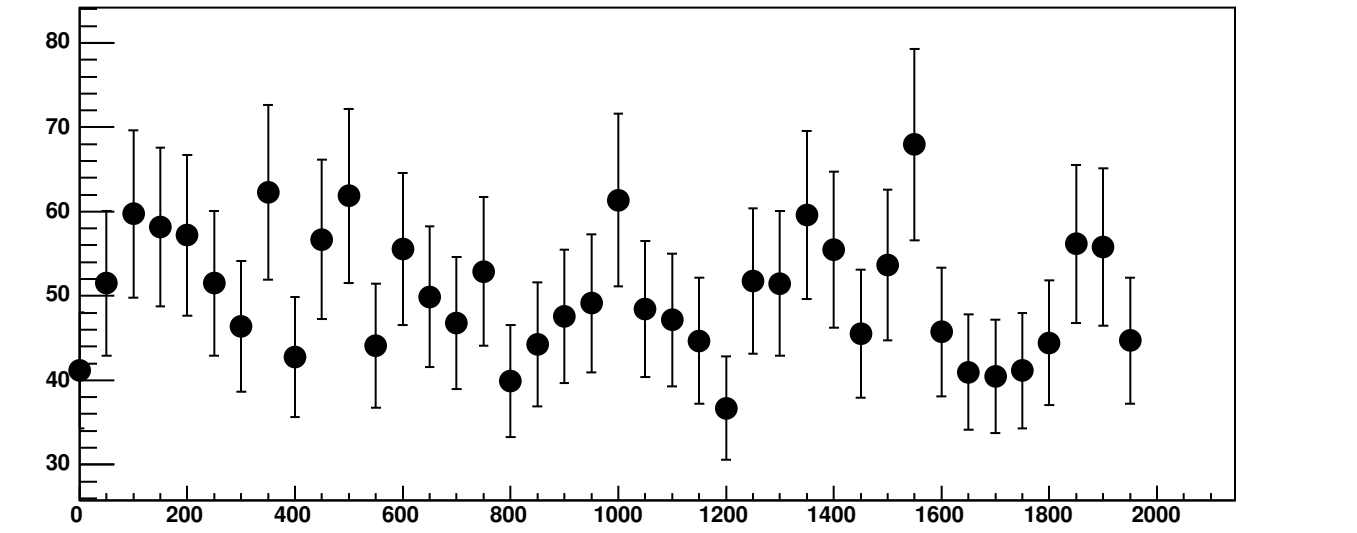

Chip 7, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

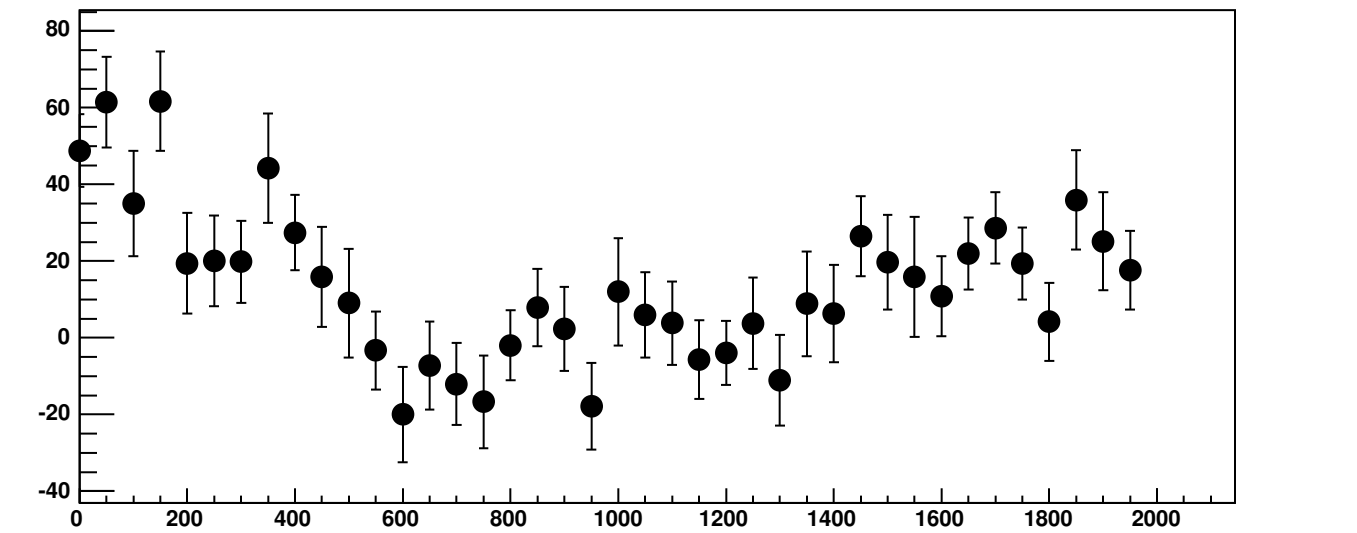
Chip 7, Channel 6, Enable 2, Hold=30, ADC Mean vs DAC

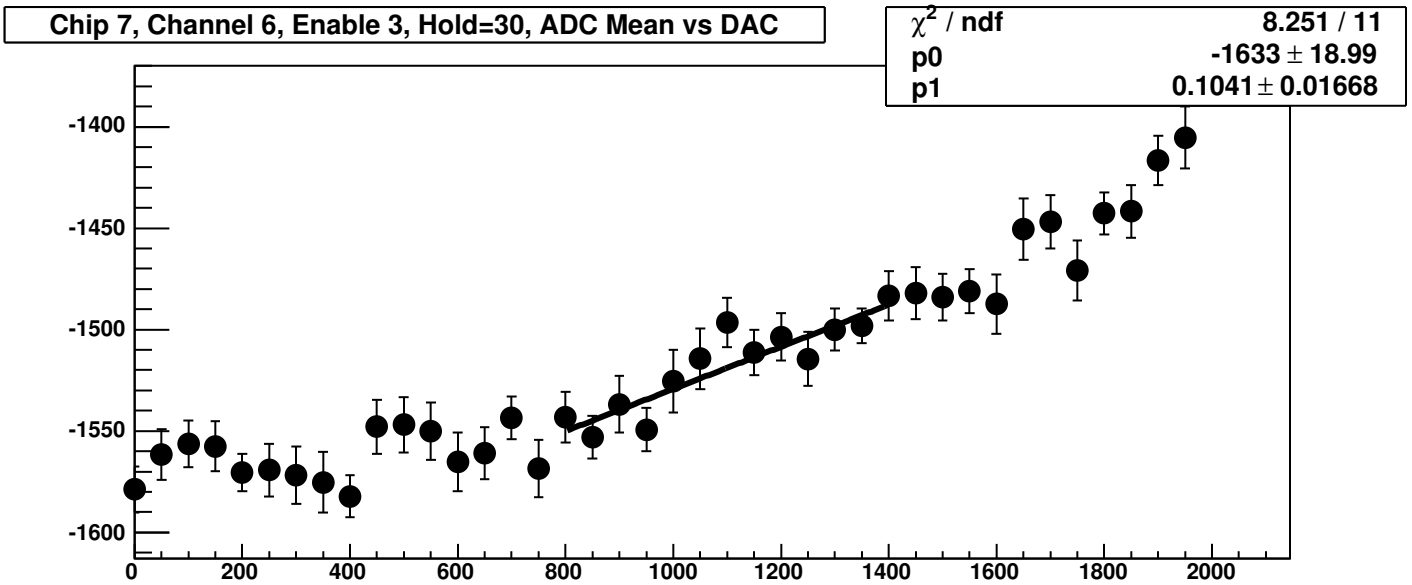


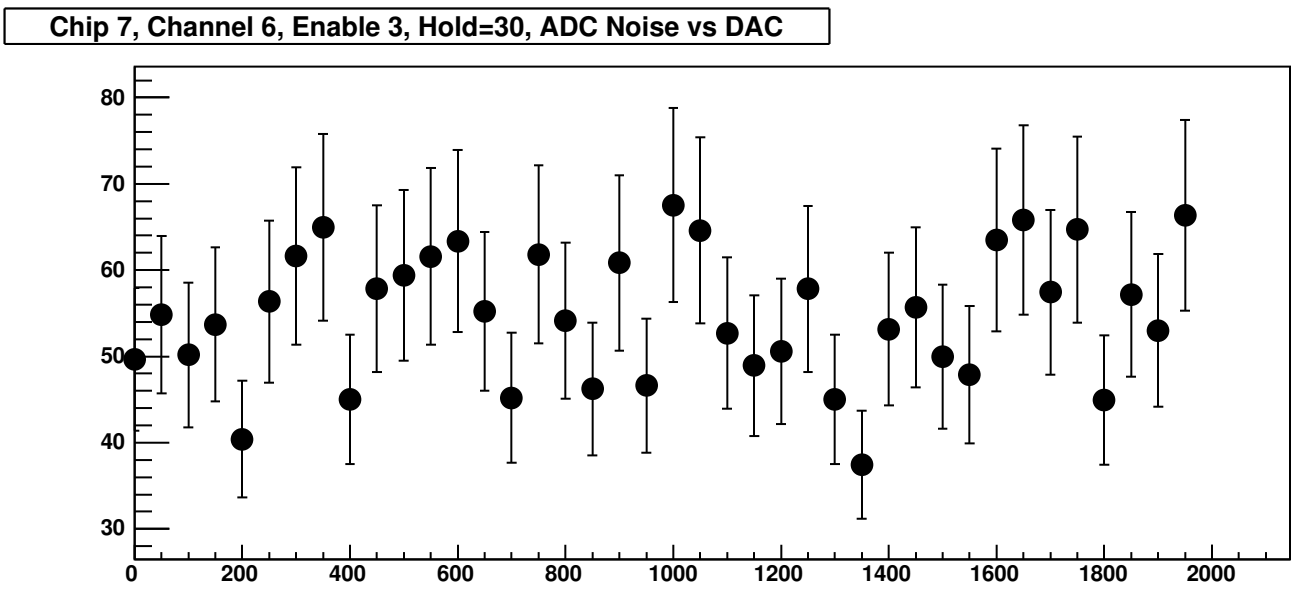
Chip 7, Channel 6, Enable 2, Hold=30, ADC Noise vs DAC

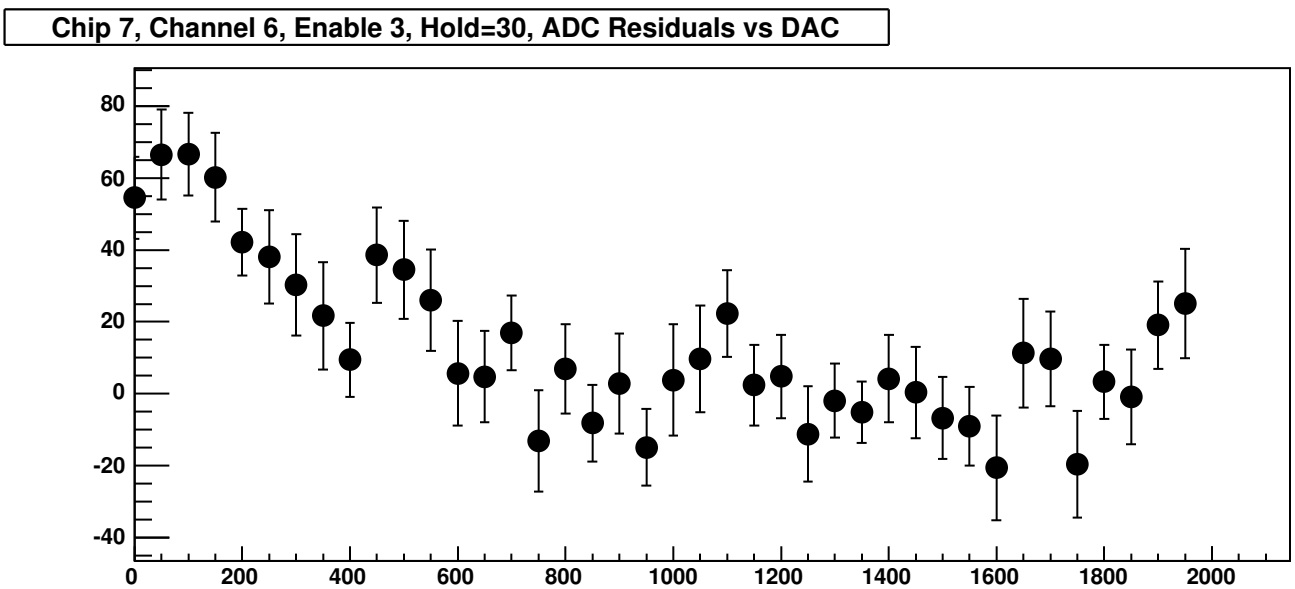


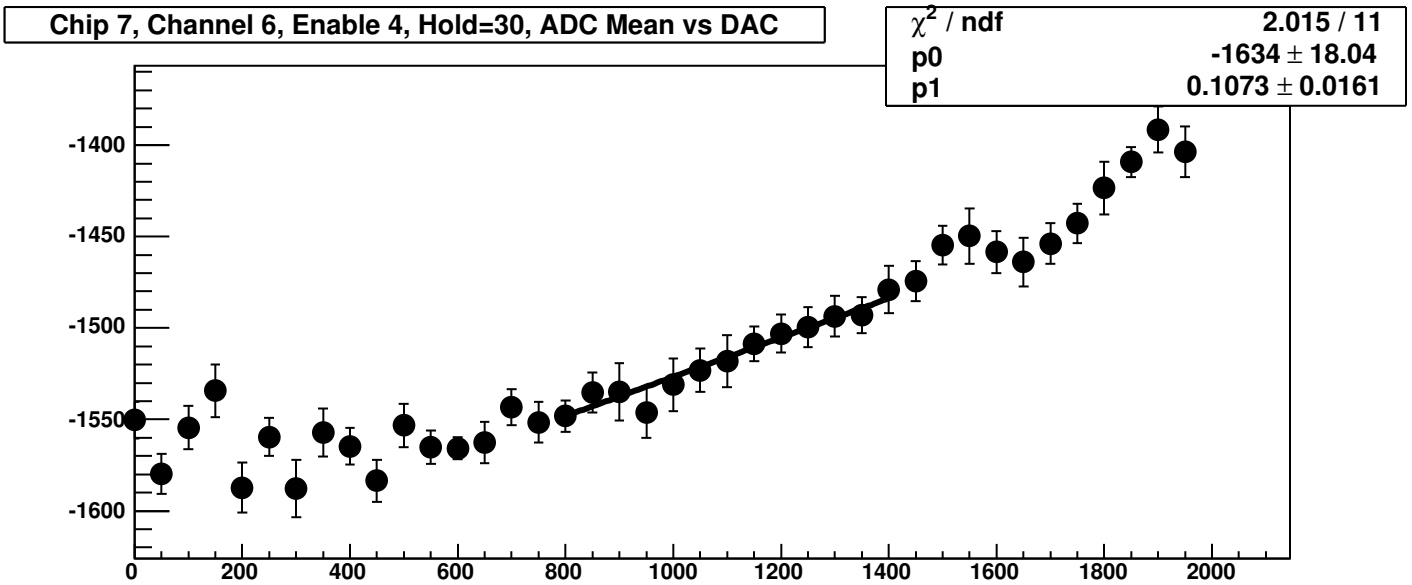
Chip 7, Channel 6, Enable 2, Hold=30, ADC Residuals vs DAC

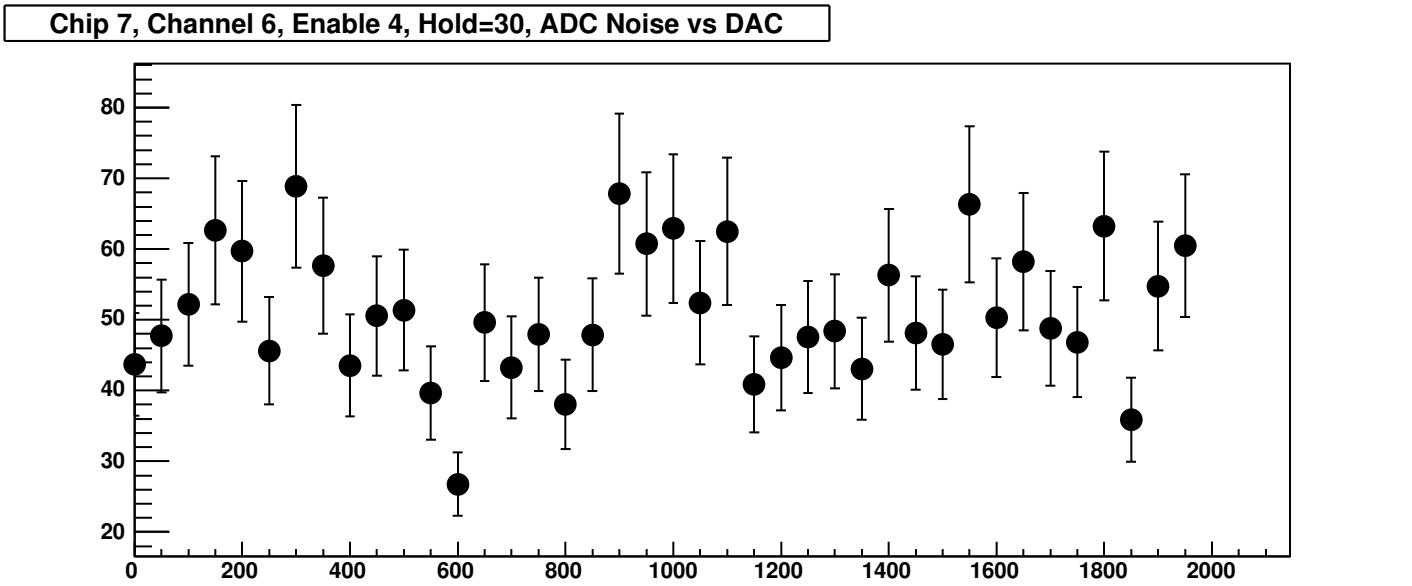


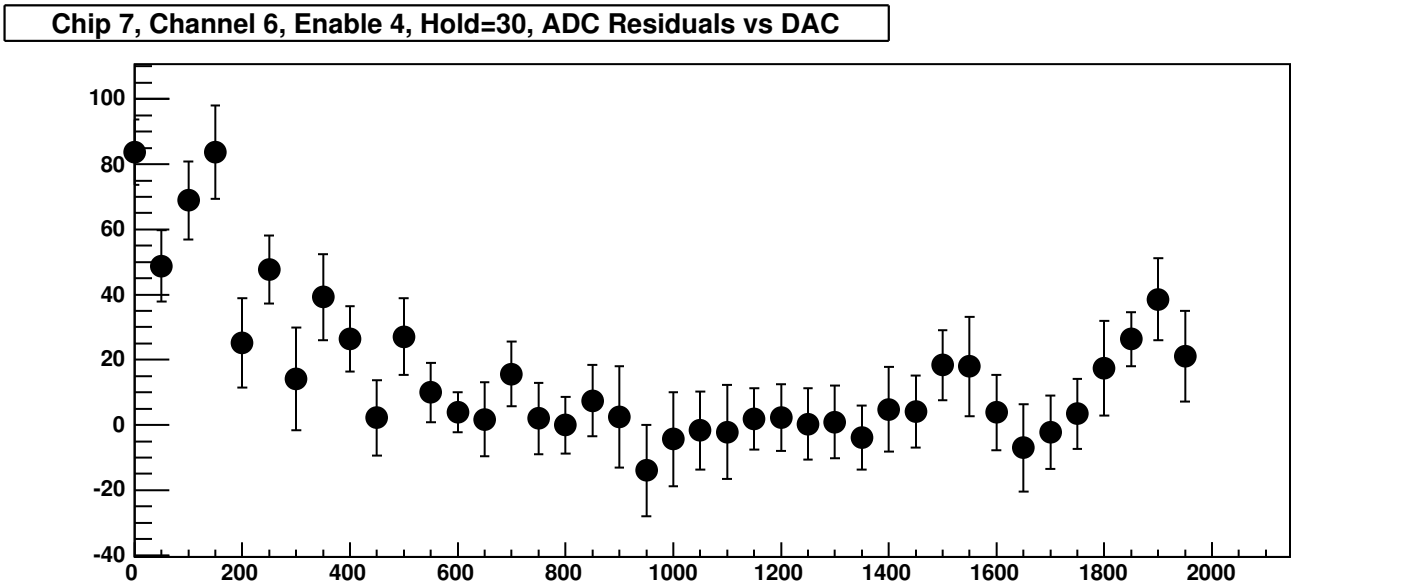

Chip 7, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 8.251 / 11
p0 -1633 ± 18.99
p1 0.1041 ± 0.01668
-1400
-1450
-1500
-1550
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

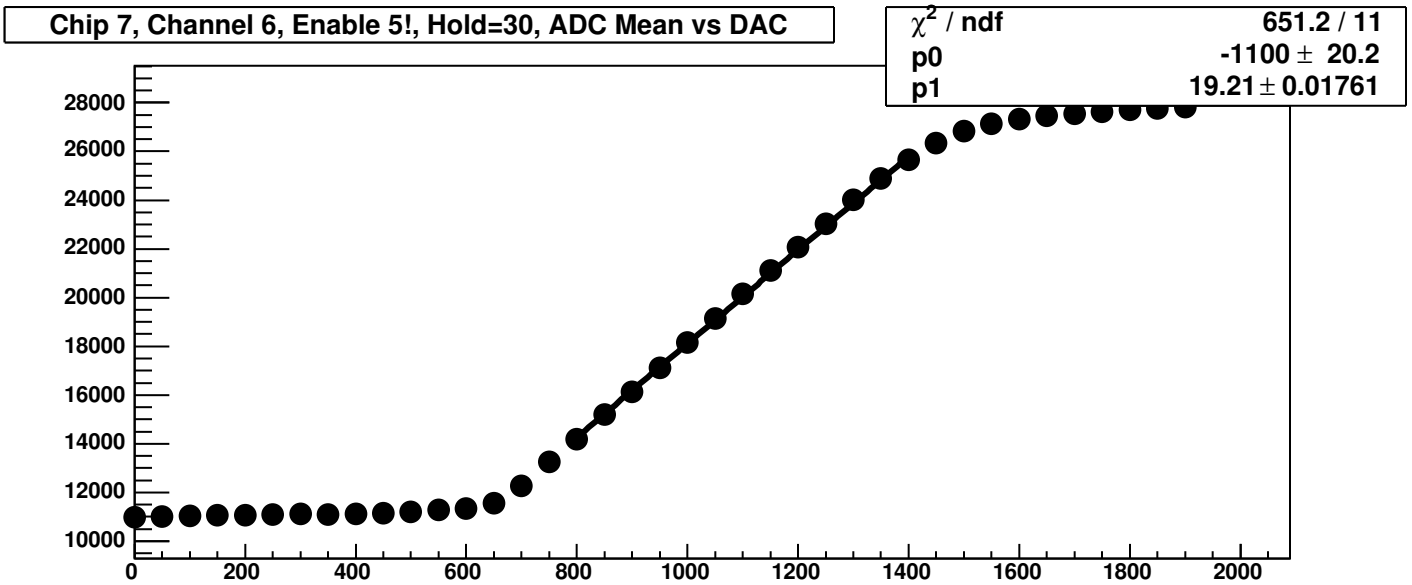

Chip 7, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

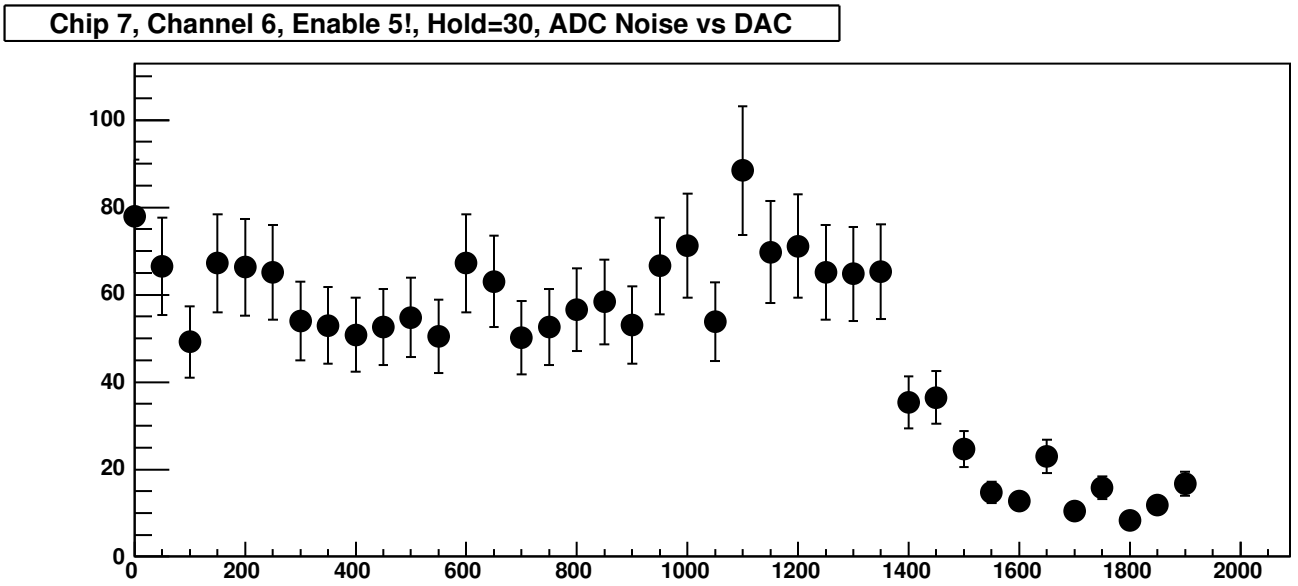

Chip 7, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

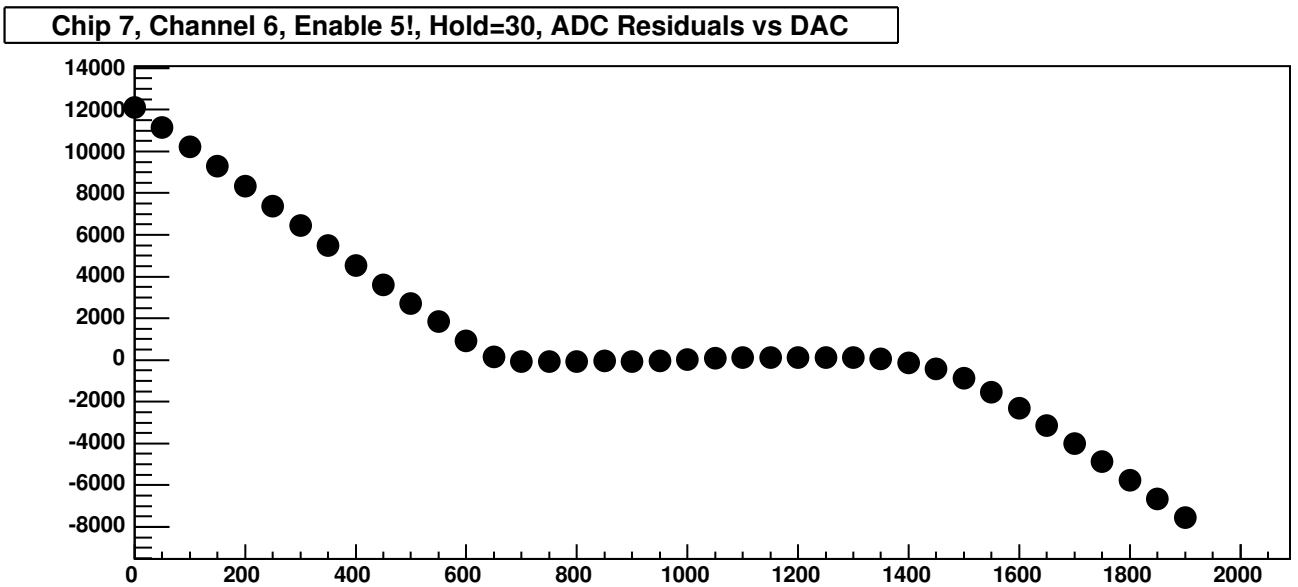

Chip 7, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 2.015 / 11
p0 -1634 ± 18.04
p1 0.1073 ± 0.0161
-1400
-1450
-1500
-1550
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

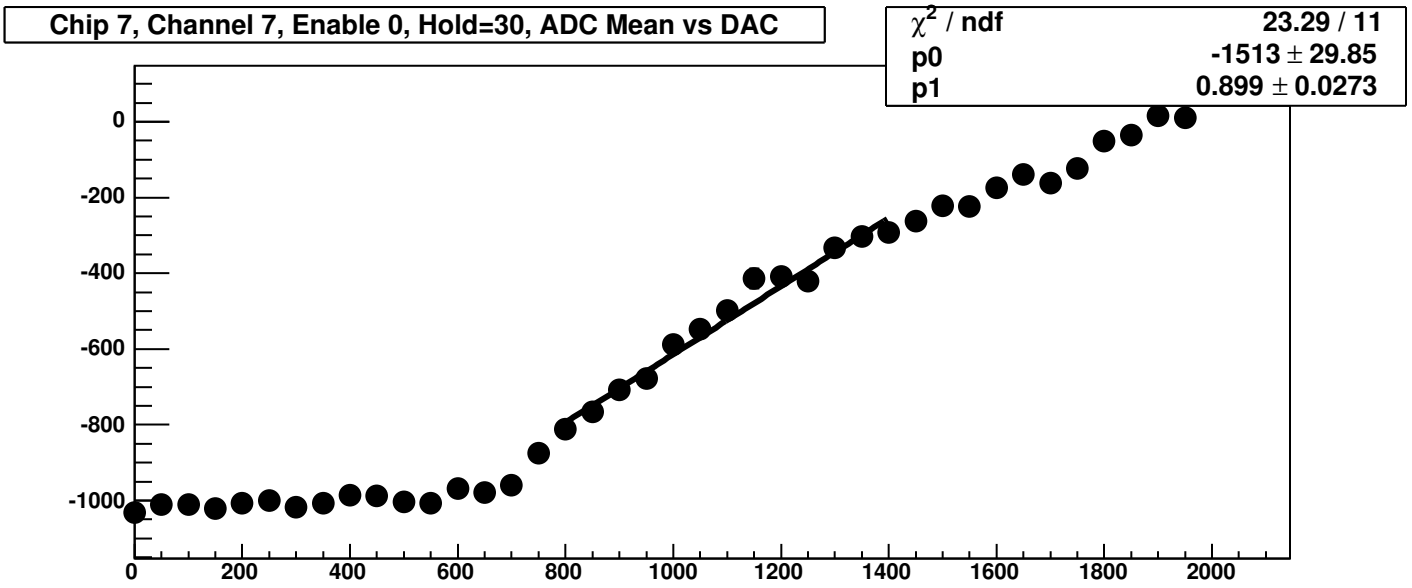

Chip 7, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

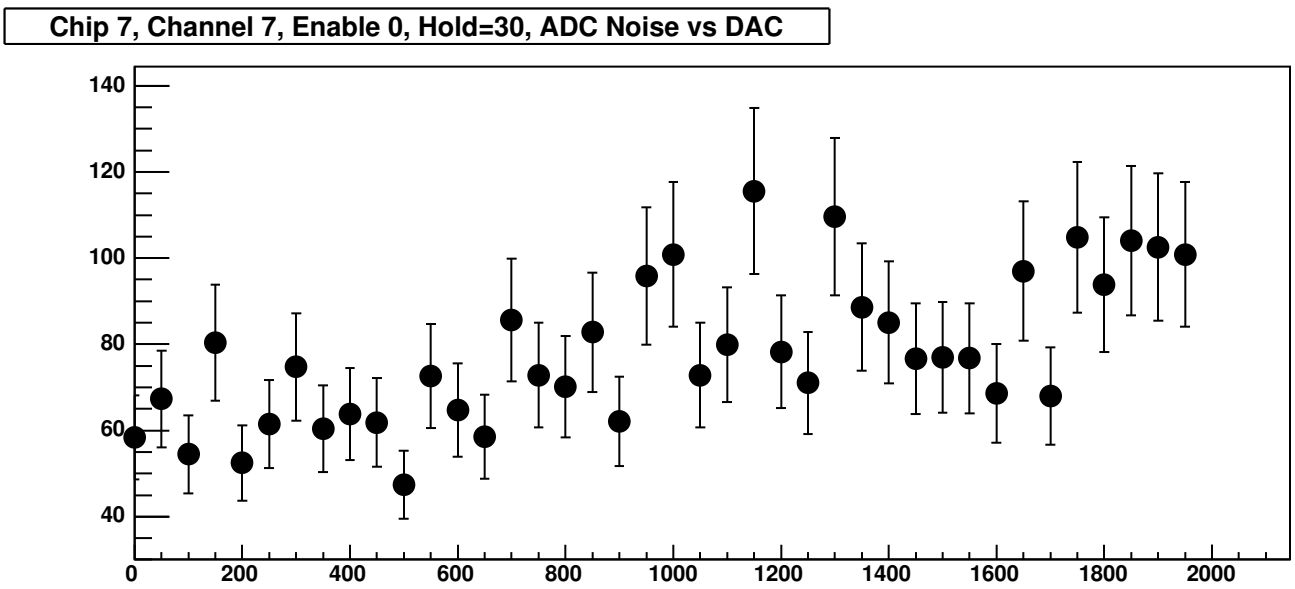

Chip 7, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

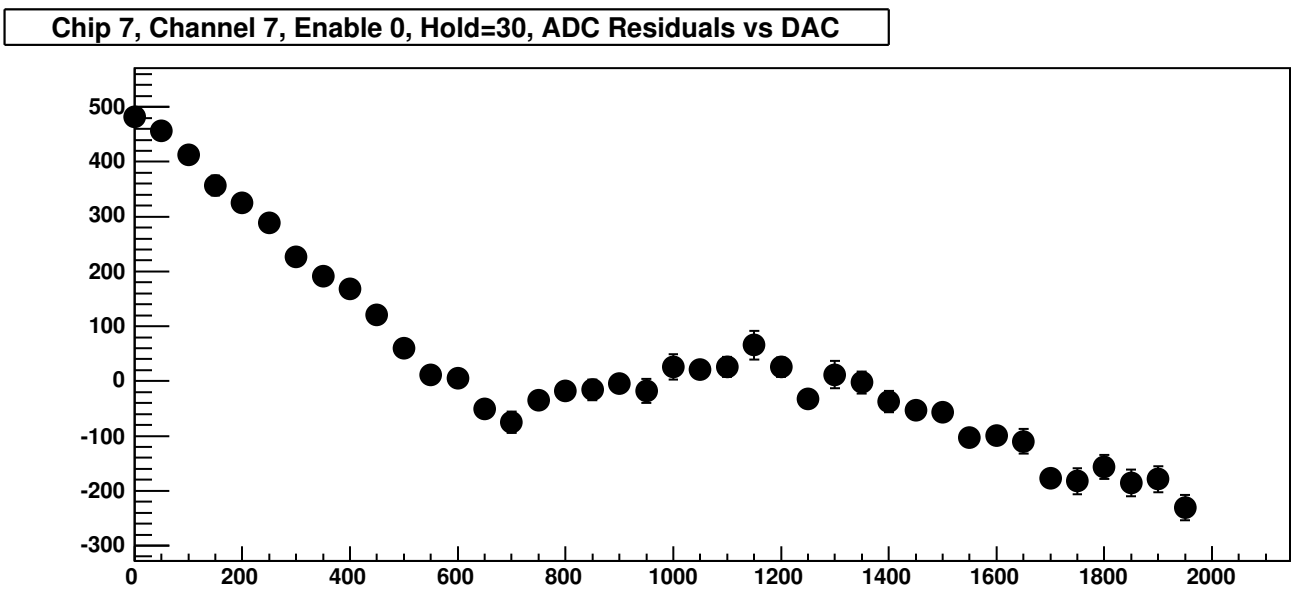

Chip 7, Channel 6, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 651.2 / 11
p0 -1100 ± 20.2
p1 19.21 ± 0.01761
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

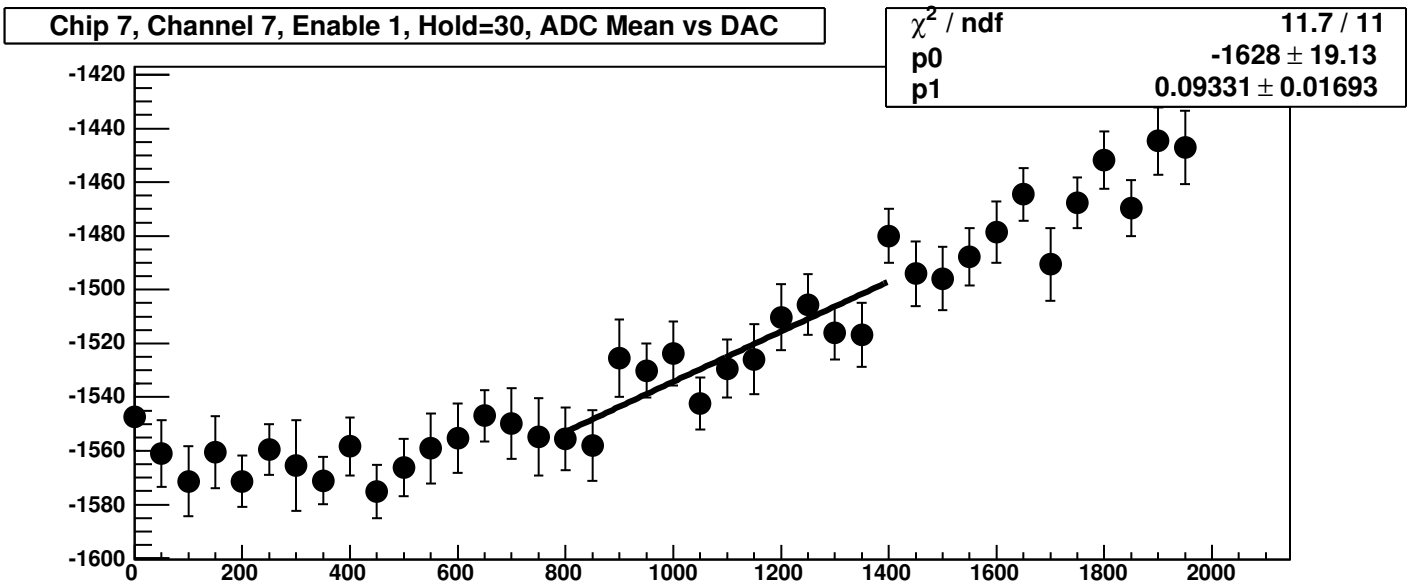

Chip 7, Channel 6, Enable 5!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

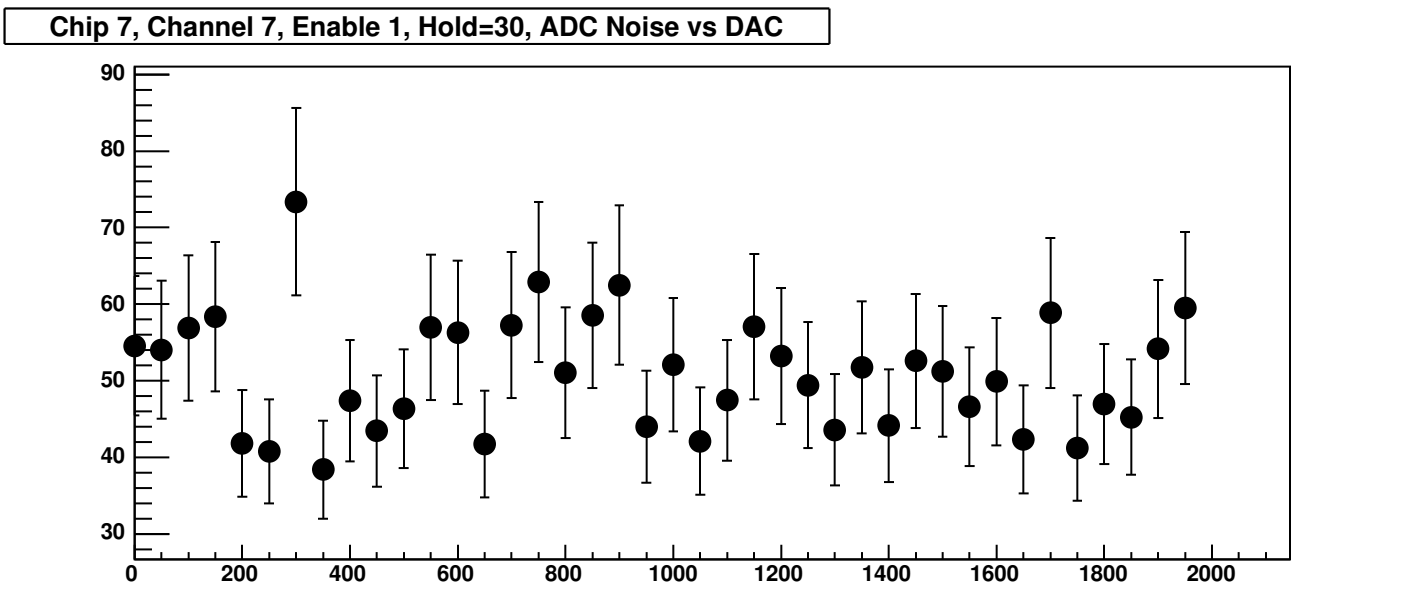

Chip 7, Channel 6, Enable 5!, Hold=30, ADC Residuals vs DAC
14000
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

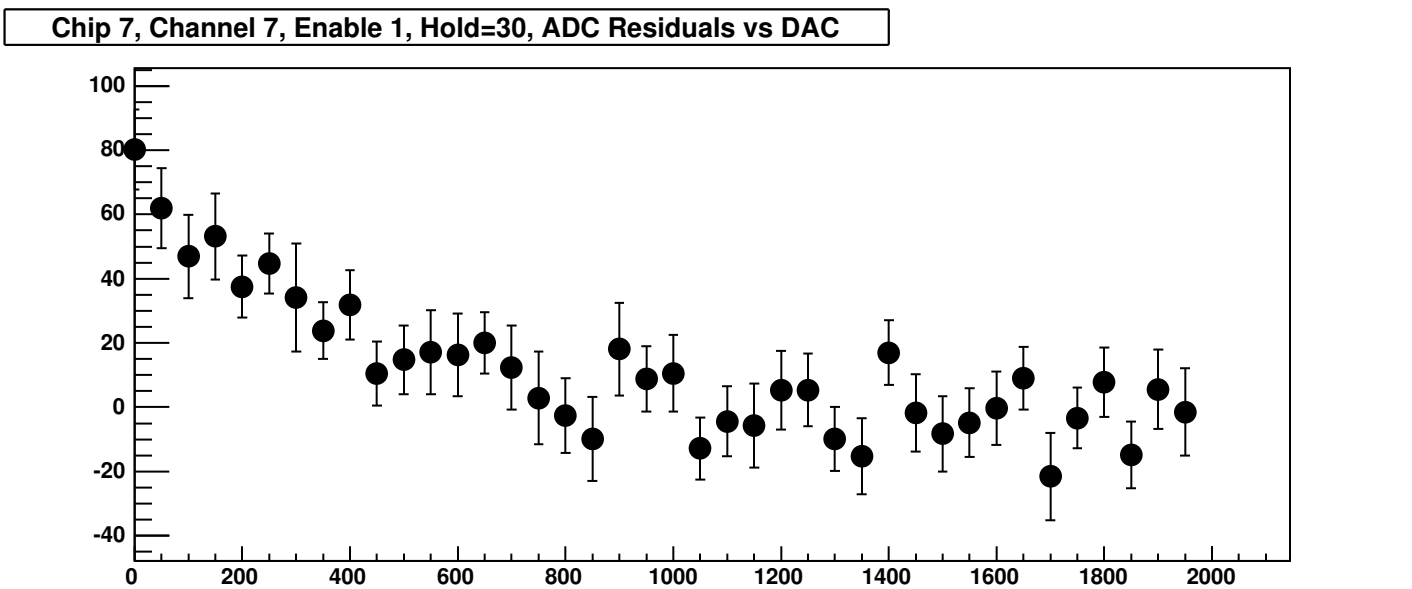

Chip 7, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 23.29 / 11
p0 -1513 ± 29.85
p1 0.899 ± 0.0273
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

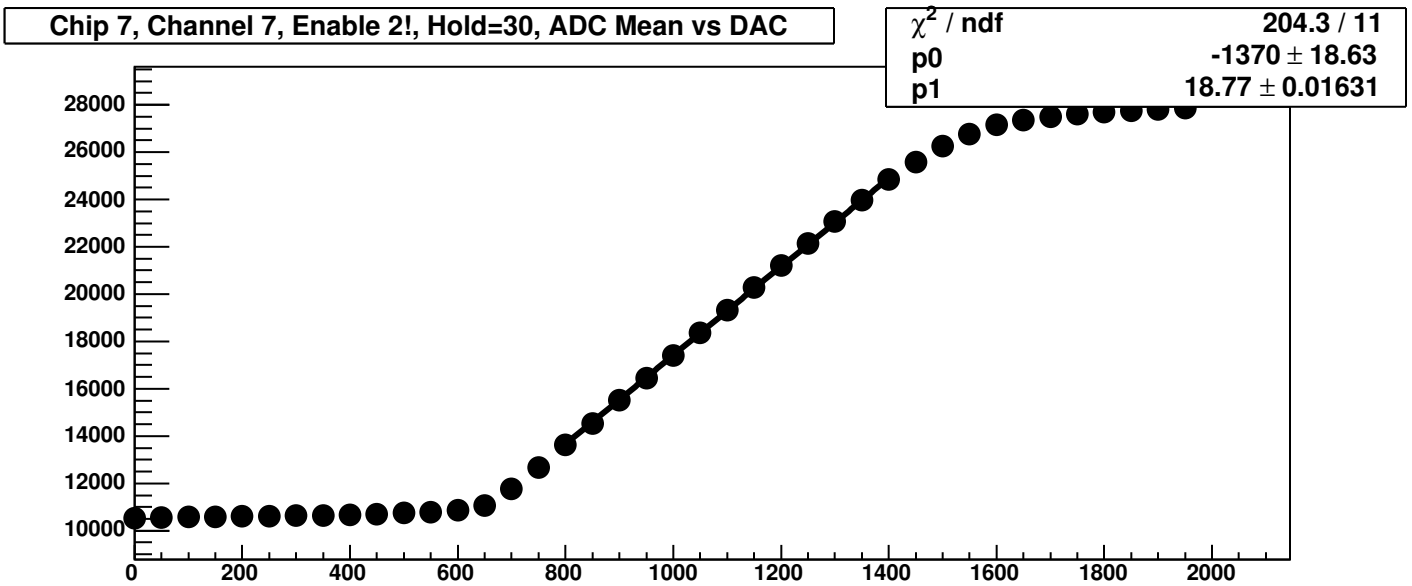

Chip 7, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

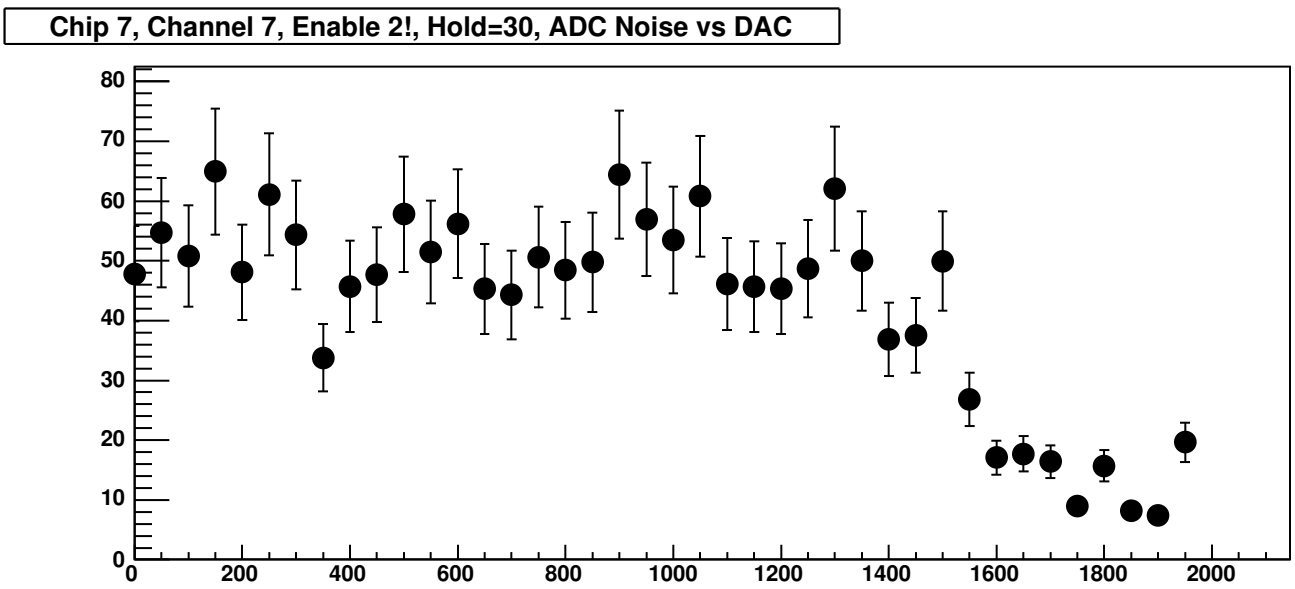

Chip 7, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
-300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

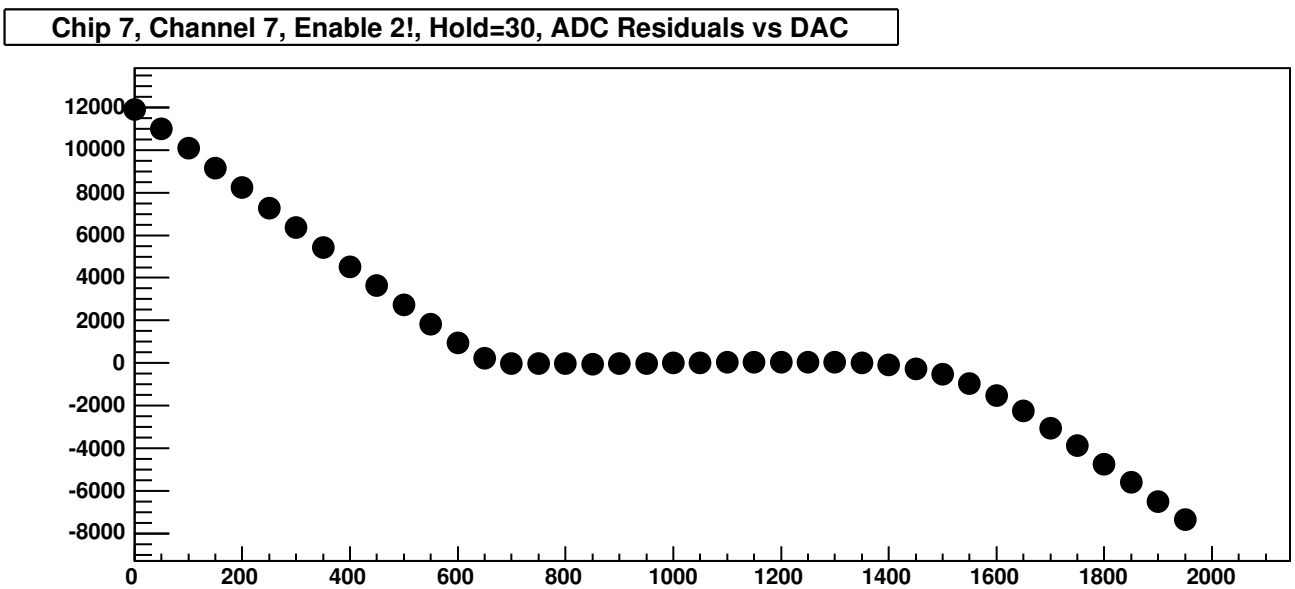

Chip 7, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 11.7 / 11
p0 -1628 ± 19.13
p1 0.09331 ± 0.01693
-1420
-1440
-1460
-1480
-1500
-1520
-1540
-1560
-1580
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

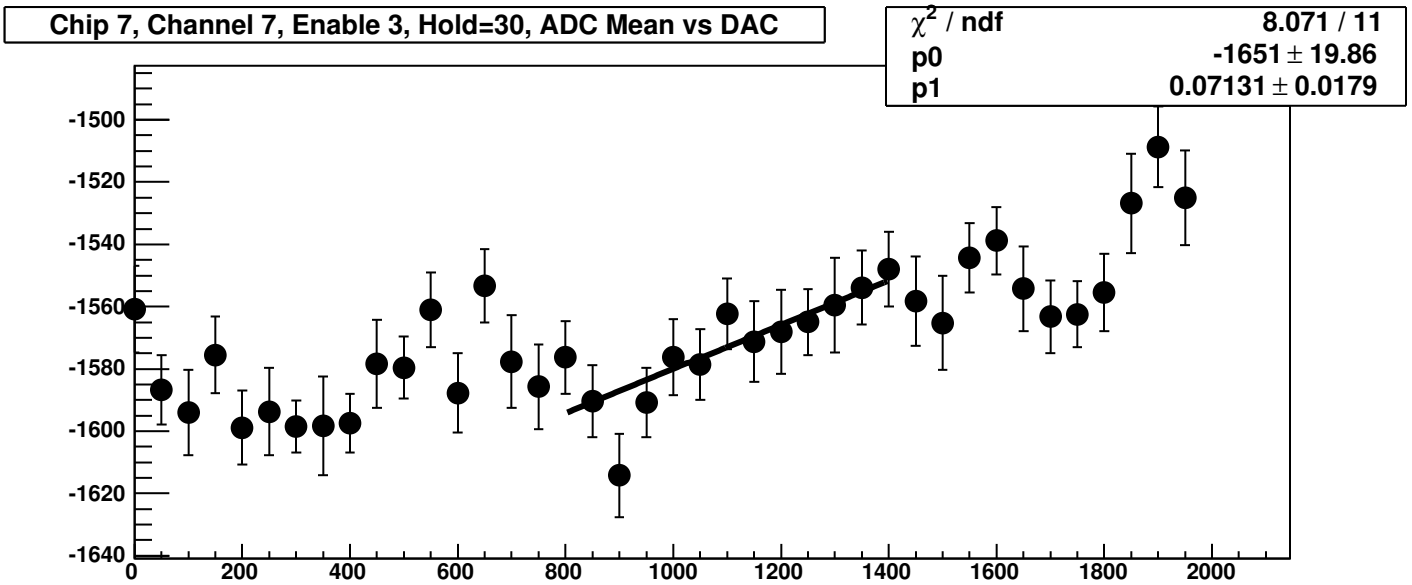

Chip 7, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

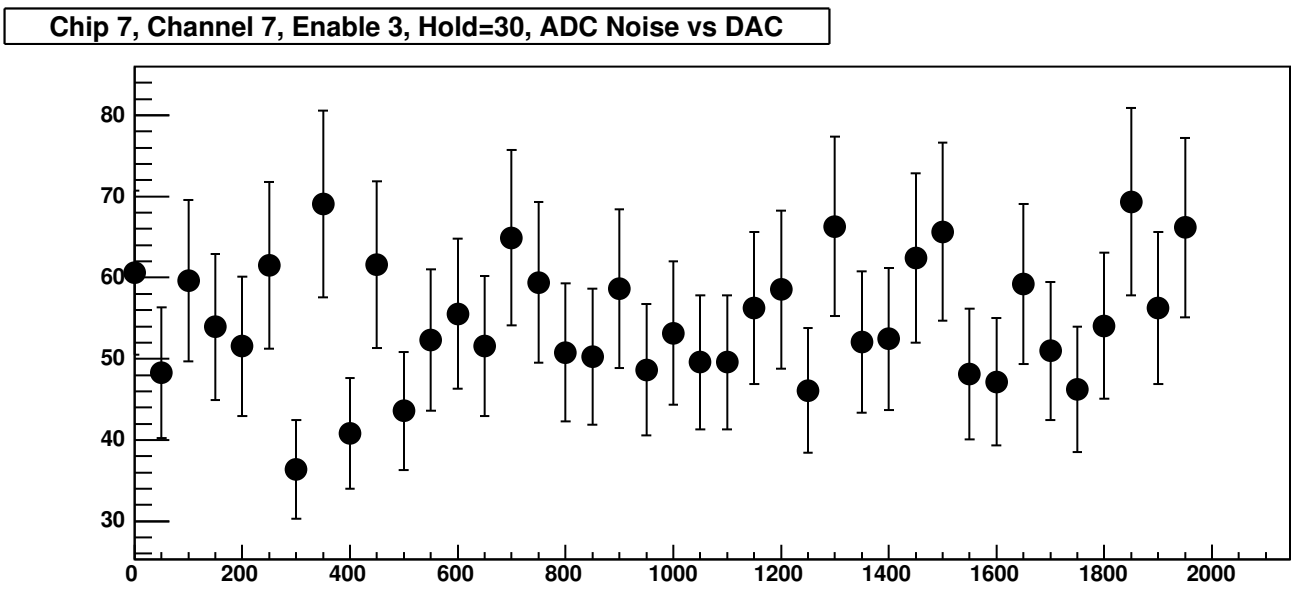

Chip 7, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

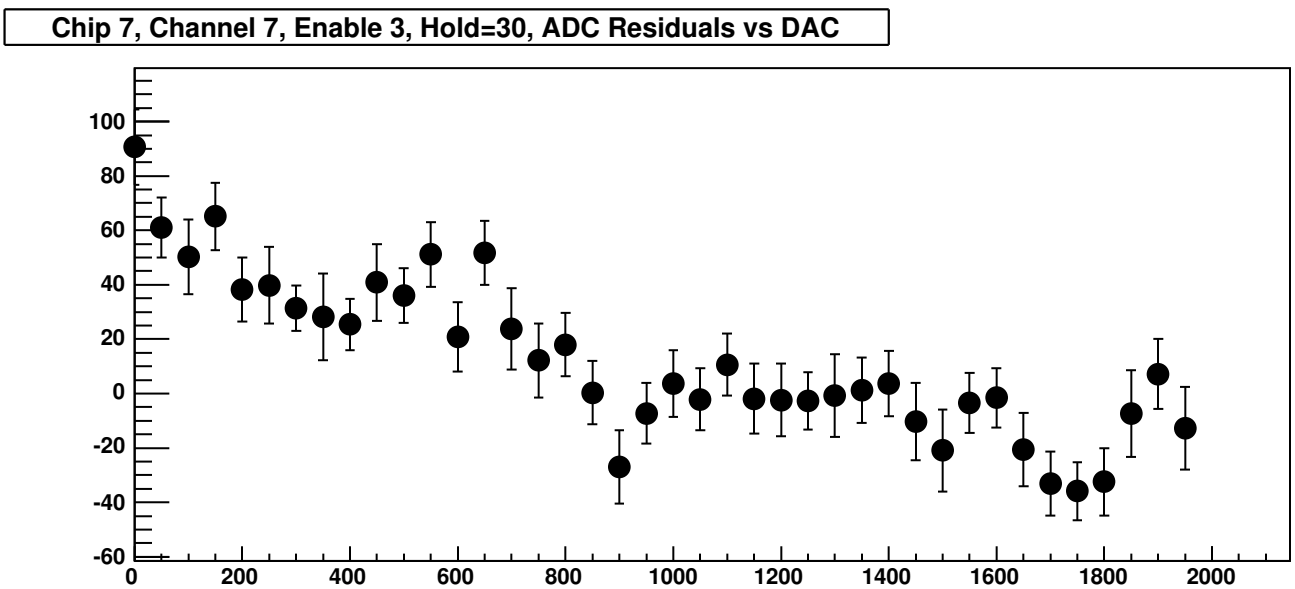

Chip 7, Channel 7, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 204.3 / 11
p0 -1370 ± 18.63
p1 18.77 ± 0.01631
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 7, Enable 2!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

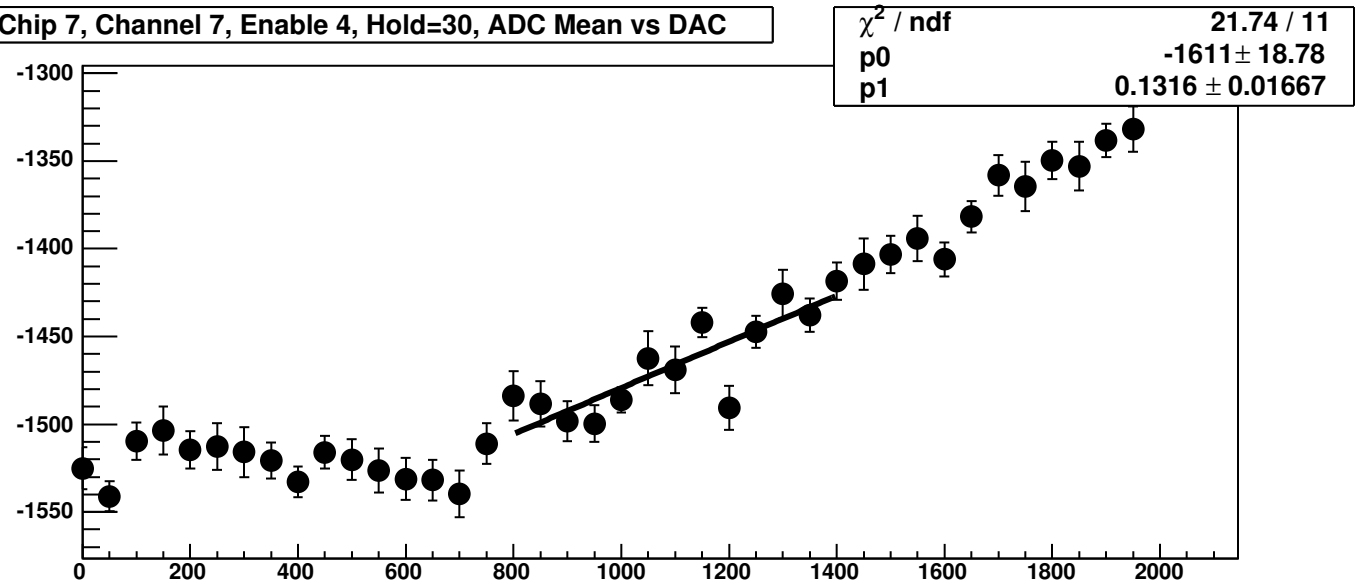

Chip 7, Channel 7, Enable 2!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 8.071 / 11
p0 -1651 ± 19.86
p1 0.07131 ± 0.0179
-1500
-1520
-1540
-1560
-1580
-1600
-1620
-1640
0
200
400
600
800
1000
1200
1400
1600
1800
2000

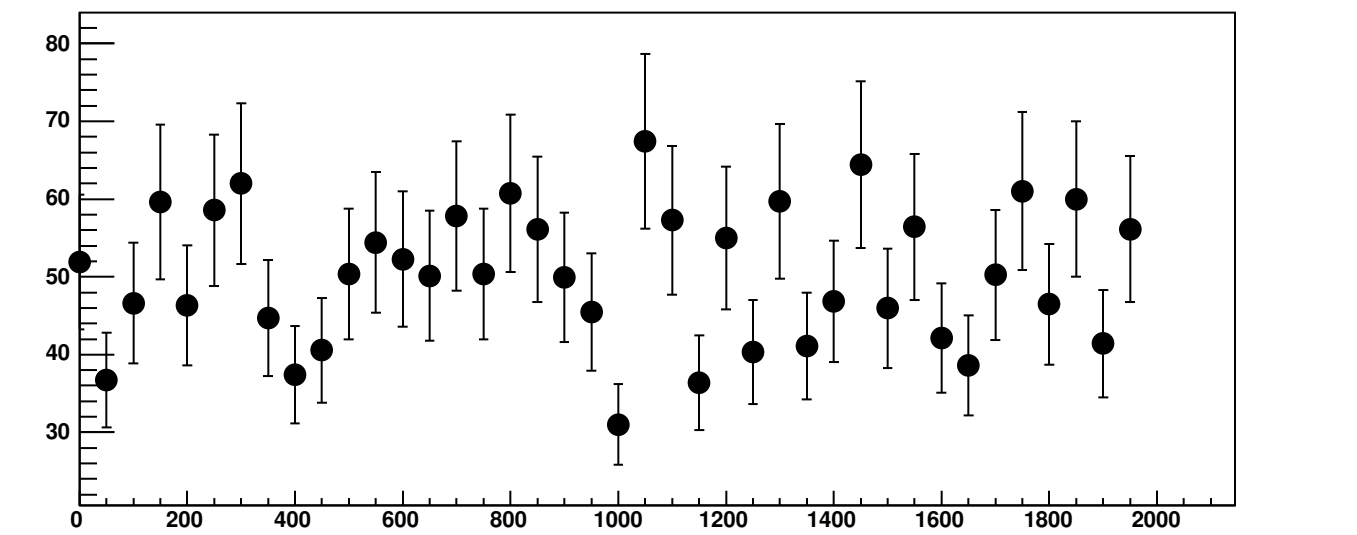

Chip 7, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

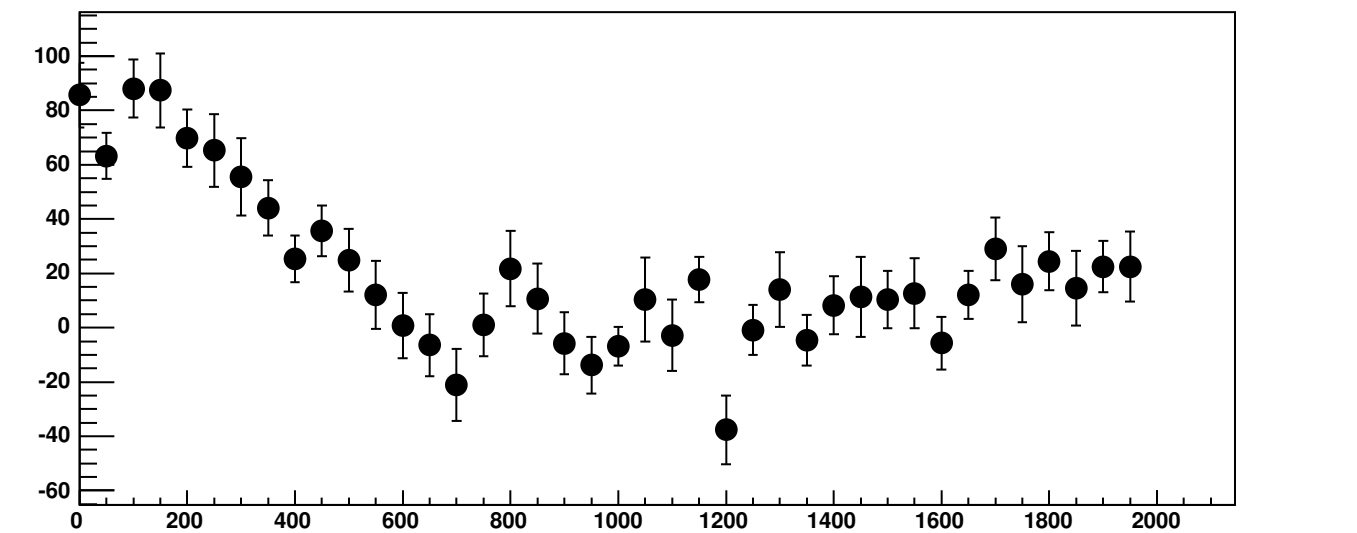
Chip 7, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC

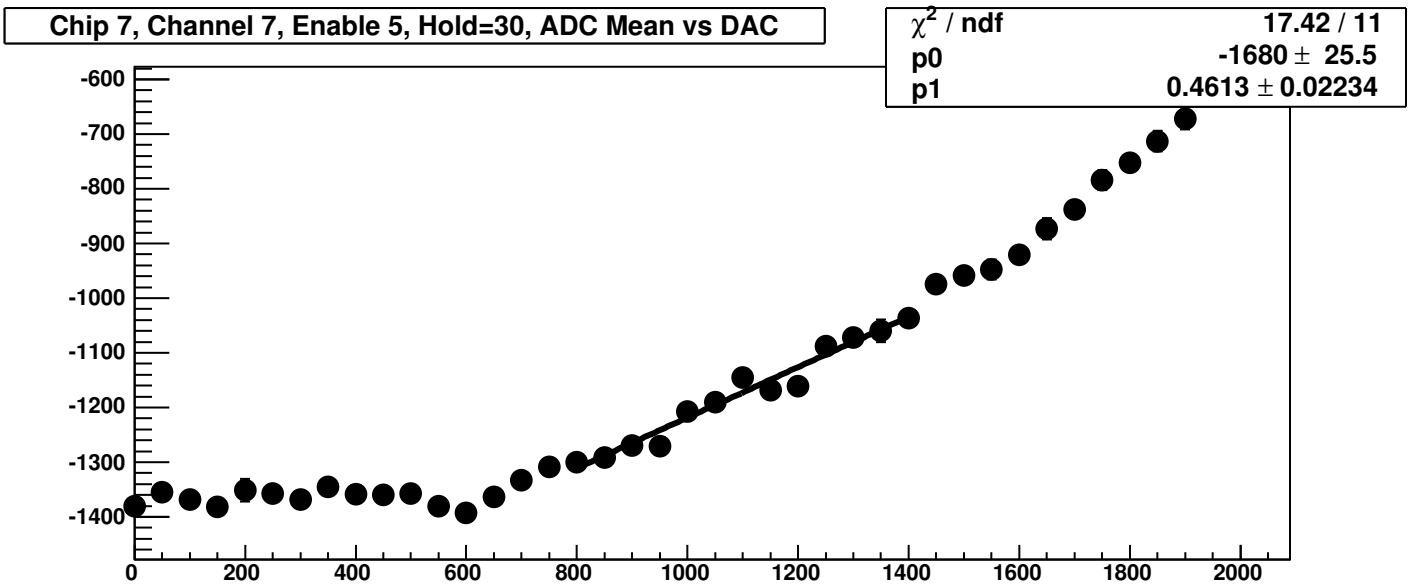


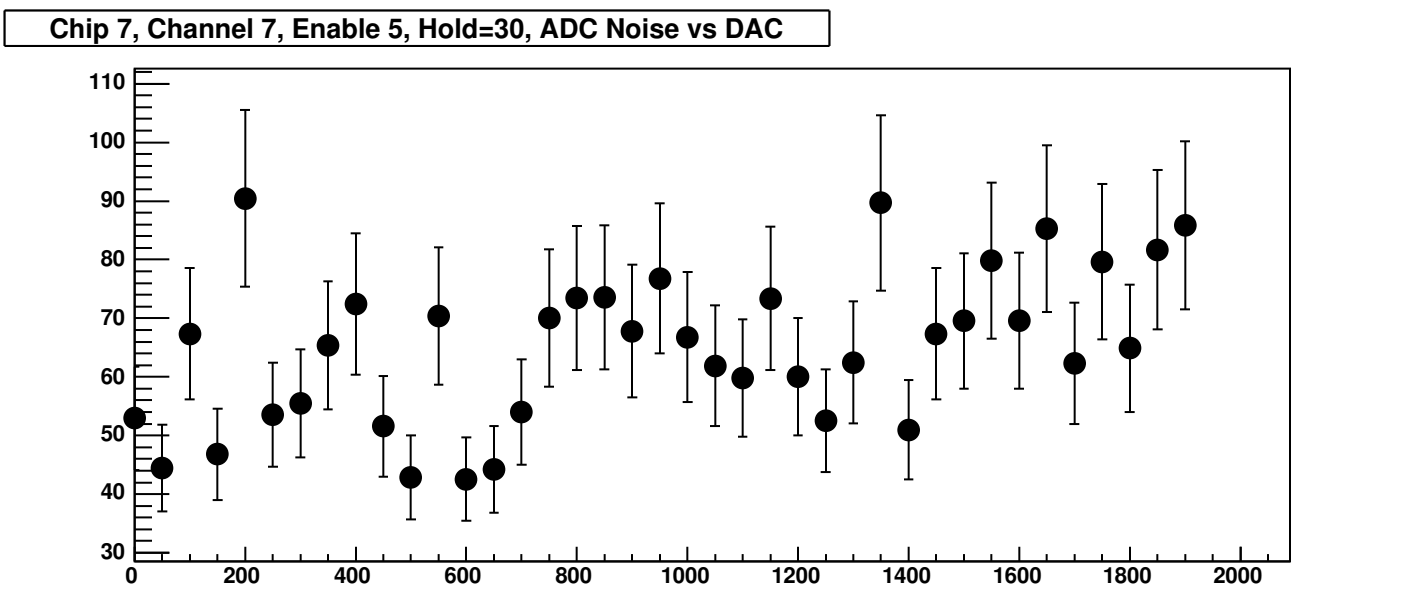
Chip 7, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC

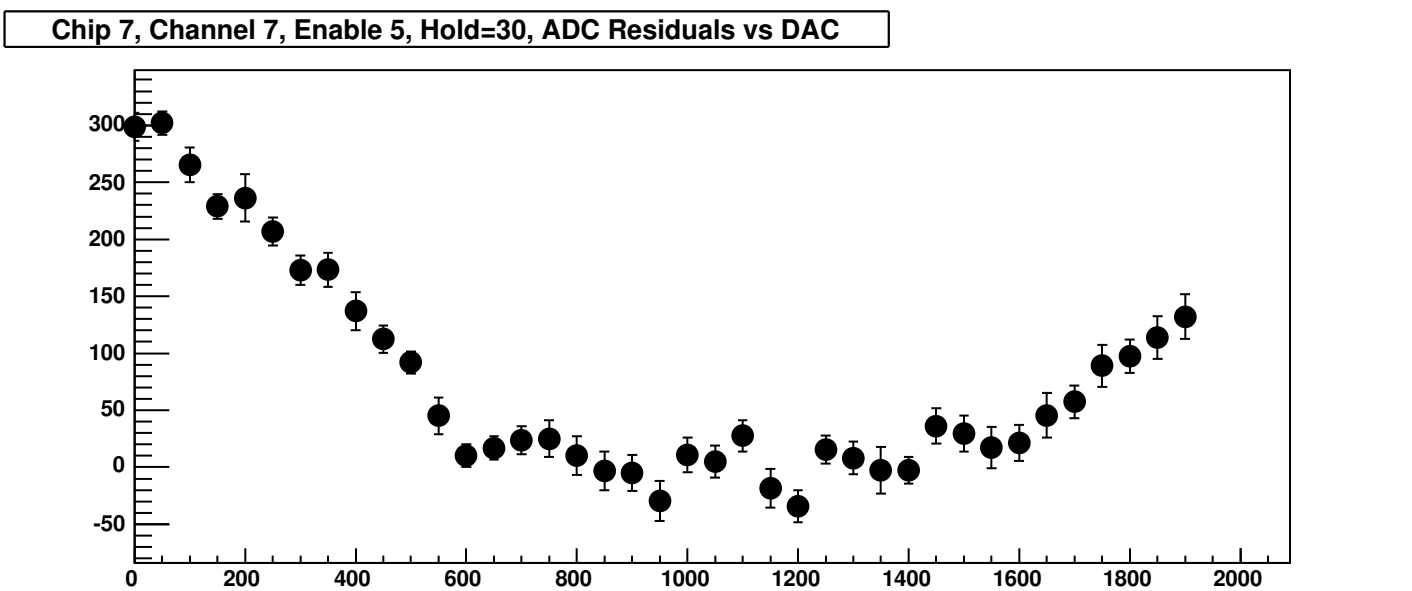


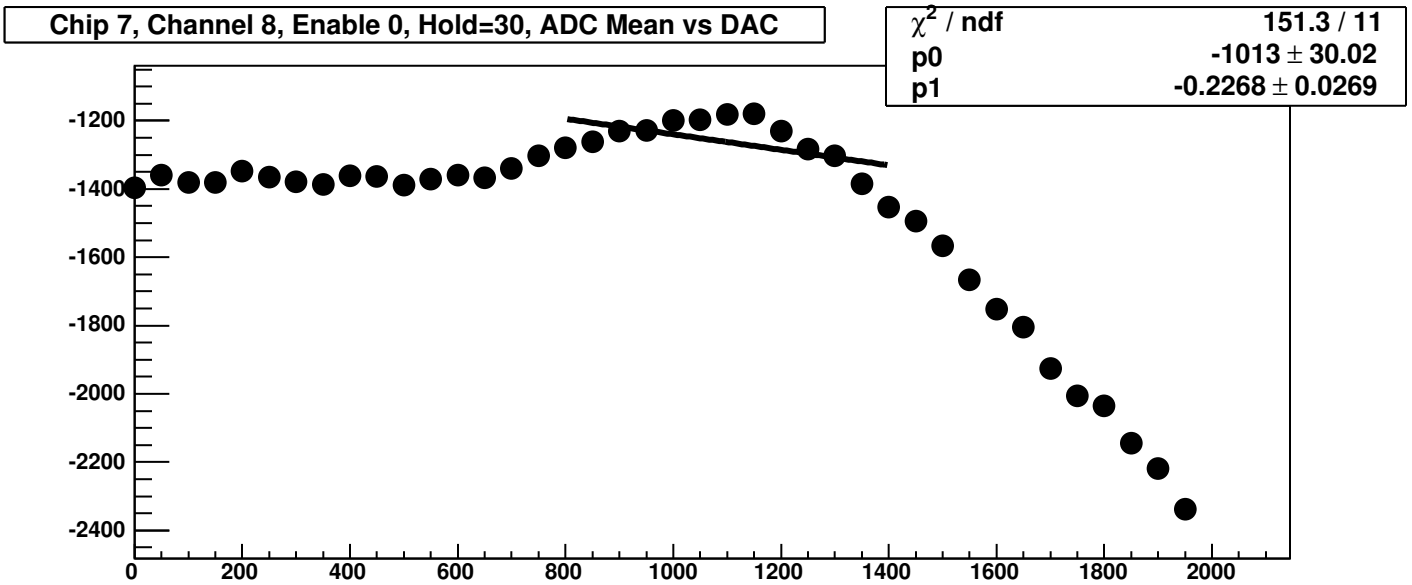
Chip 7, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC

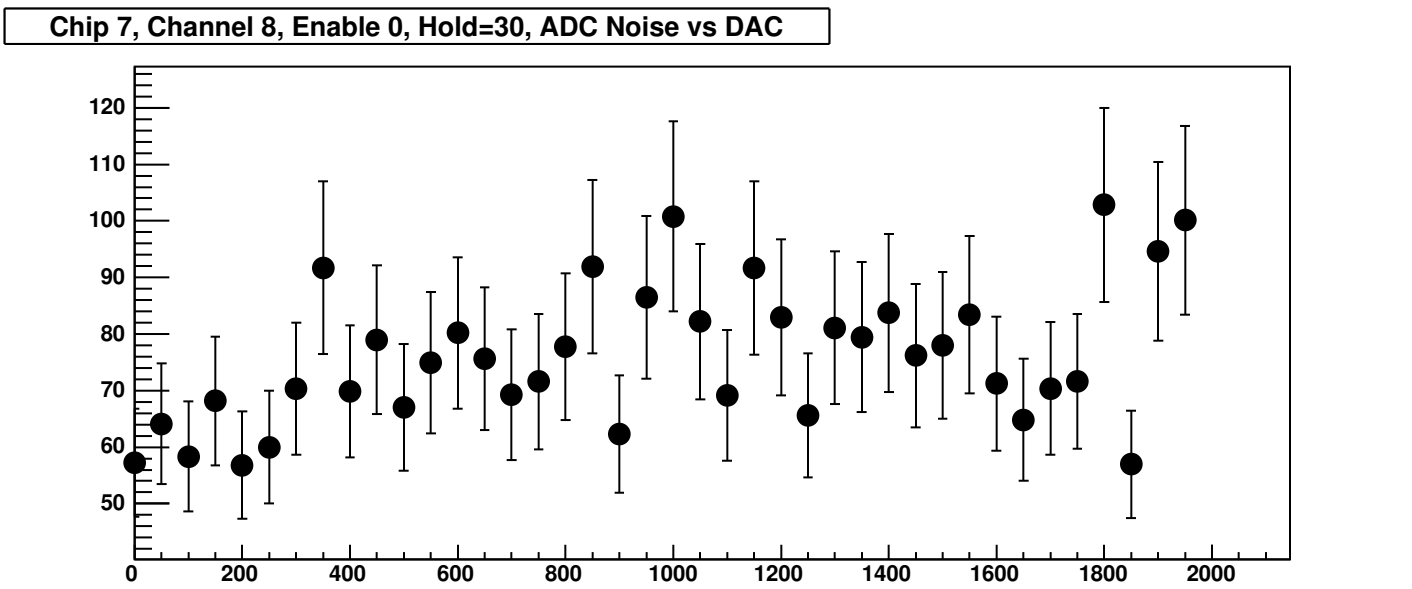


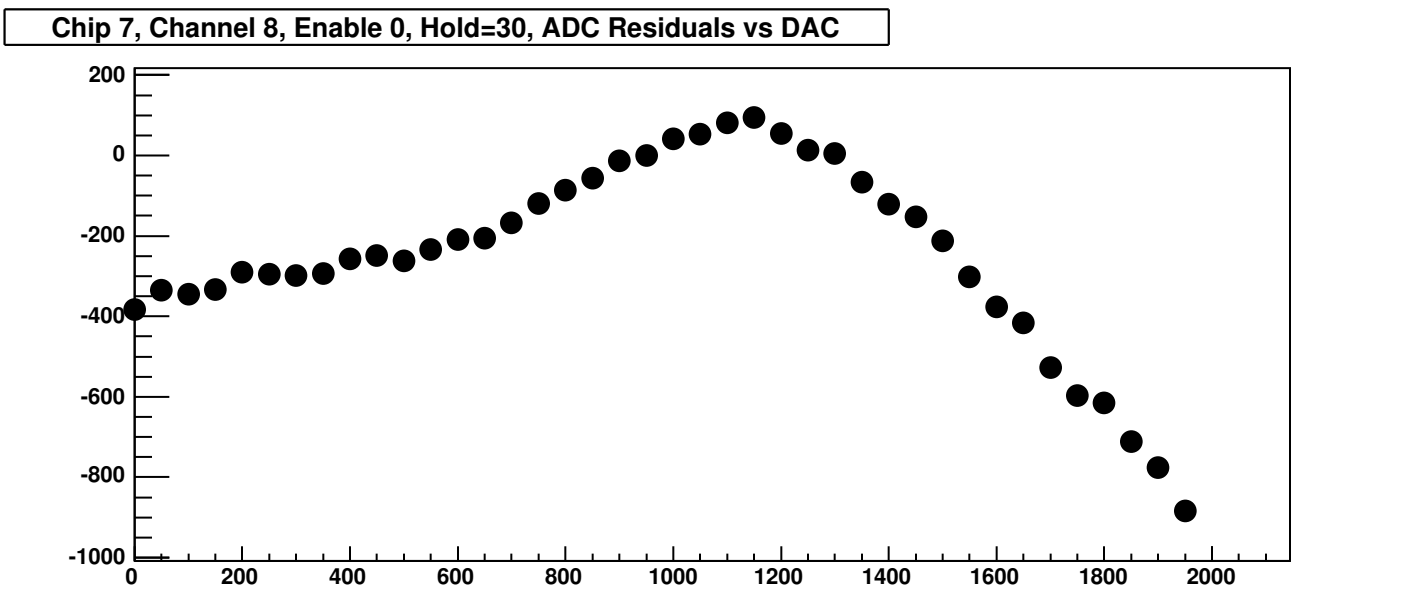

Chip 7, Channel 7, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 17.42 / 11
p0 -1680 ± 25.5
p1 0.4613 ± 0.02234
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

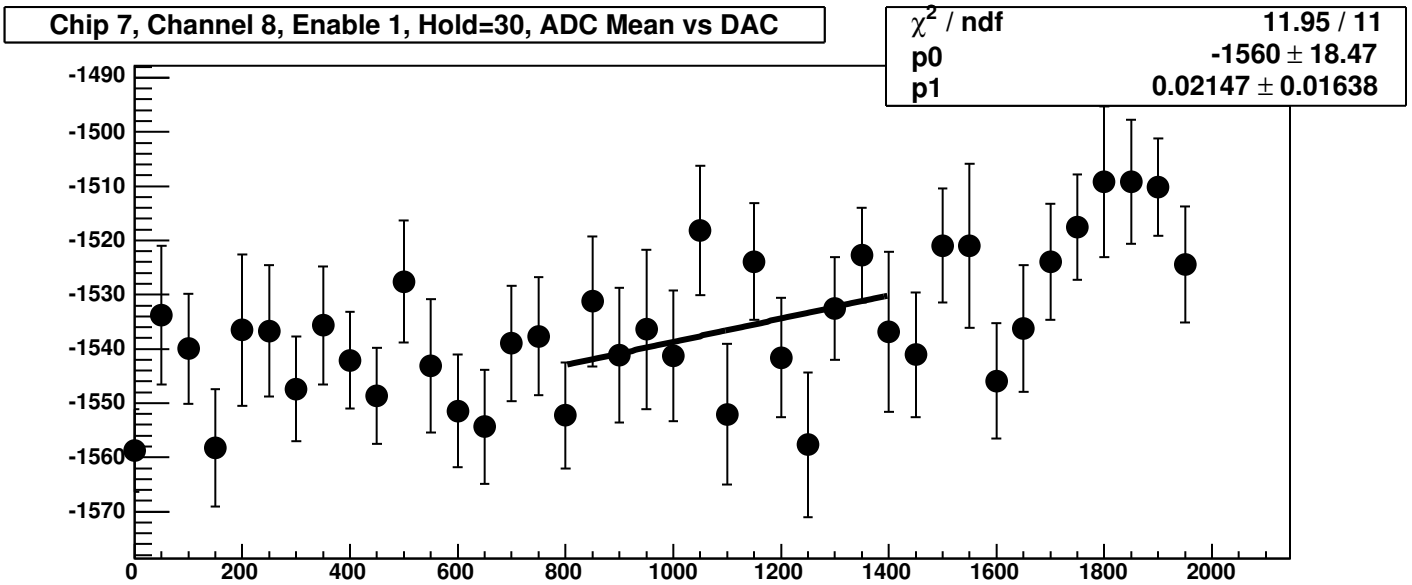

Chip 7, Channel 7, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

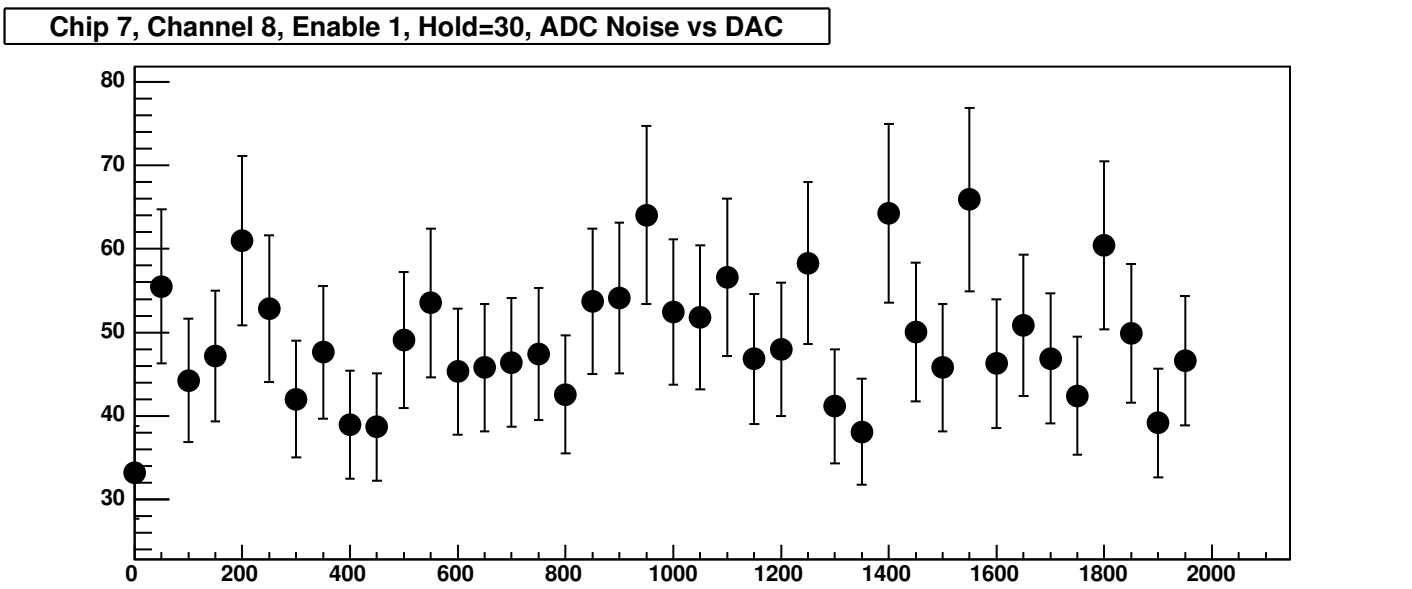

Chip 7, Channel 7, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

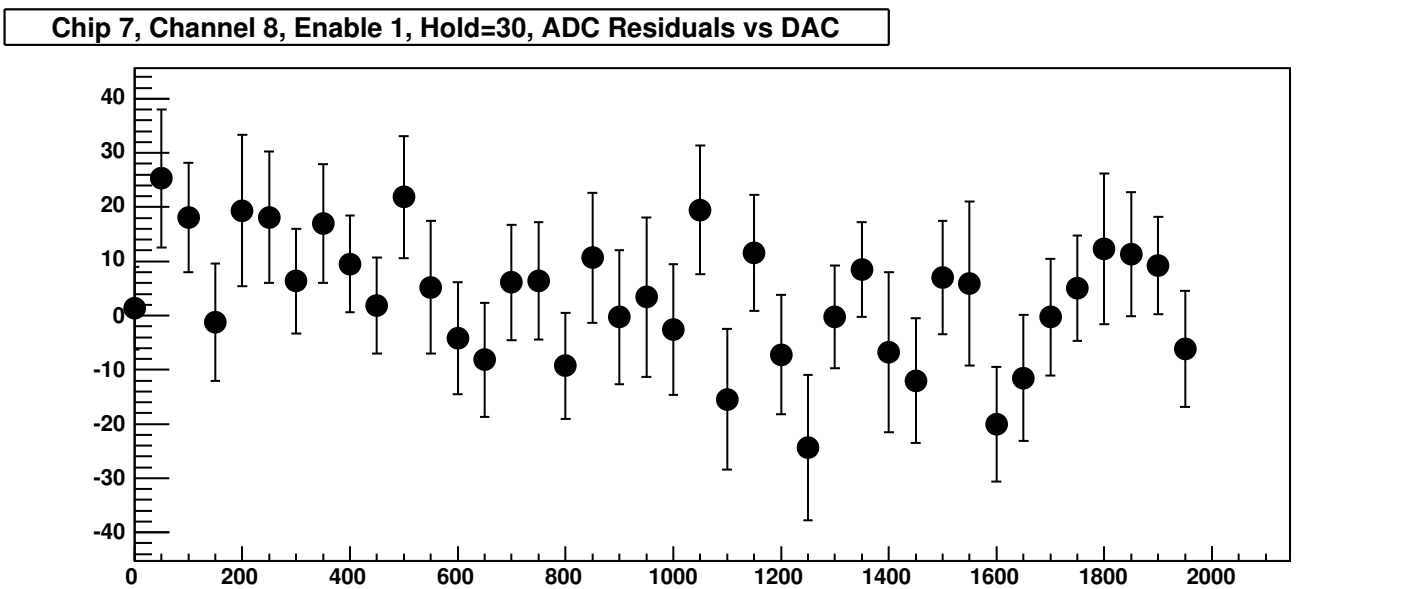

Chip 7, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 151.3 / 11
p0 -1013 ± 30.02
p1 -0.2268 ± 0.0269
-1200
-1400
-1600
-1800
-2000
-2200
-2400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

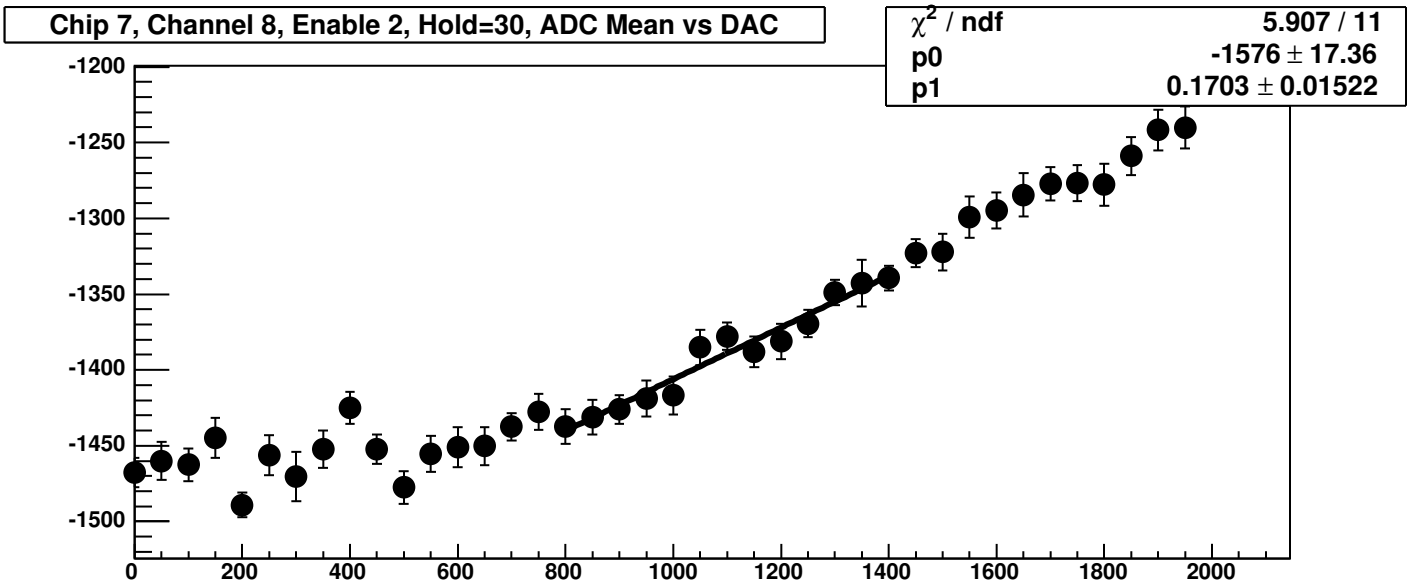

Chip 7, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC
120
110
100
90
80
70
60
50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

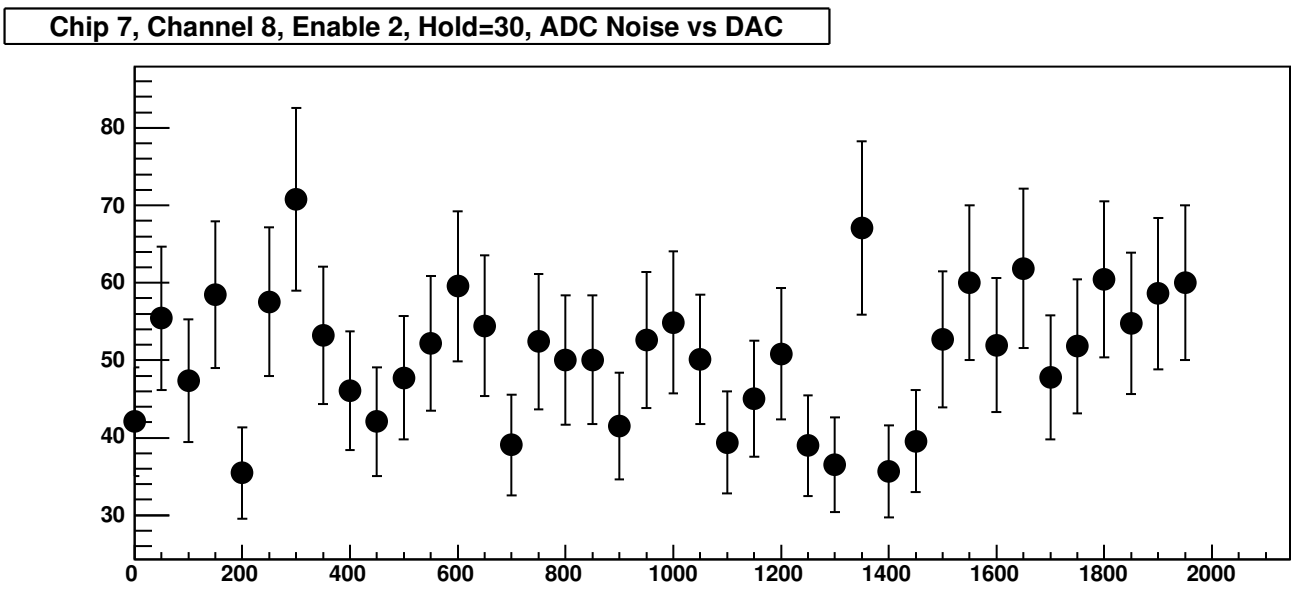

Chip 7, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

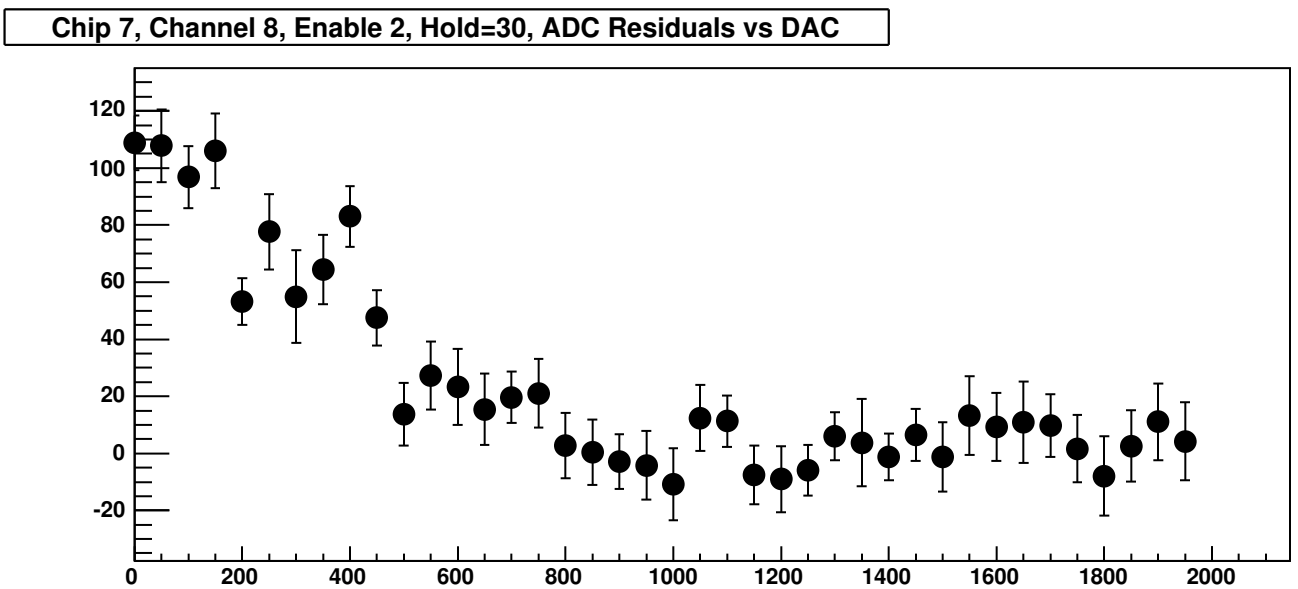

Chip 7, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 11.95 / 11
p0 -1560 ± 18.47
p1 0.02147 ± 0.01638
-1490
-1500
-1510
-1520
-1530
-1540
-1550
-1560
-1570
0
200
400
600
800
1000
1200
1400
1600
1800
2000

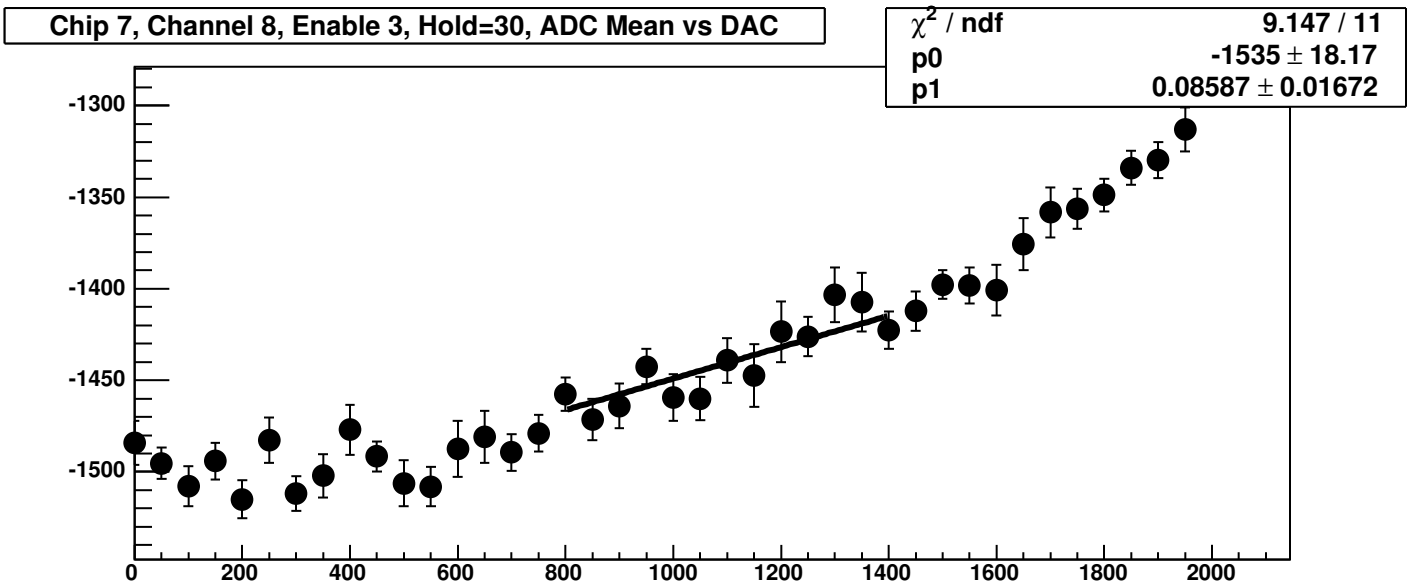

Chip 7, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

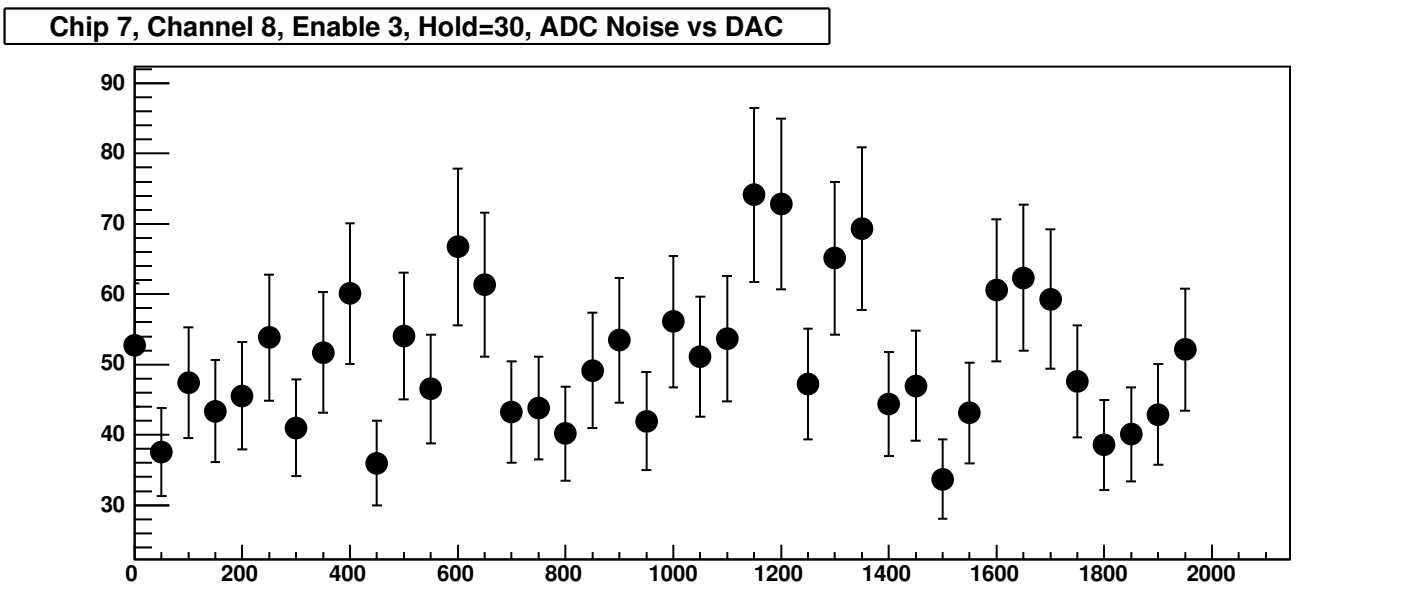

Chip 7, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
40
30
20
10
0
-10
-20
-30
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

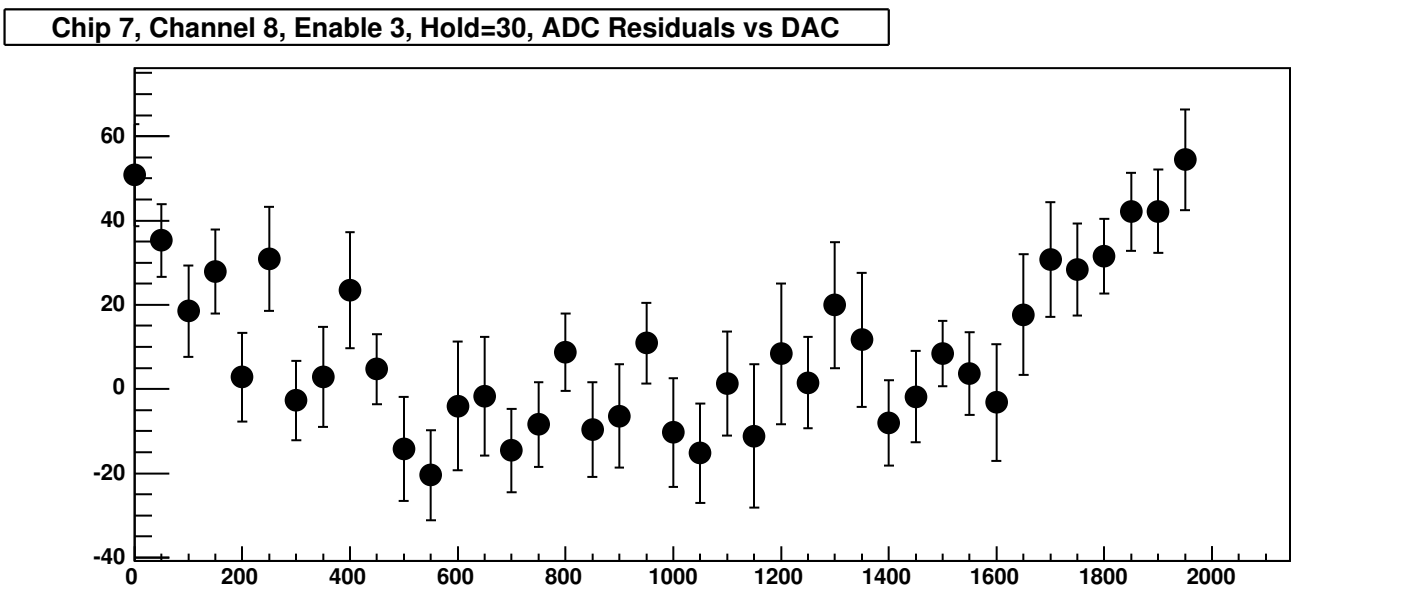

Chip 7, Channel 8, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 5.907 / 11
p0 -1576 ± 17.36
p1 0.1703 ± 0.01522
-1200
-1250
-1300
-1350
-1400
-1450
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 8, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

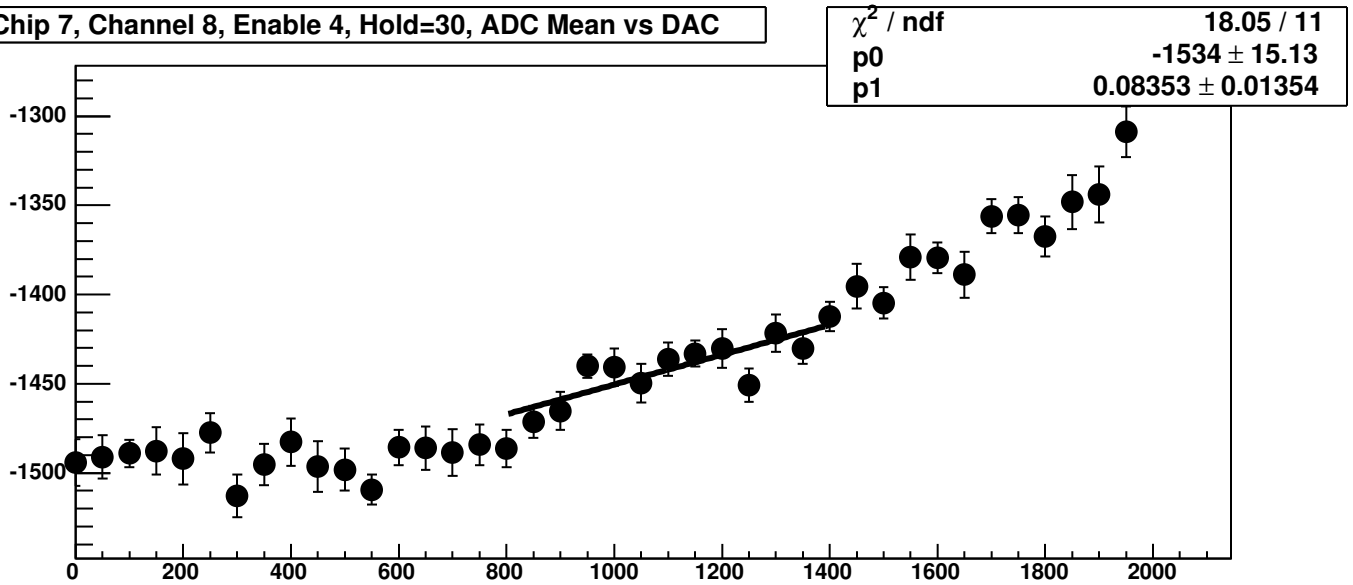

Chip 7, Channel 8, Enable 2, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 9.147 / 11
p0 -1535 ± 18.17
p1 0.08587 ± 0.01672
-1300
-1350
-1400
-1450
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

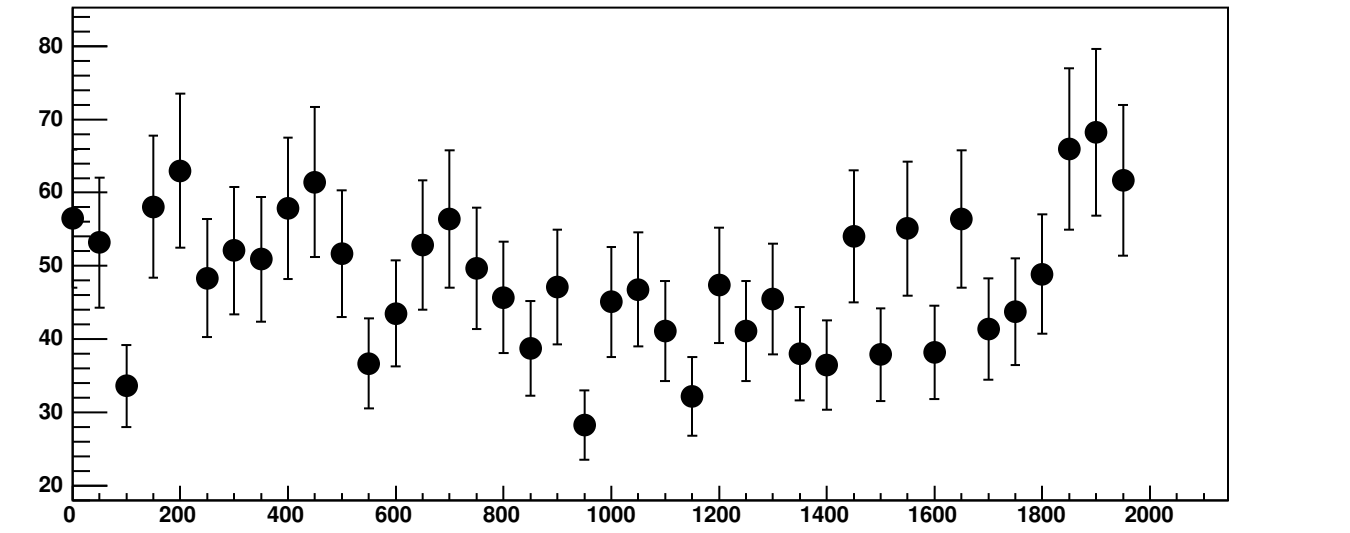

Chip 7, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

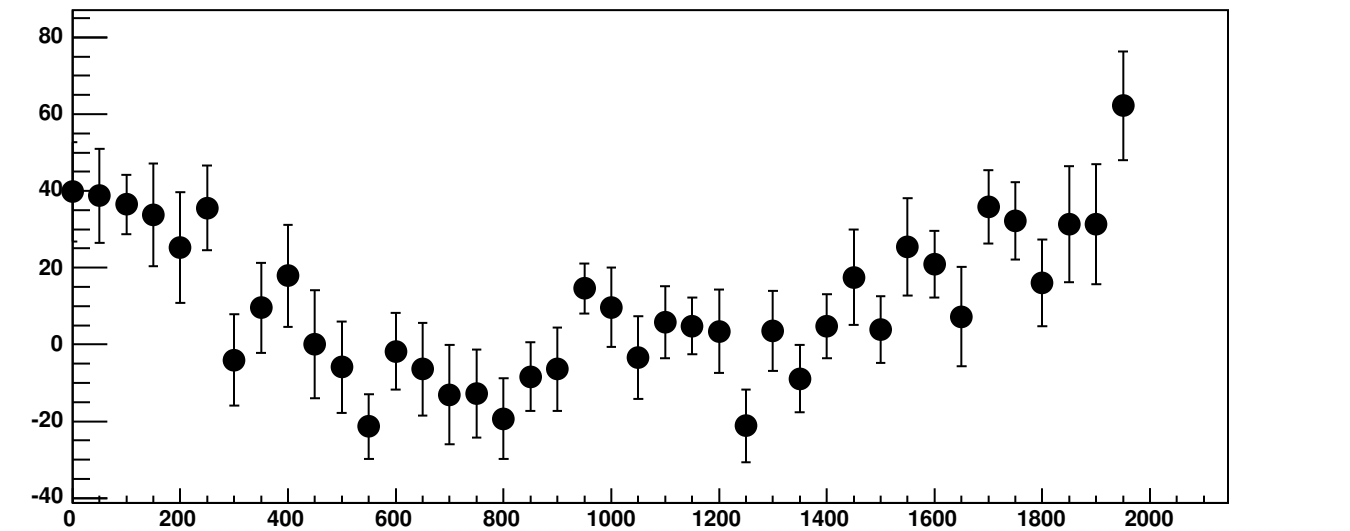
Chip 7, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC

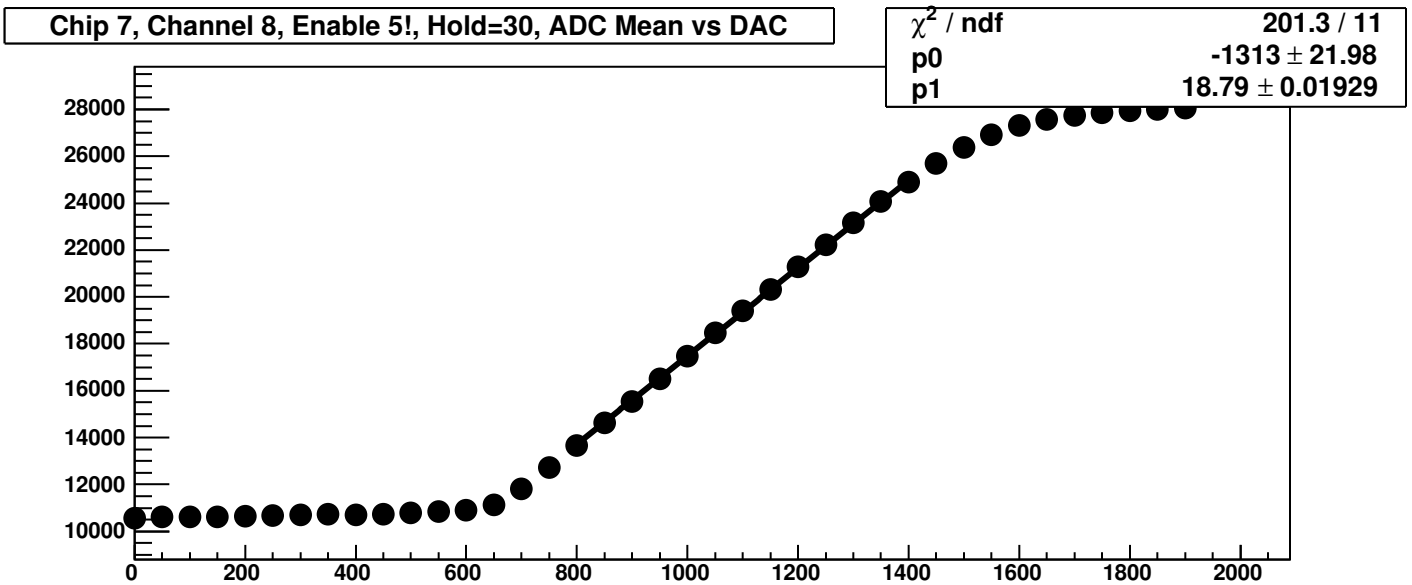


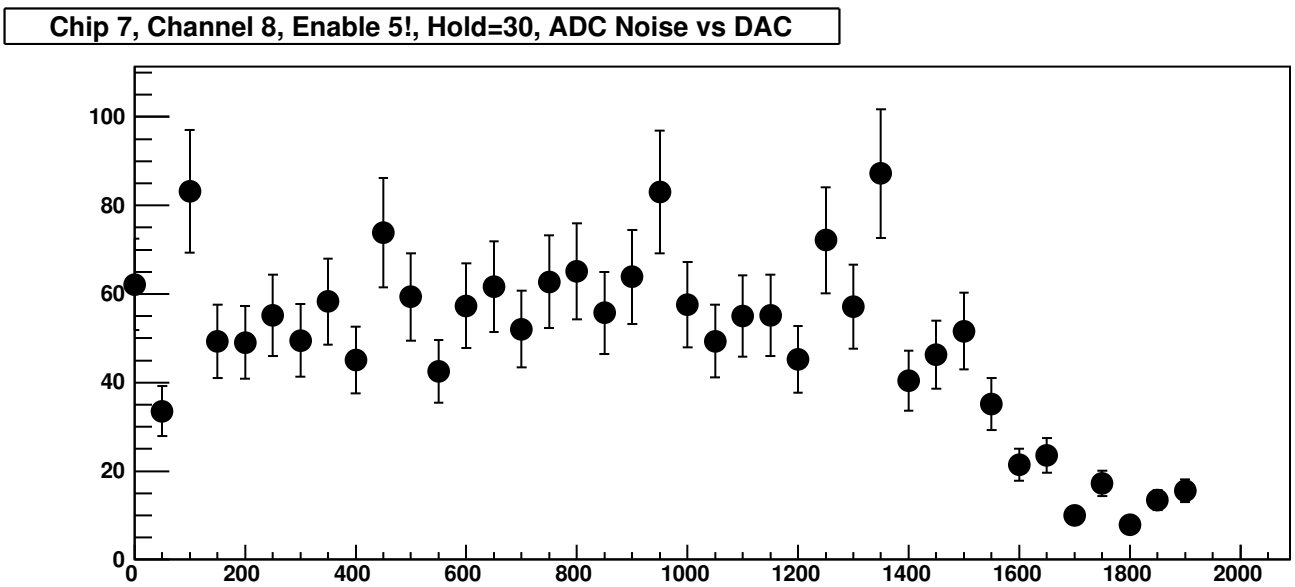
Chip 7, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC

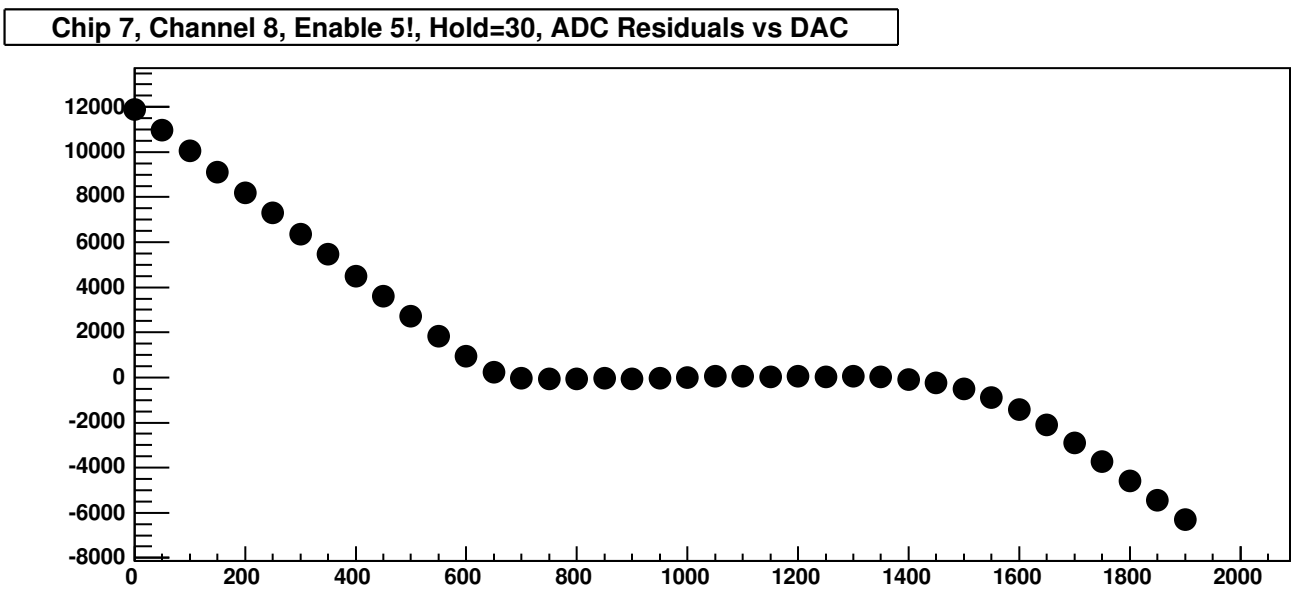


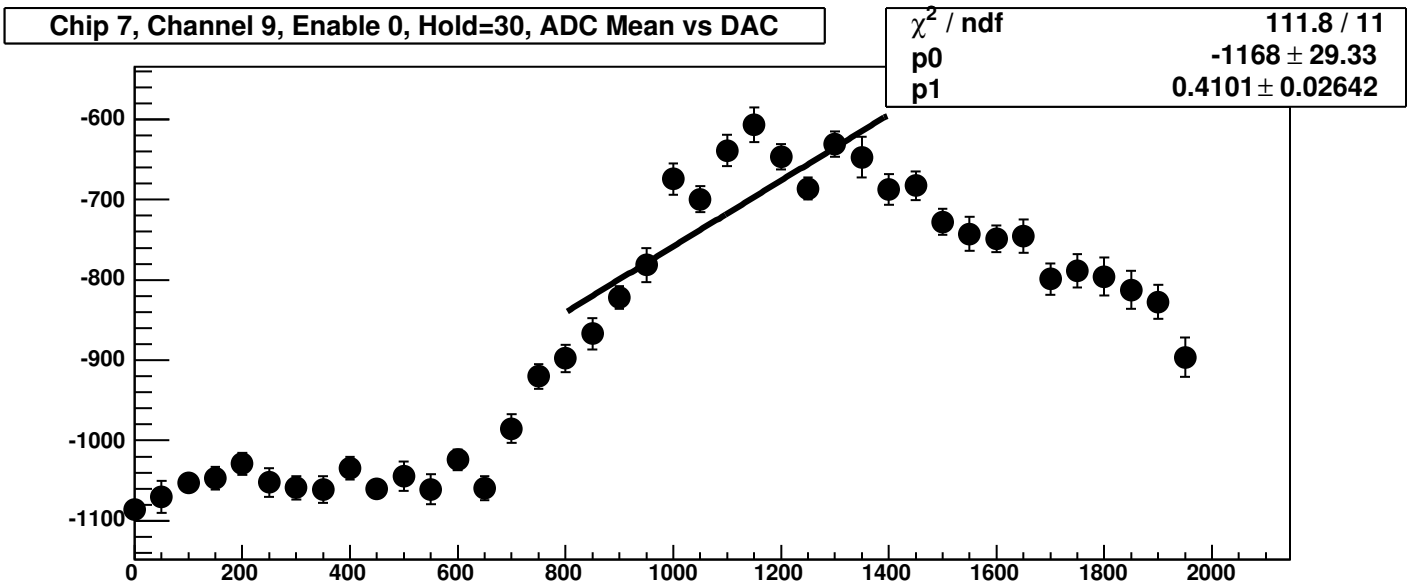
Chip 7, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC

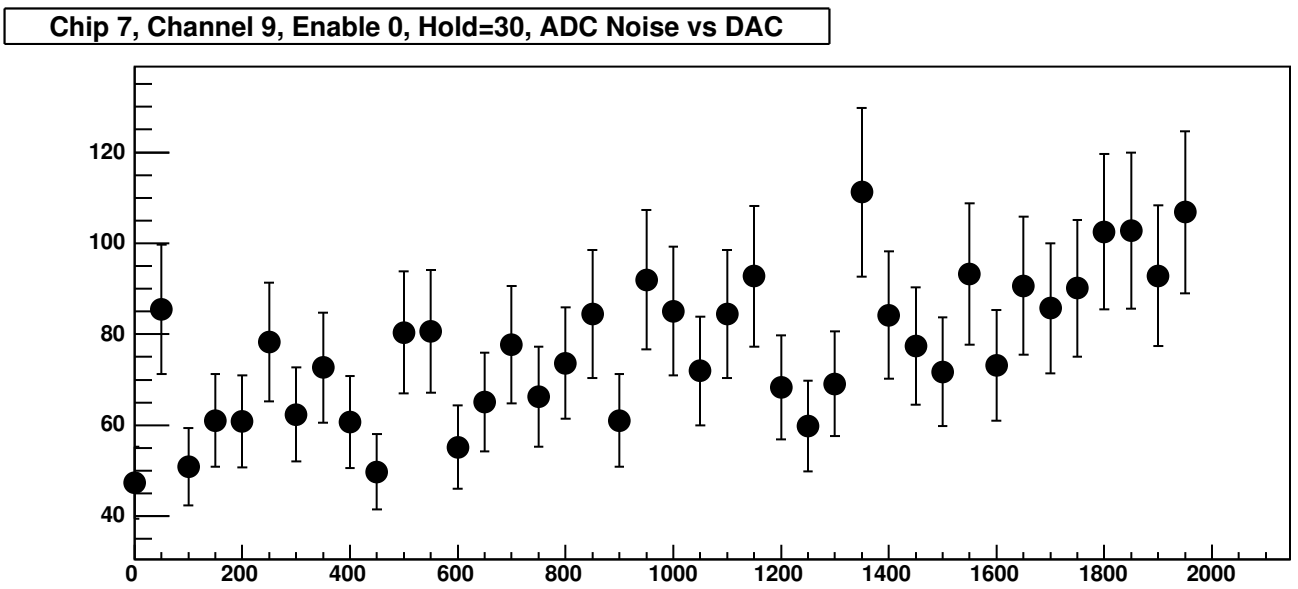


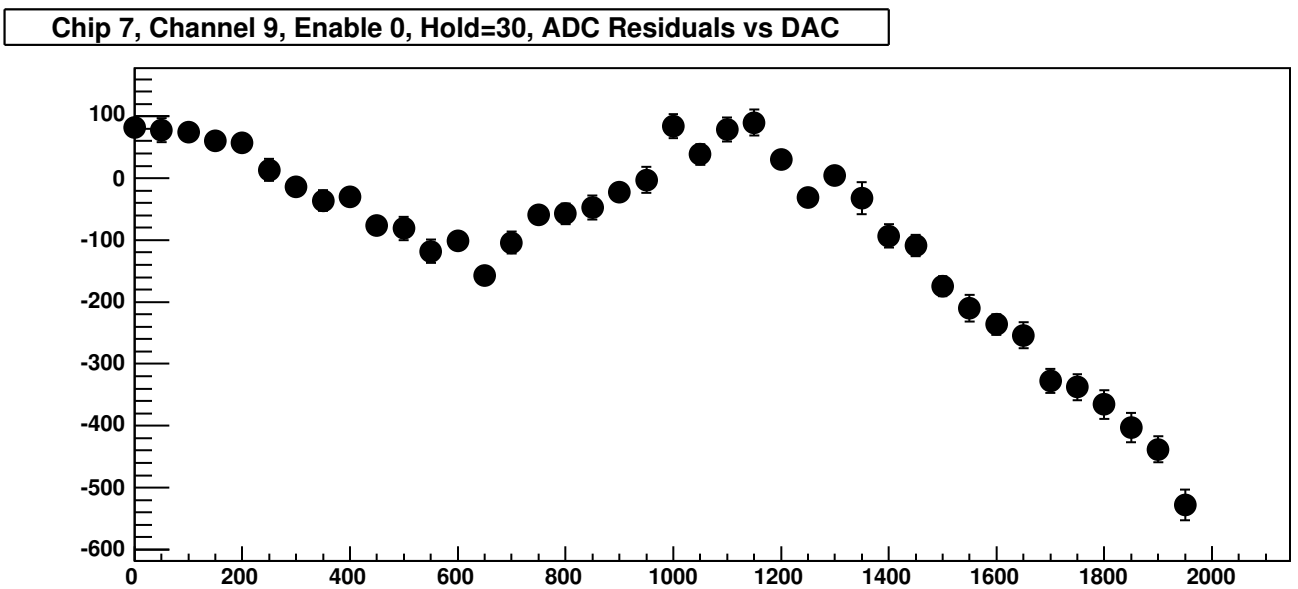

Chip 7, Channel 8, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 201.3 / 11
p0 -1313 ± 21.98
p1 18.79 ± 0.01929
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

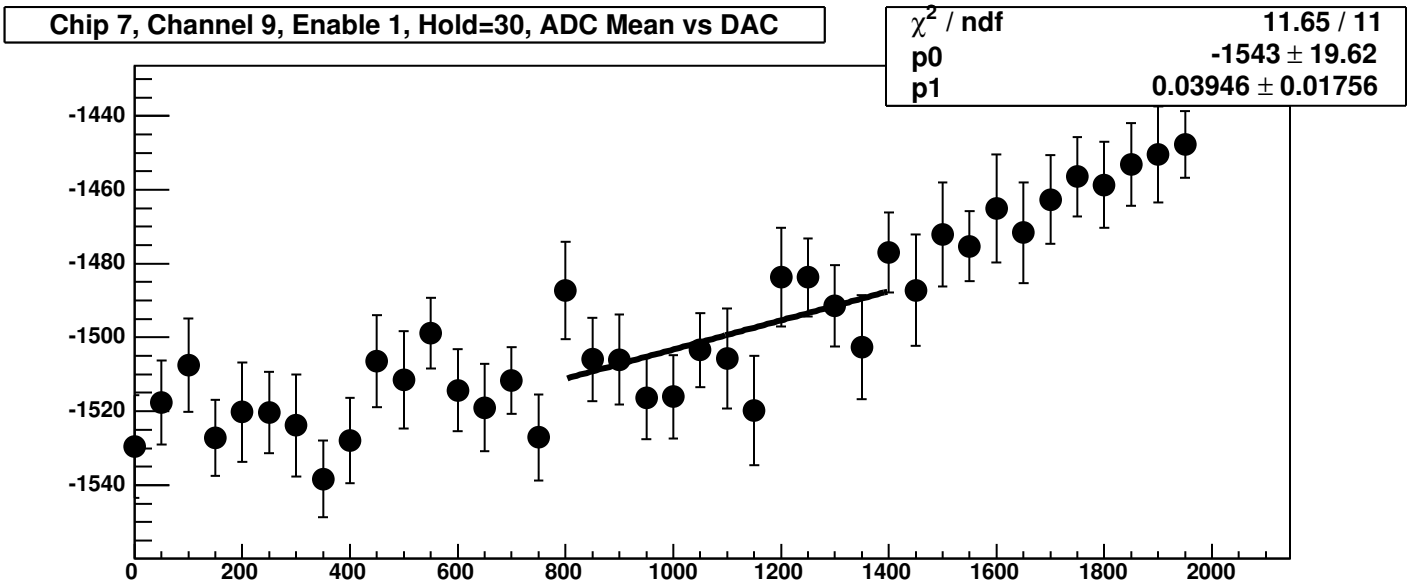

Chip 7, Channel 8, Enable 5!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

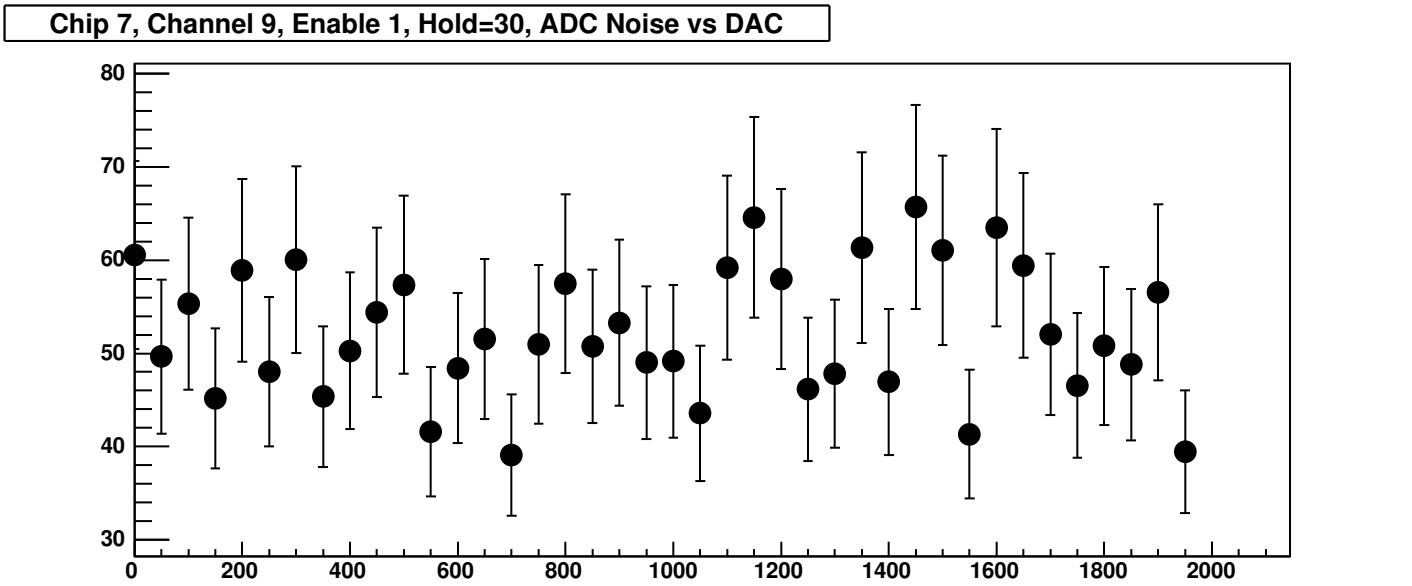

Chip 7, Channel 8, Enable 5!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

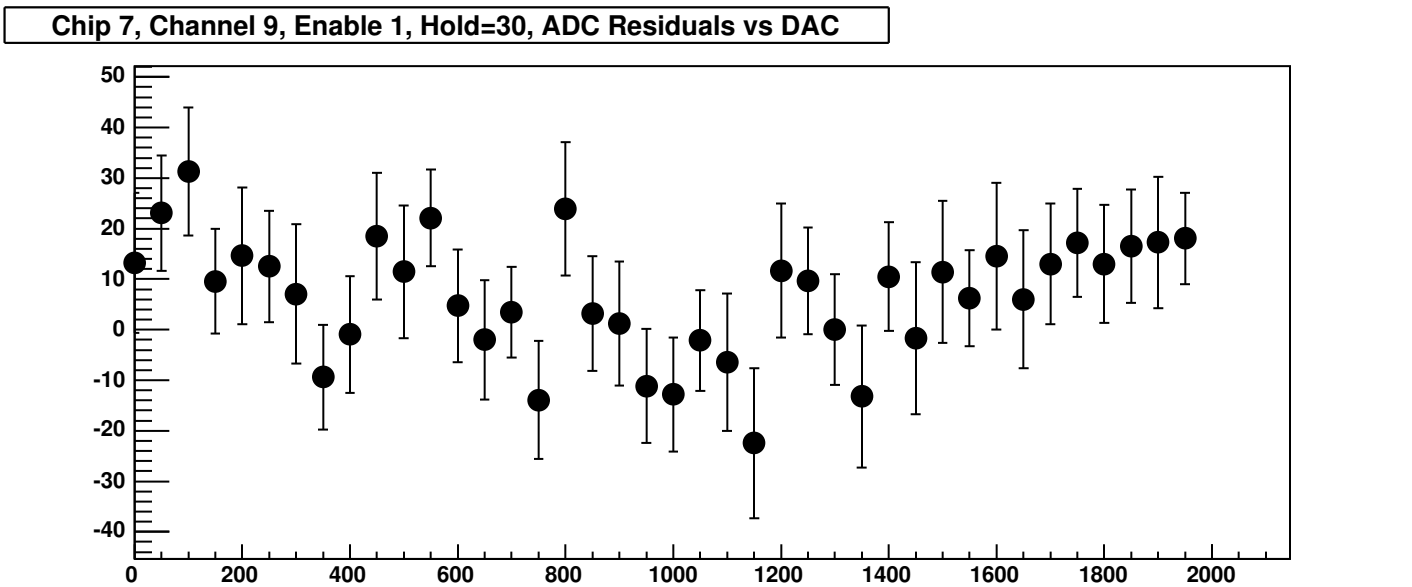

Chip 7, Channel 9, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 111.8 / 11
p0 -1168 ± 29.33
p1 0.4101 ± 0.02642
-600
-700
-800
-900
-1000
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 9, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

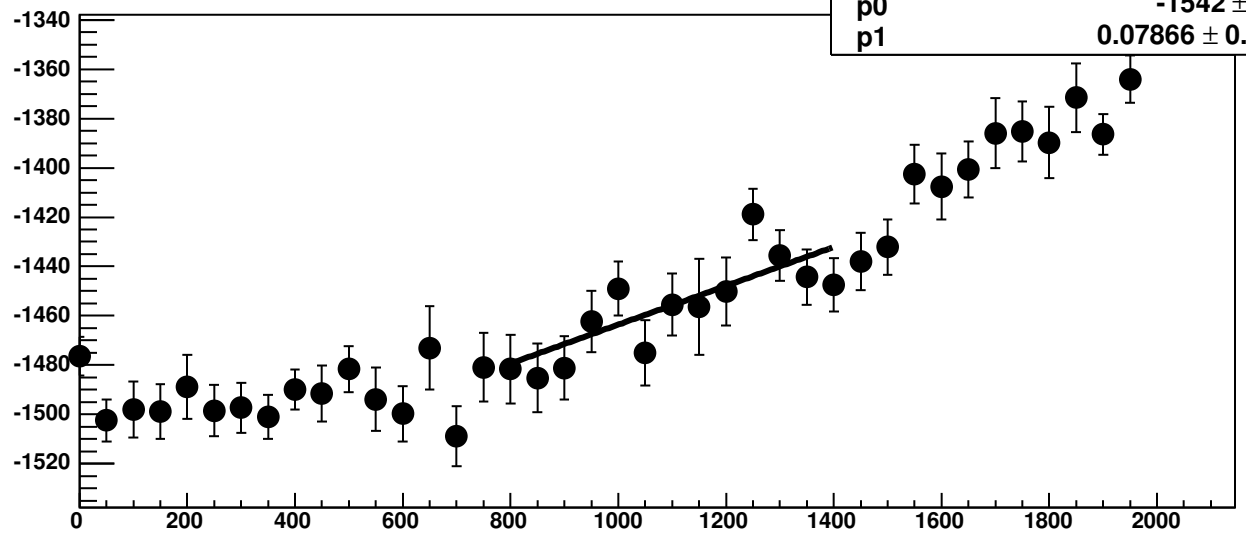

Chip 7, Channel 9, Enable 0, Hold=30, ADC Residuals vs DAC
100
0
-100
-200
-300
-400
-500
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 11.65 / 11
p0 -1543 ± 19.62
p1 0.03946 ± 0.01756
-1440
-1460
-1480
-1500
-1520
-1540
0
200
400
600
800
1000
1200
1400
1600
1800
2000

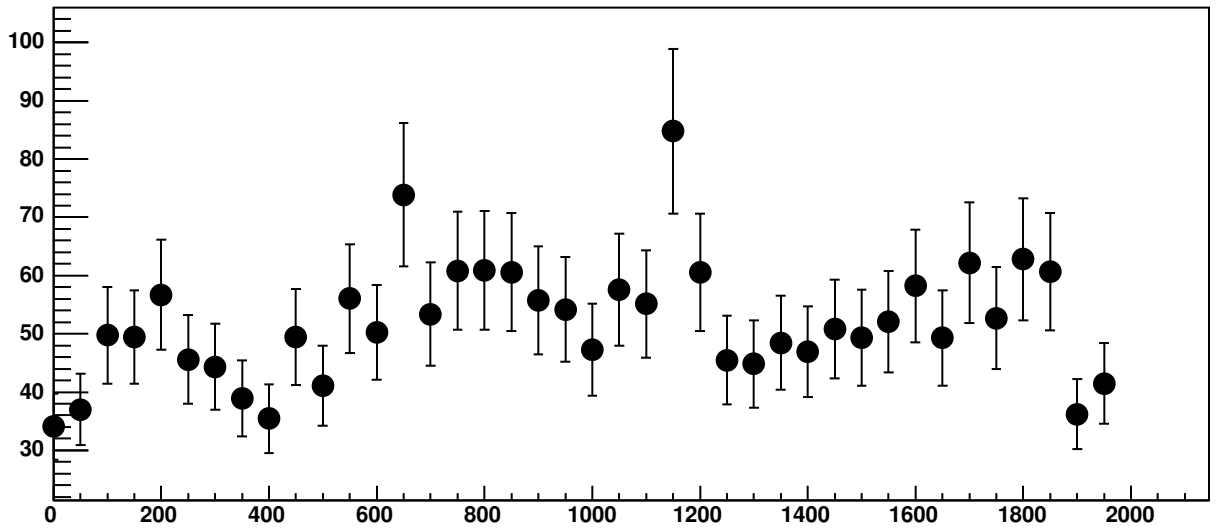

Chip 7, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC
50
40
30
20
10
0
-10
-20
-30
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

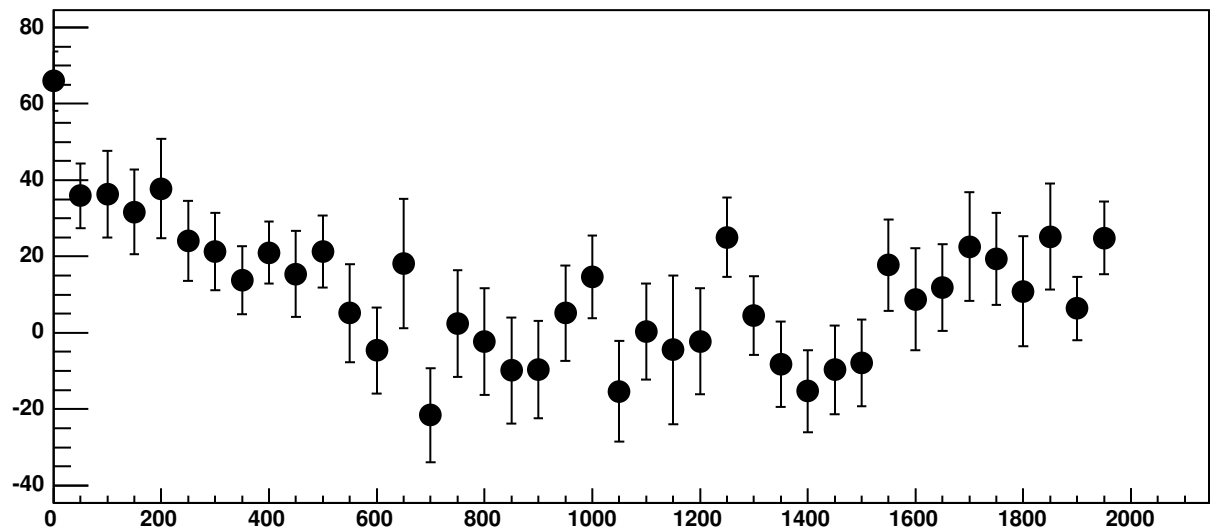
Chip 7, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC

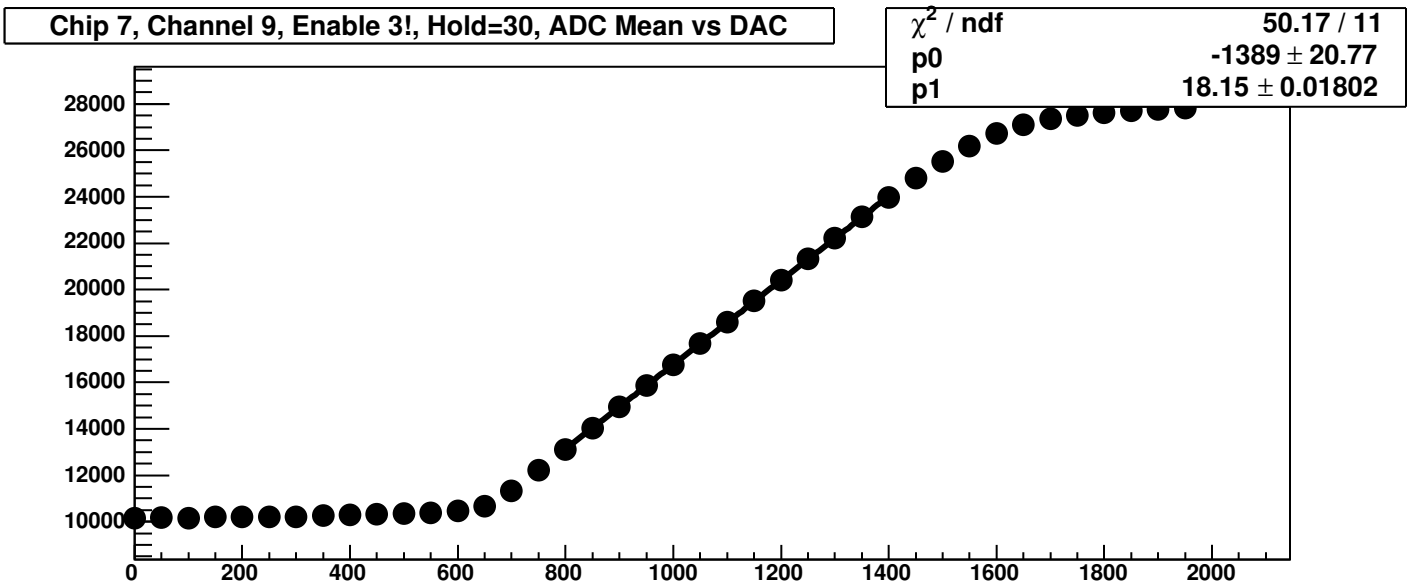


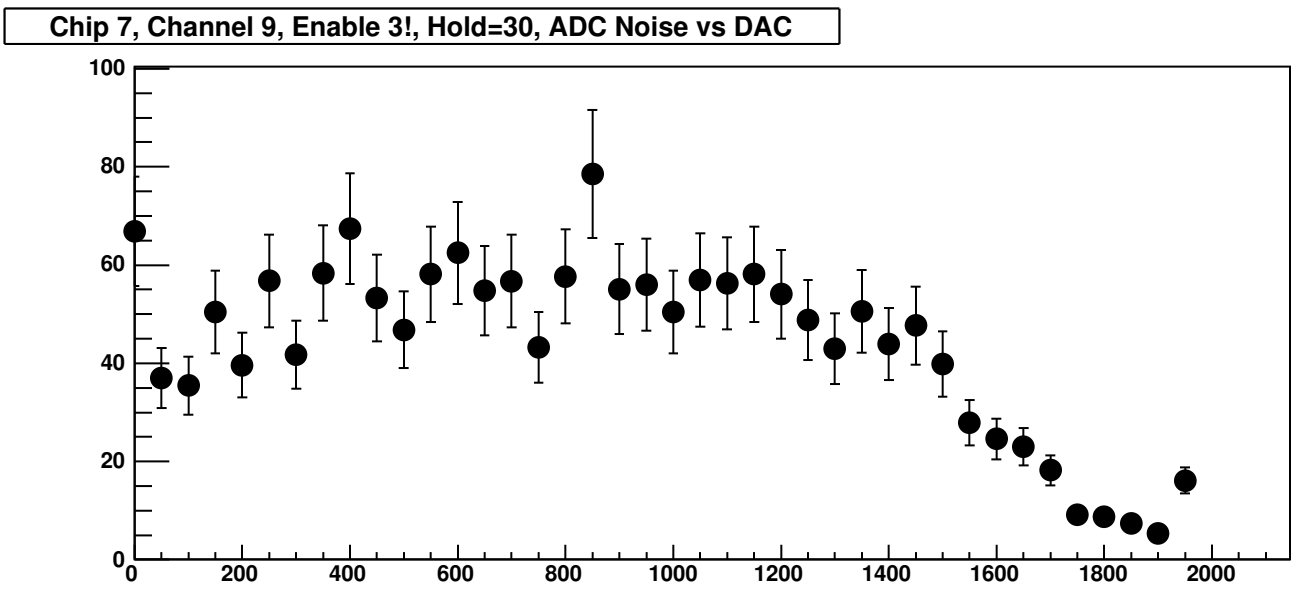
Chip 7, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC

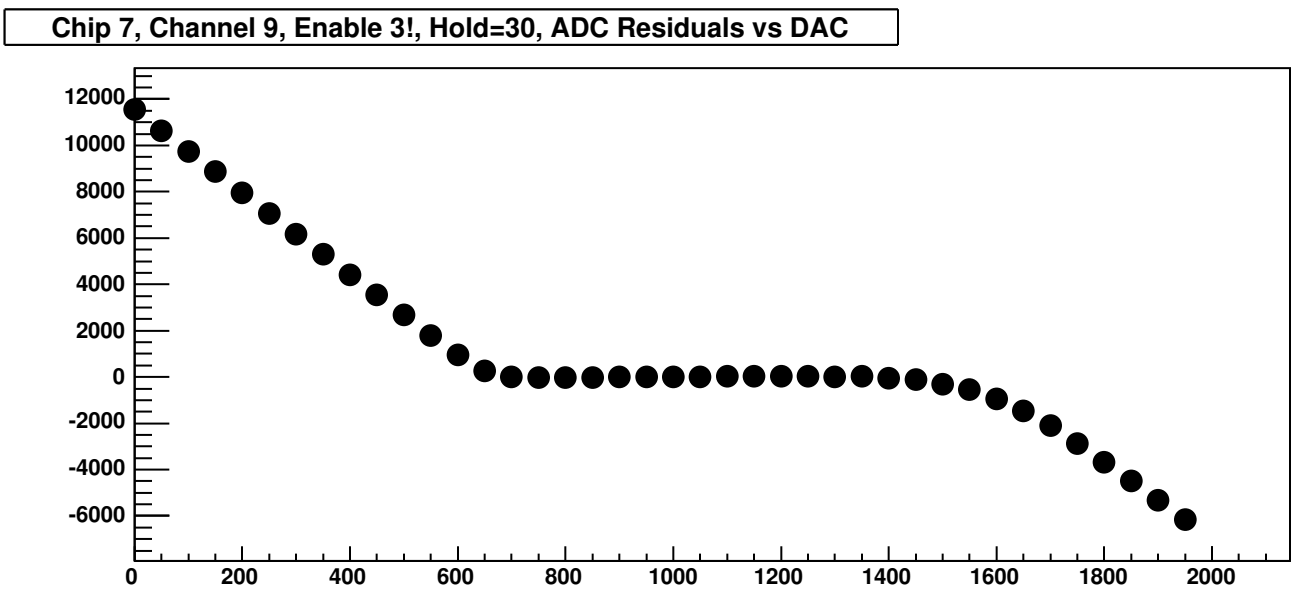


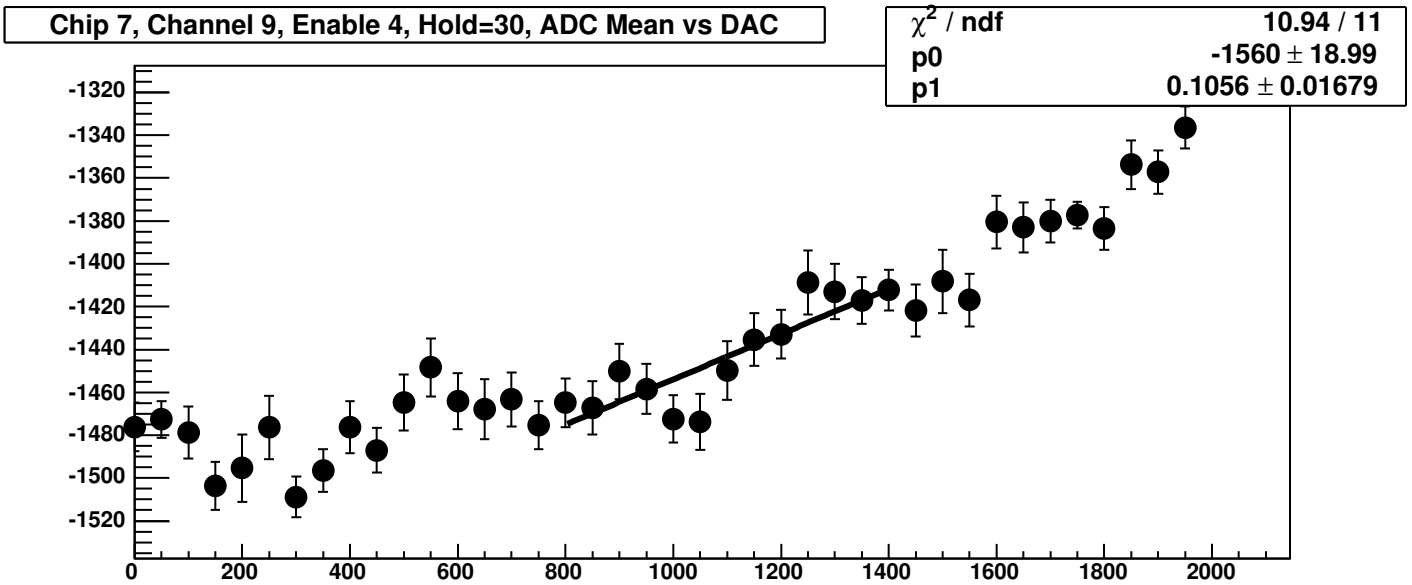
Chip 7, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC

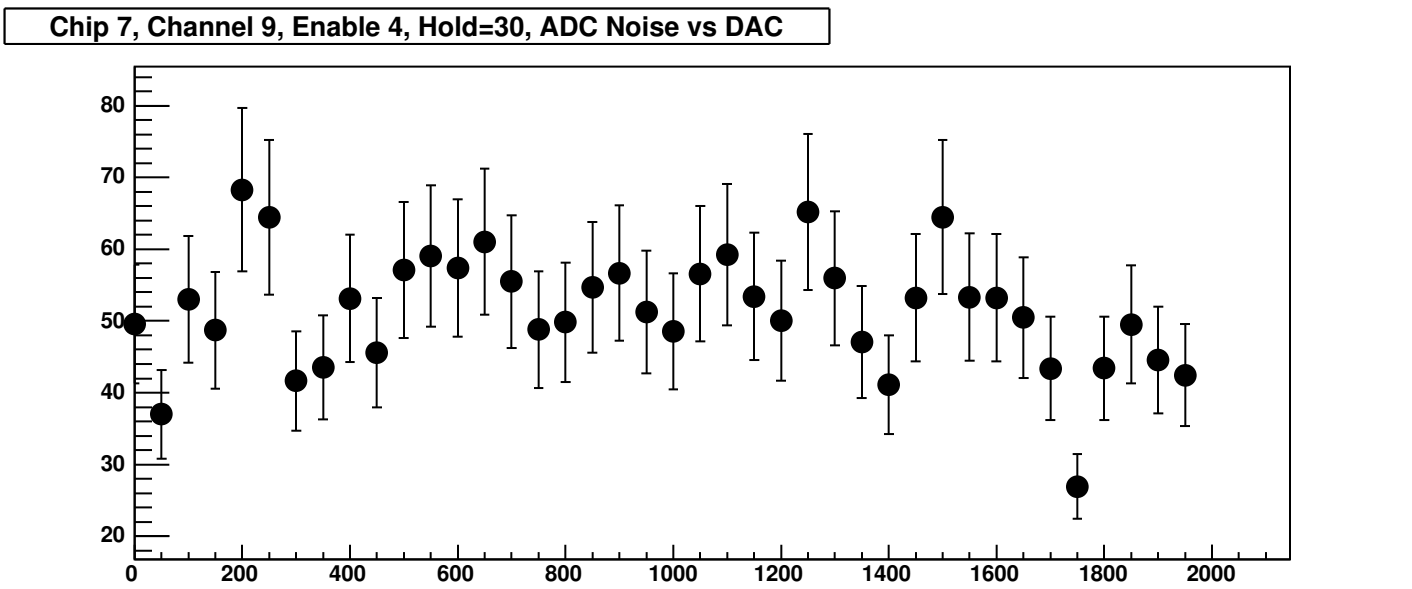


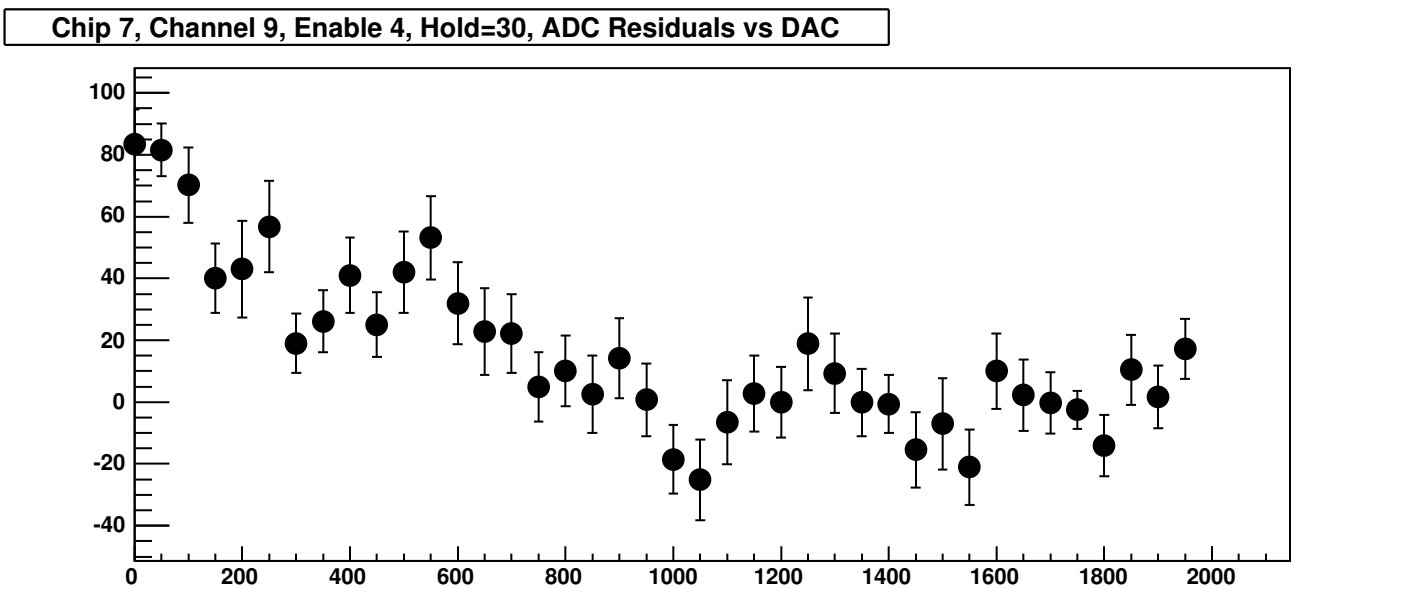

Chip 7, Channel 9, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 50.17 / 11
p0 -1389 ± 20.77
p1 18.15 ± 0.01802
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

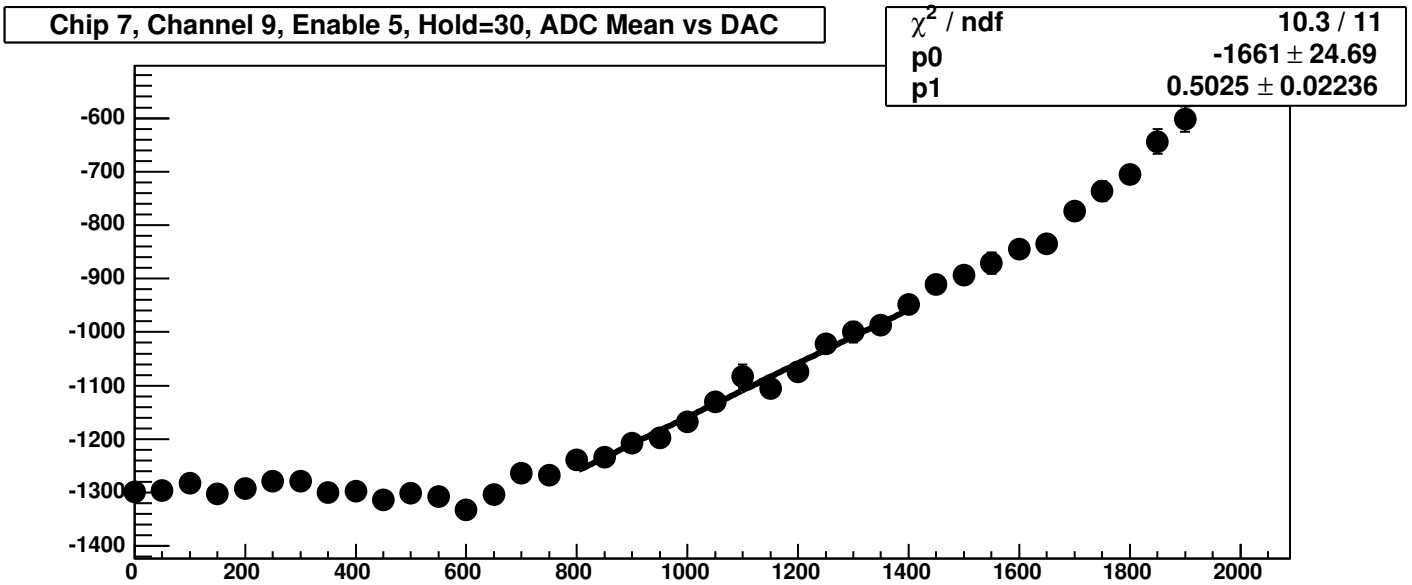

Chip 7, Channel 9, Enable 3!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

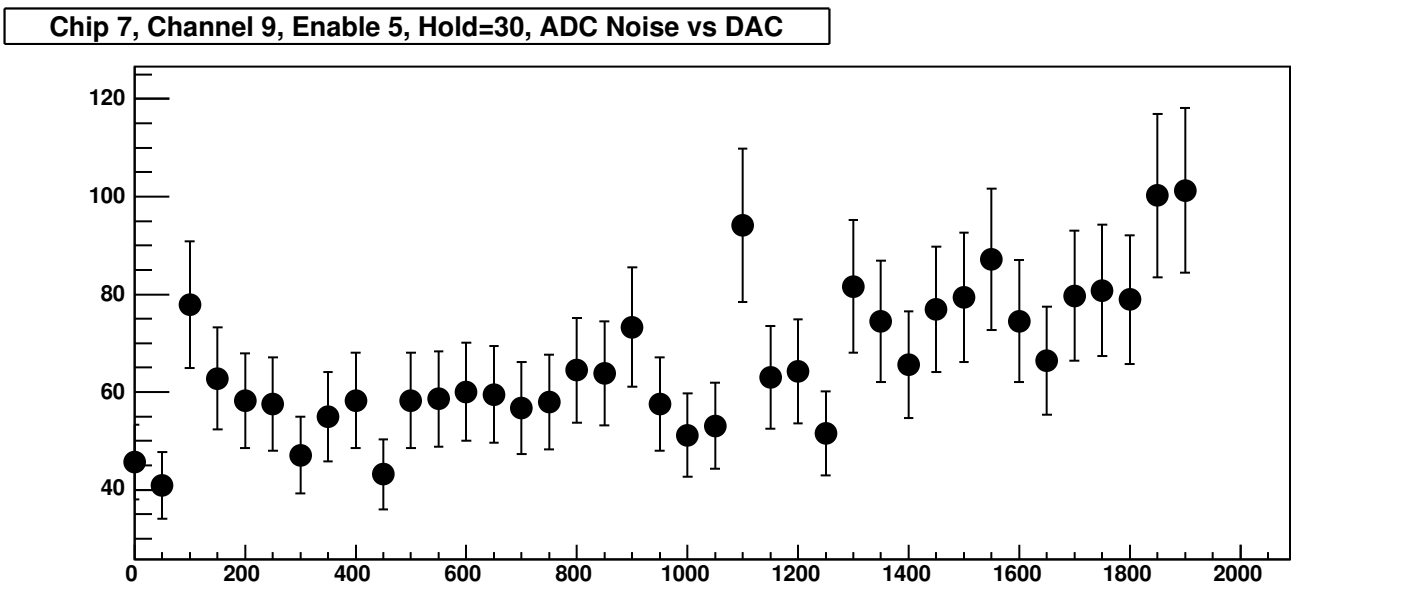

Chip 7, Channel 9, Enable 3!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

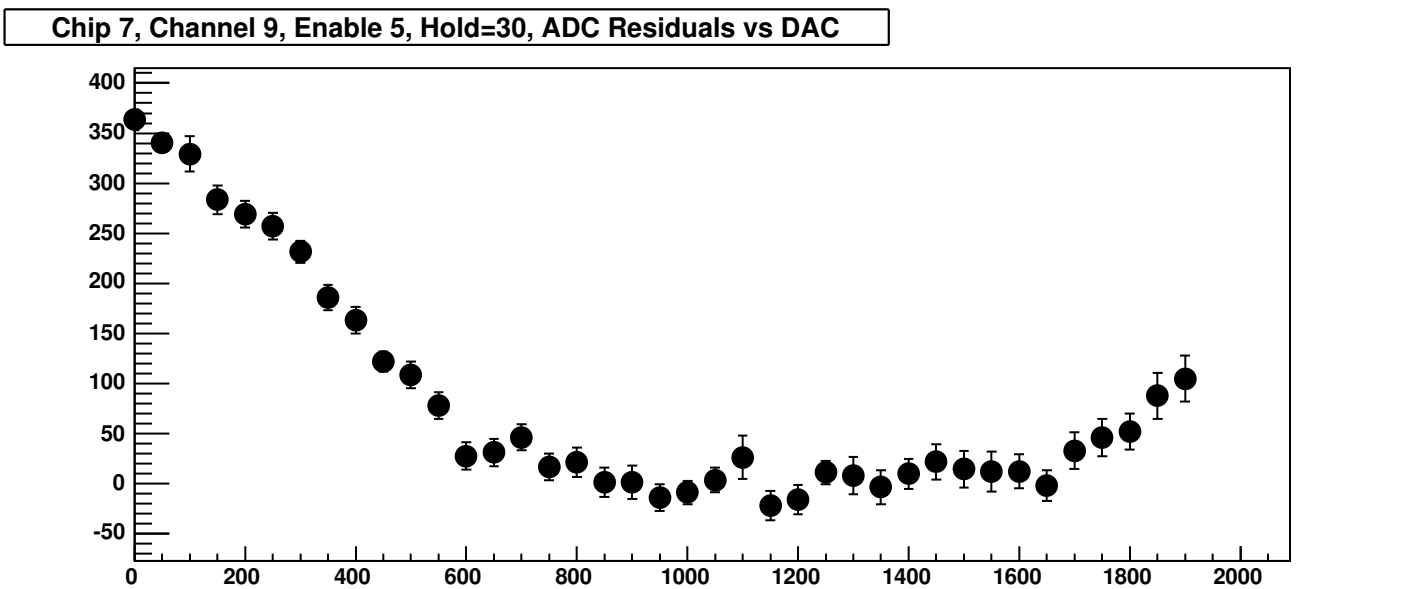

Chip 7, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 10.94 / 11
p0 -1560 ± 18.99
p1 0.1056 ± 0.01679
-1320
-1340
-1360
-1380
-1400
-1420
-1440
-1460
-1480
-1500
-1520
0
200
400
600
800
1000
1200
1400
1600
1800
2000

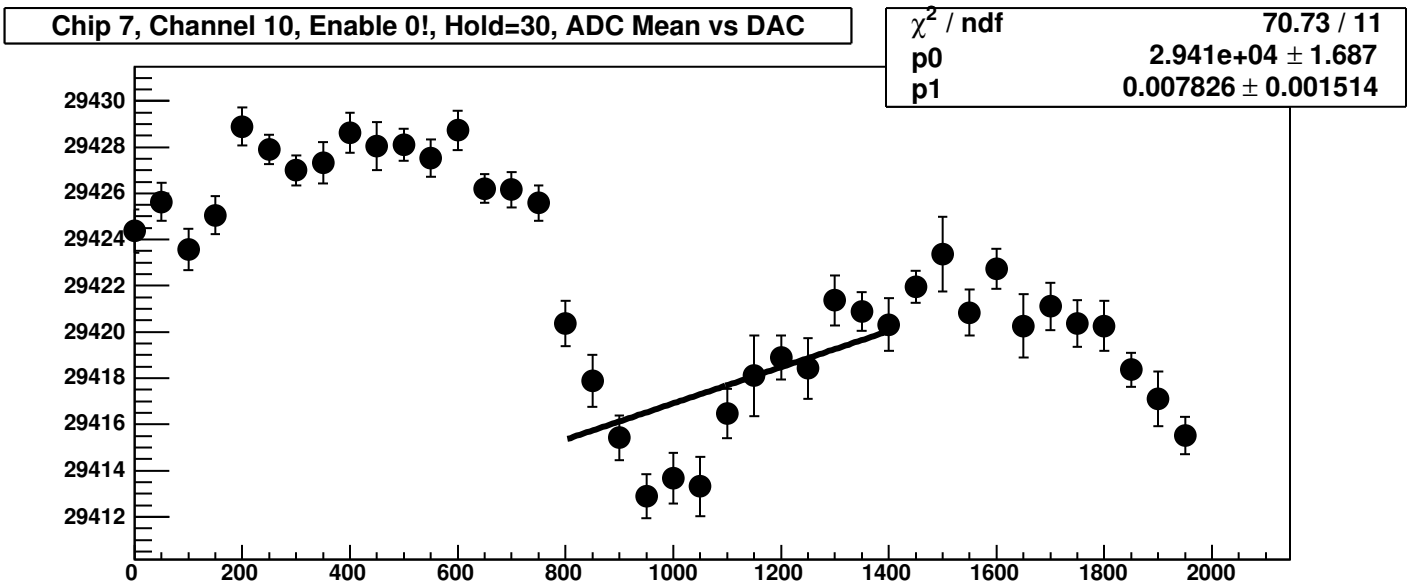

Chip 7, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

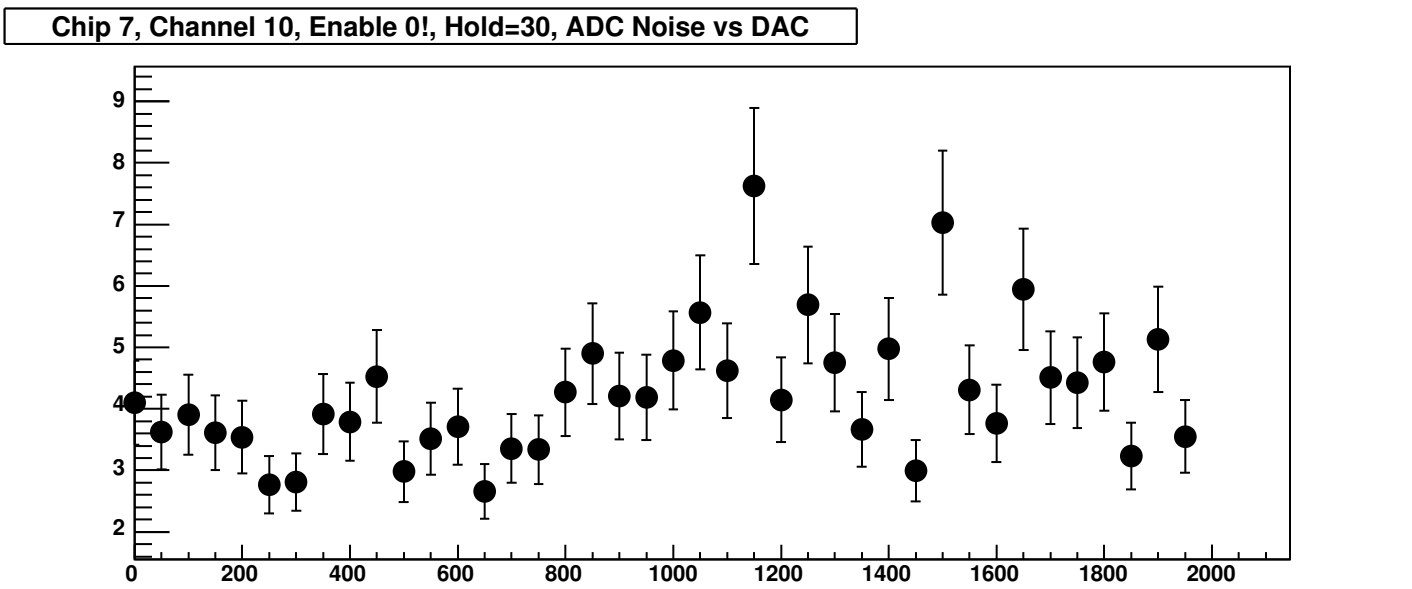

Chip 7, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

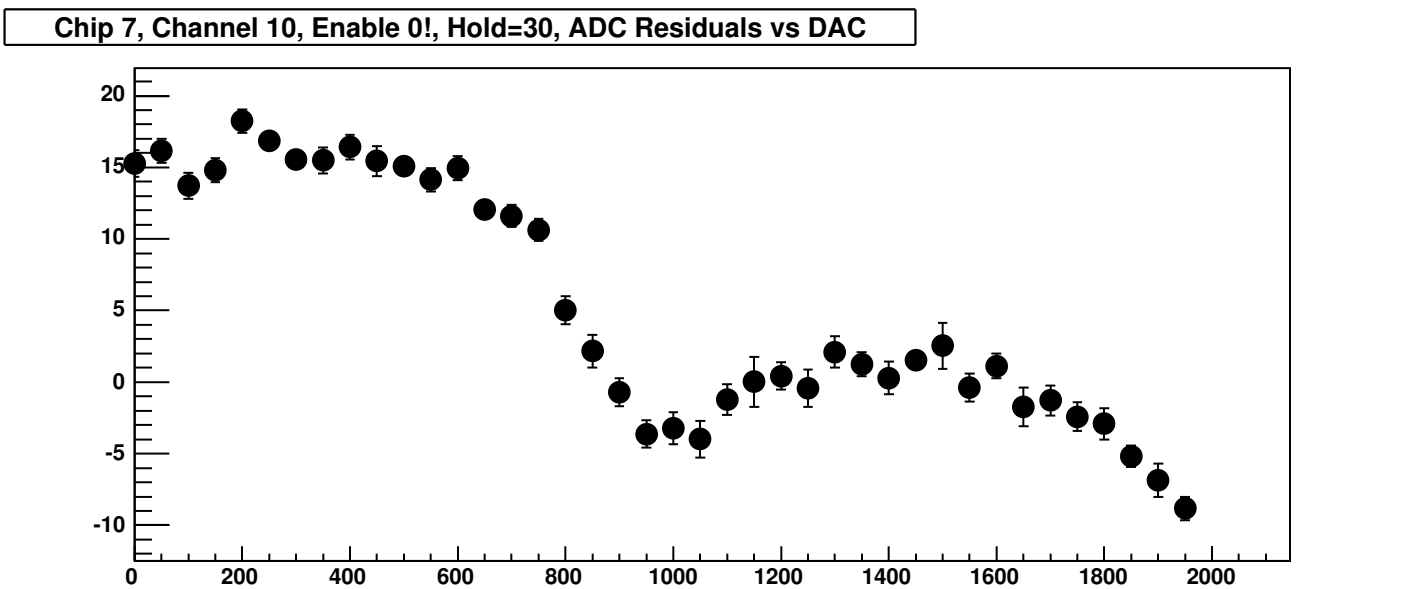

Chip 7, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 10.3 / 11
p0 -1661 ± 24.69
p1 0.5025 ± 0.02236
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

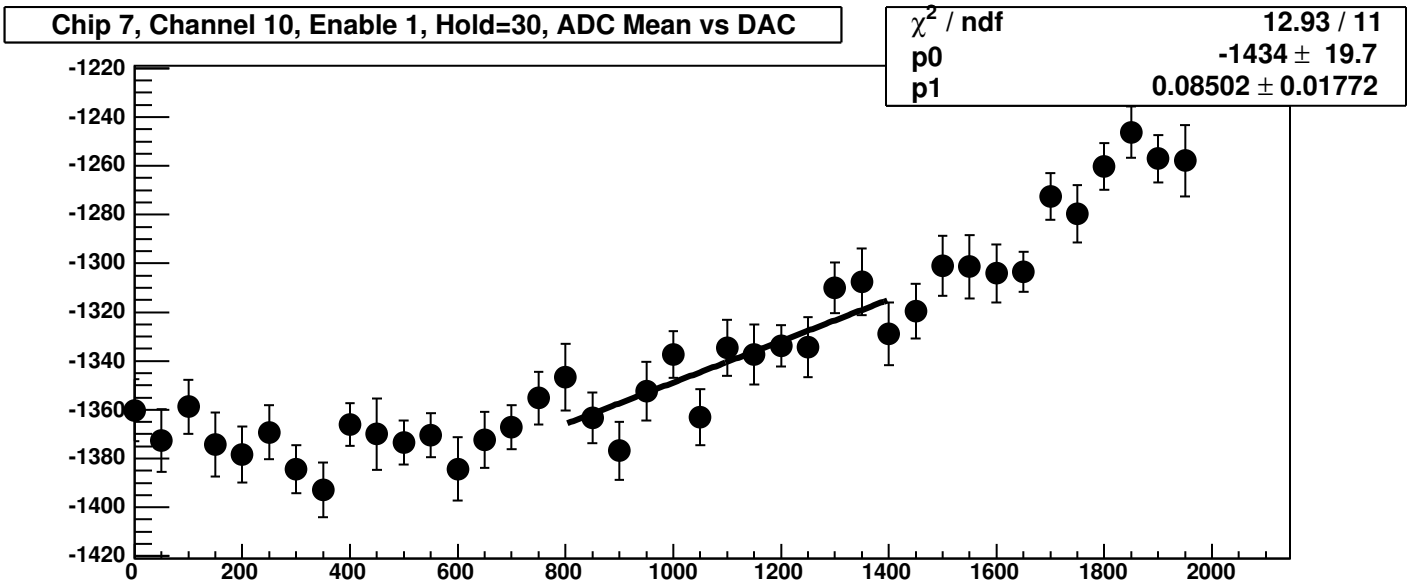

Chip 7, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

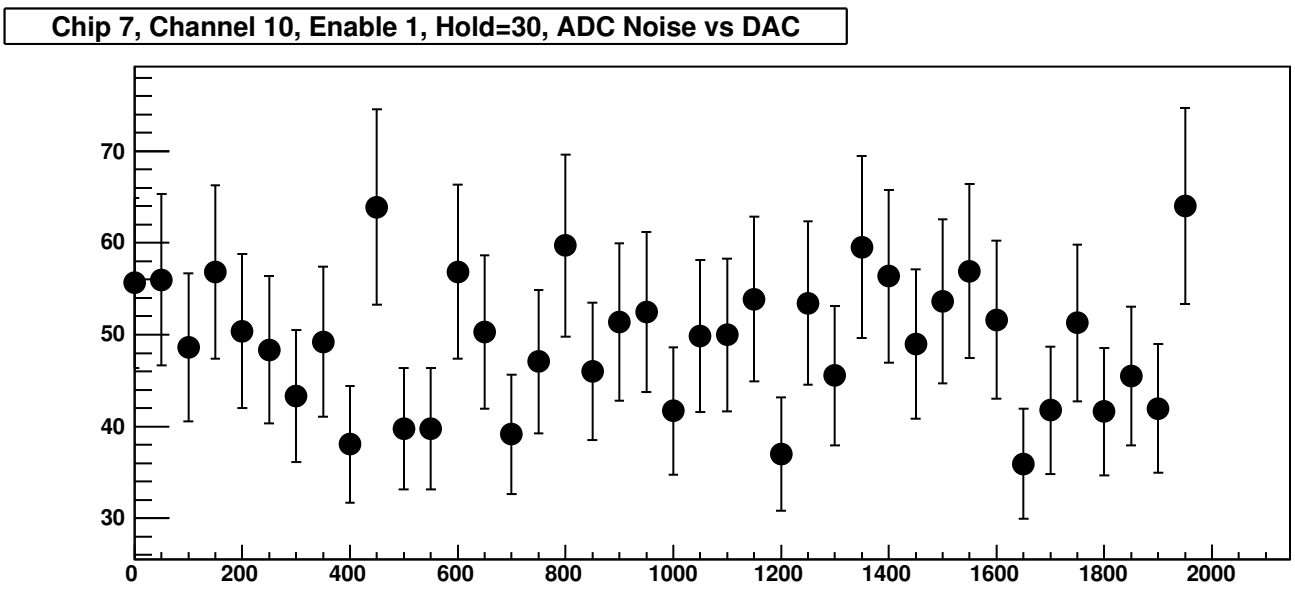

Chip 7, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
400
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

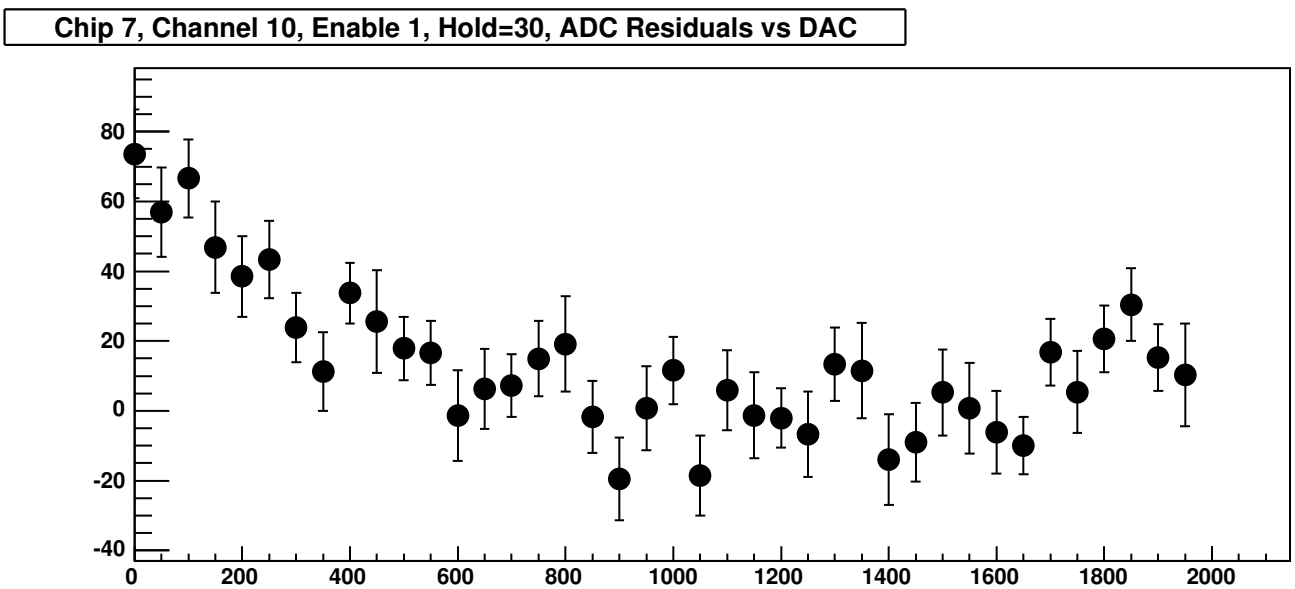

Chip 7, Channel 10, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 70.73 / 11
p0 2.941e+04 ± 1.687
p1 0.007826 ± 0.001514
29430
29428
29426
29424
29422
29420
29418
29416
29414
29412
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 10, Enable 0!, Hold=30, ADC Noise vs DAC
9
8
7
6
5
4
3
2
0
200
400
600
800
1000
1200
1400
1600
1800
2000

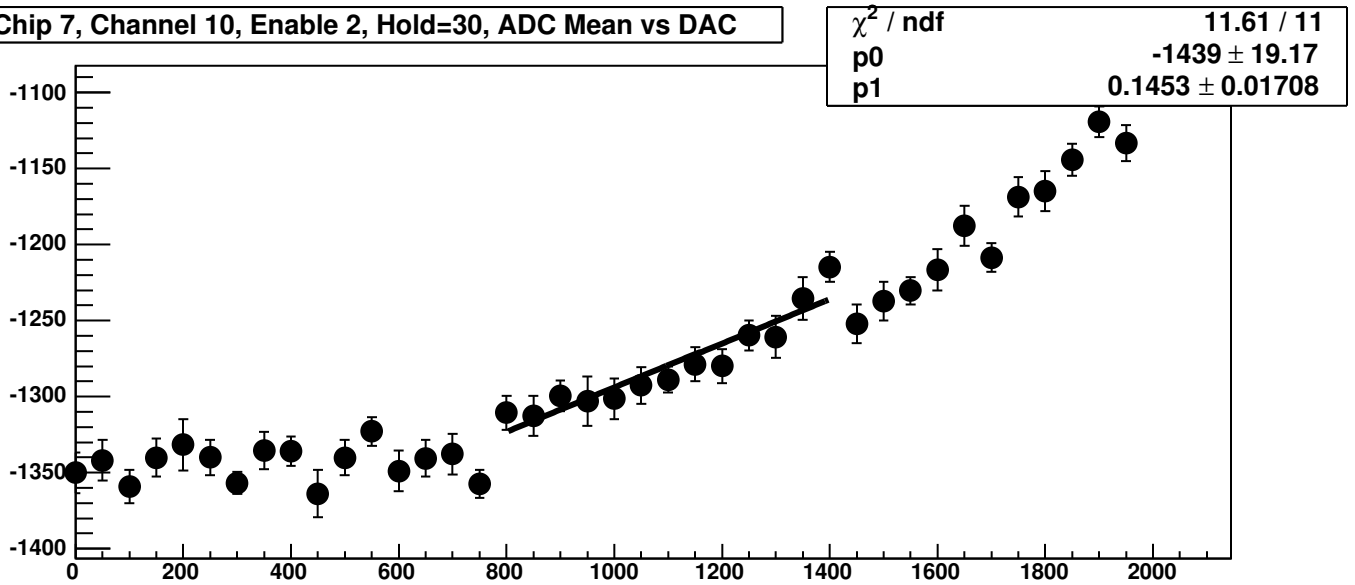

Chip 7, Channel 10, Enable 0!, Hold=30, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 12.93 / 11
p0 -1434 ± 19.7
p1 0.08502 ± 0.01772
-1220
-1240
-1260
-1280
-1300
-1320
-1340
-1360
-1380
-1400
-1420
0
200
400
600
800
1000
1200
1400
1600
1800
2000

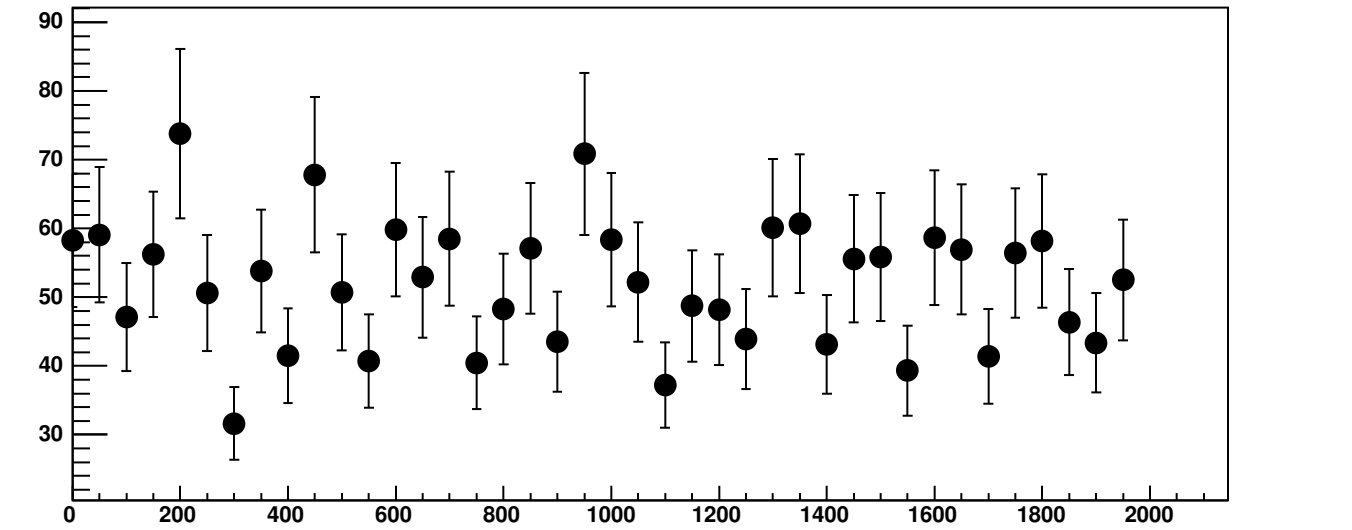

Chip 7, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

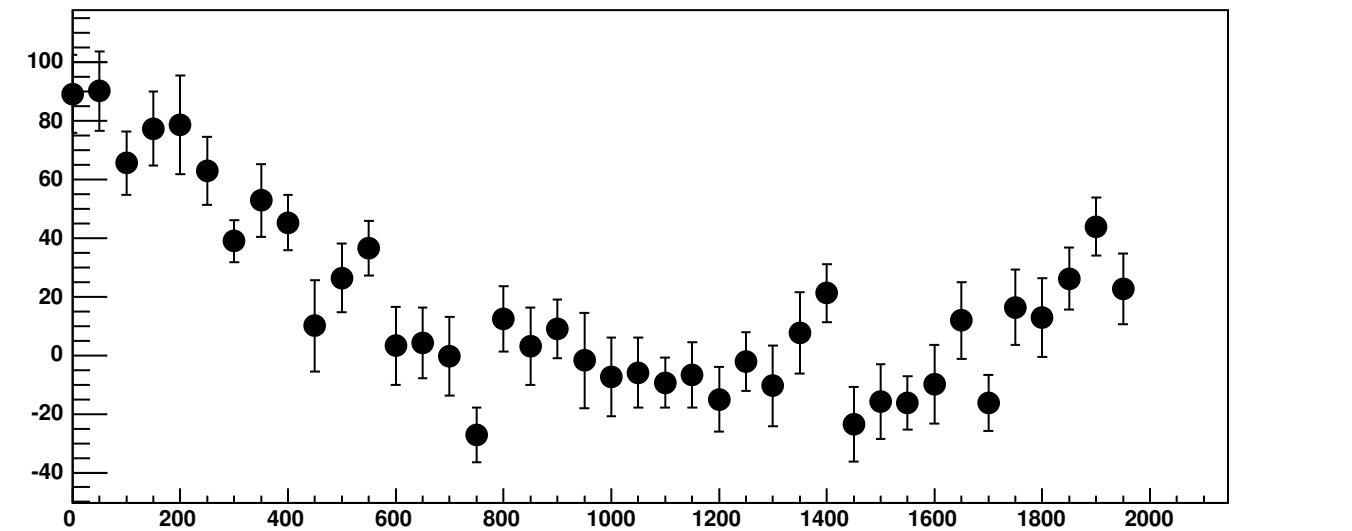
Chip 7, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC



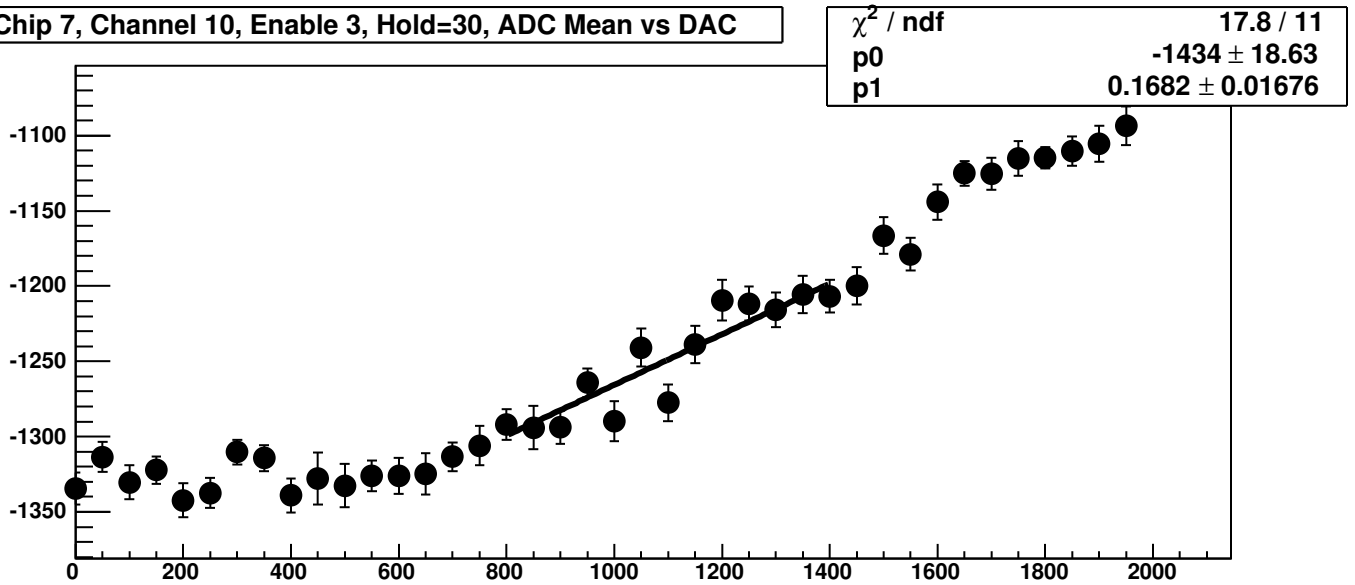
Chip 7, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC



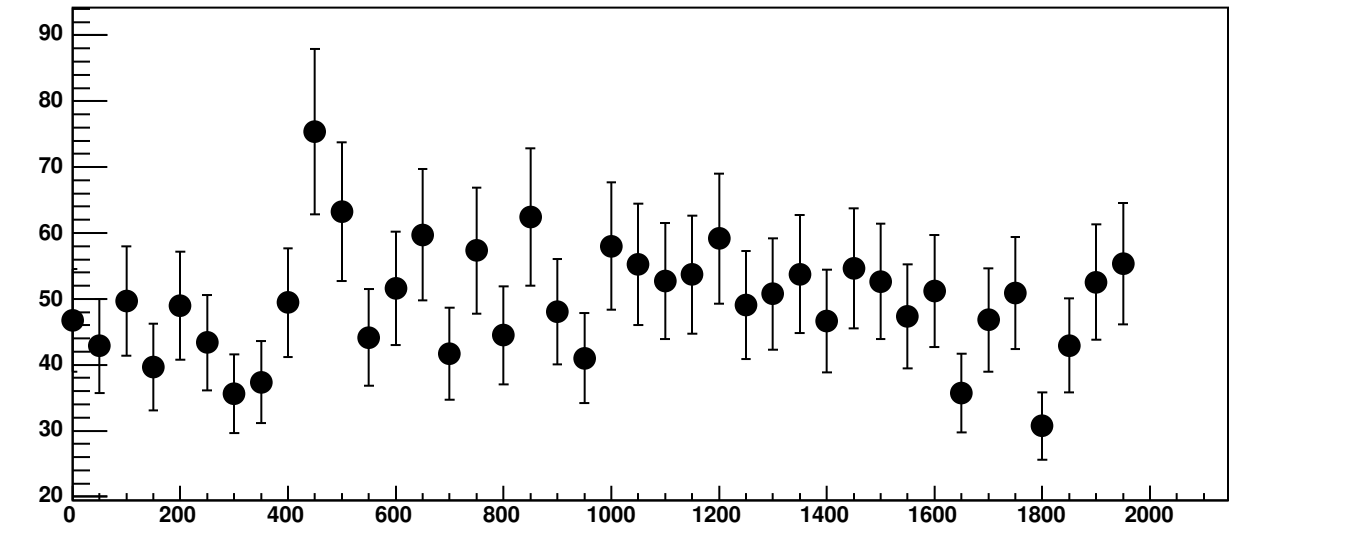
Chip 7, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC



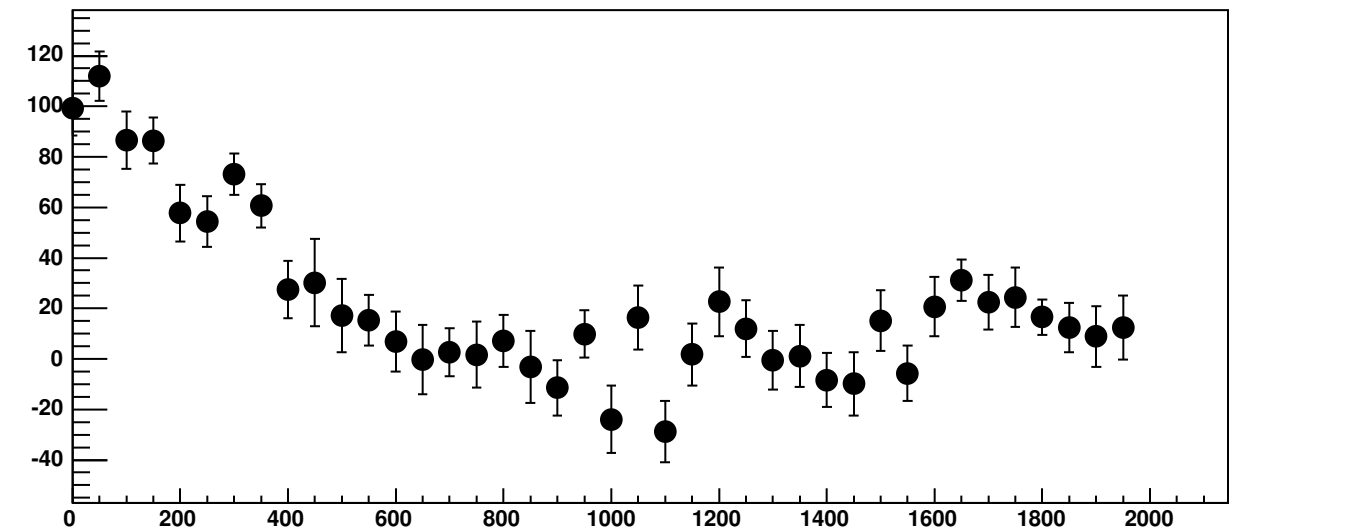
Chip 7, Channel 10, Enable 3, Hold=30, ADC Mean vs DAC

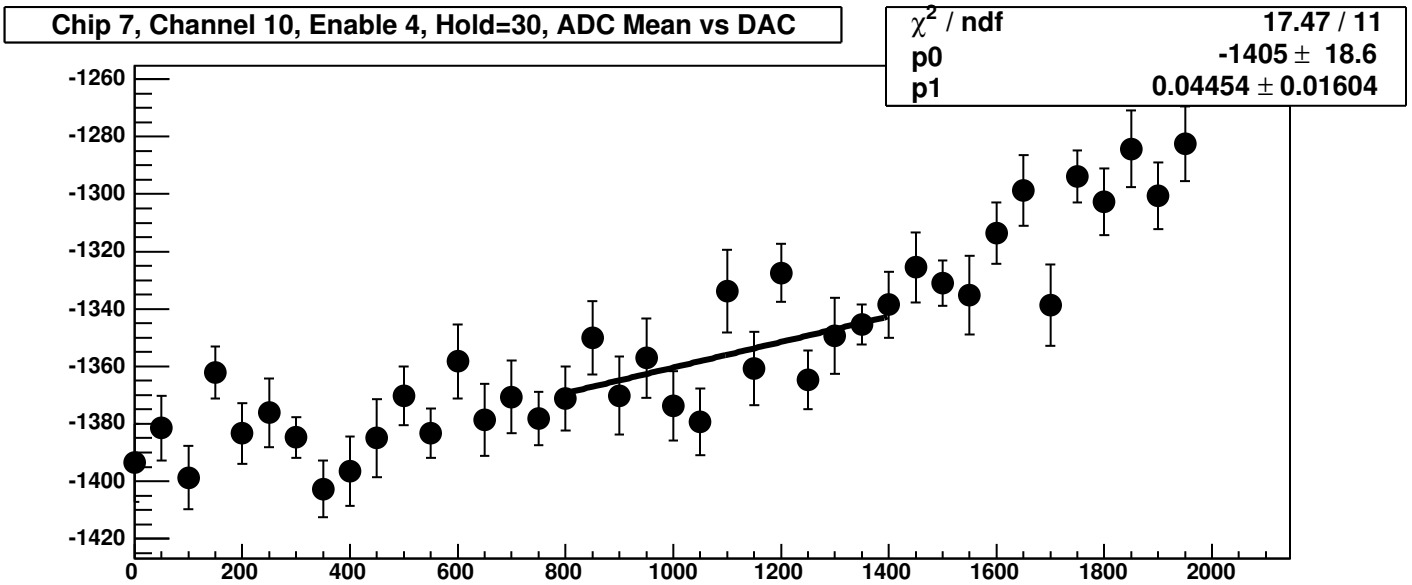


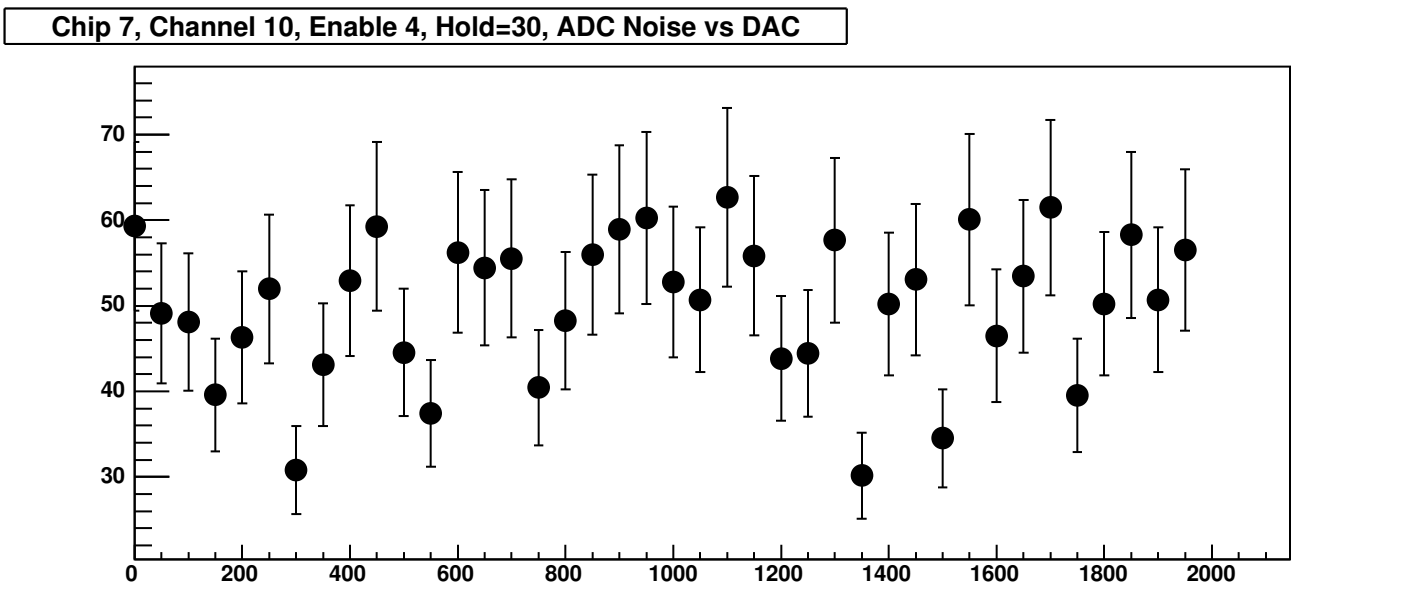
Chip 7, Channel 10, Enable 3, Hold=30, ADC Noise vs DAC

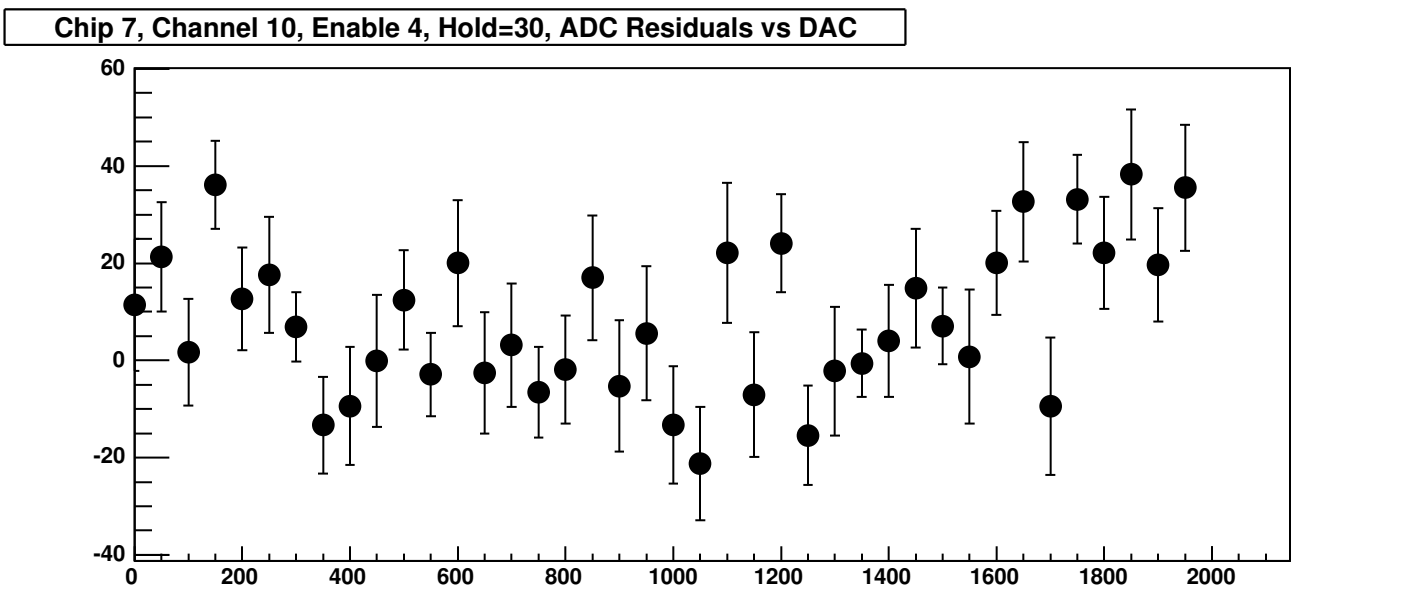


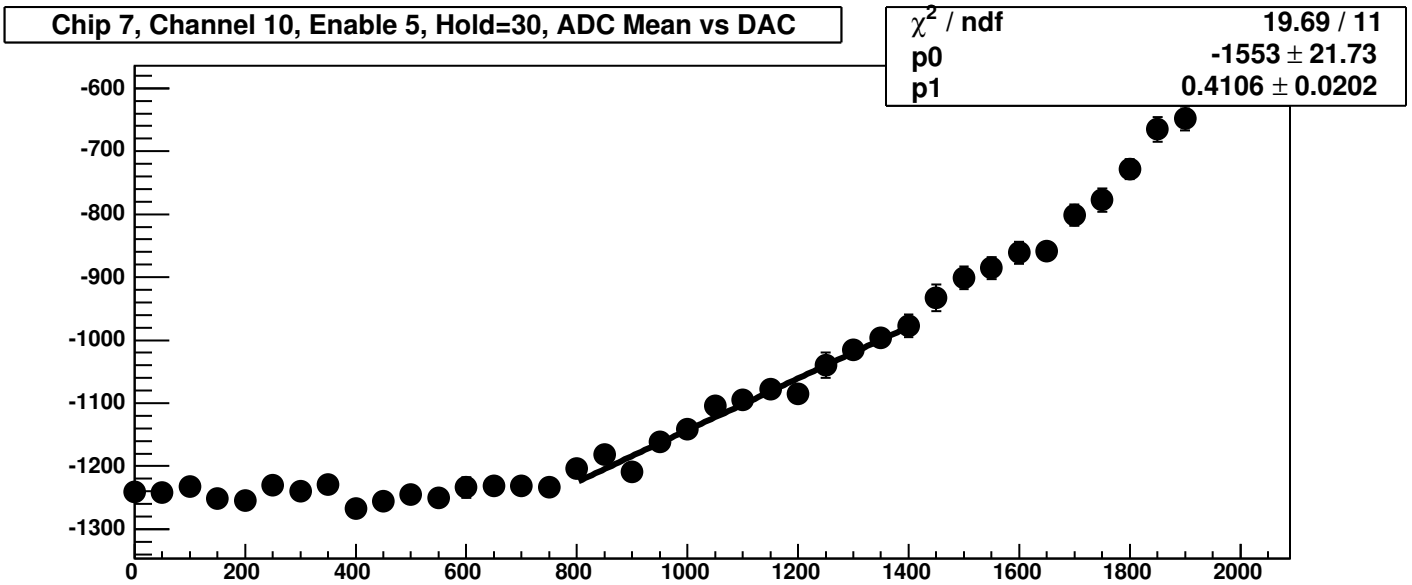
Chip 7, Channel 10, Enable 3, Hold=30, ADC Residuals vs DAC

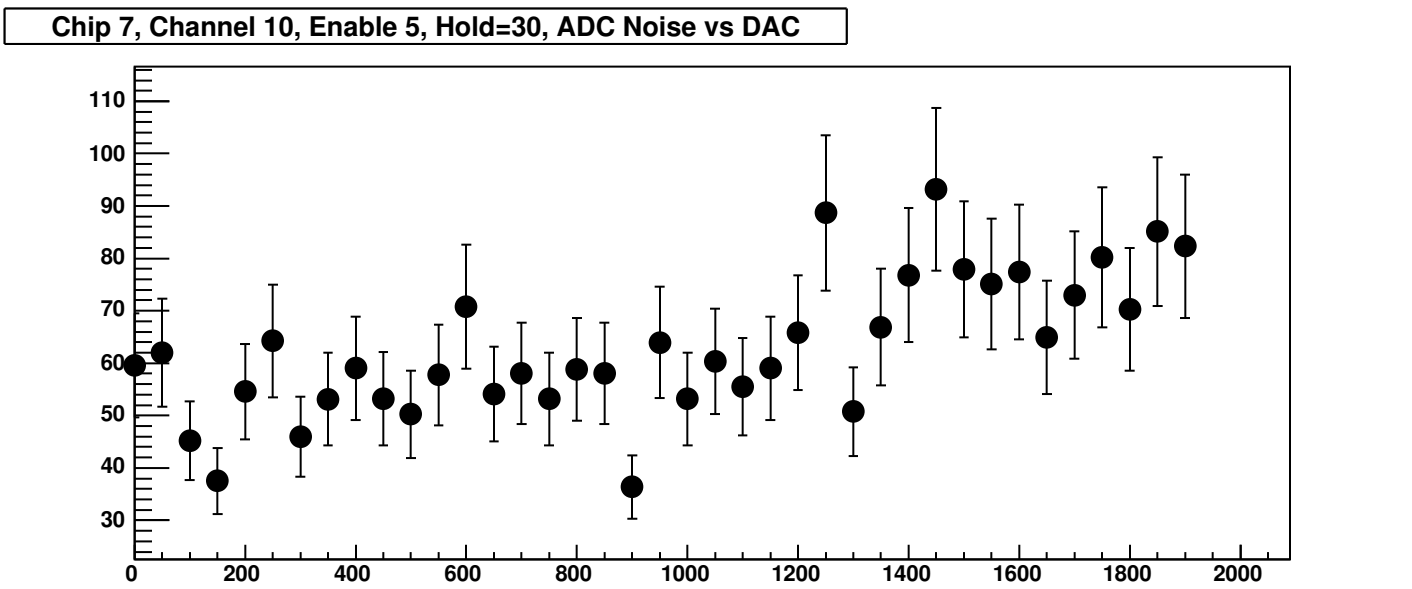


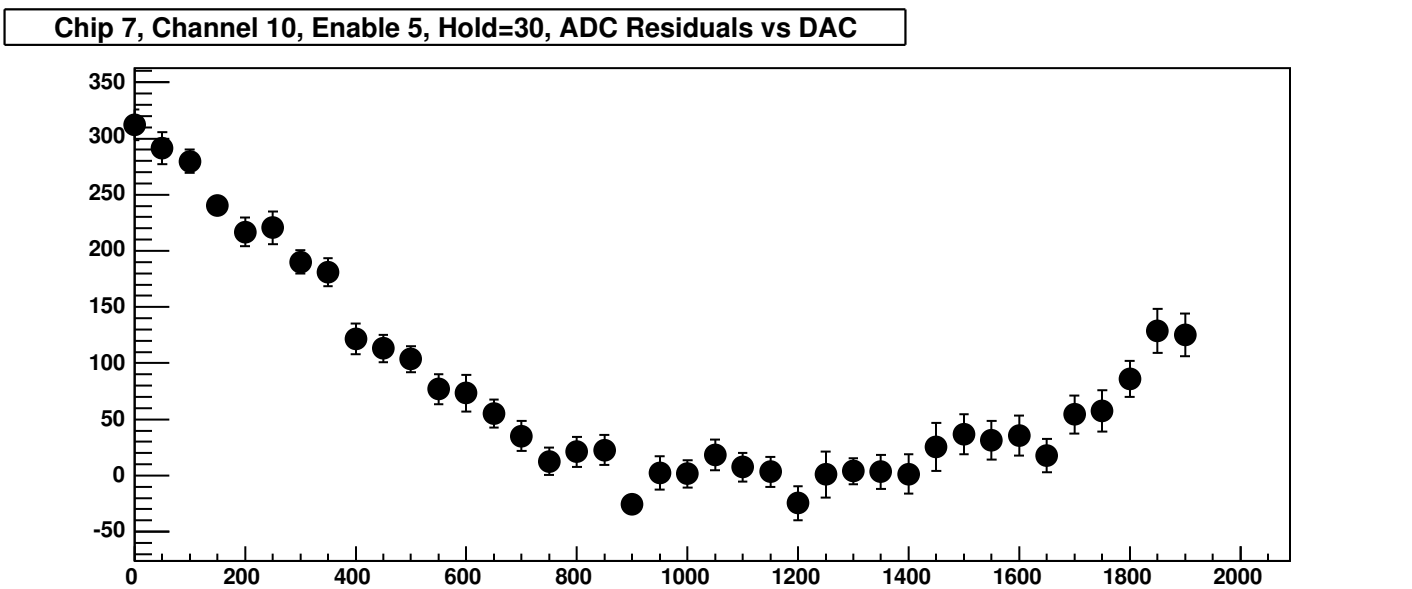

Chip 7, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 17.47 / 11
p0 -1405 ± 18.6
p1 0.04454 ± 0.01604
-1260
-1280
-1300
-1320
-1340
-1360
-1380
-1400
-1420
0
200
400
600
800
1000
1200
1400
1600
1800
2000

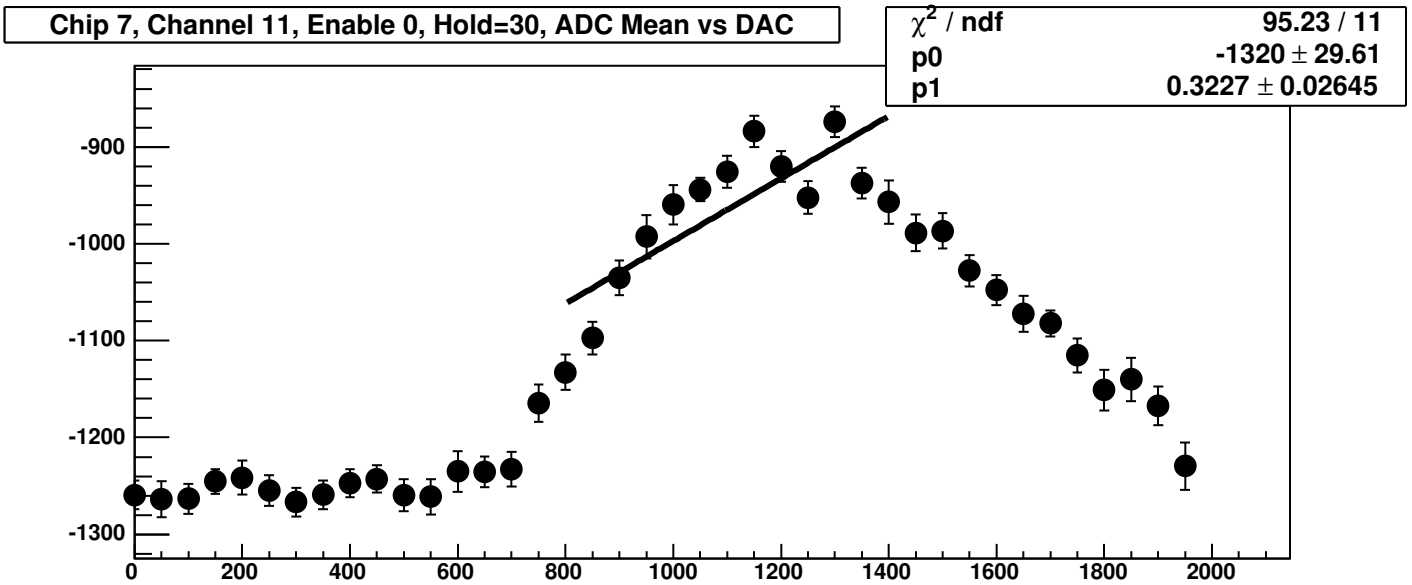

Chip 7, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

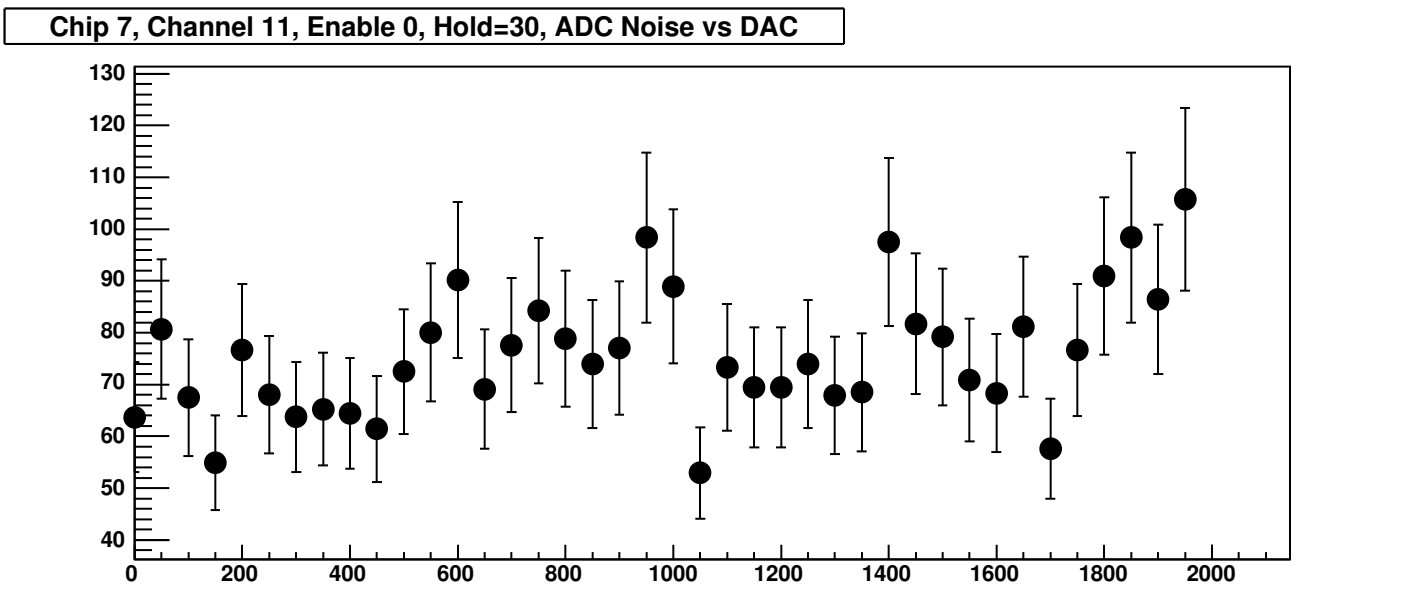

Chip 7, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

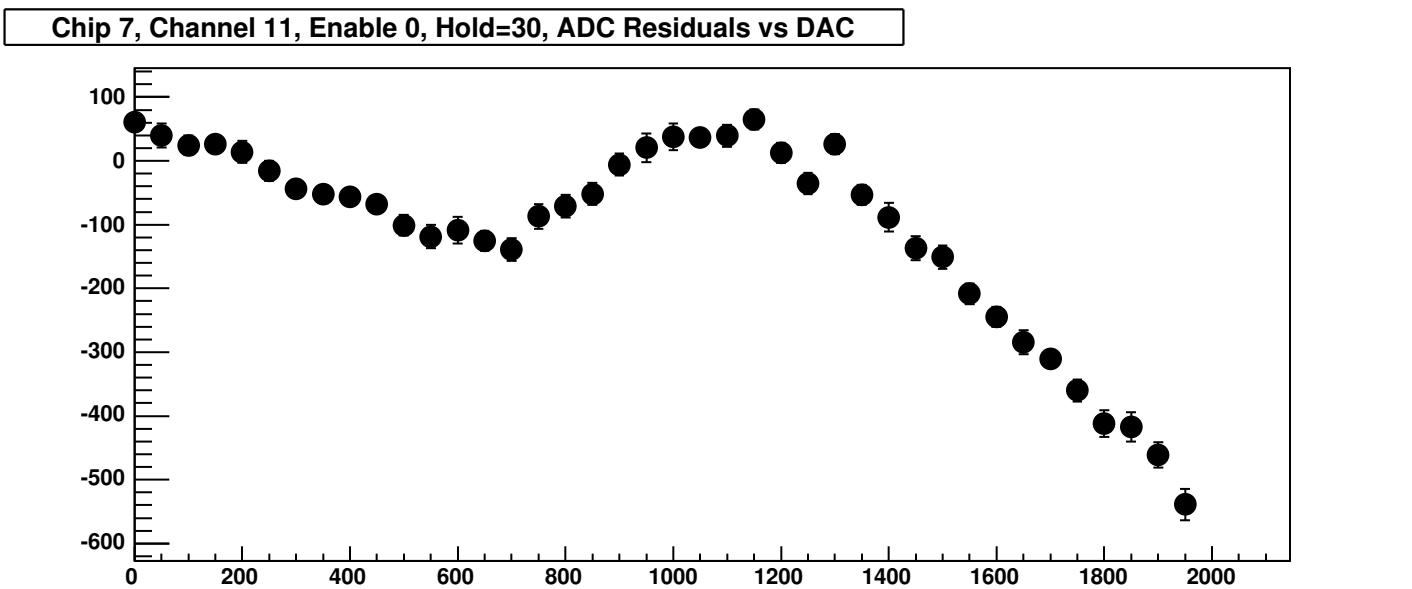

Chip 7, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 19.69 / 11
p0 -1553 ± 21.73
p1 0.4106 ± 0.0202
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

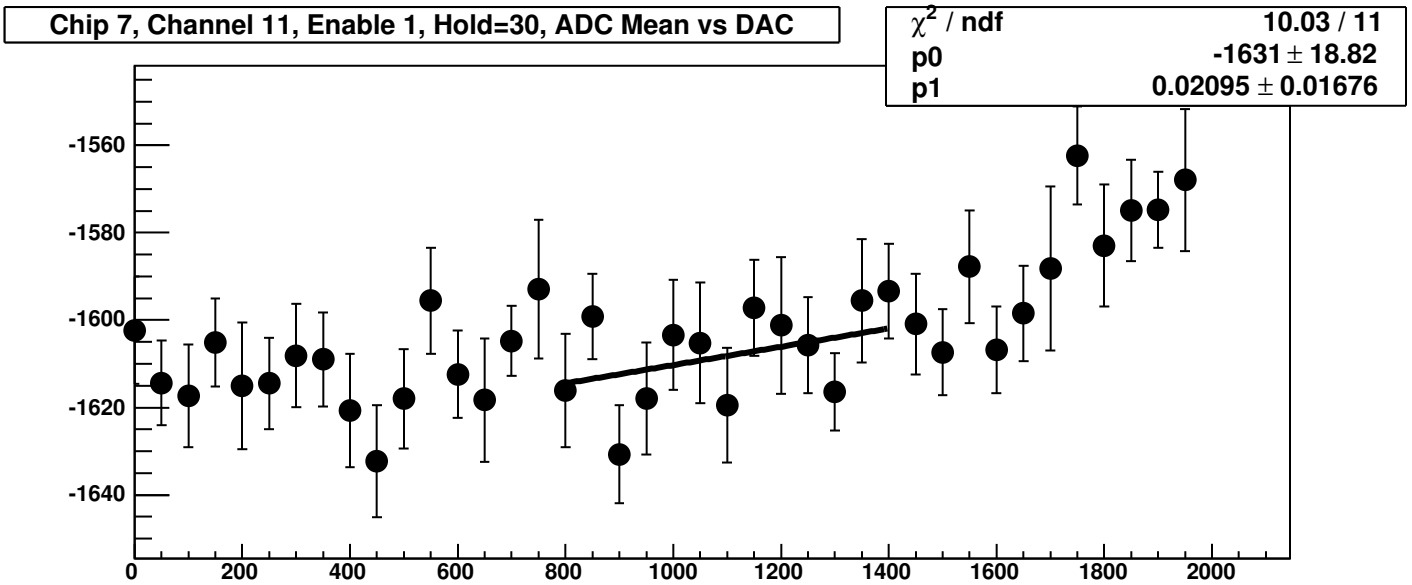

Chip 7, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

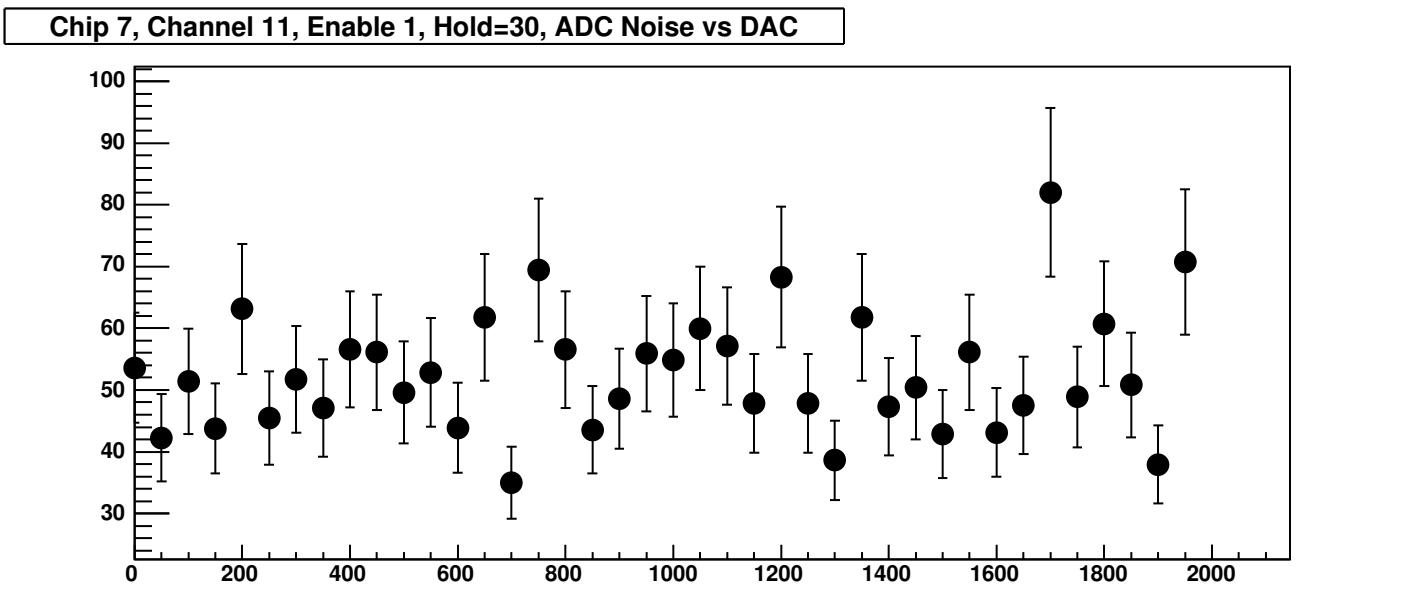

Chip 7, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

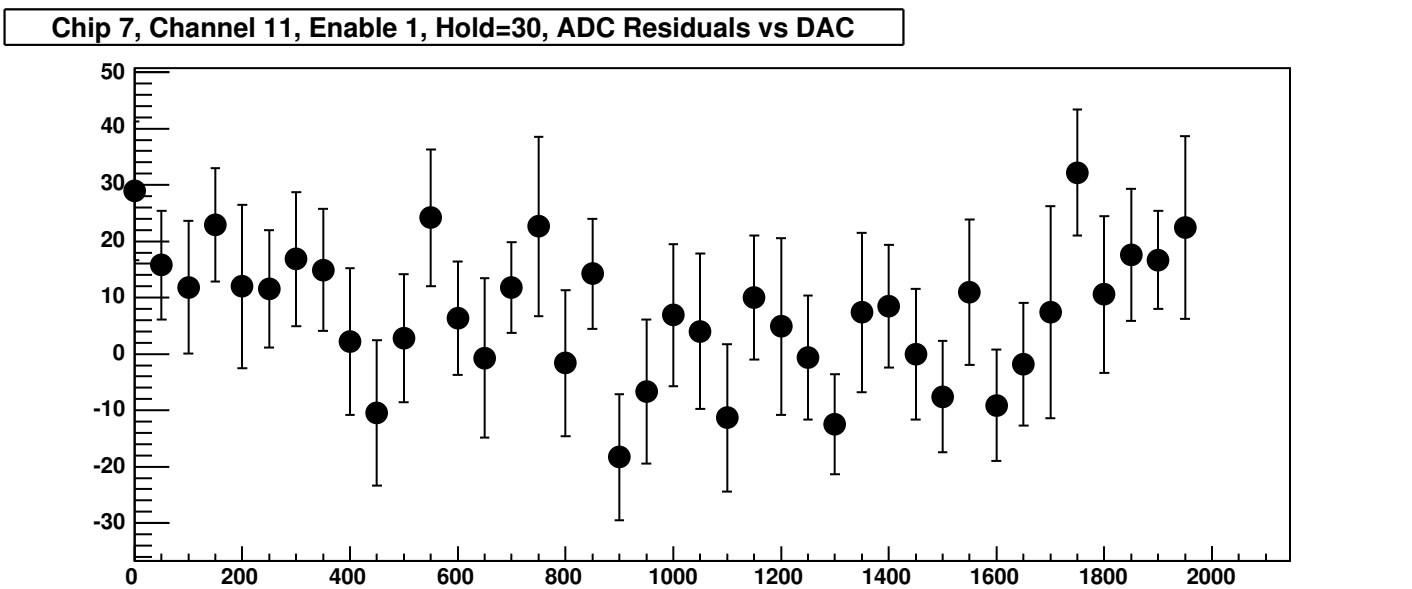

Chip 7, Channel 11, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 95.23 / 11
p0 -1320 ± 29.61
p1 0.3227 ± 0.02645
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 11, Enable 0, Hold=30, ADC Noise vs DAC
130
120
110
100
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

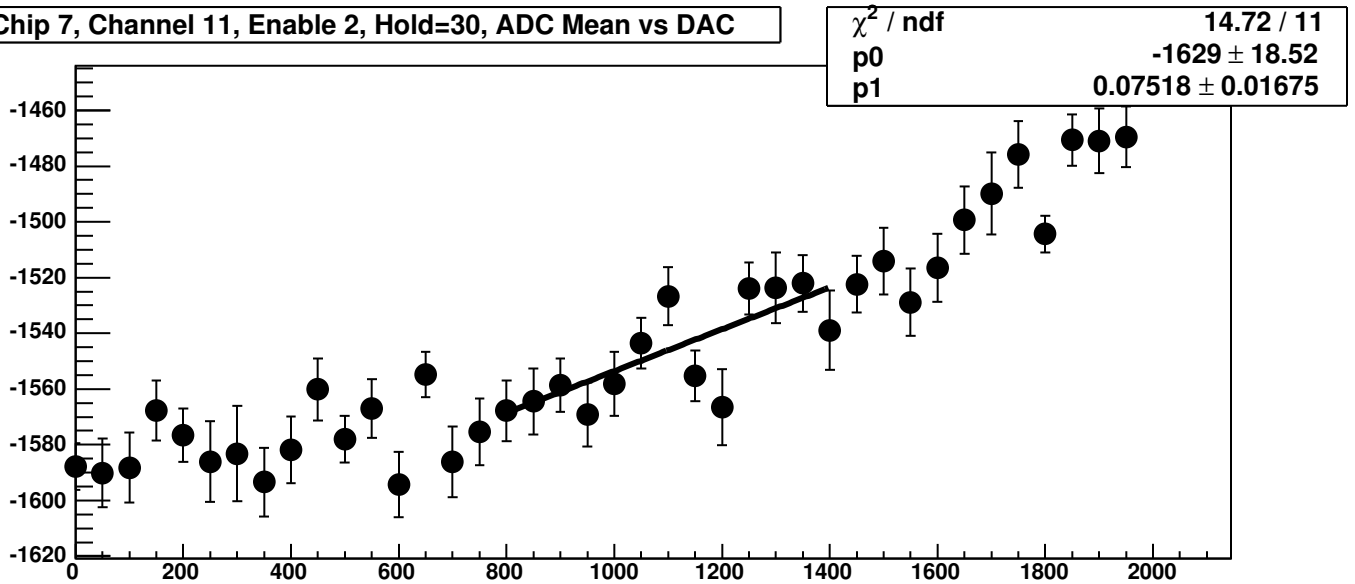

Chip 7, Channel 11, Enable 0, Hold=30, ADC Residuals vs DAC
100
0
-100
-200
-300
-400
-500
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 10.03 / 11
p0 -1631 ± 18.82
p1 0.02095 ± 0.01676
-1560
-1580
-1600
-1620
-1640
0
200
400
600
800
1000
1200
1400
1600
1800
2000

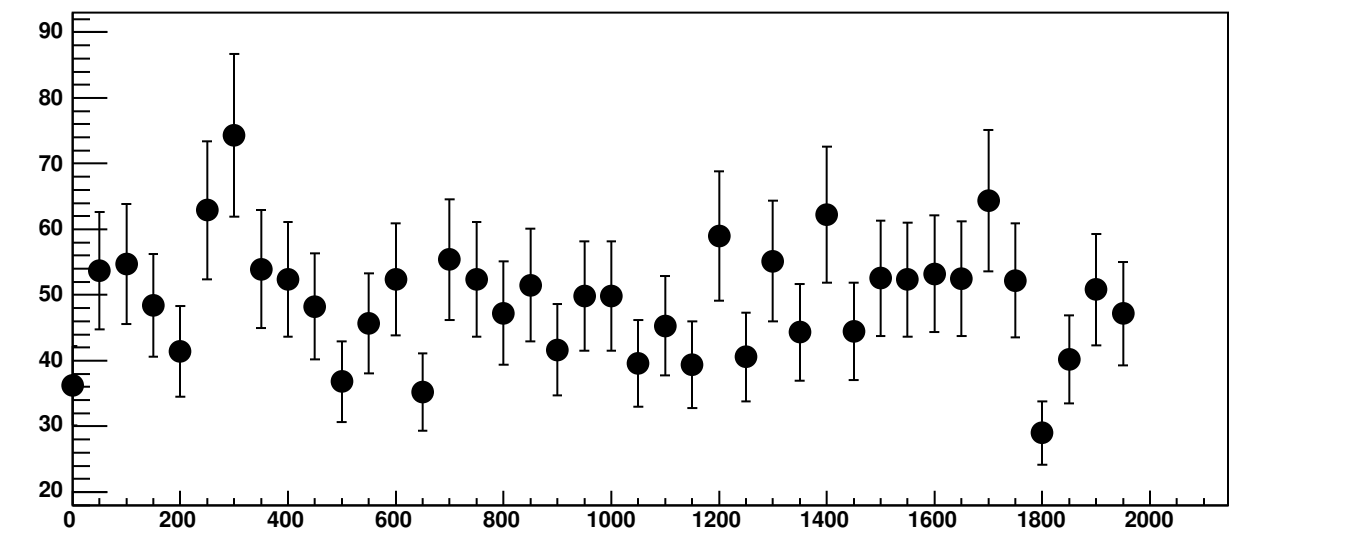

Chip 7, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC
50
40
30
20
10
0
-10
-20
-30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

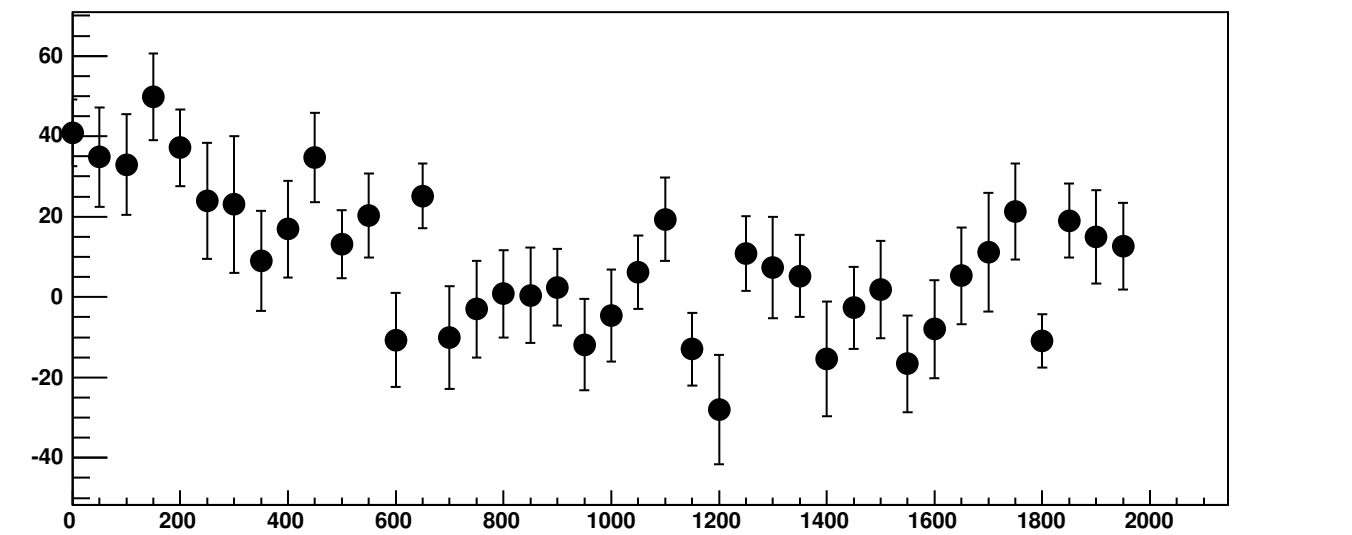
Chip 7, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC



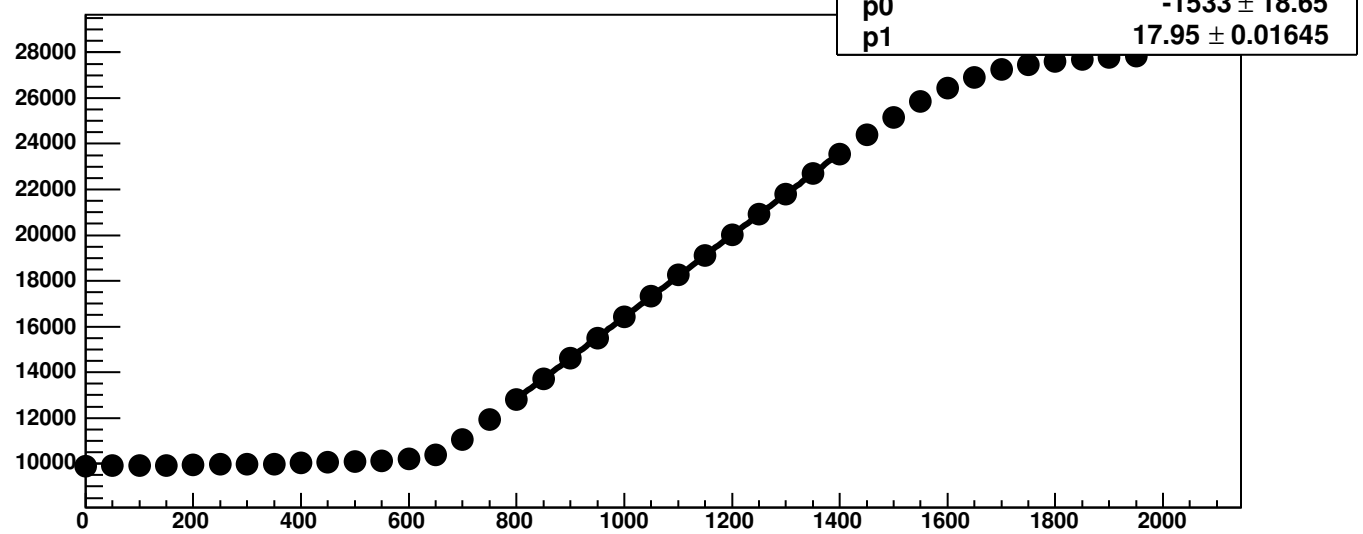
Chip 7, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC



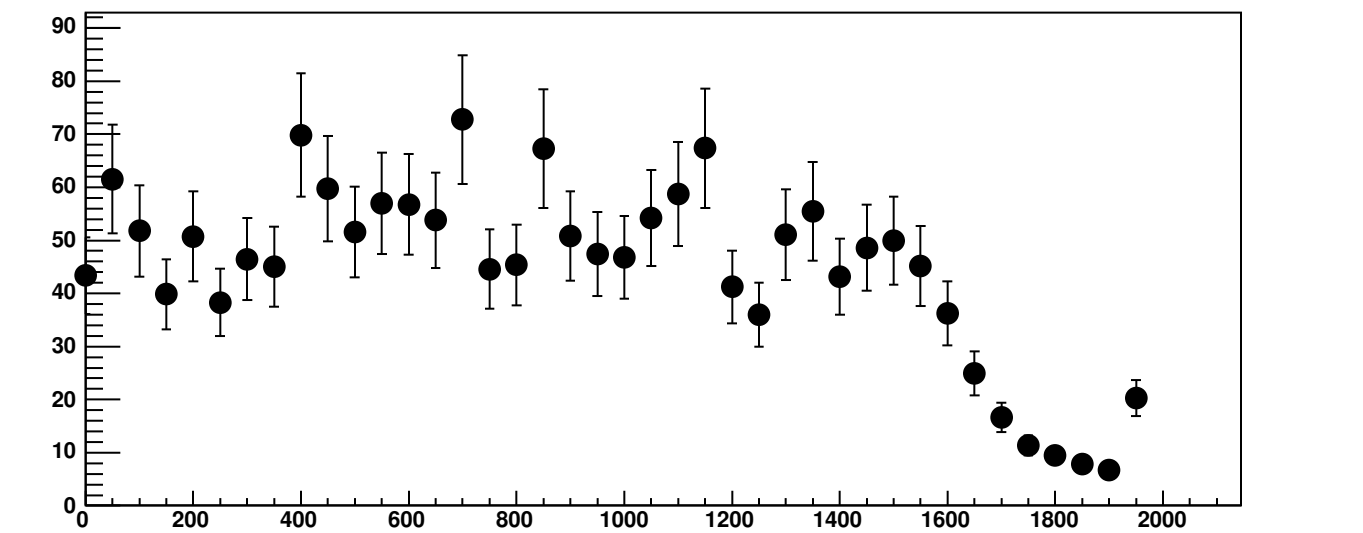
Chip 7, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC



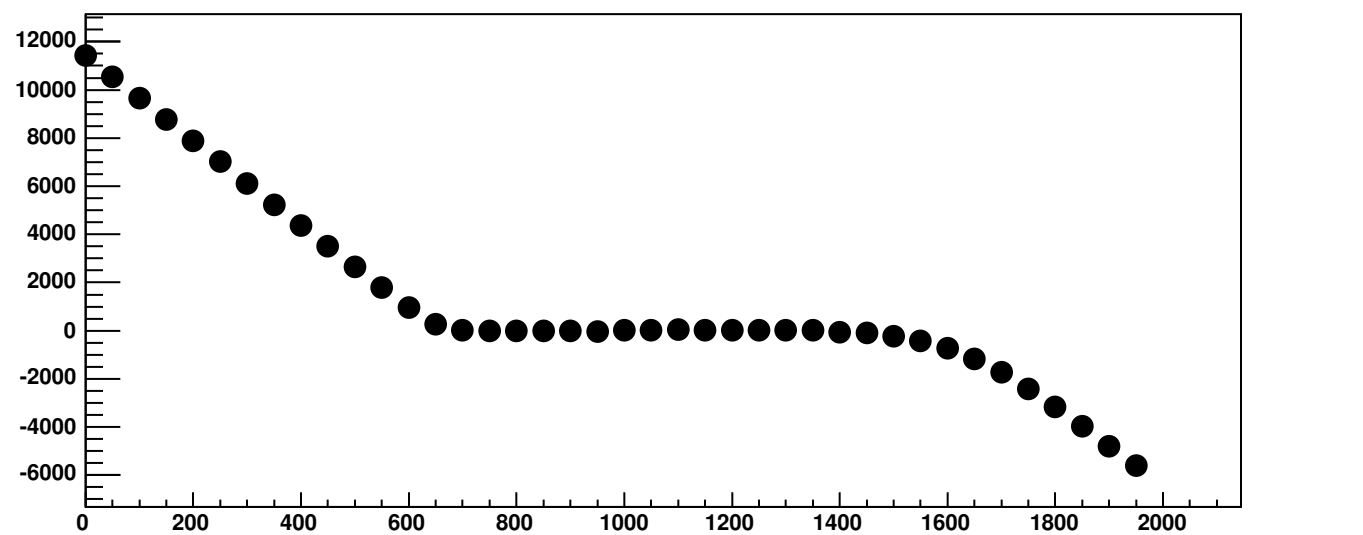
Chip 7, Channel 11, Enable 3!, Hold=30, ADC Mean vs DAC

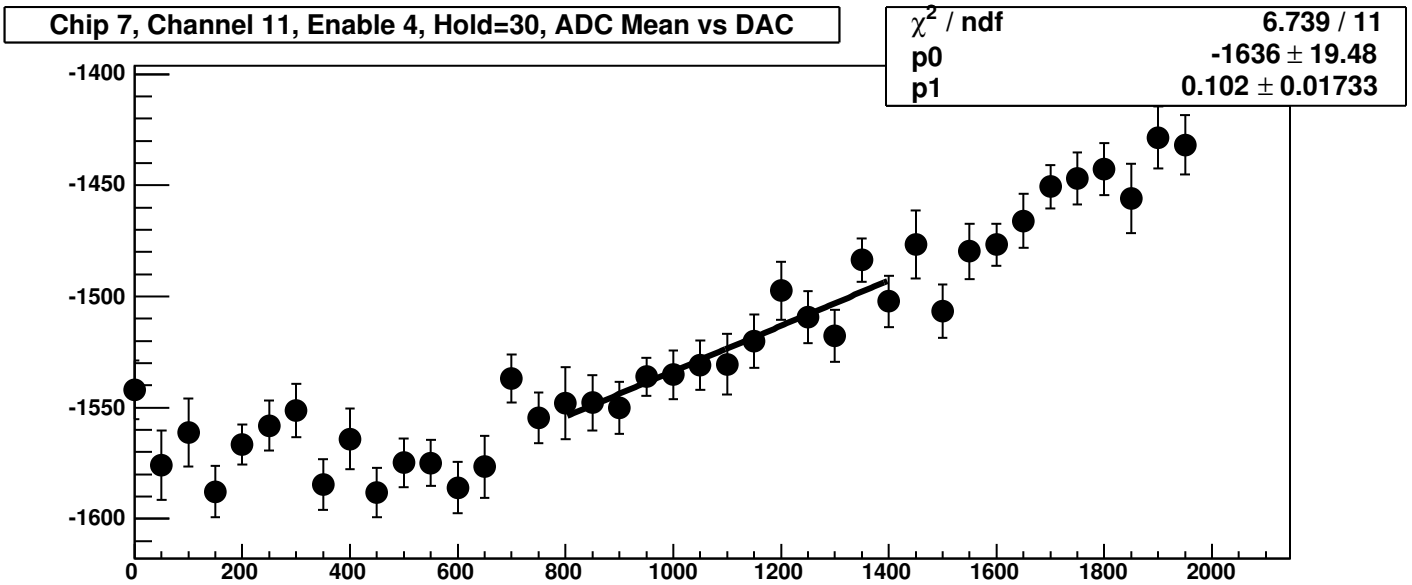


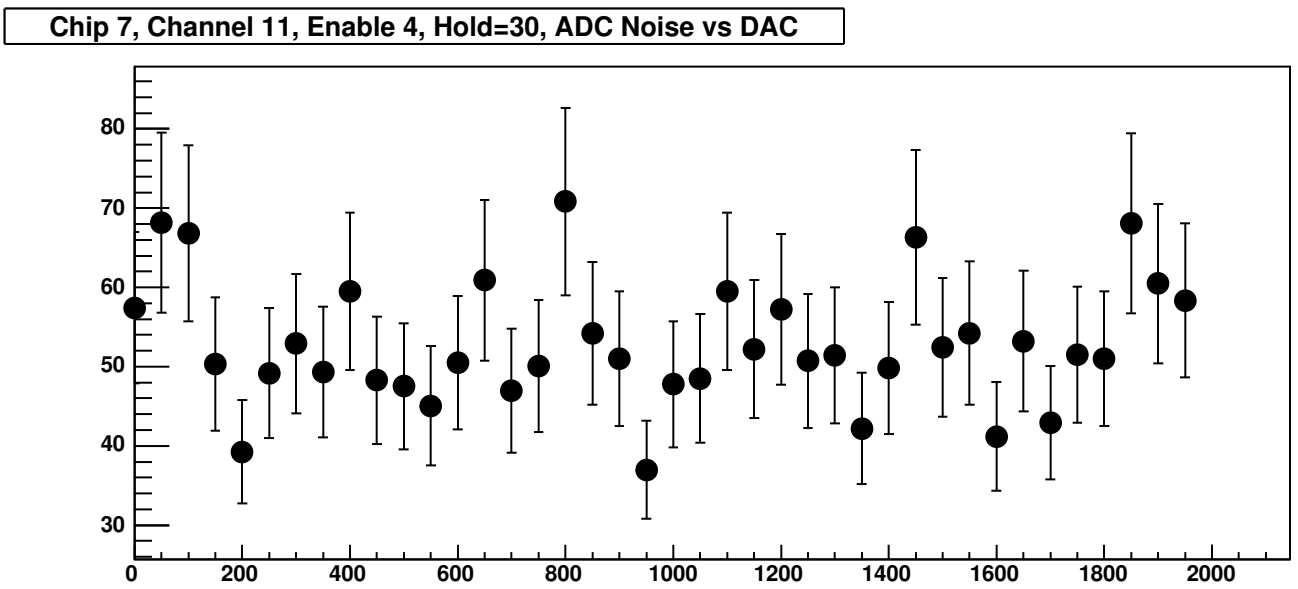
Chip 7, Channel 11, Enable 3!, Hold=30, ADC Noise vs DAC

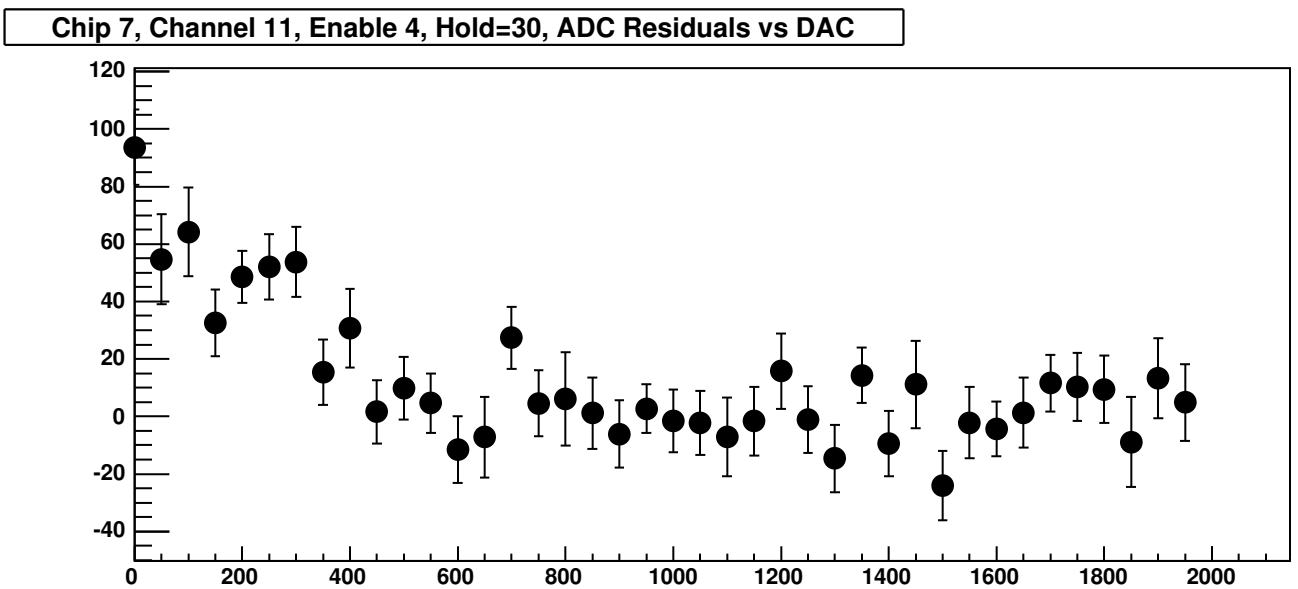


Chip 7, Channel 11, Enable 3!, Hold=30, ADC Residuals vs DAC

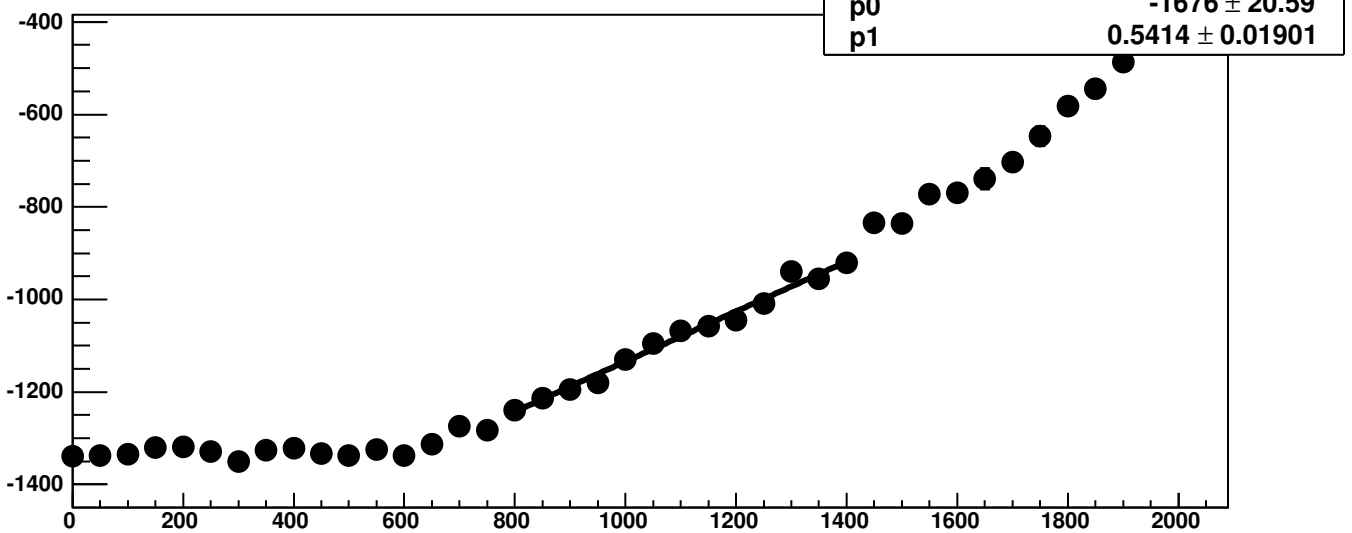



Chip 7, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6.739 / 11
p0 -1636 ± 19.48
p1 0.102 ± 0.01733
-1400
-1450
-1500
-1550
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

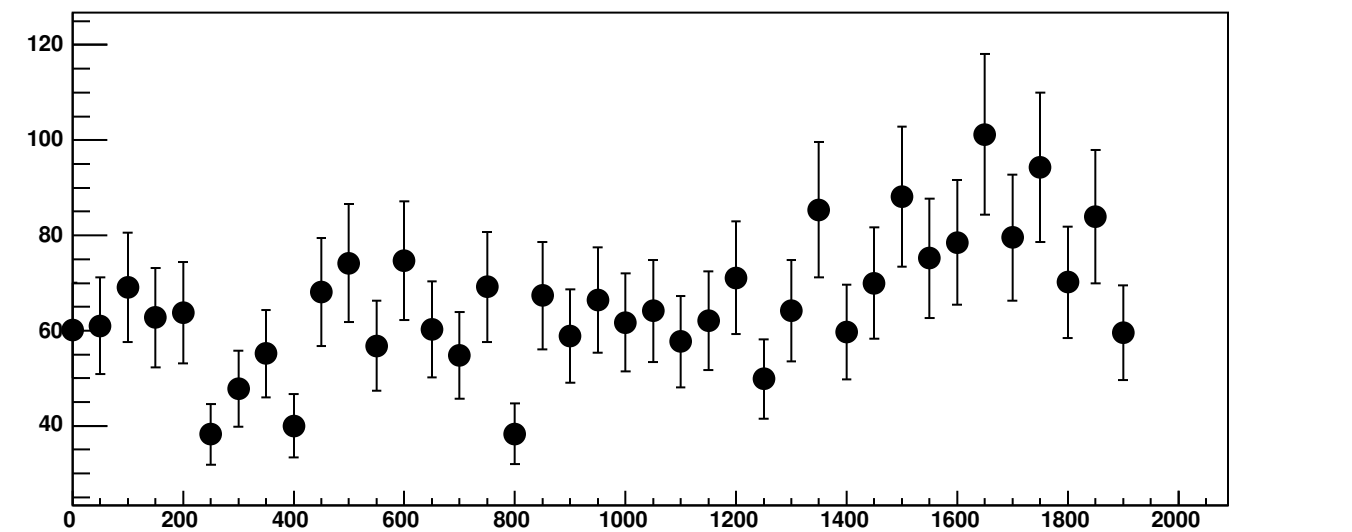

Chip 7, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

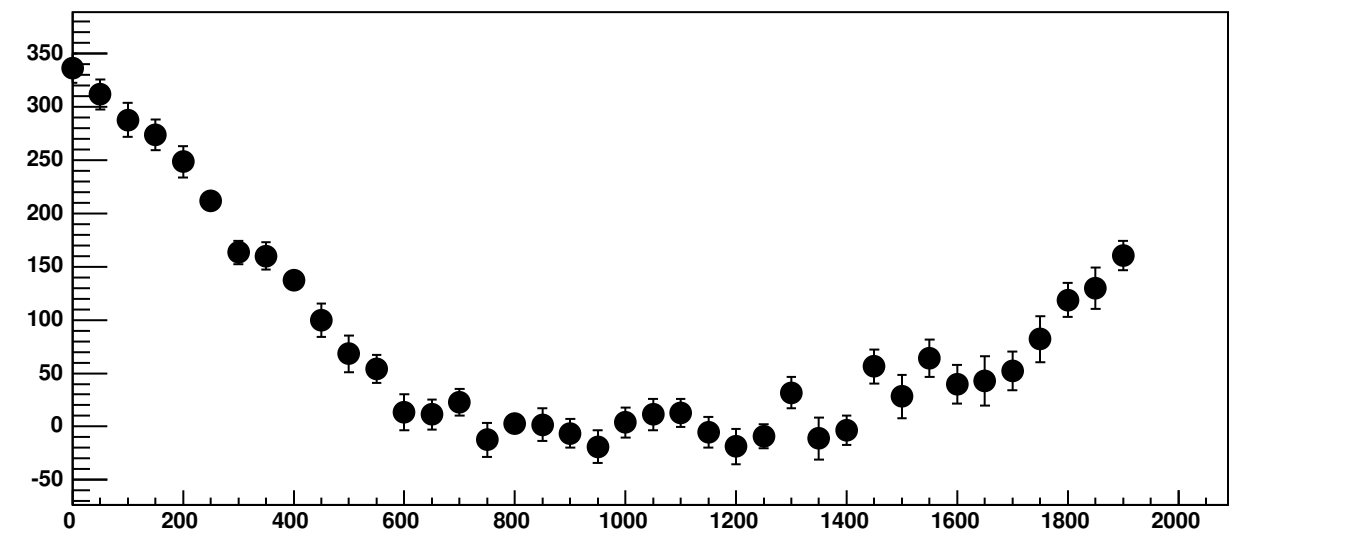
Chip 7, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC

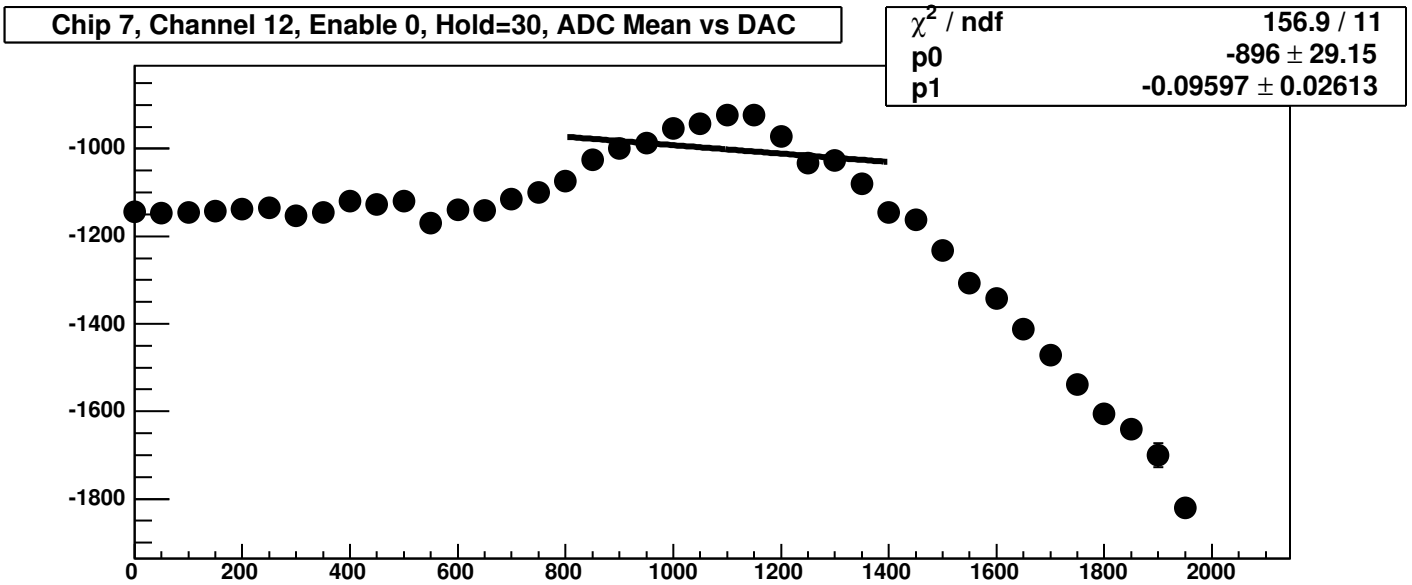


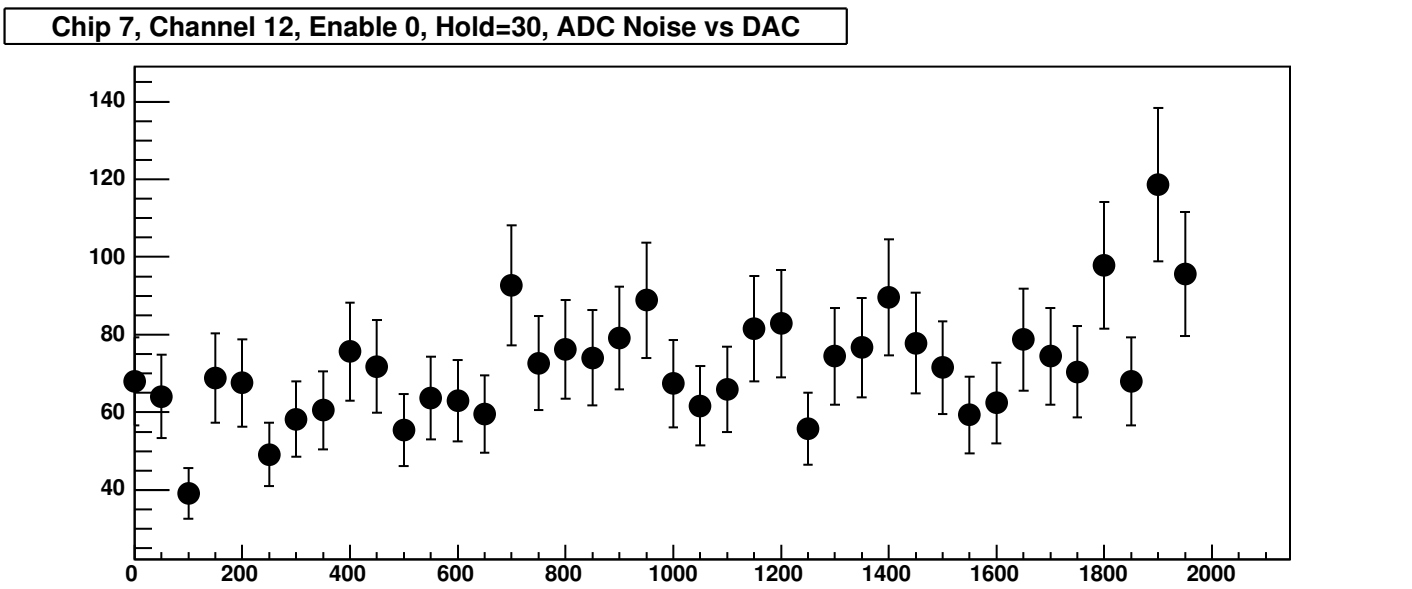
Chip 7, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC

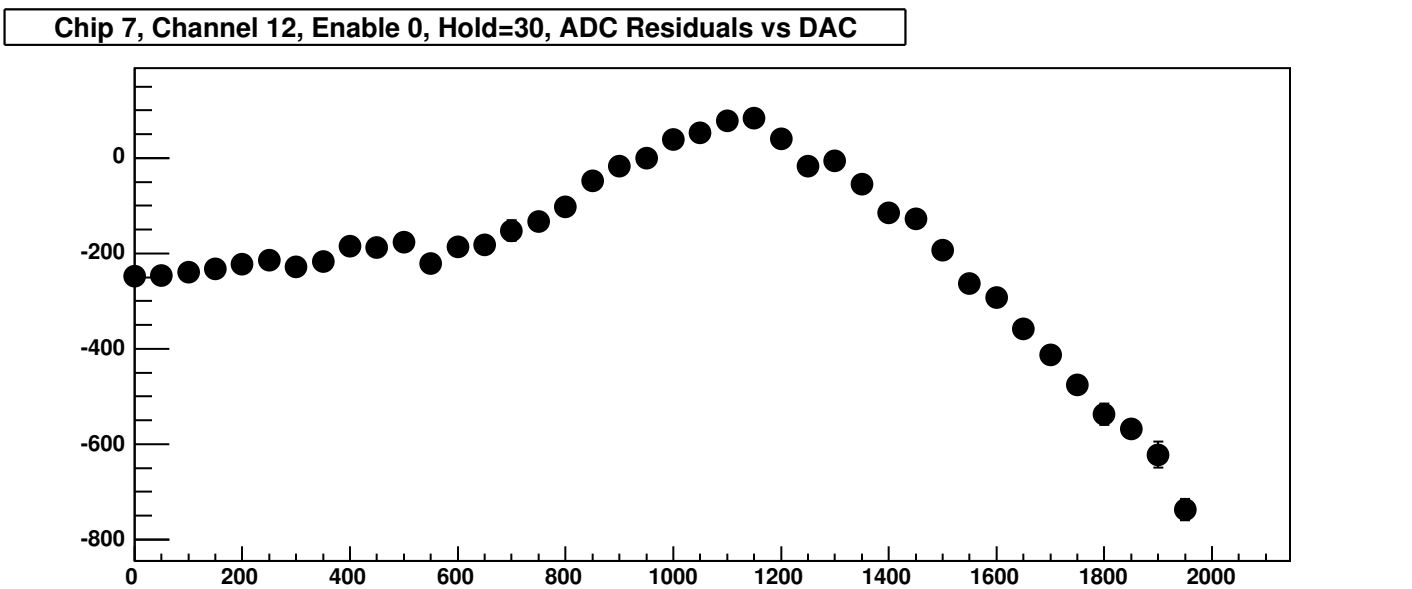


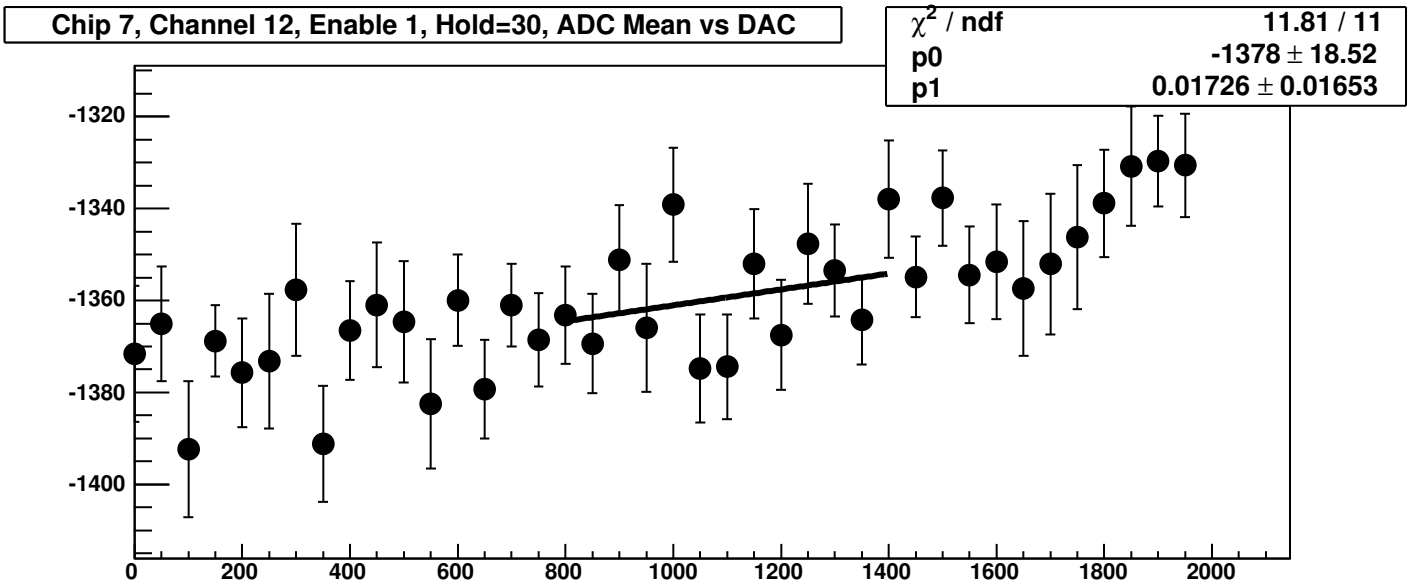
Chip 7, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC

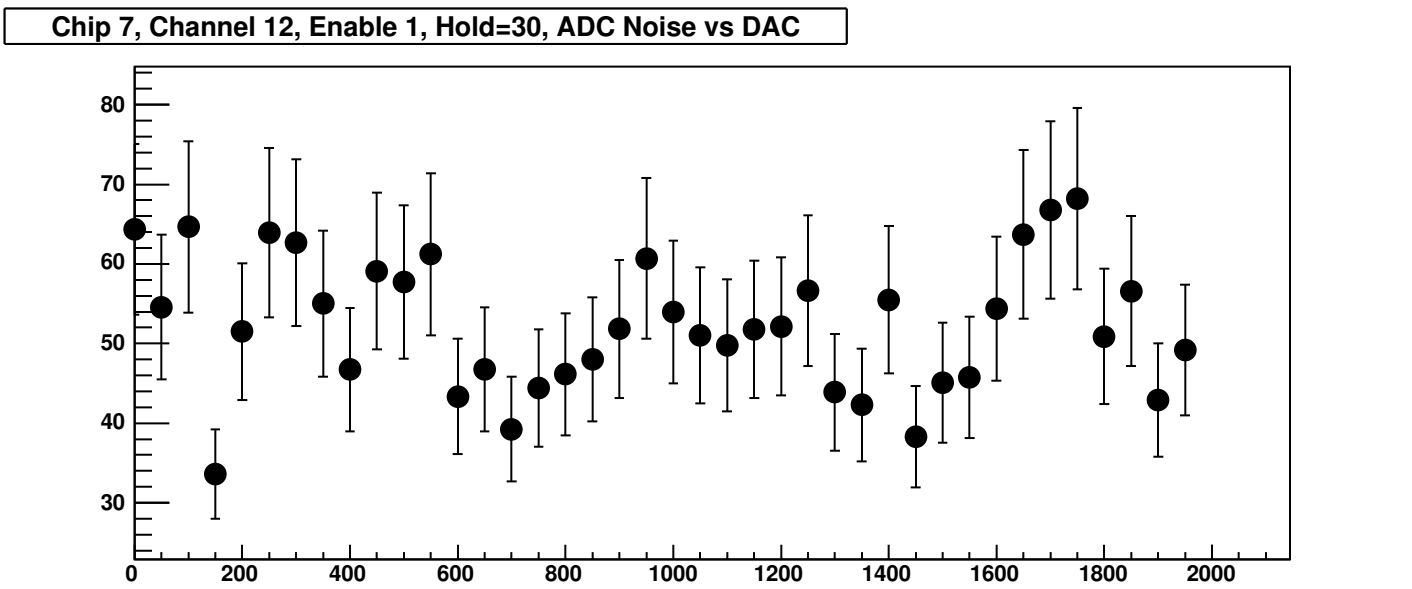


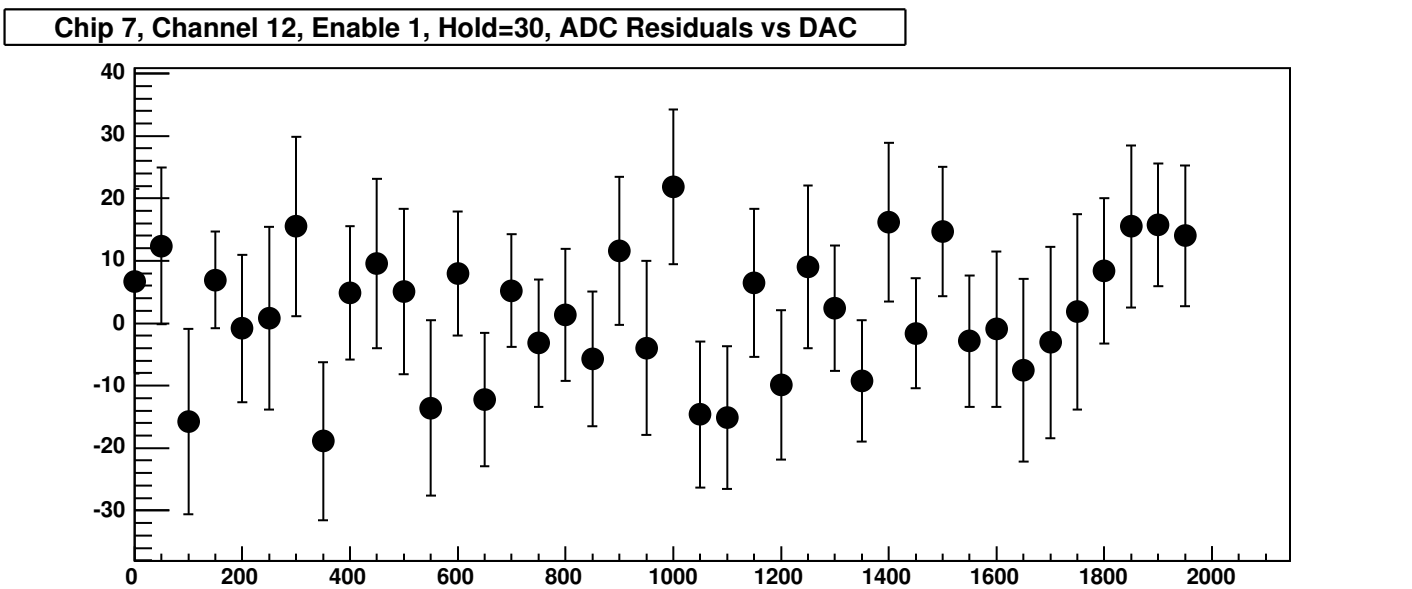

Chip 7, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 156.9 / 11
p0 -896 ± 29.15
p1 -0.09597 ± 0.02613
-1000
-1200
-1400
-1600
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

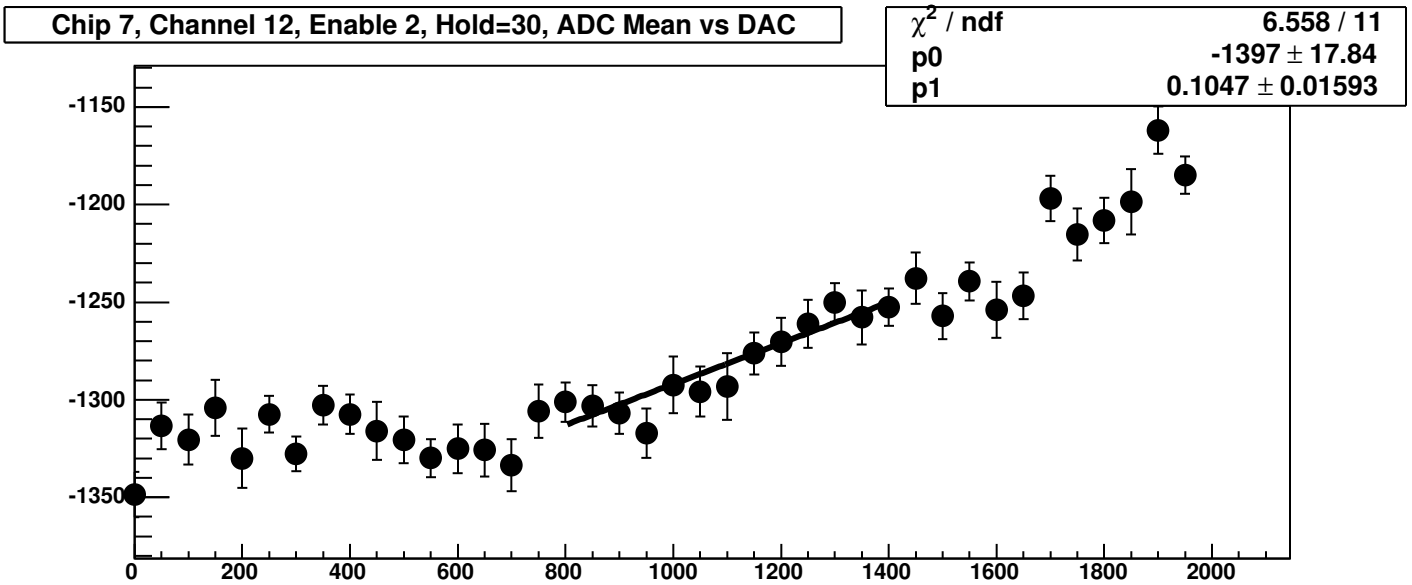

Chip 7, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

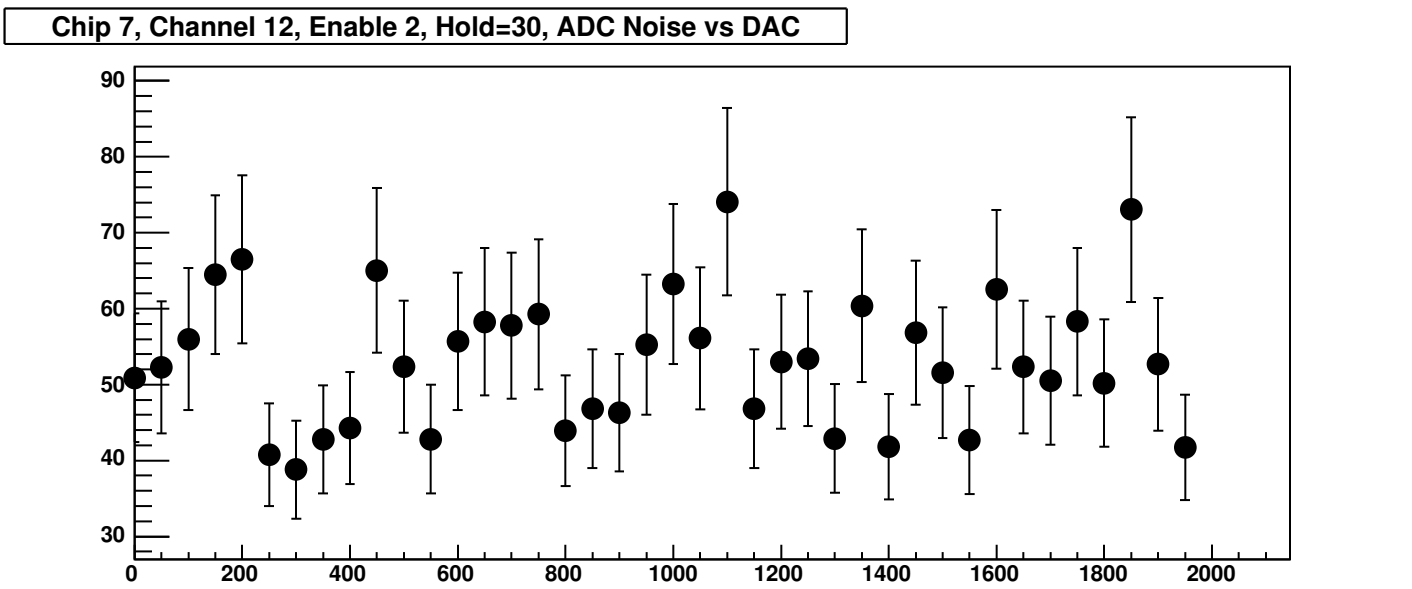

Chip 7, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

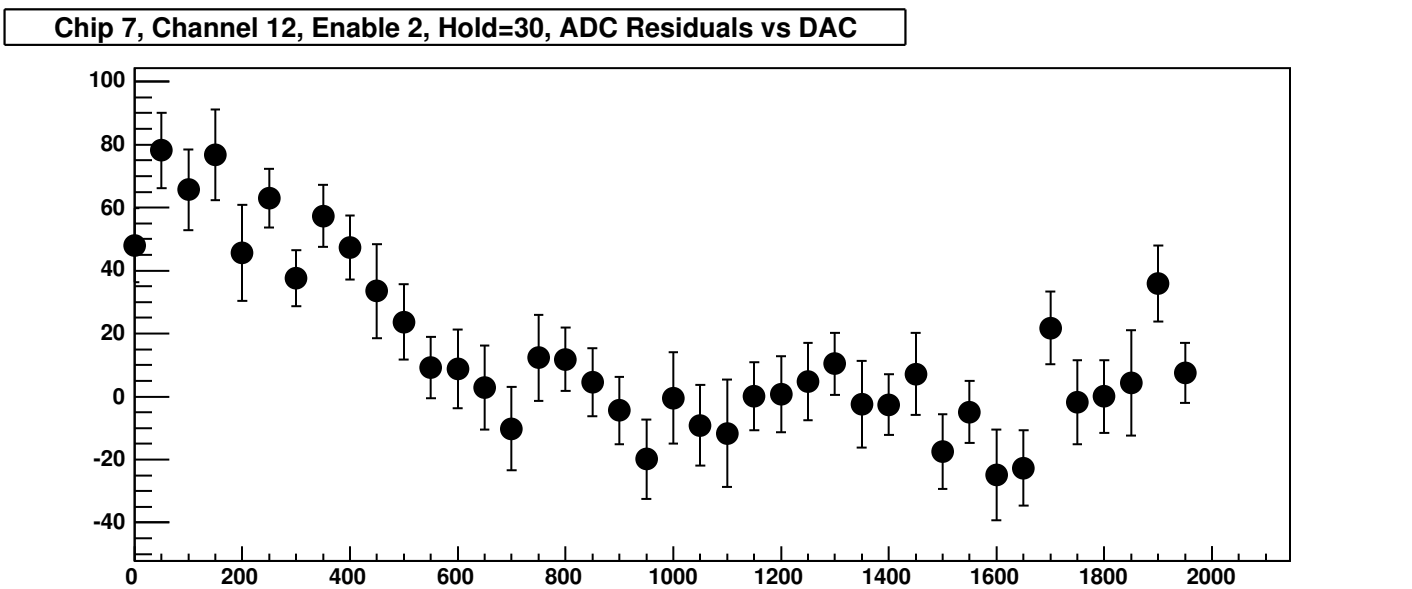

Chip 7, Channel 12, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 11.81 / 11
p0 -1378 ± 18.52
p1 0.01726 ± 0.01653
-1320
-1340
-1360
-1380
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

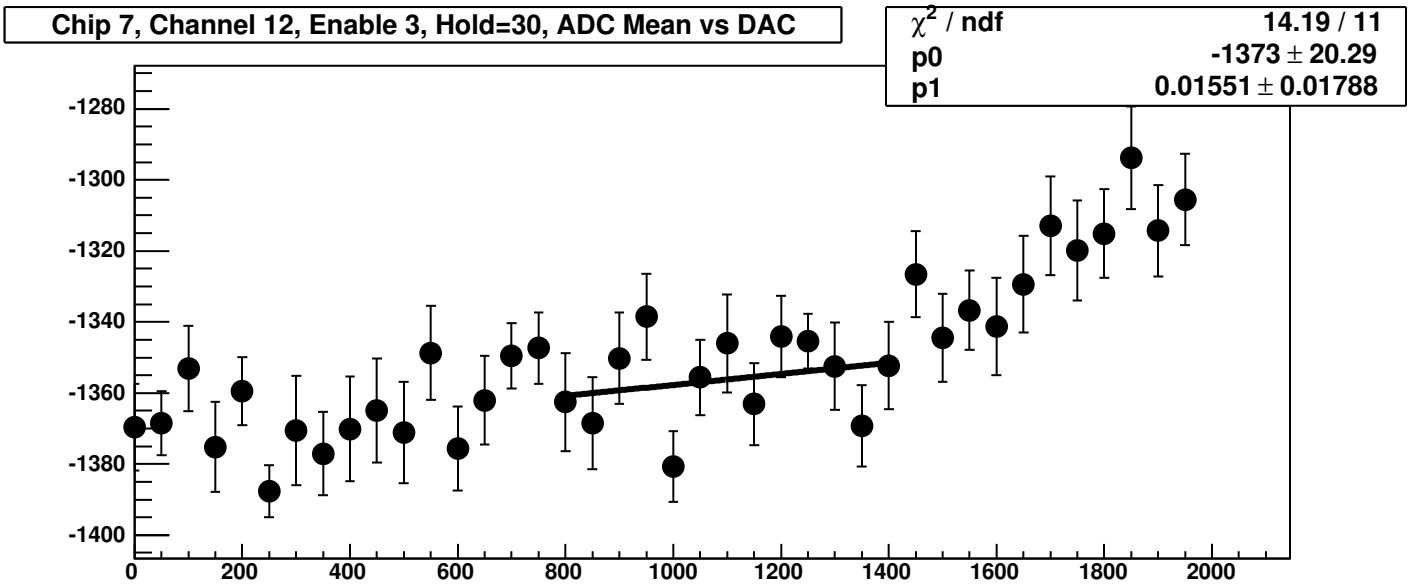

Chip 7, Channel 12, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

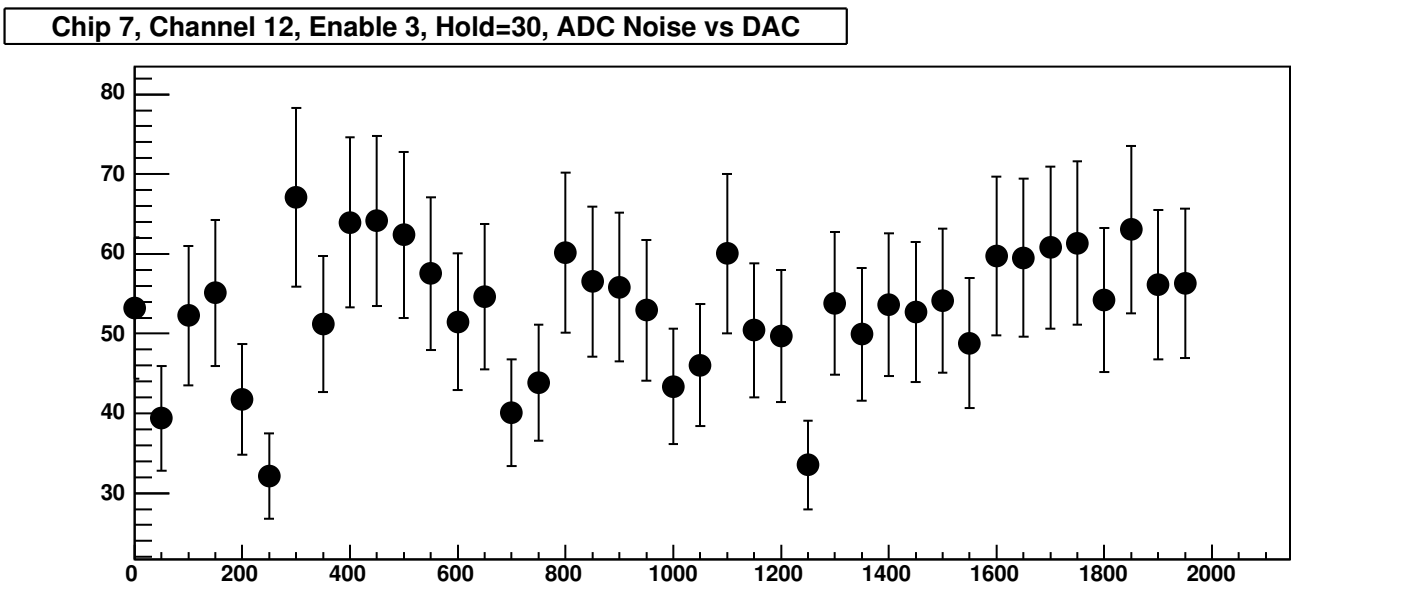

Chip 7, Channel 12, Enable 1, Hold=30, ADC Residuals vs DAC
40
30
20
10
0
-10
-20
-30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

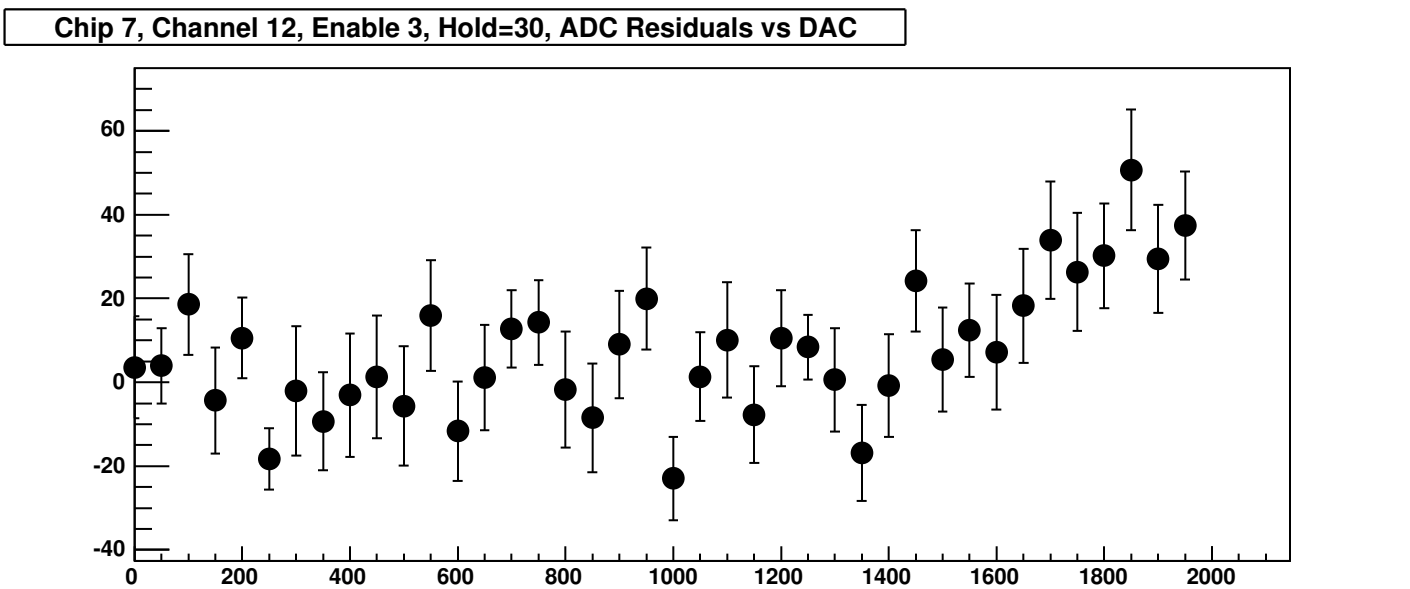

Chip 7, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 6.558 / 11
p0 -1397 ± 17.84
p1 0.1047 ± 0.01593
-1150
-1200
-1250
-1300
-1350
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

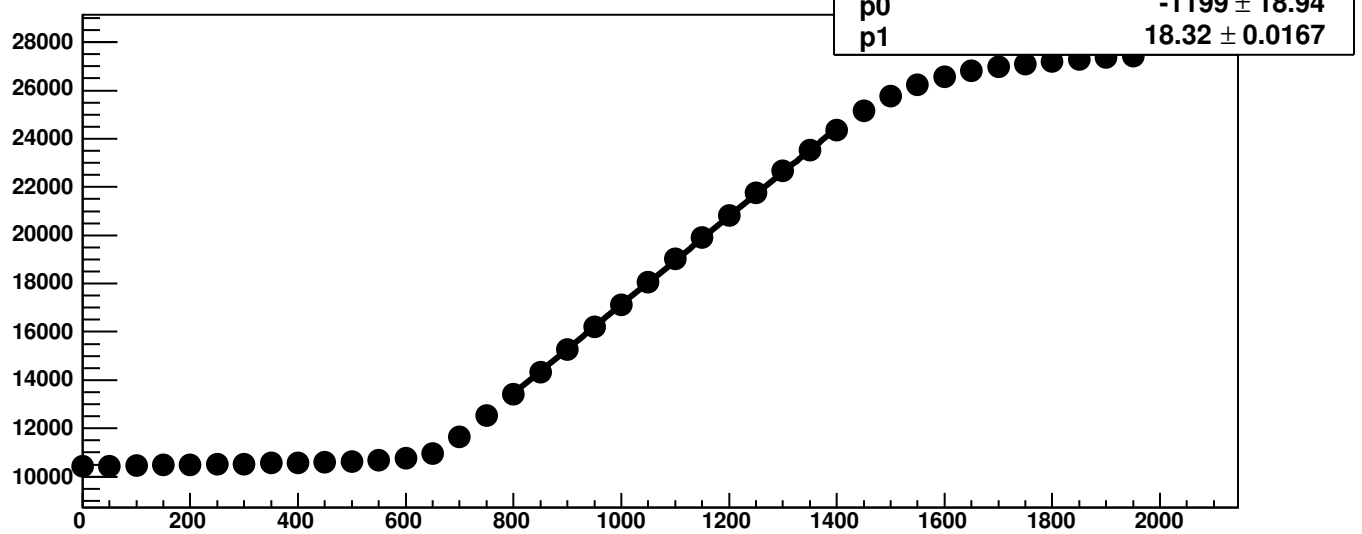

Chip 7, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 14.19 / 11
p0 -1373 ± 20.29
p1 0.01551 ± 0.01788
-1280
-1300
-1320
-1340
-1360
-1380
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

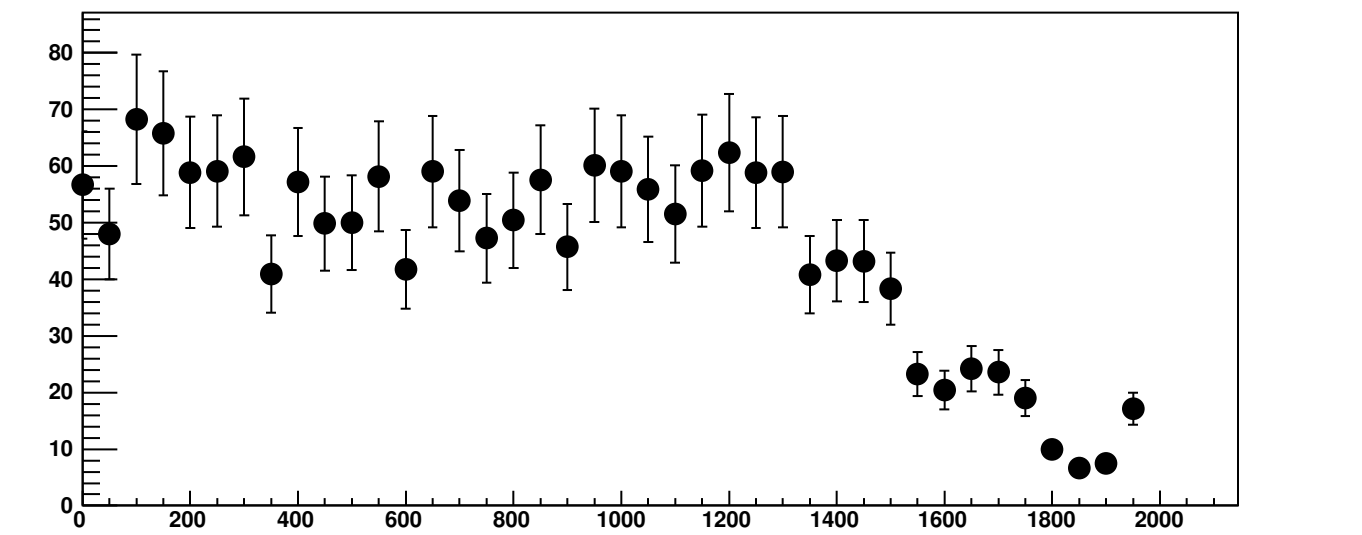

Chip 7, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

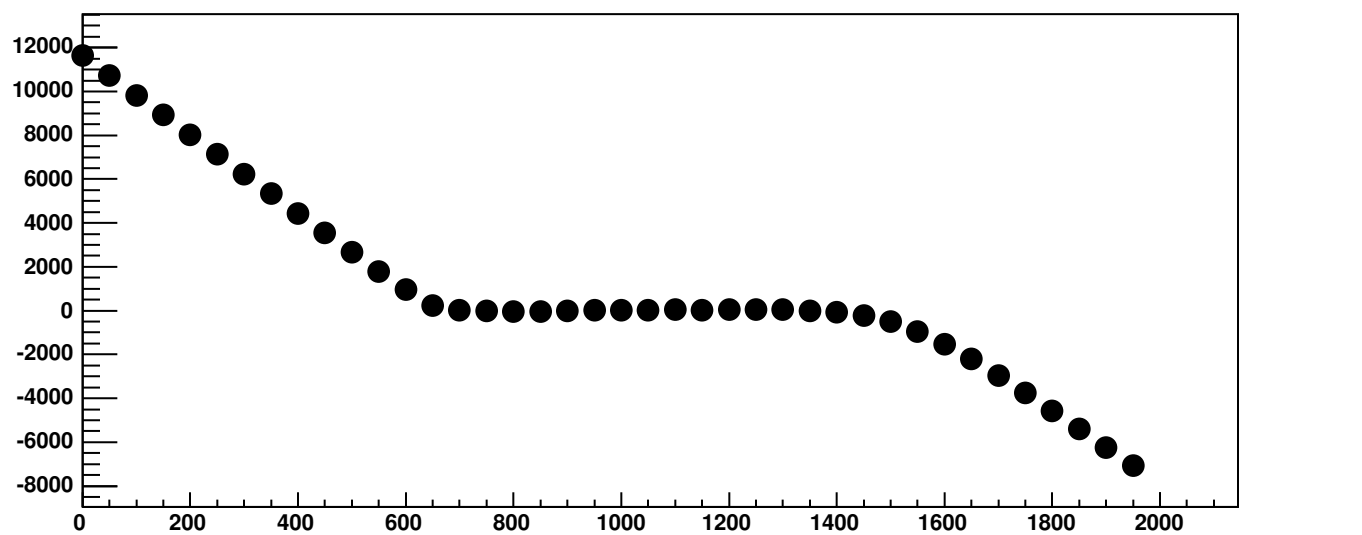
Chip 7, Channel 12, Enable 4!, Hold=30, ADC Mean vs DAC

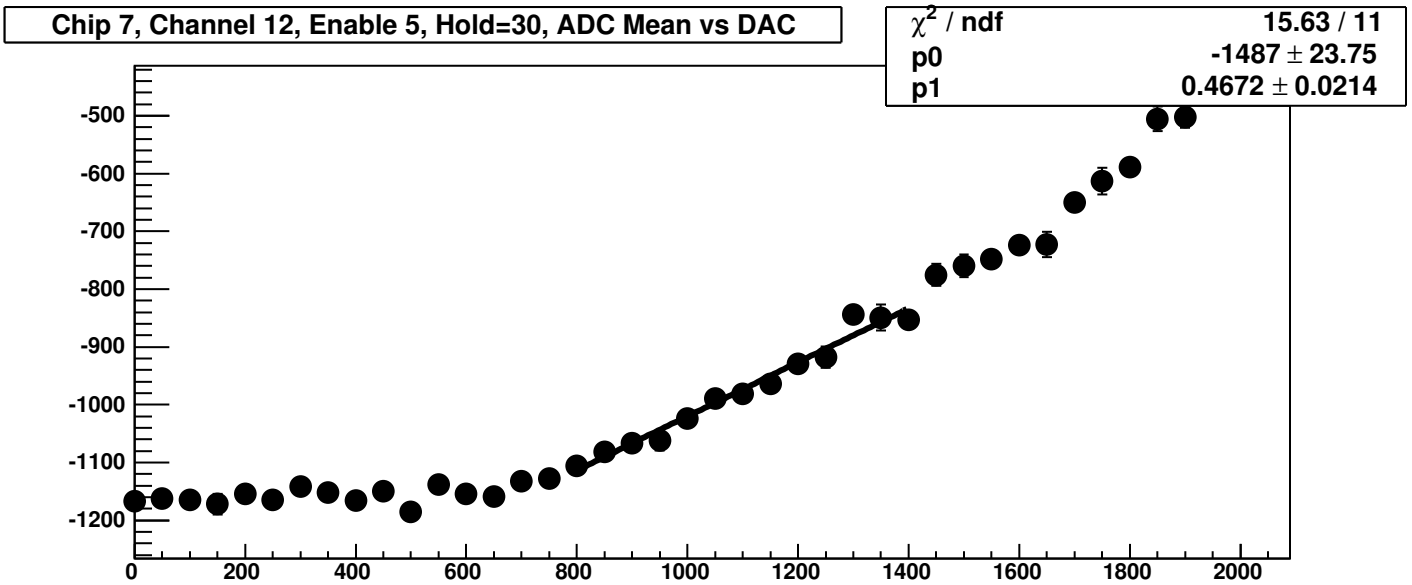


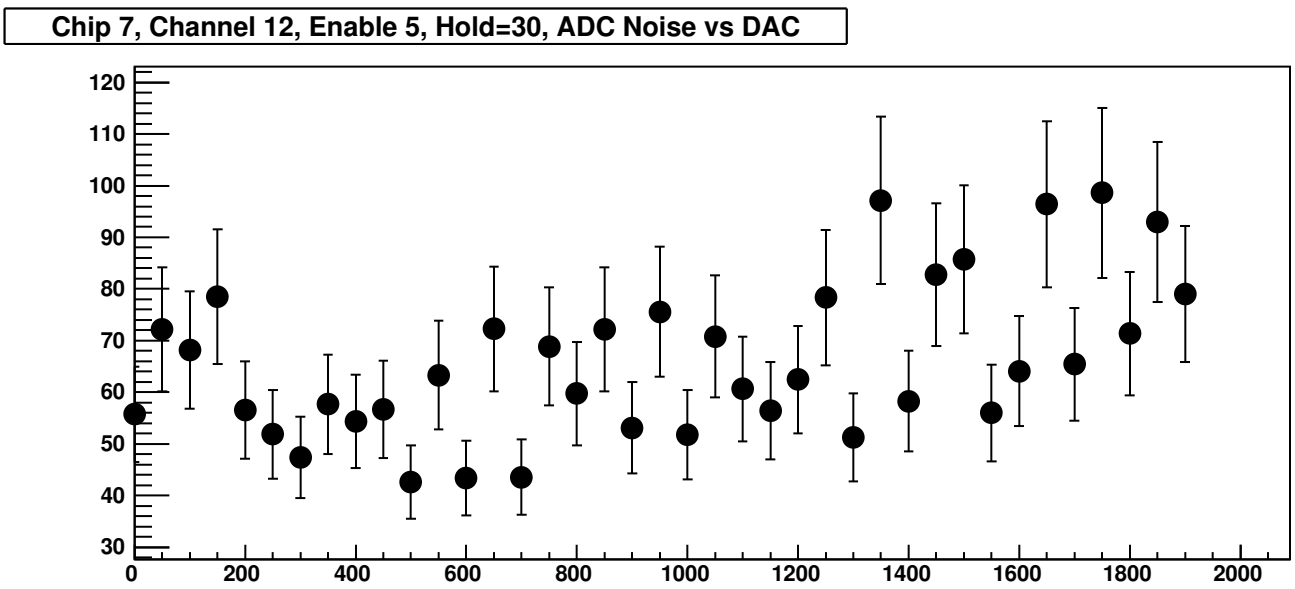
Chip 7, Channel 12, Enable 4!, Hold=30, ADC Noise vs DAC

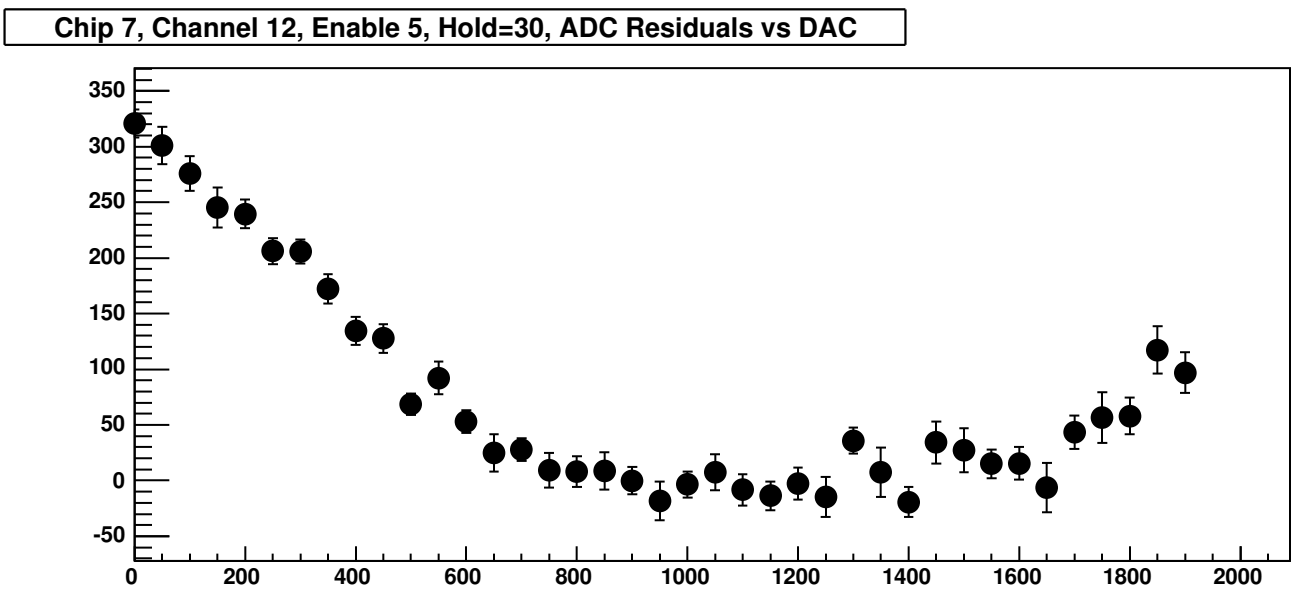


Chip 7, Channel 12, Enable 4!, Hold=30, ADC Residuals vs DAC

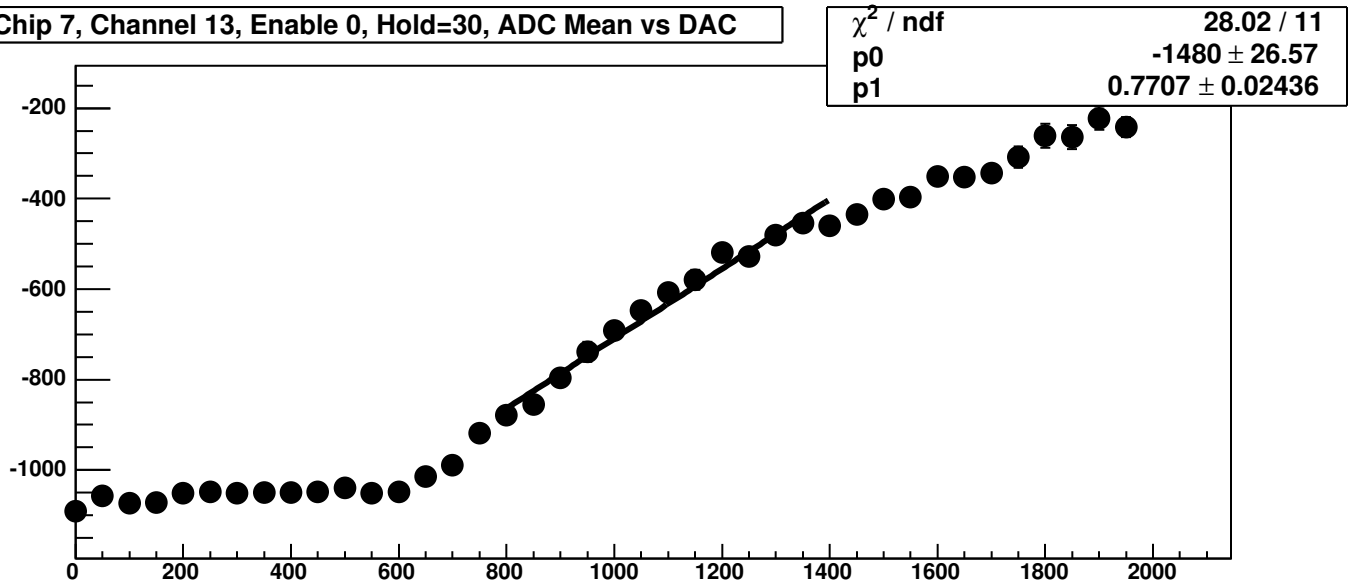



Chip 7, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 15.63 / 11
p0 -1487 ± 23.75
p1 0.4672 ± 0.0214
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

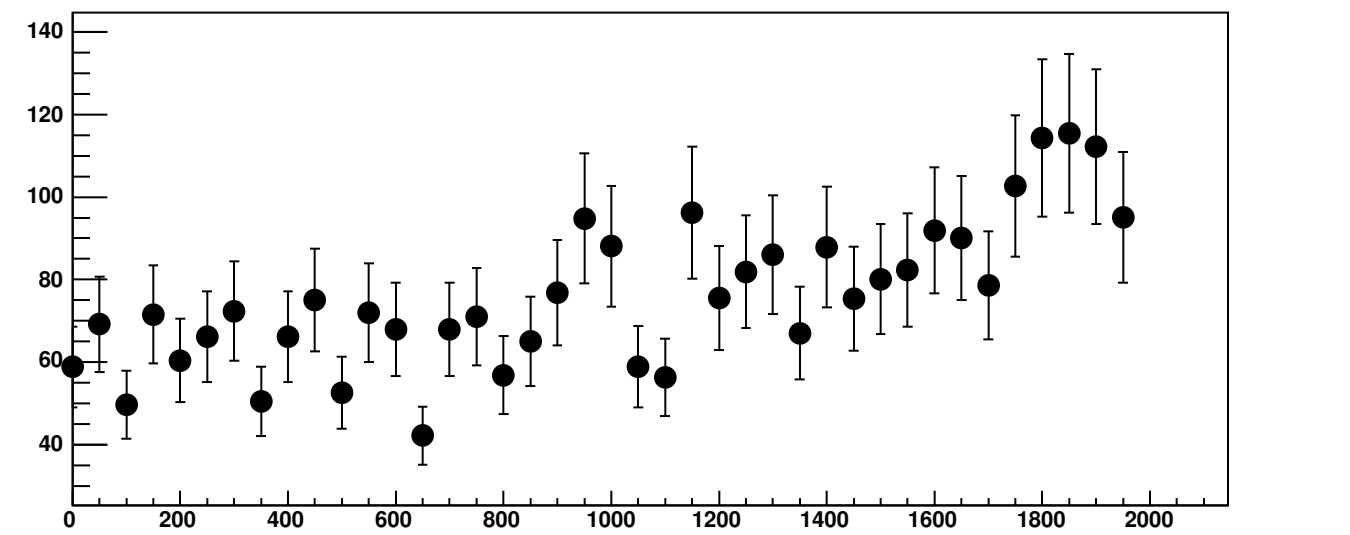

Chip 7, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
120
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

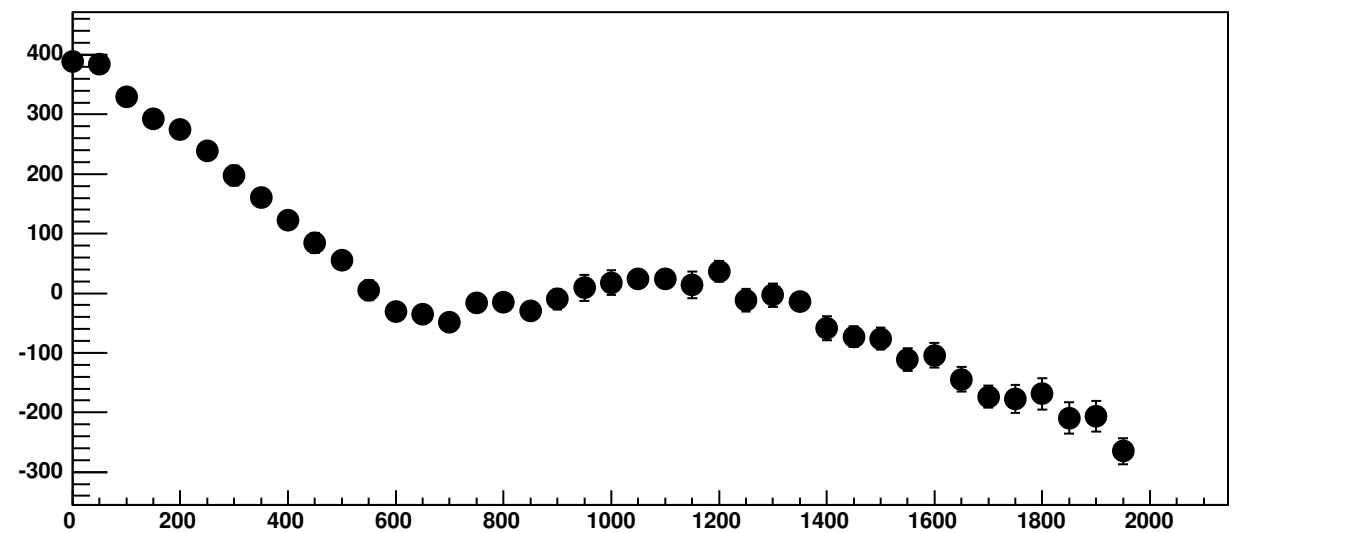
Chip 7, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC



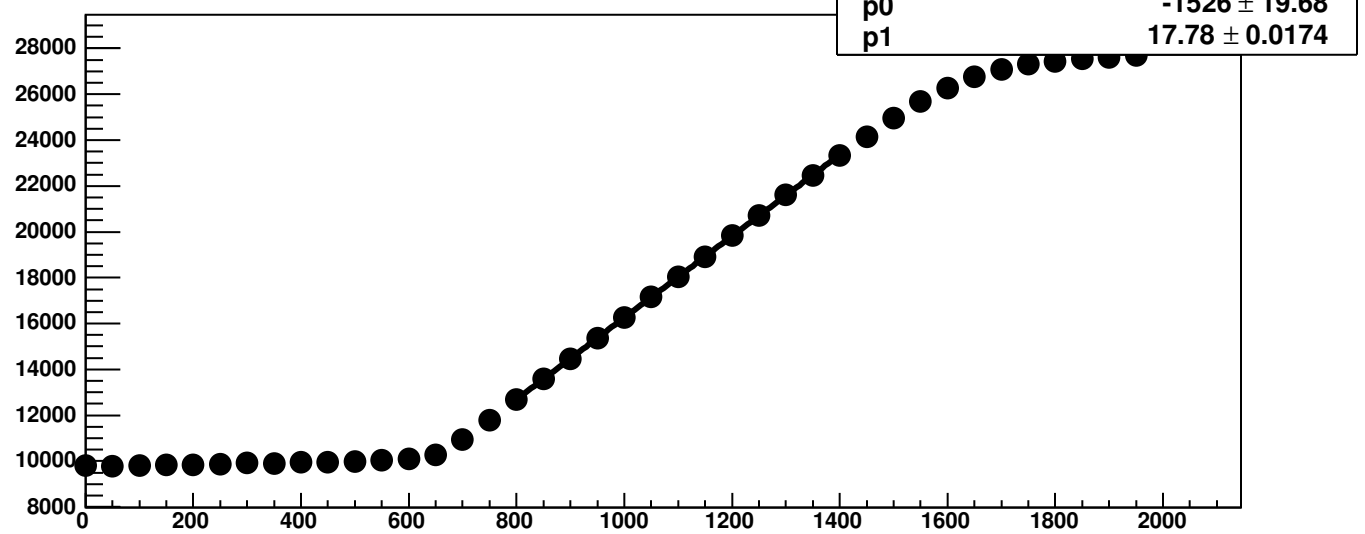
Chip 7, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC



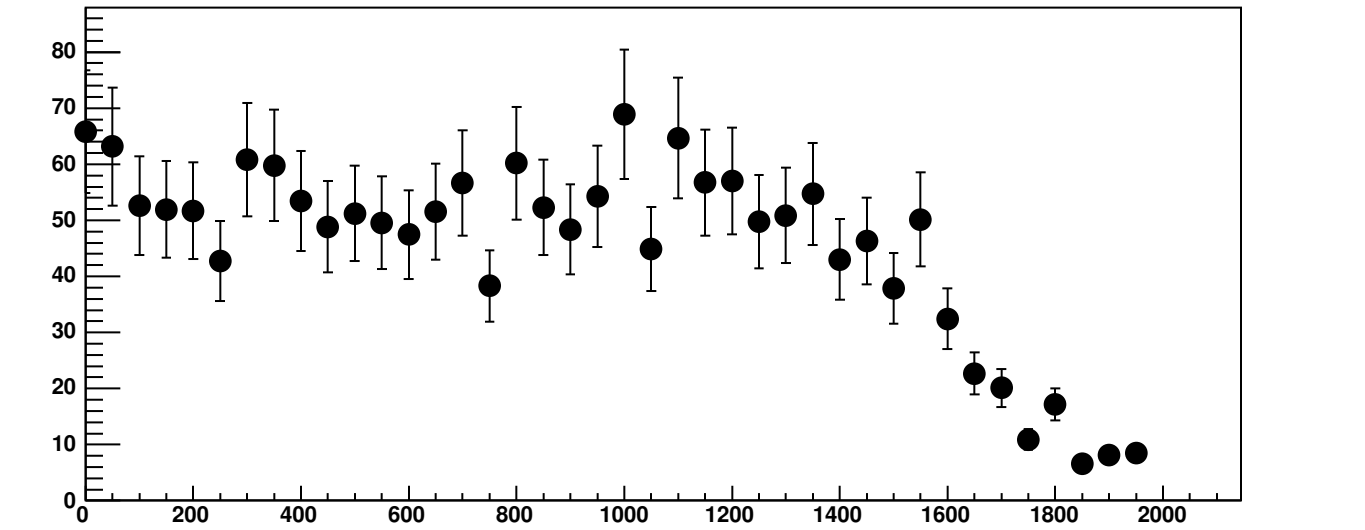
Chip 7, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC



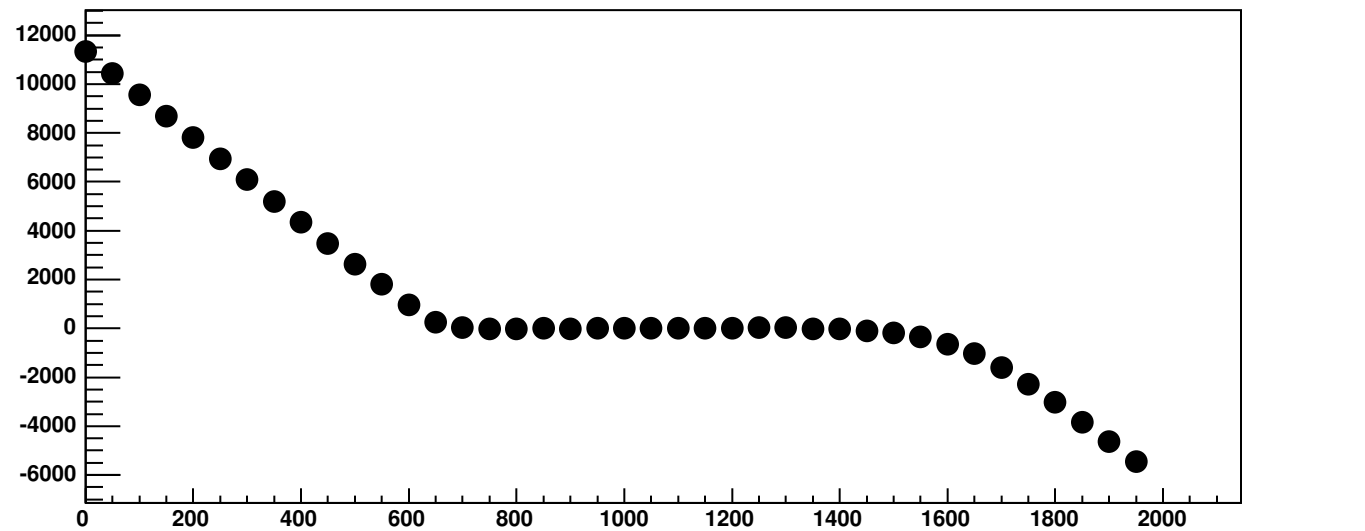
Chip 7, Channel 13, Enable 1!, Hold=30, ADC Mean vs DAC

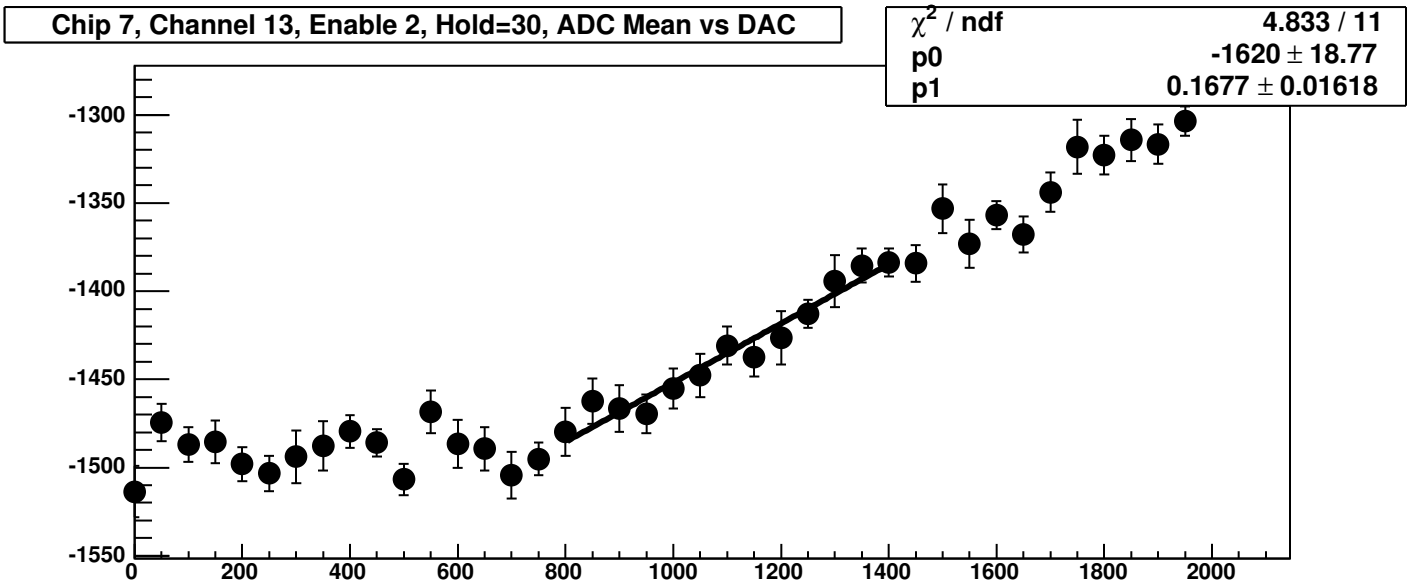


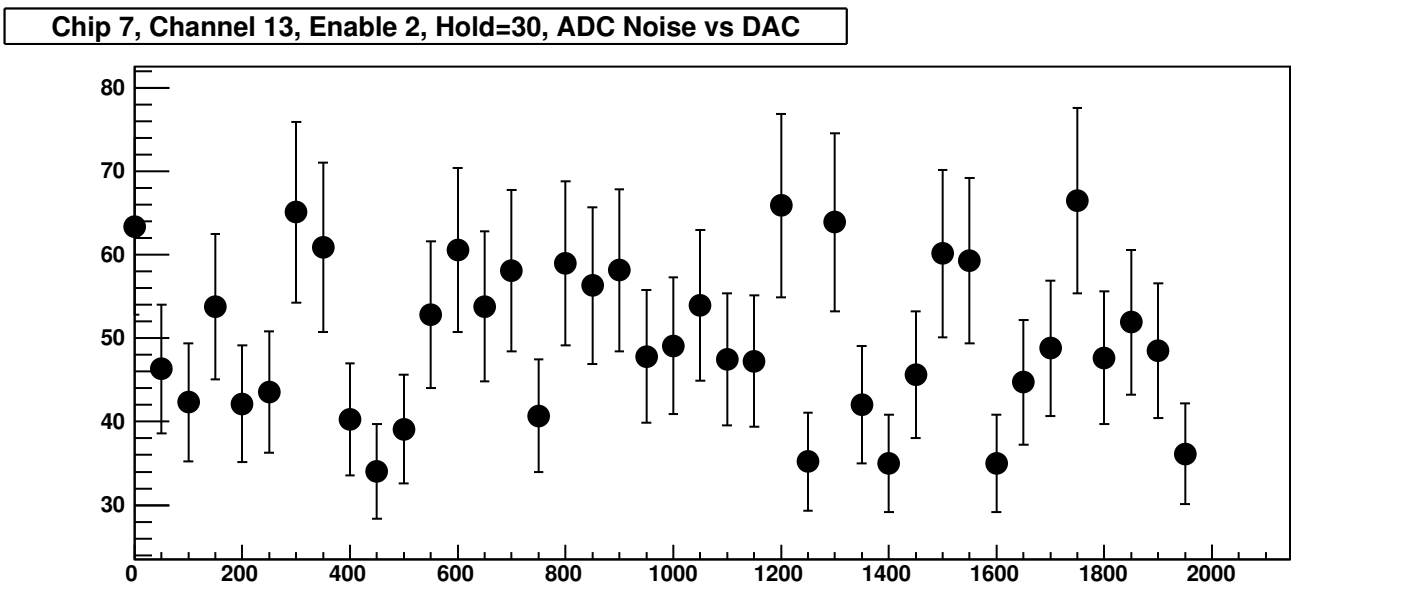
Chip 7, Channel 13, Enable 1!, Hold=30, ADC Noise vs DAC

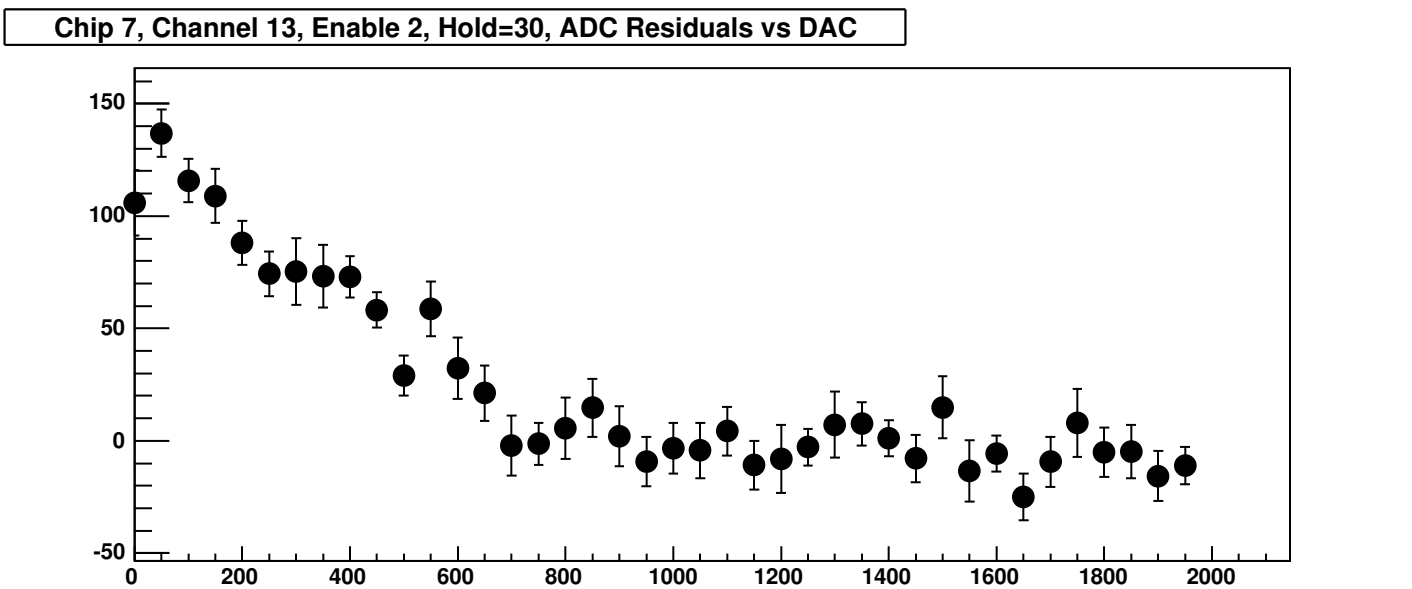


Chip 7, Channel 13, Enable 1!, Hold=30, ADC Residuals vs DAC

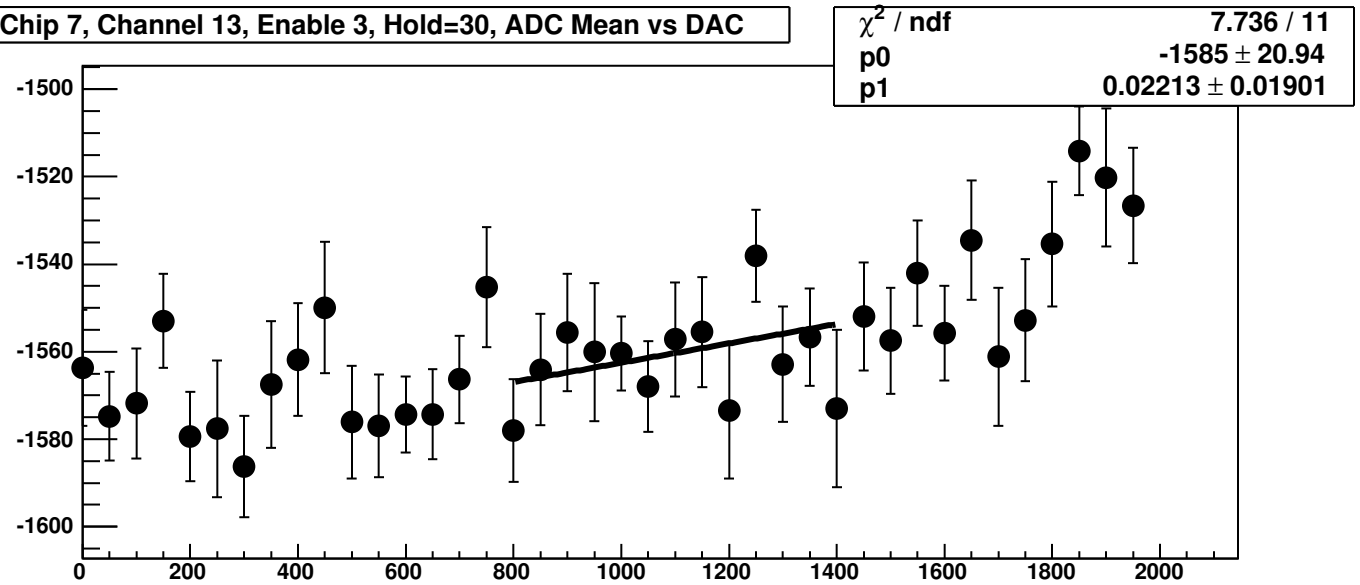



Chip 7, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4.833 / 11
p0 -1620 ± 18.77
p1 0.1677 ± 0.01618
-1300
-1350
-1400
-1450
-1500
-1550
0
200
400
600
800
1000
1200
1400
1600
1800
2000

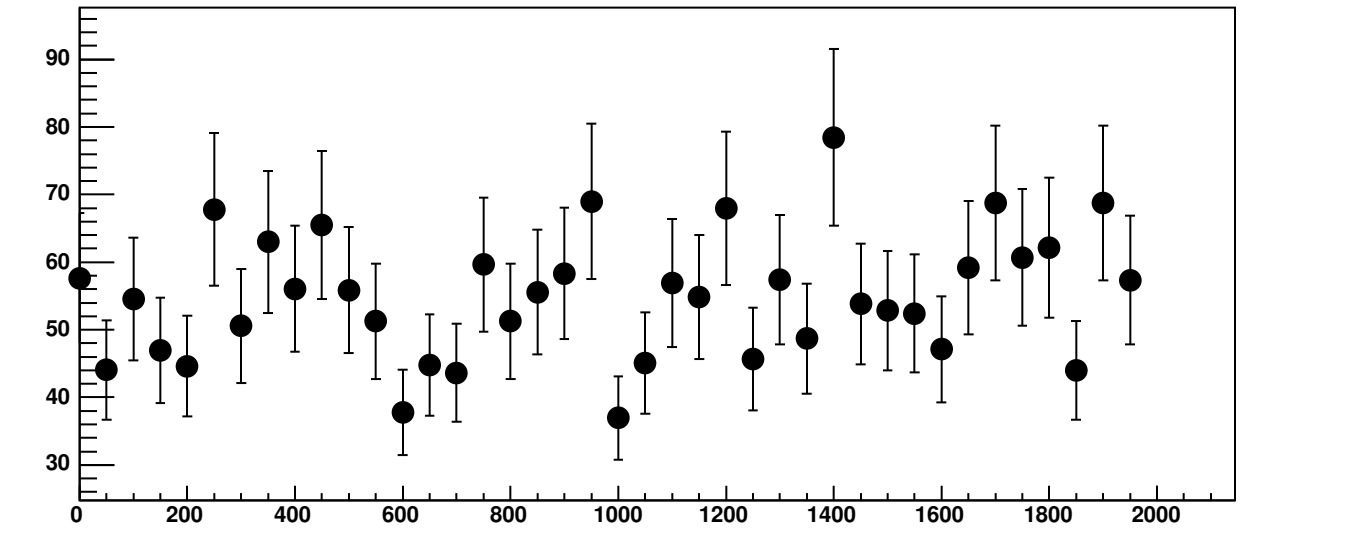

Chip 7, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

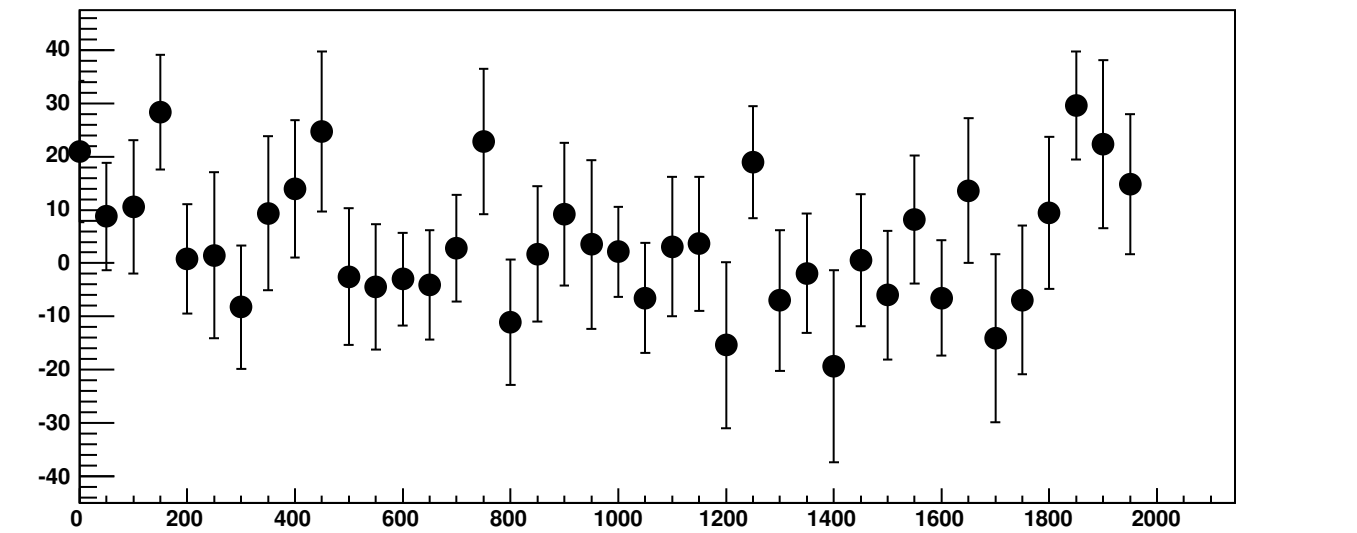
Chip 7, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC

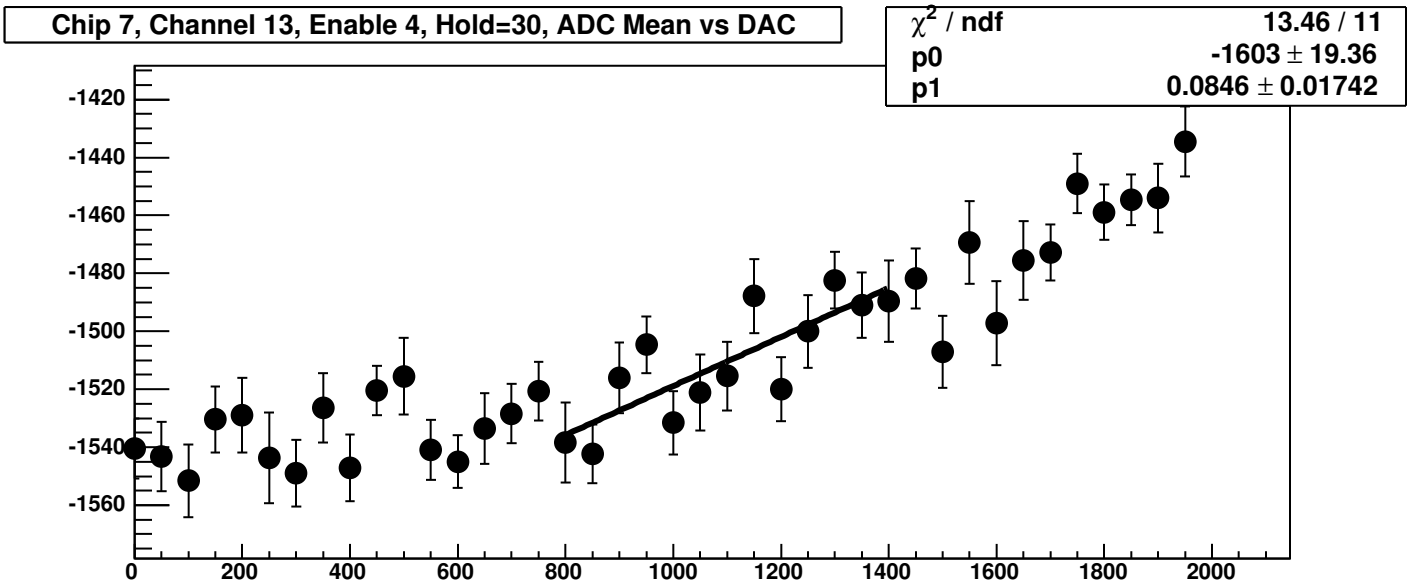


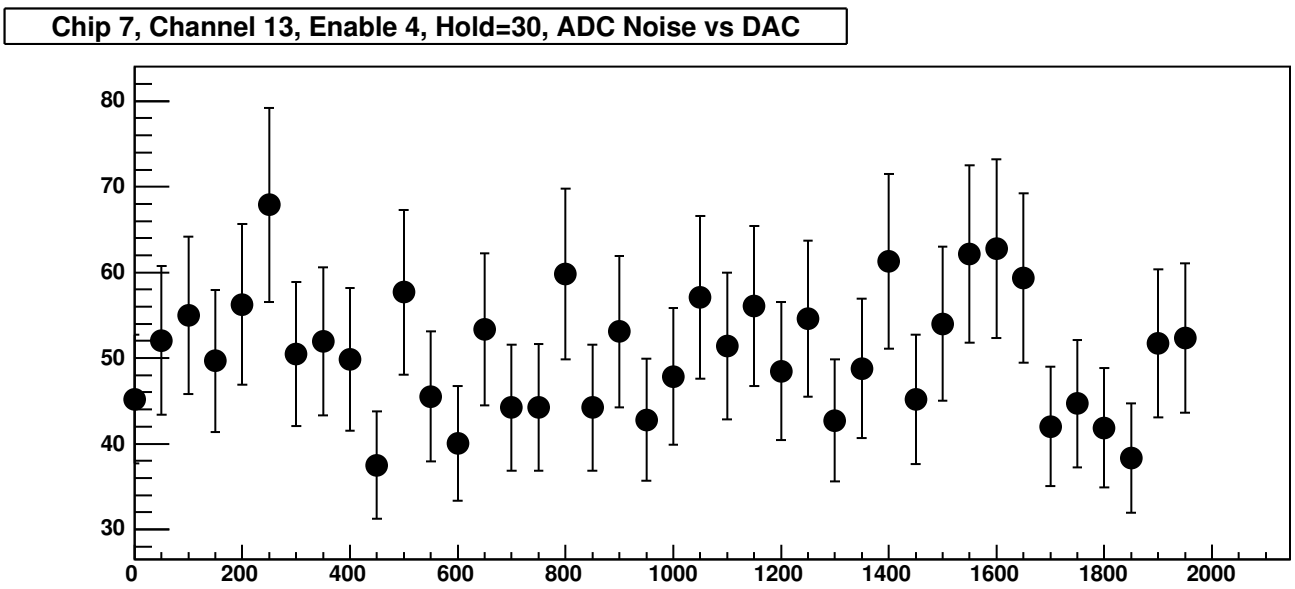
Chip 7, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC

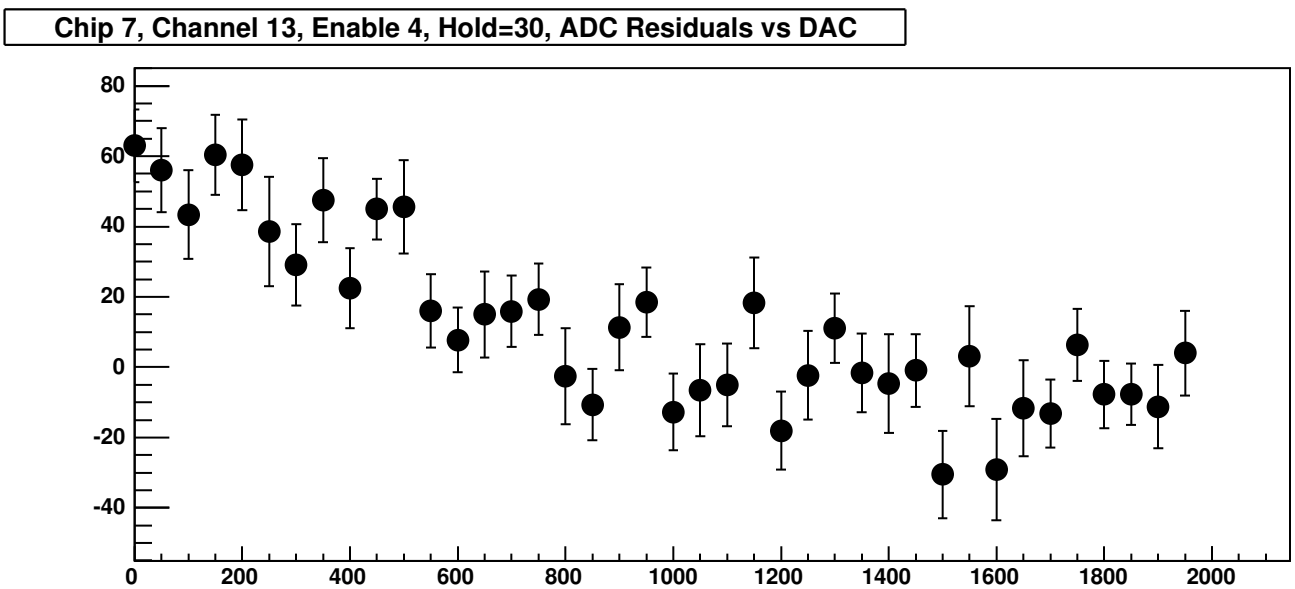


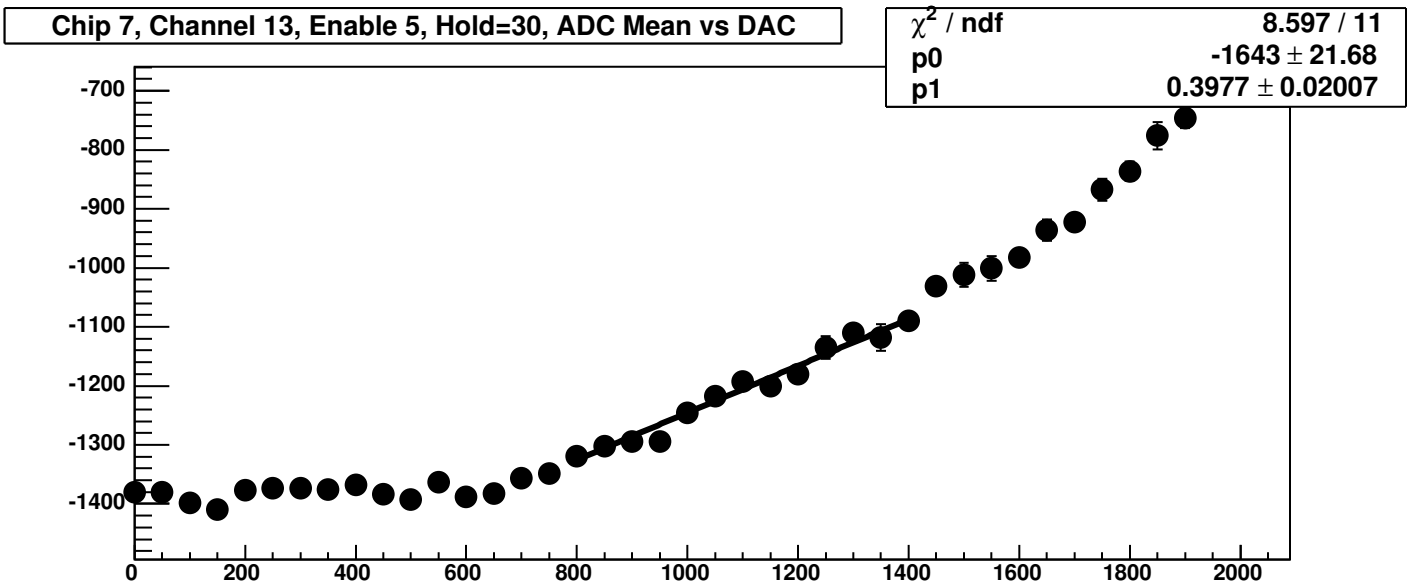
Chip 7, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC

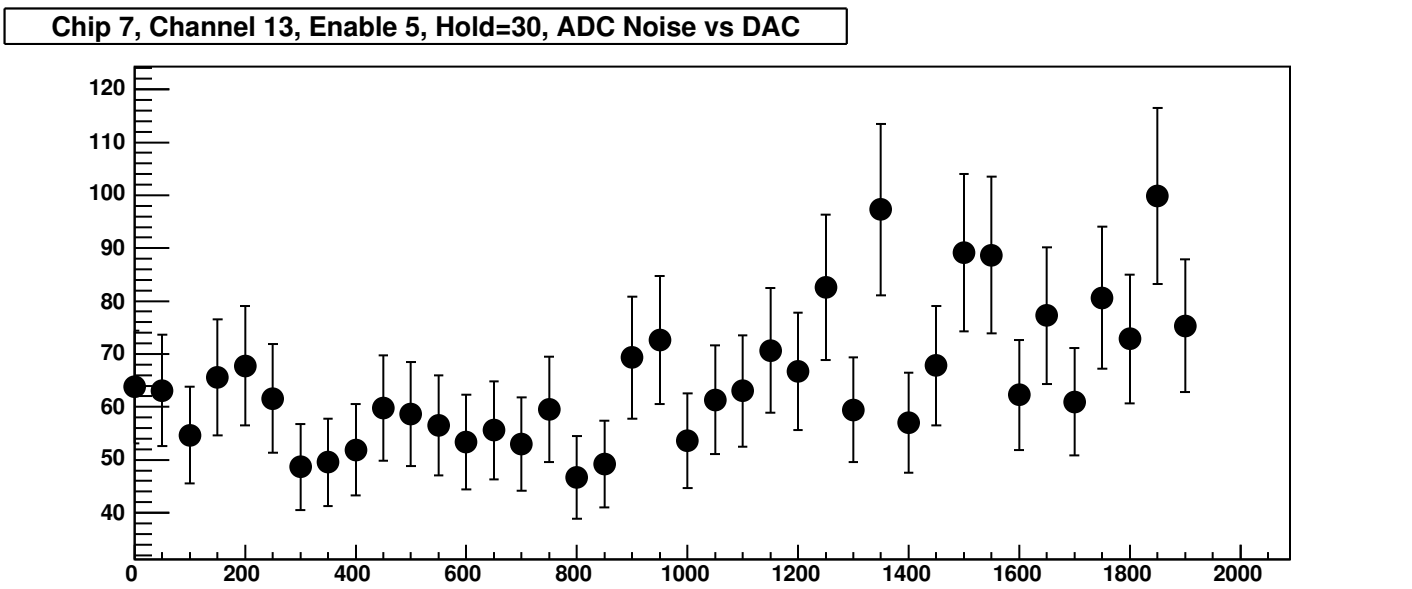


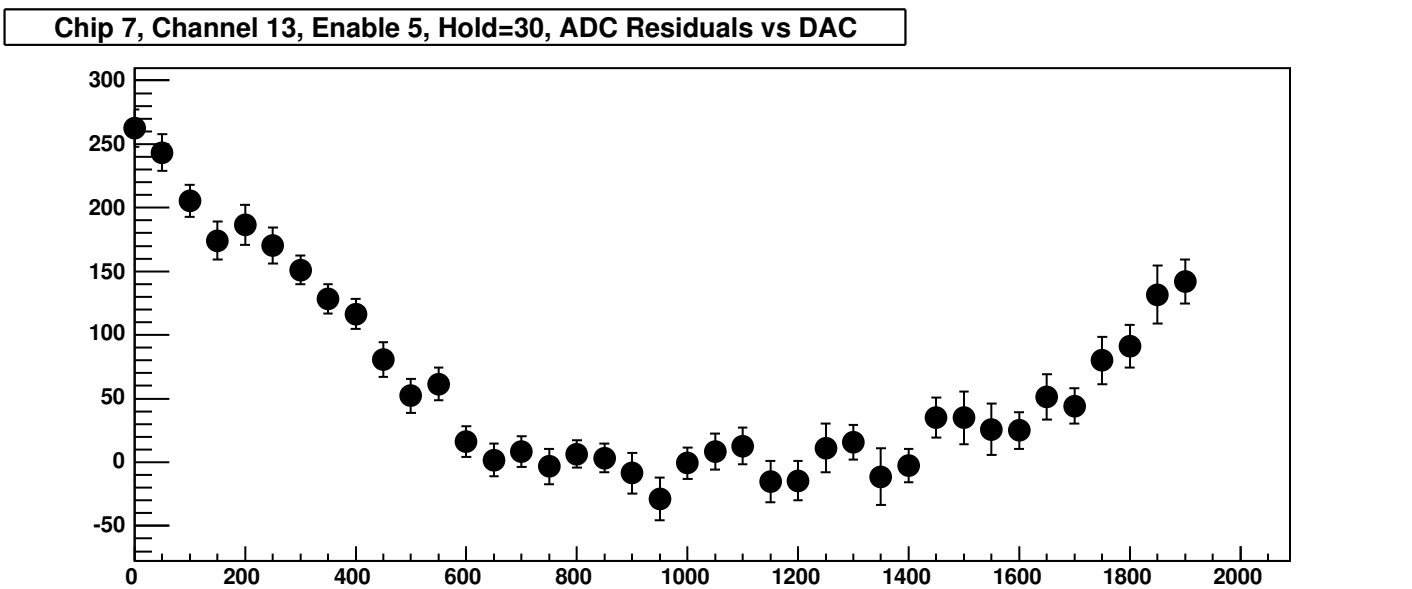

Chip 7, Channel 13, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 13.46 / 11
p0 -1603 ± 19.36
p1 0.0846 ± 0.01742
-1420
-1440
-1460
-1480
-1500
-1520
-1540
-1560
0
200
400
600
800
1000
1200
1400
1600
1800
2000

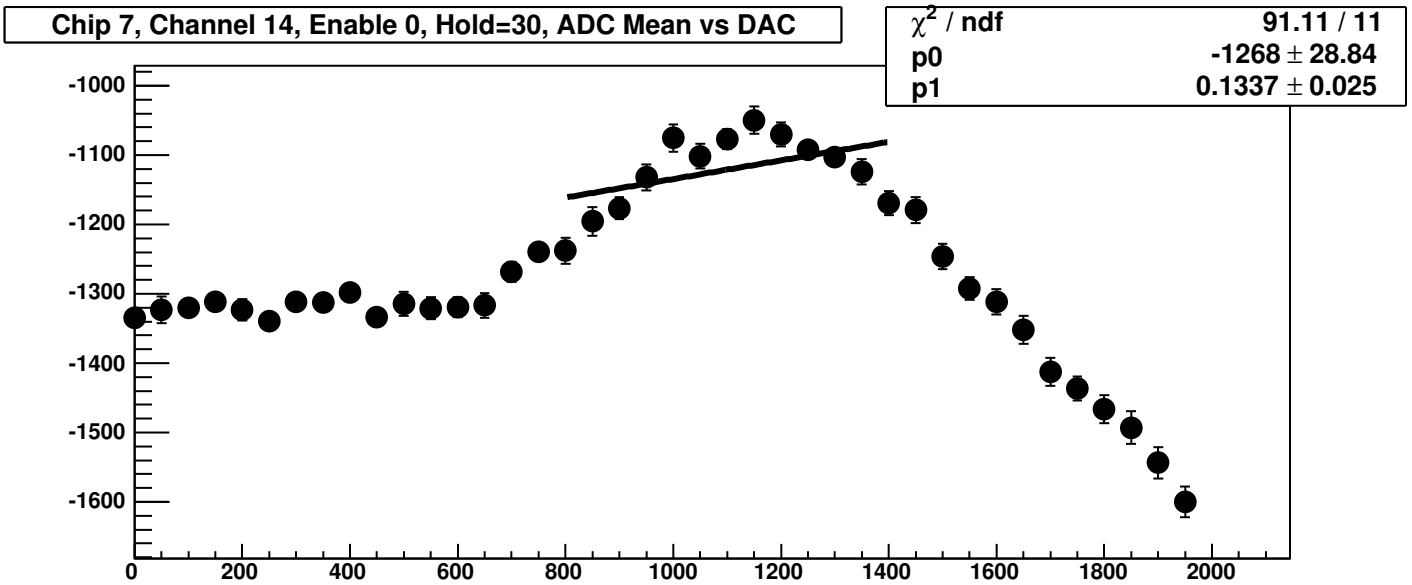

Chip 7, Channel 13, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

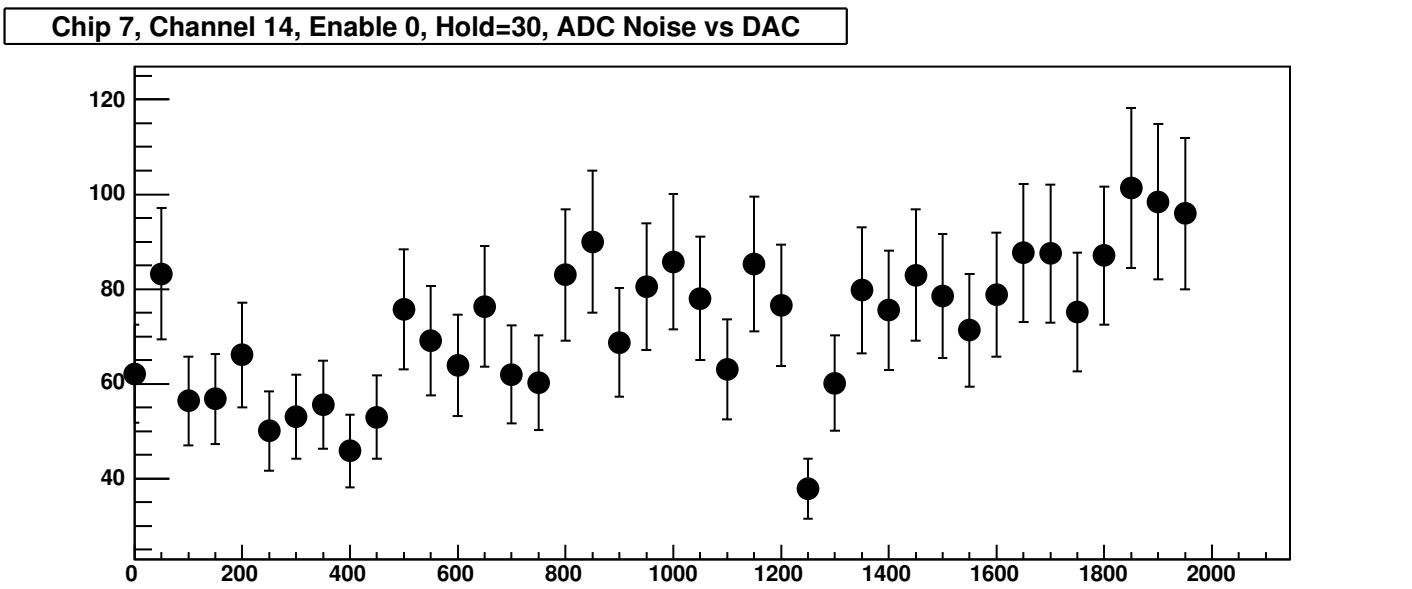

Chip 7, Channel 13, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

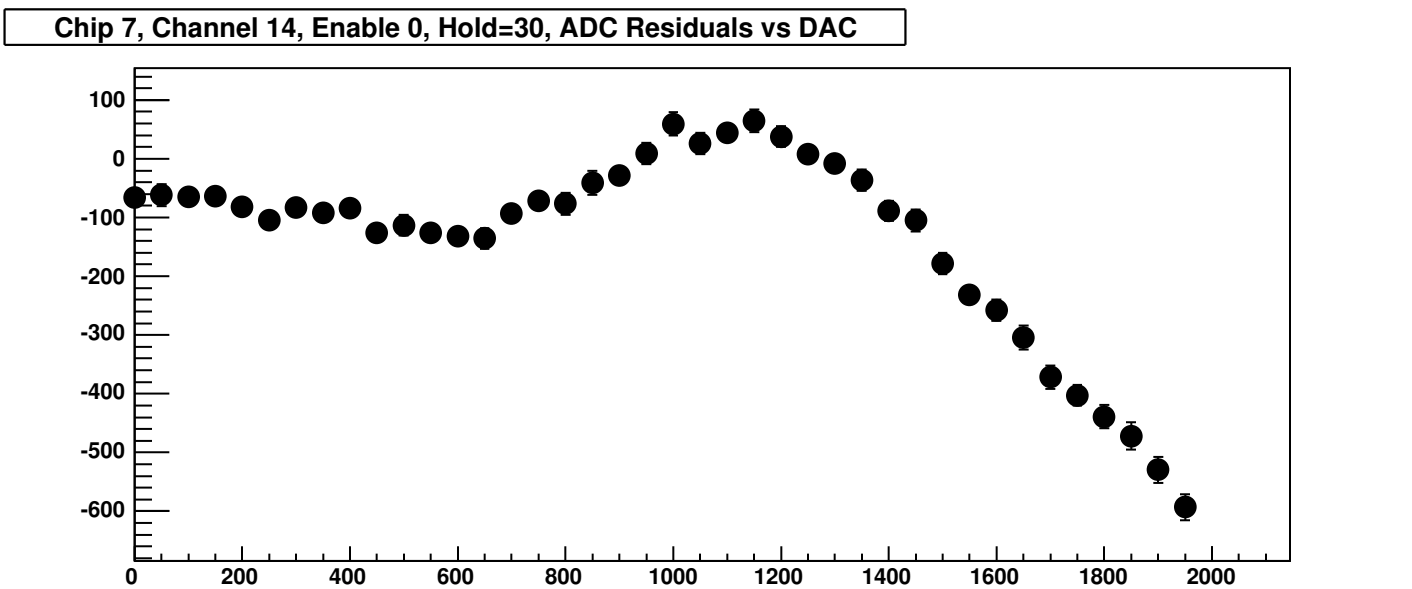

Chip 7, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8.597 / 11
p0 -1643 ± 21.68
p1 0.3977 ± 0.02007
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

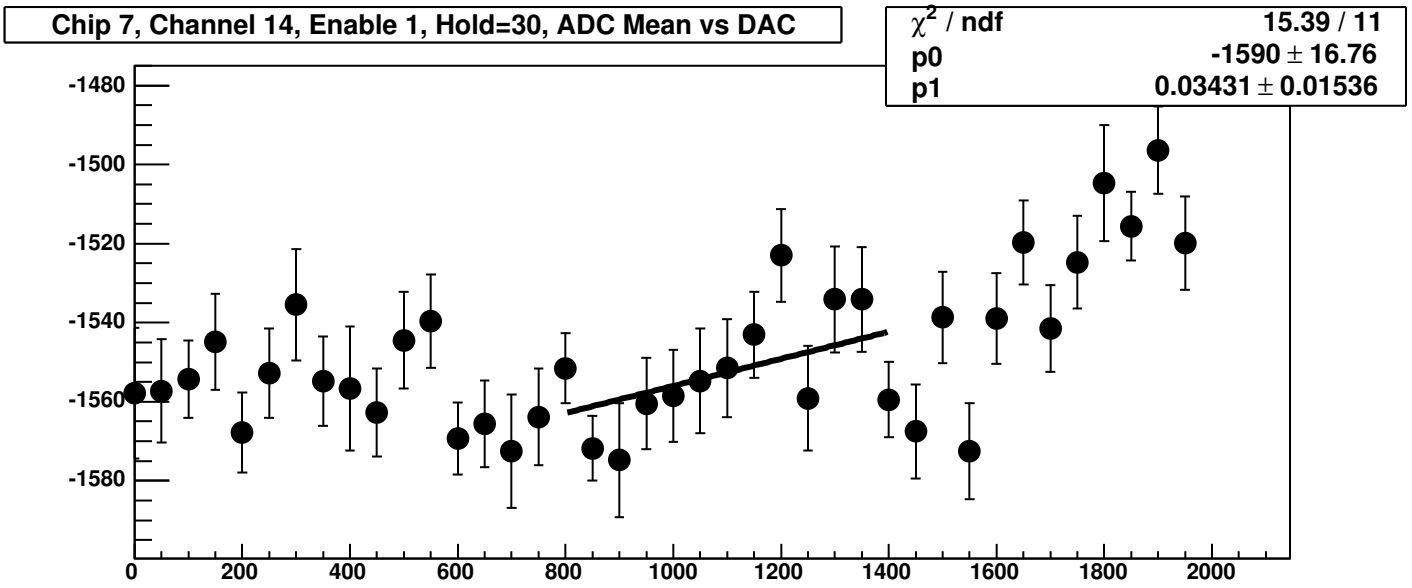

Chip 7, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
120
110
100
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

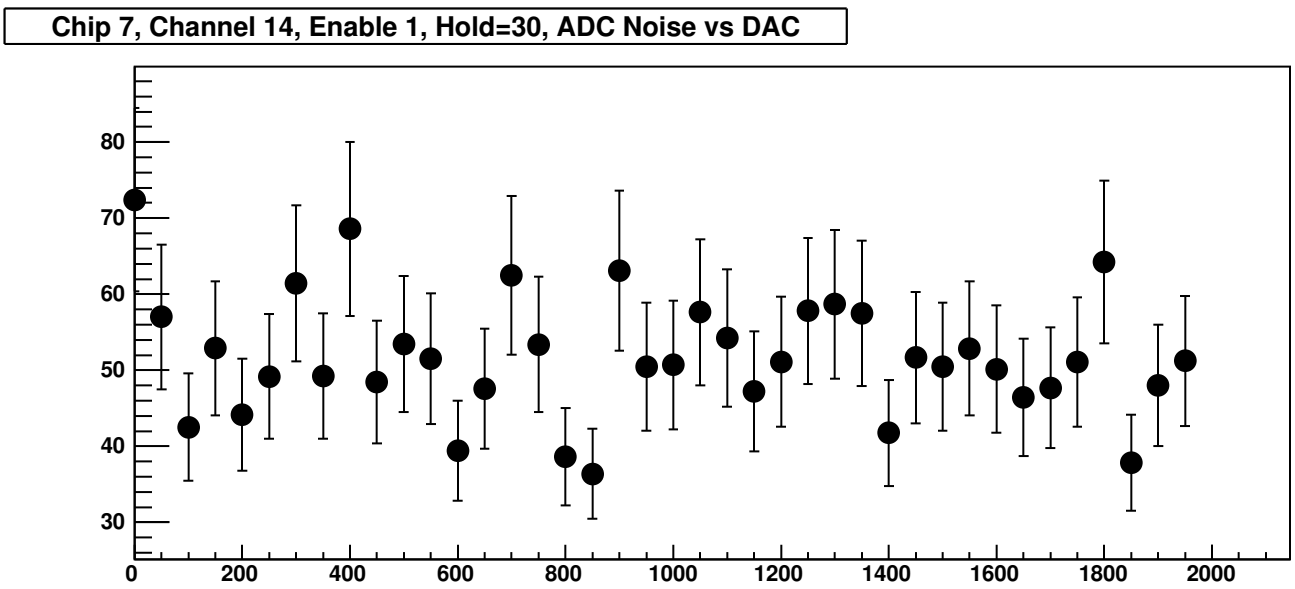

Chip 7, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

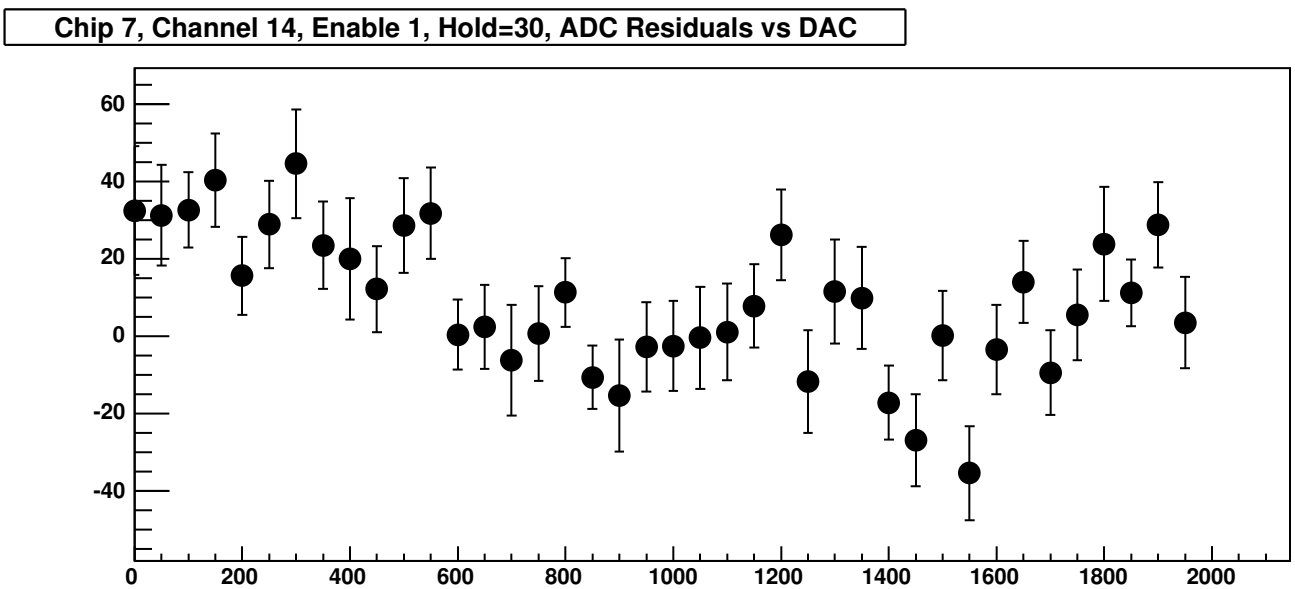

Chip 7, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ^2 / ndf 91.11 / 11
p0 -1268 ± 28.84
p1 0.1337 ± 0.025
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

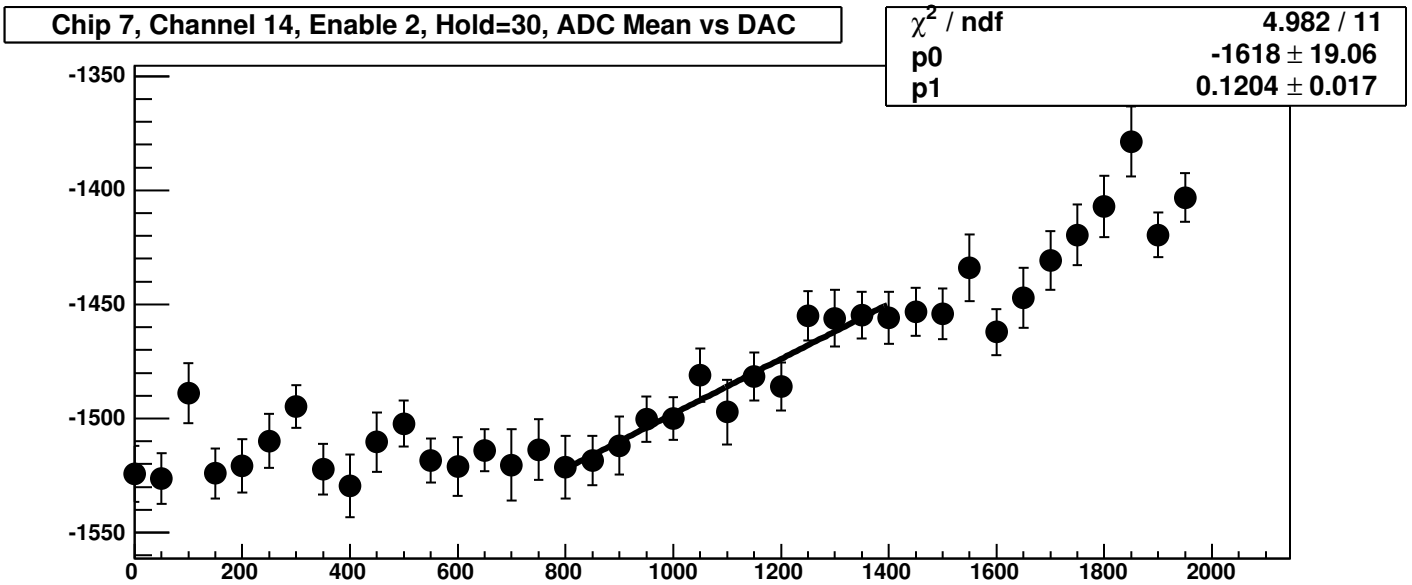

Chip 7, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

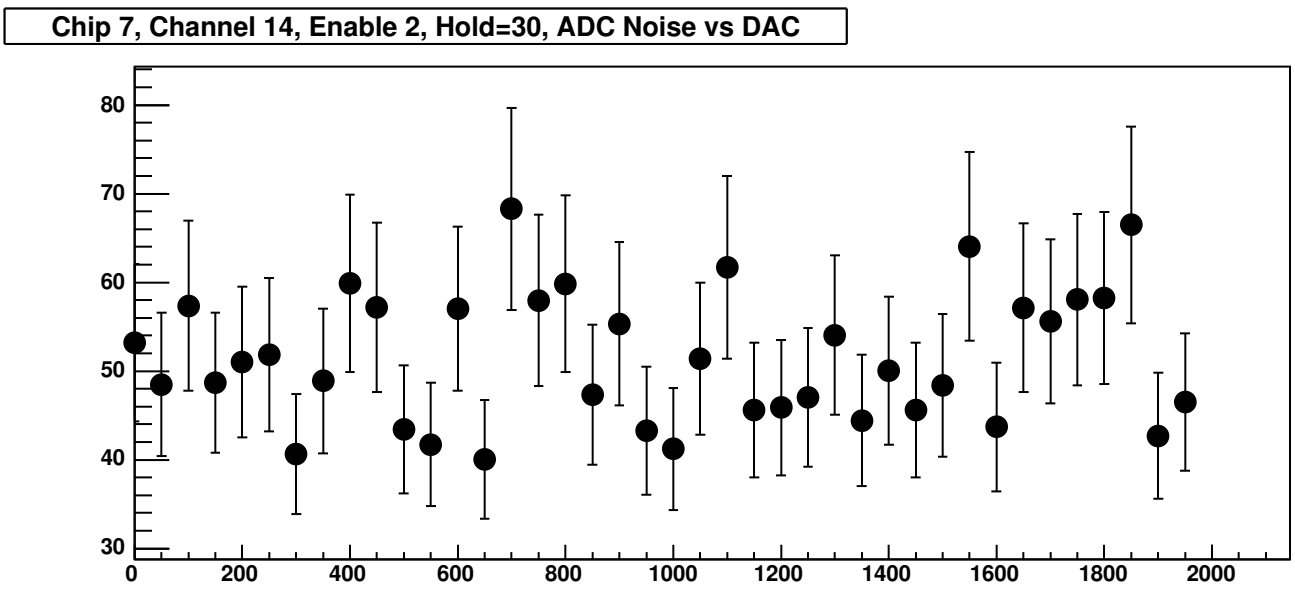

Chip 7, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
100
0
-100
-200
-300
-400
-500
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

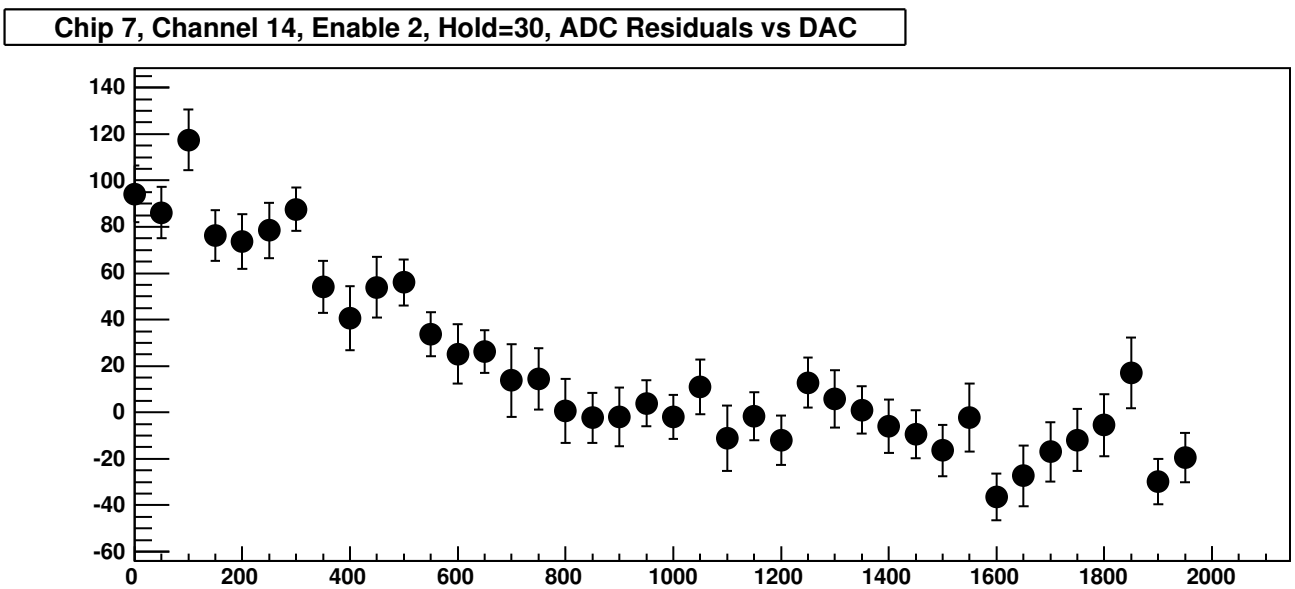

Chip 7, Channel 14, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 15.39 / 11
p0 -1590 ± 16.76
p1 0.03431 ± 0.01536
-1480
-1500
-1520
-1540
-1560
-1580
0
200
400
600
800
1000
1200
1400
1600
1800
2000

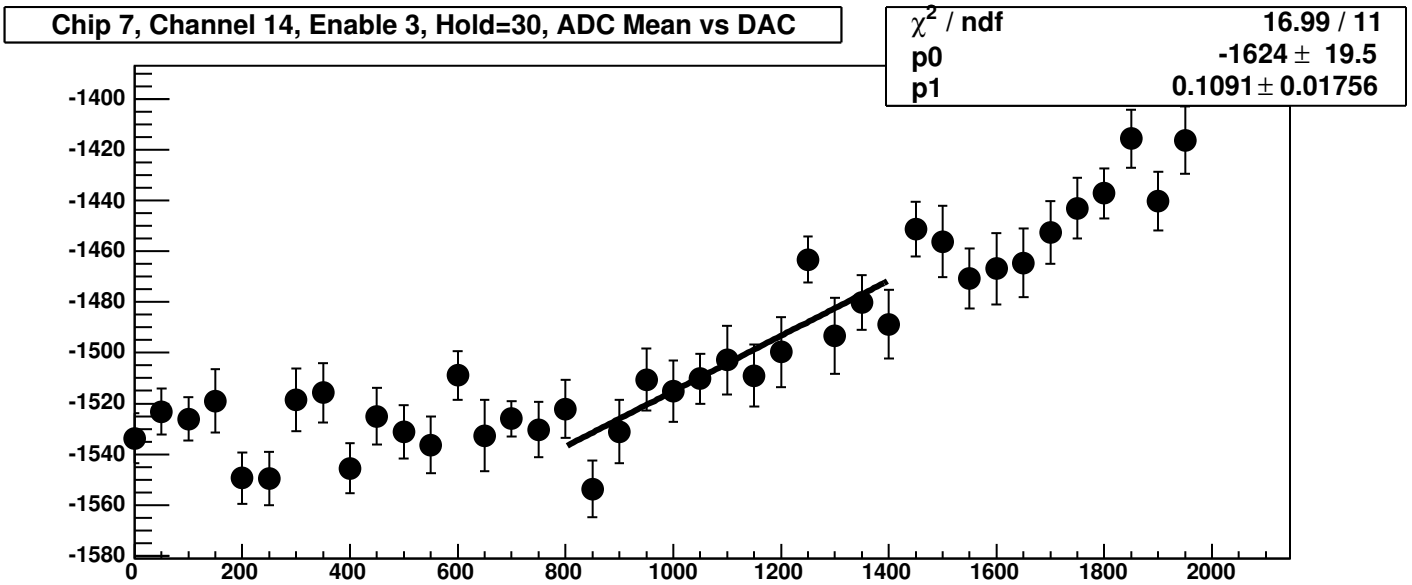

Chip 7, Channel 14, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

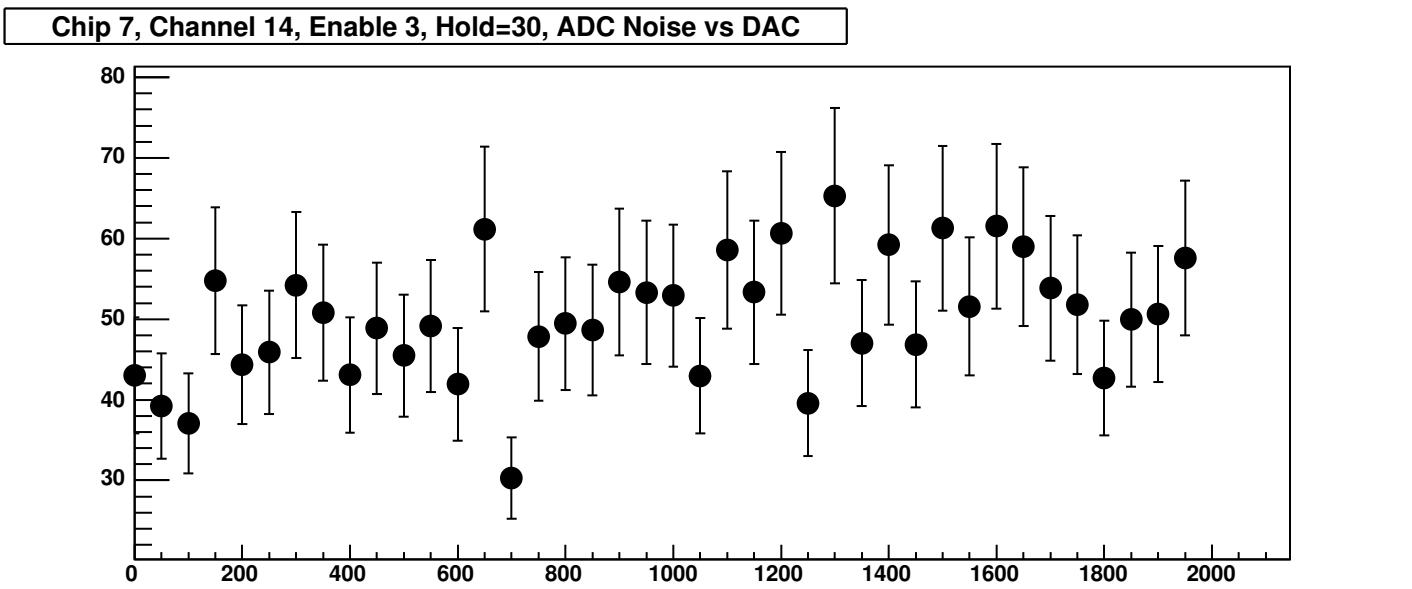

Chip 7, Channel 14, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

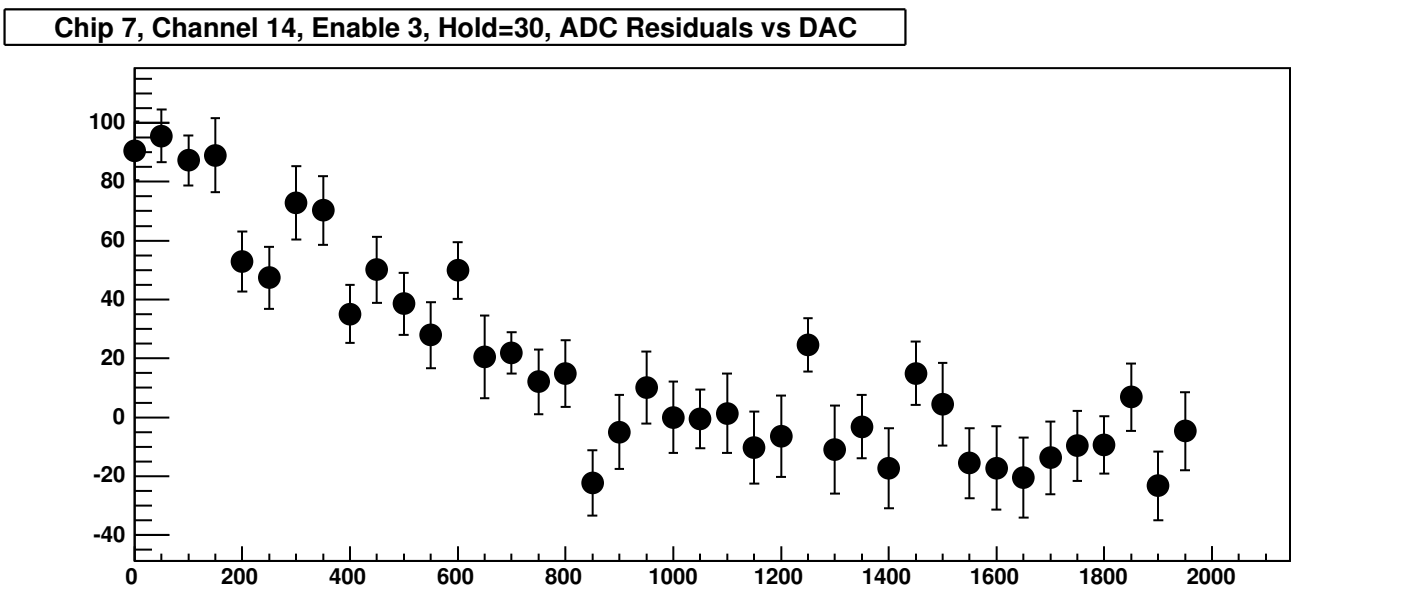

Chip 7, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4.982 / 11
p0 -1618 ± 19.06
p1 0.1204 ± 0.017
-1350
-1400
-1450
-1500
-1550
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

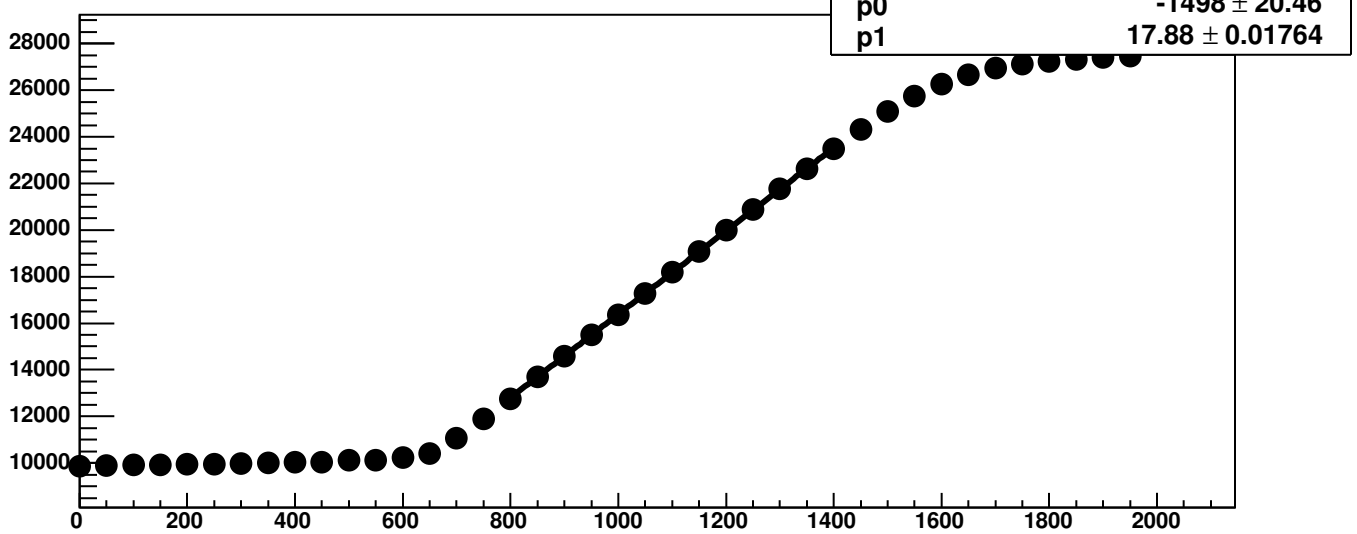

Chip 7, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 16.99 / 11
p0 -1624 ± 19.5
p1 0.1091 ± 0.01756
-1400
-1420
-1440
-1460
-1480
-1500
-1520
-1540
-1560
-1580
0
200
400
600
800
1000
1200
1400
1600
1800
2000

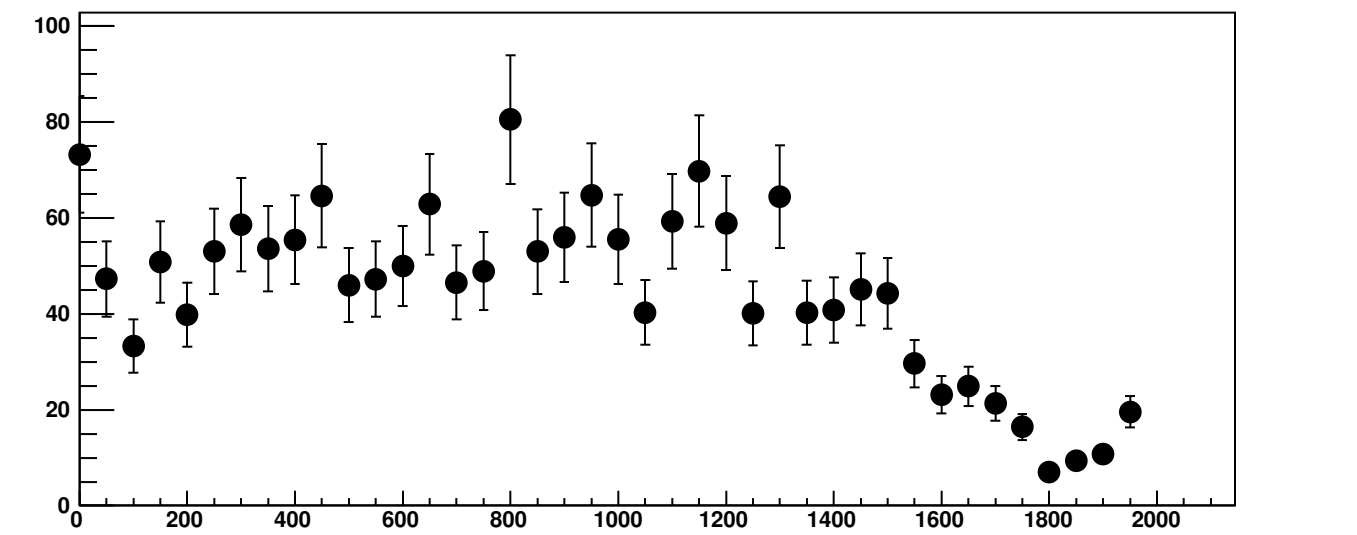

Chip 7, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

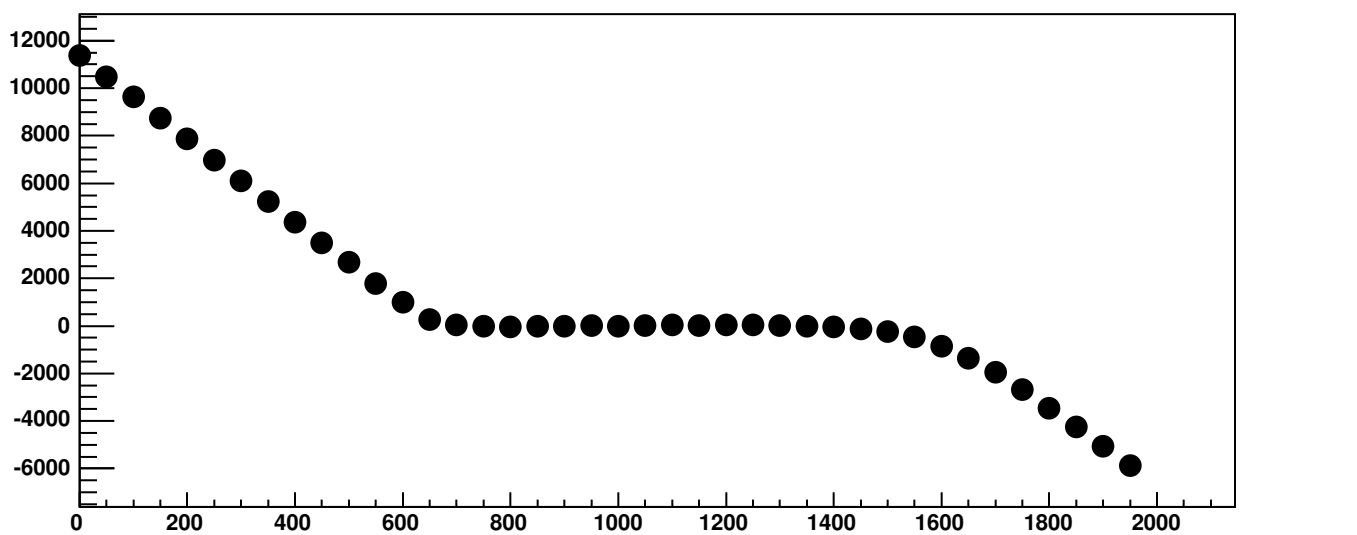
Chip 7, Channel 14, Enable 4!, Hold=30, ADC Mean vs DAC

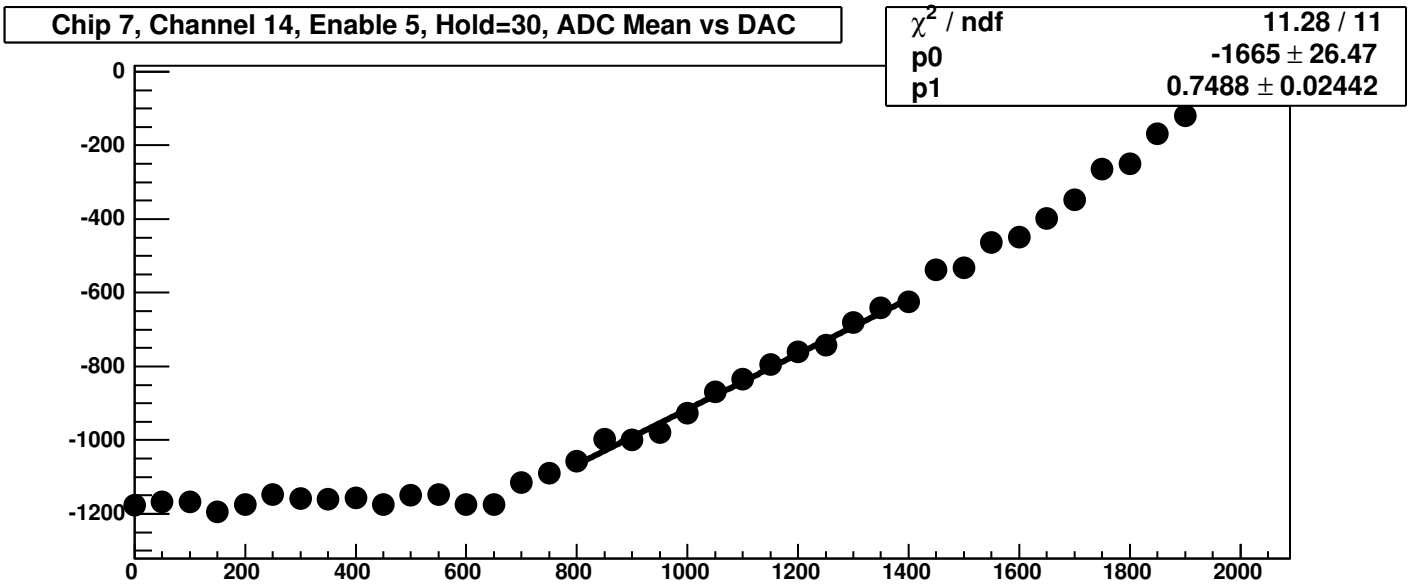


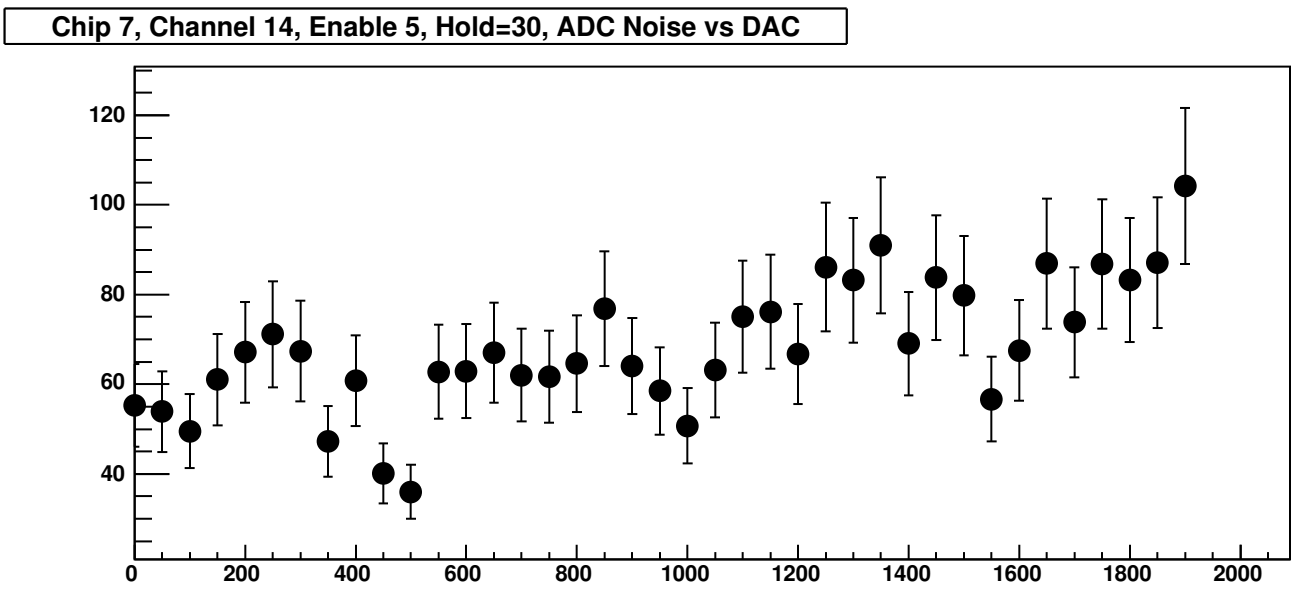
Chip 7, Channel 14, Enable 4!, Hold=30, ADC Noise vs DAC

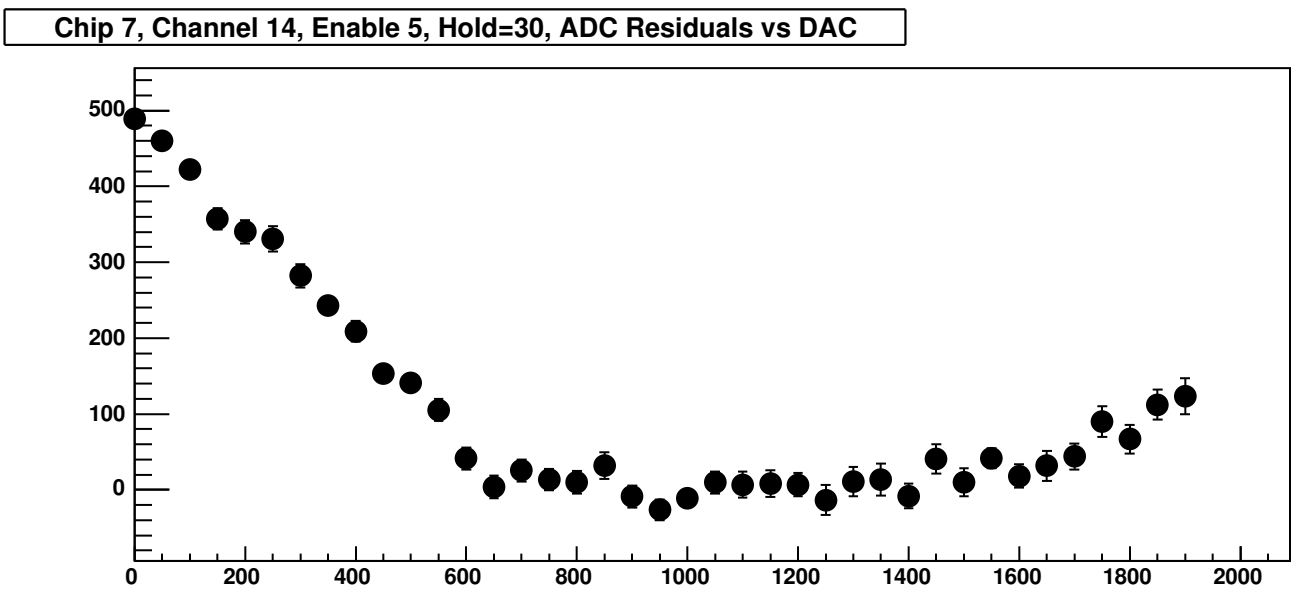


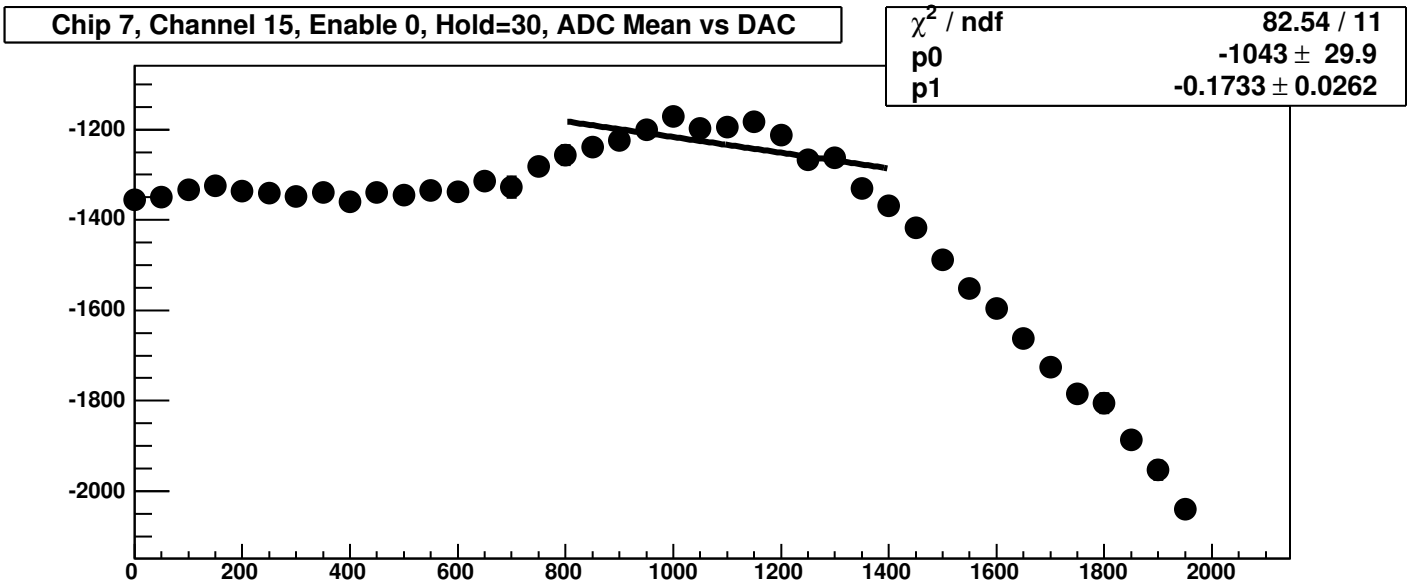
Chip 7, Channel 14, Enable 4!, Hold=30, ADC Residuals vs DAC

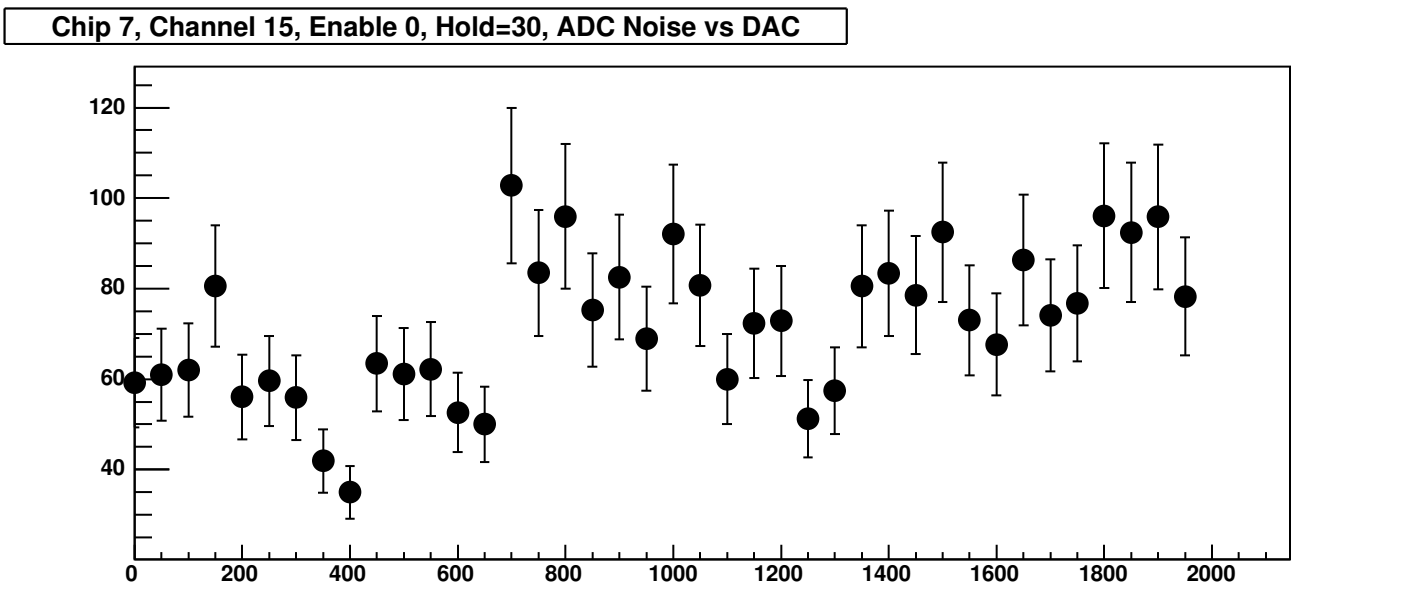


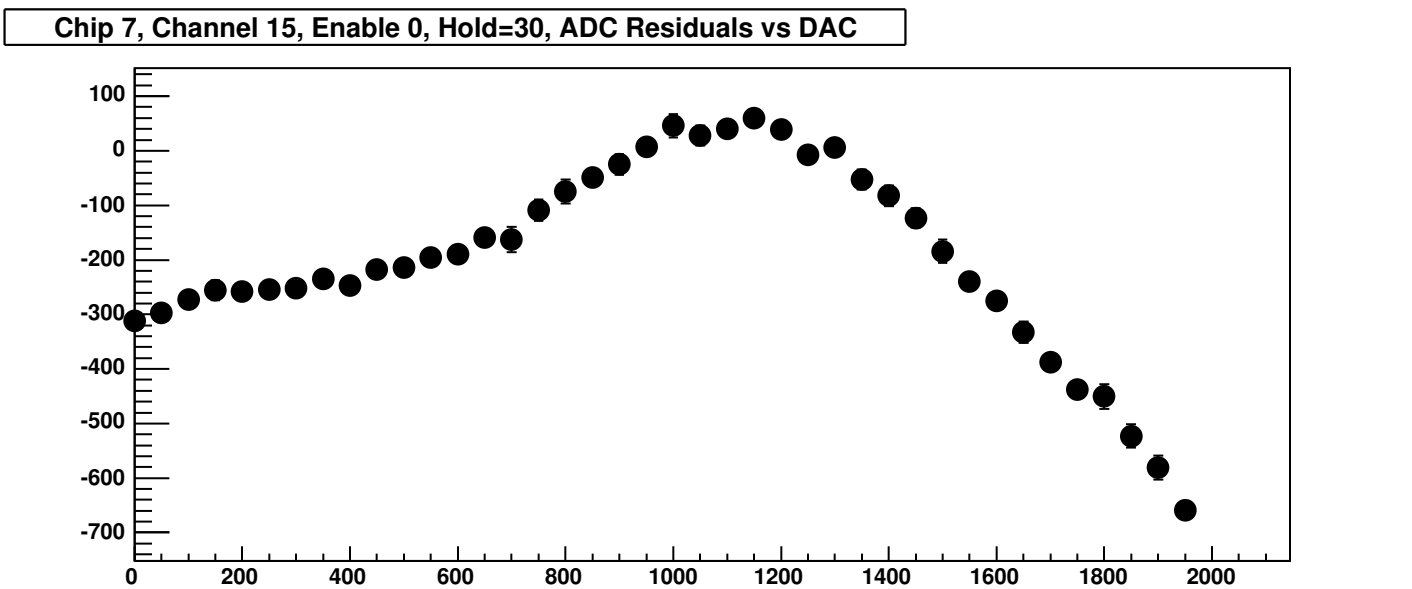

Chip 7, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 11.28 / 11
p0 -1665 ± 26.47
p1 0.7488 ± 0.02442
0
-200
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

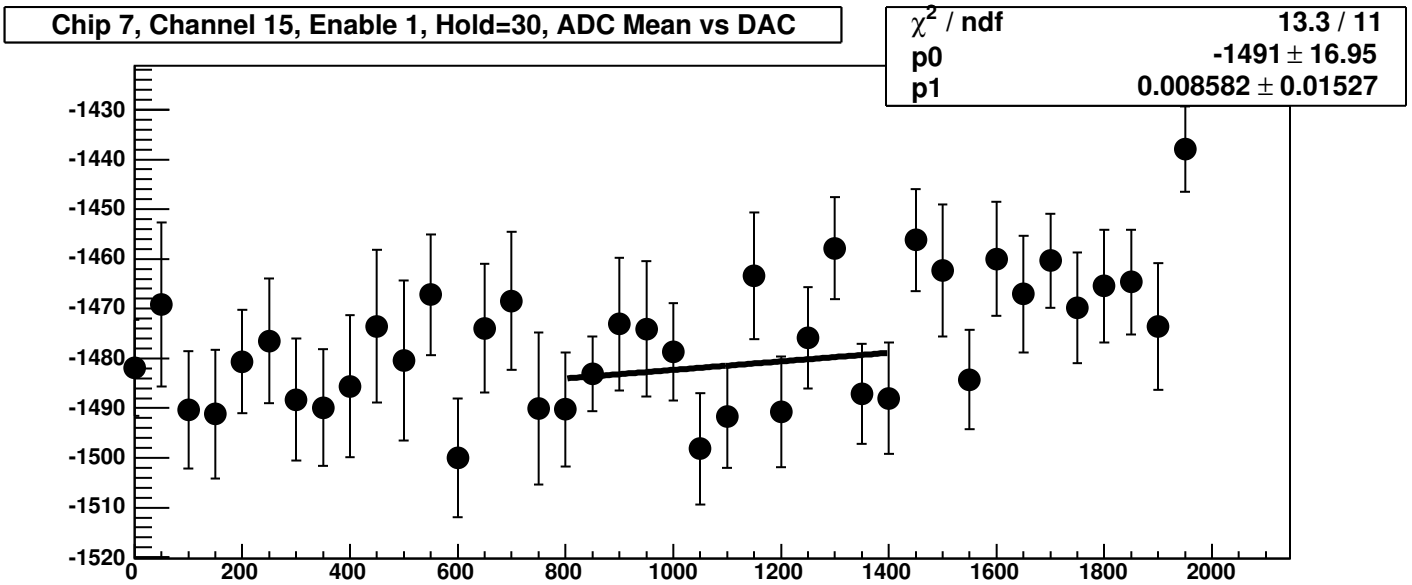

Chip 7, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

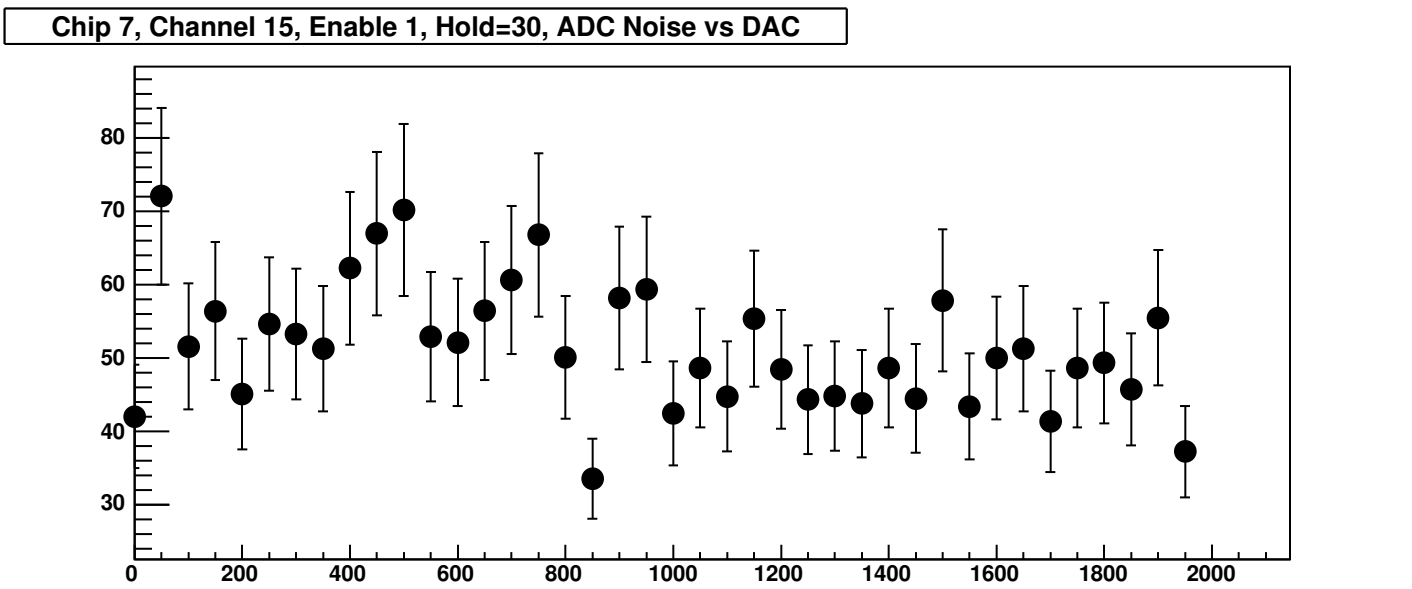

Chip 7, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
500
400
300
200
100
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

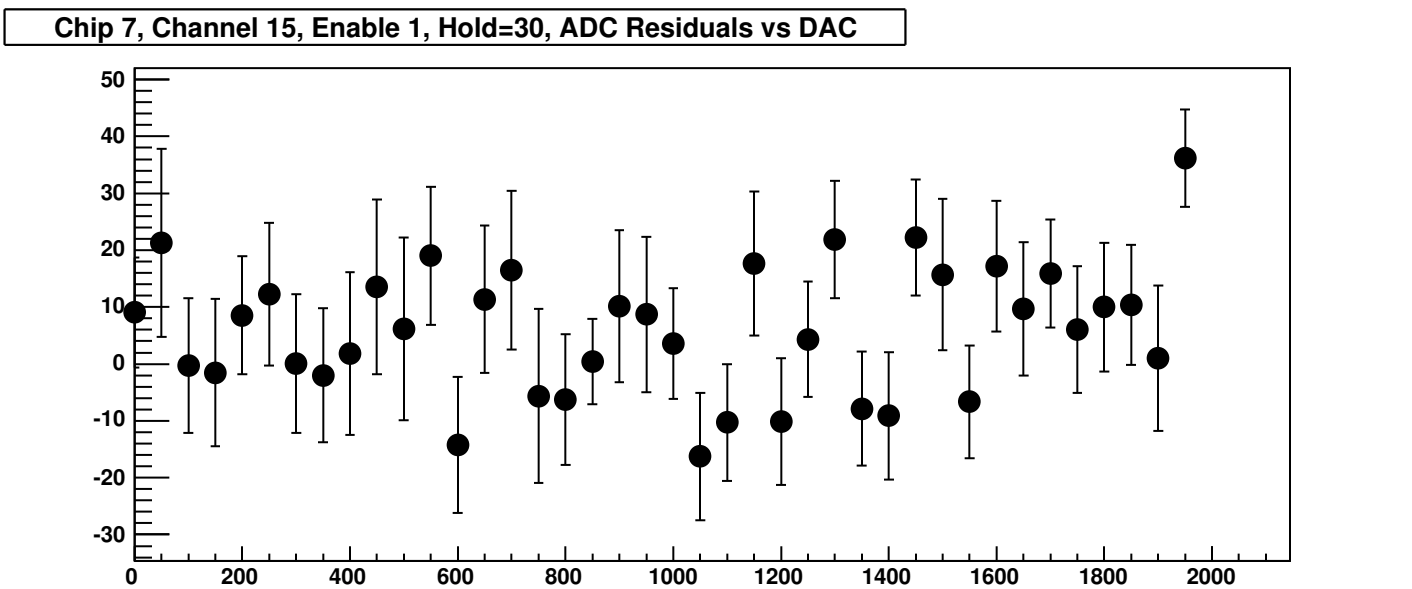

Chip 7, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 82.54 / 11
p0 -1043 ± 29.9
p1 -0.1733 ± 0.0262
-1200
-1400
-1600
-1800
-2000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

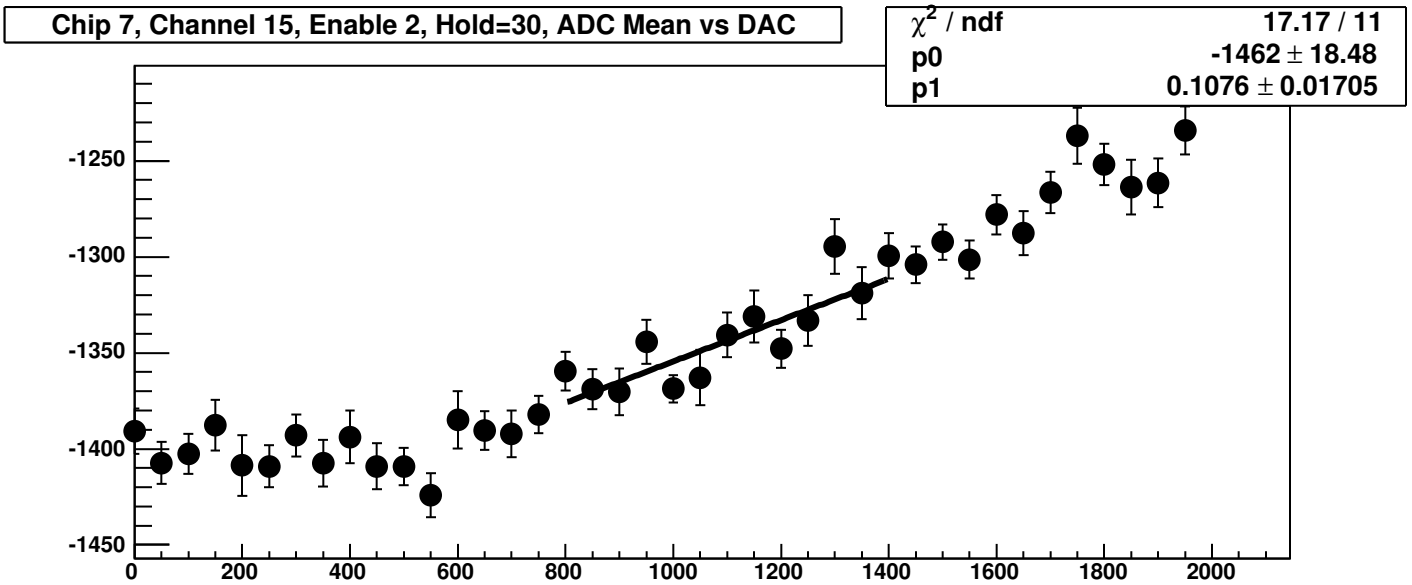

Chip 7, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

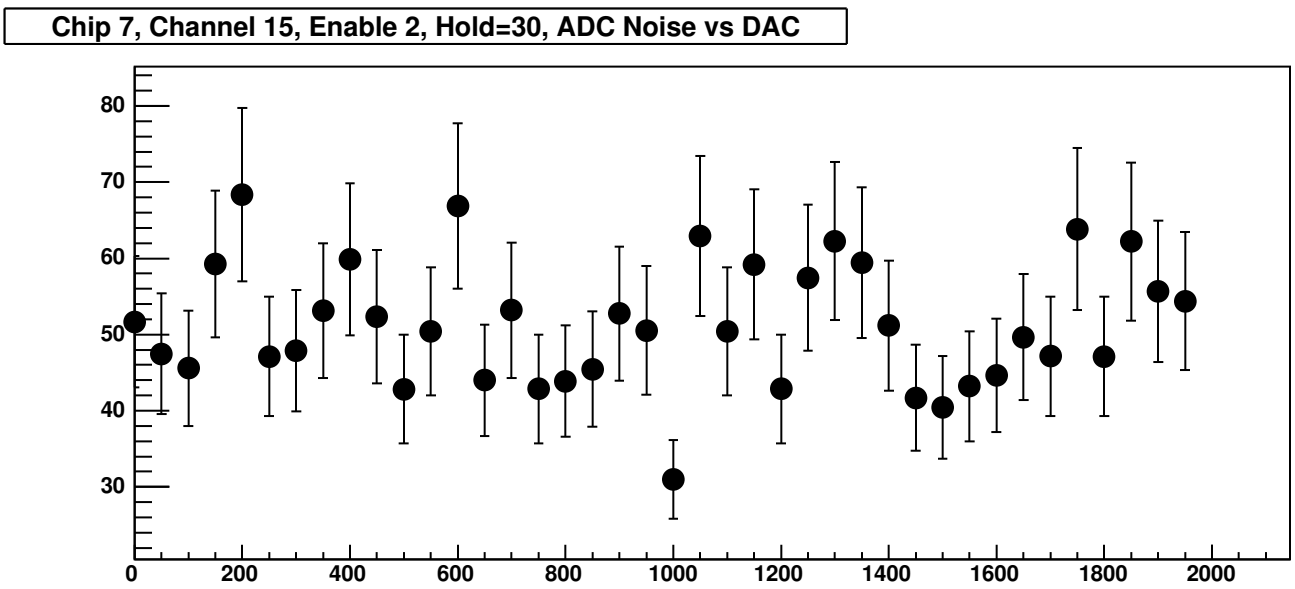

Chip 7, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
100
0
-100
-200
-300
-400
-500
-600
-700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

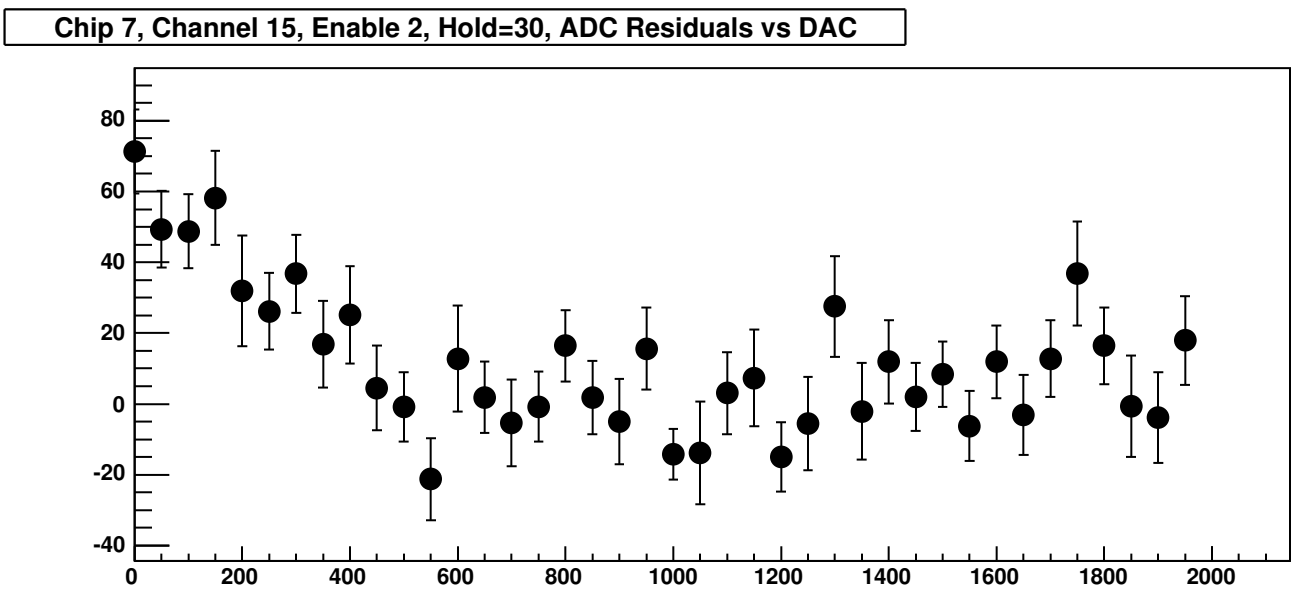

Chip 7, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 13.3 / 11
p0 -1491 ± 16.95
p1 0.008582 ± 0.01527
-1430
-1440
-1450
-1460
-1470
-1480
-1490
-1500
-1510
-1520
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC
50
40
30
20
10
0
-10
-20
-30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 15, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 17.17 / 11
p0 -1462 ± 18.48
p1 0.1076 ± 0.01705
-1250
-1300
-1350
-1400
-1450
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 15, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 15, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
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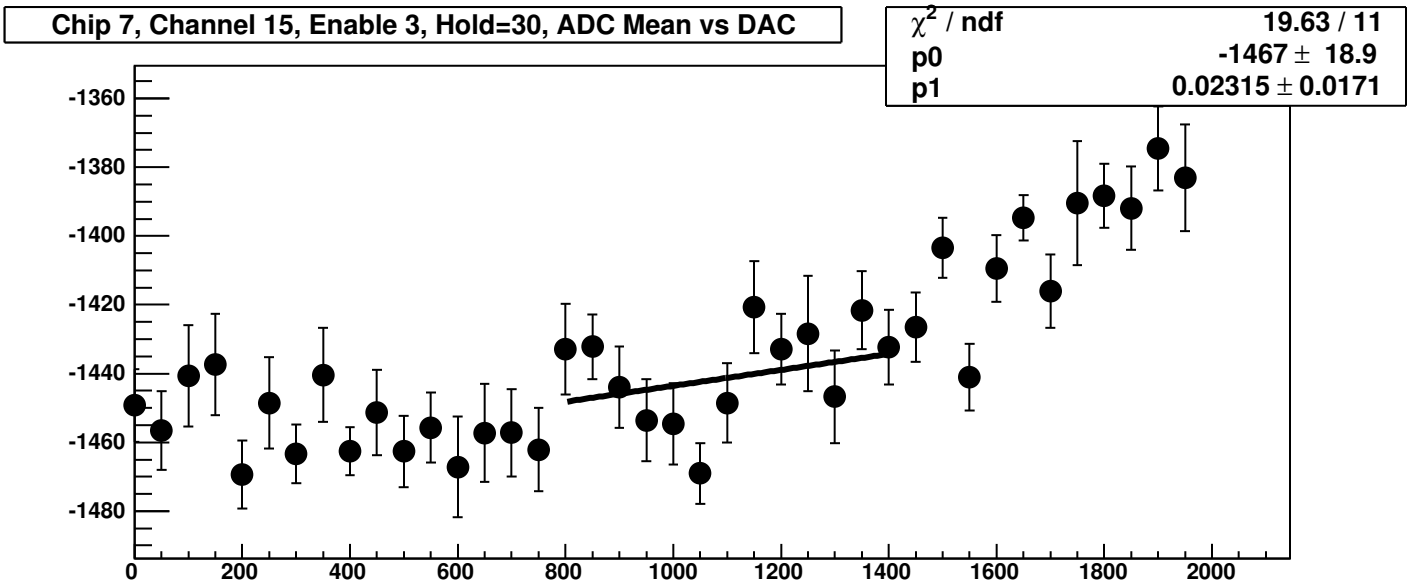
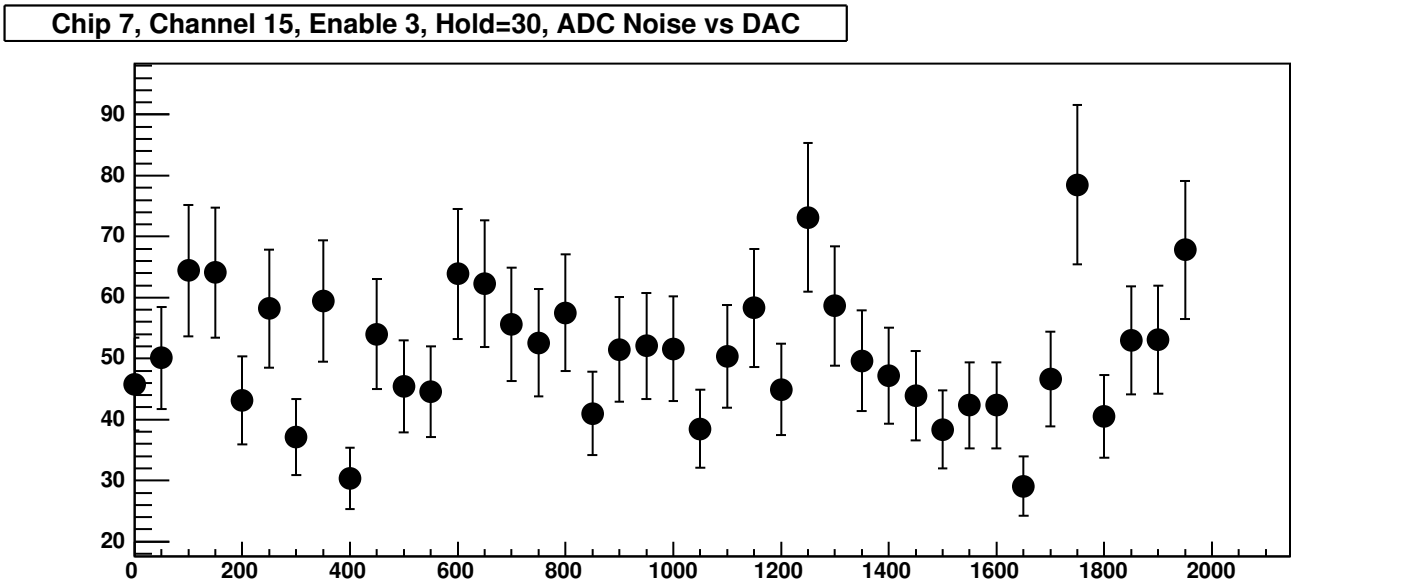
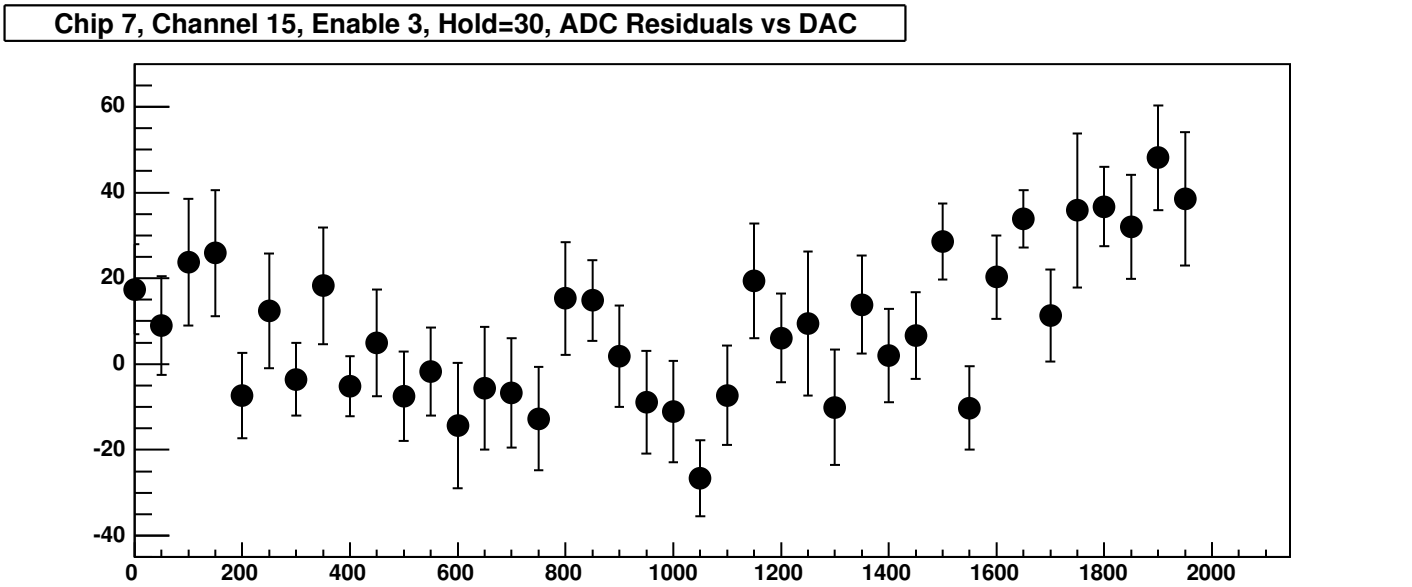
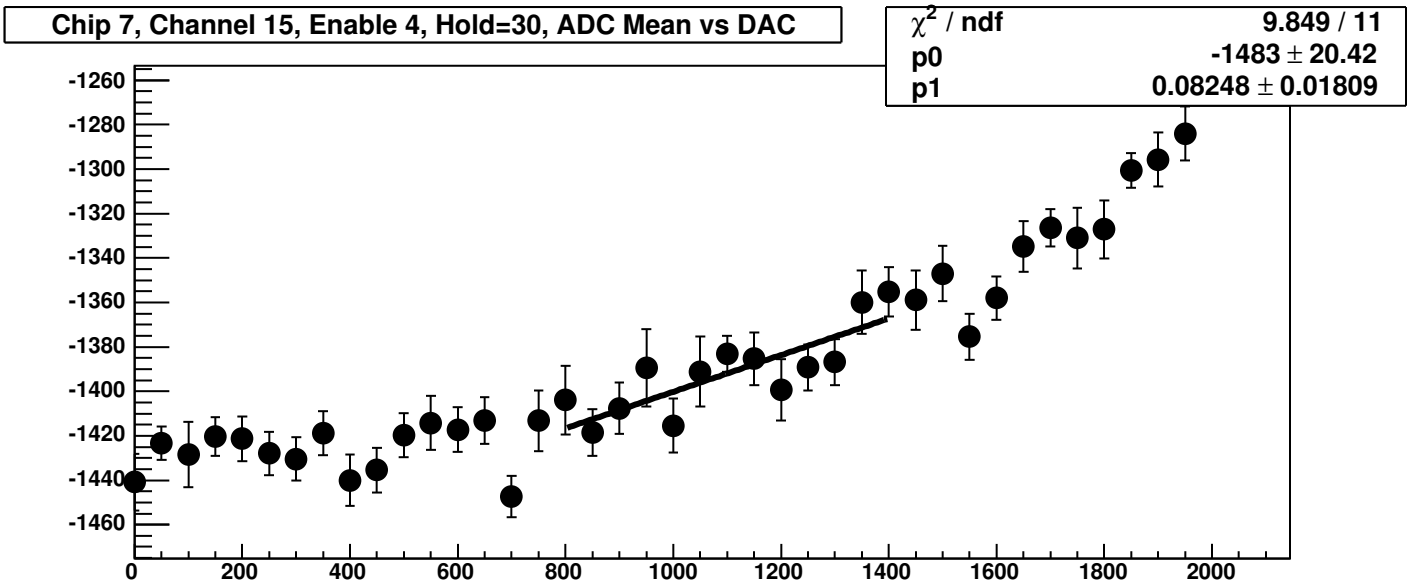
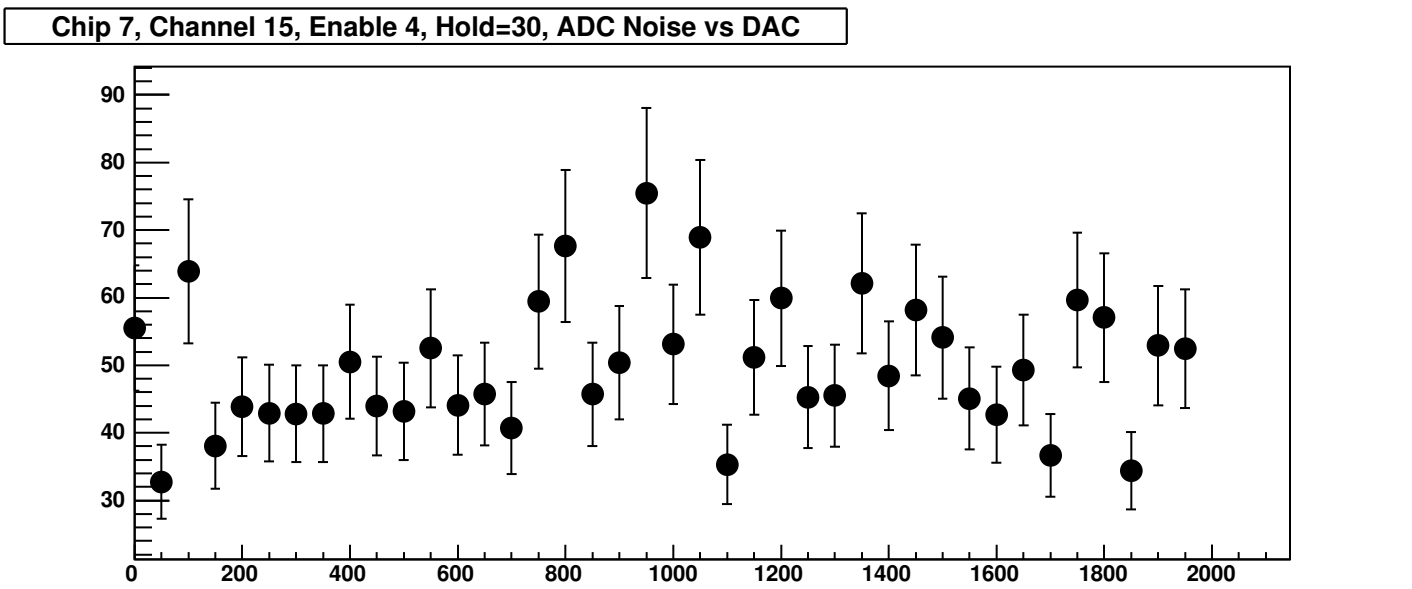
1000
1200
1400
1600
1800
2000

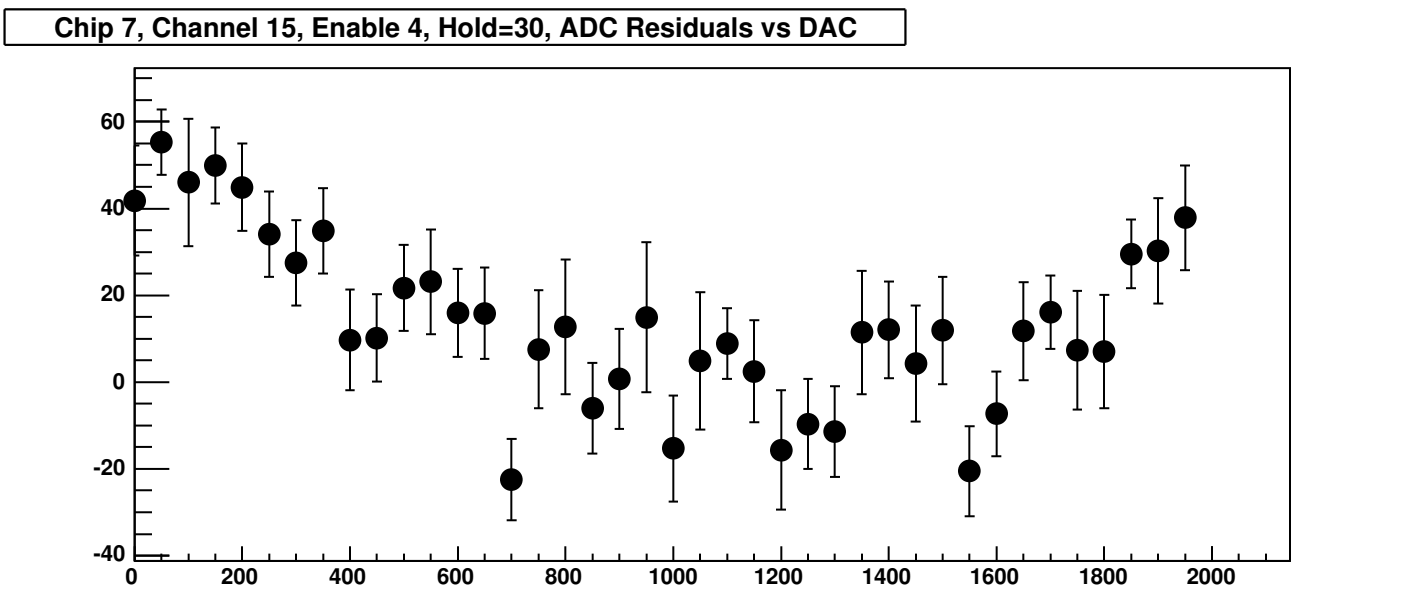

Chip 7, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 19.63 / 11
p0 -1467 ± 18.9
p1 0.02315 ± 0.0171
-1360
-1380
-1400
-1420
-1440
-1460
-1480
0
200
400
600
800
1000
1200
1400
1600
1800
2000

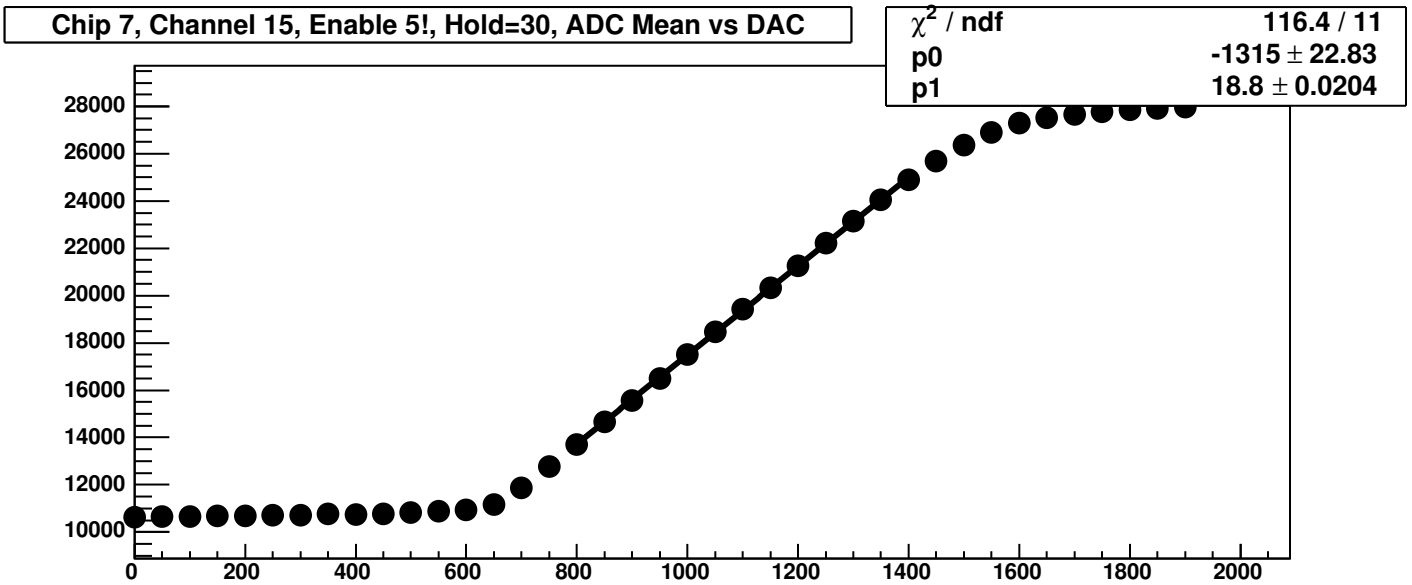

Chip 7, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

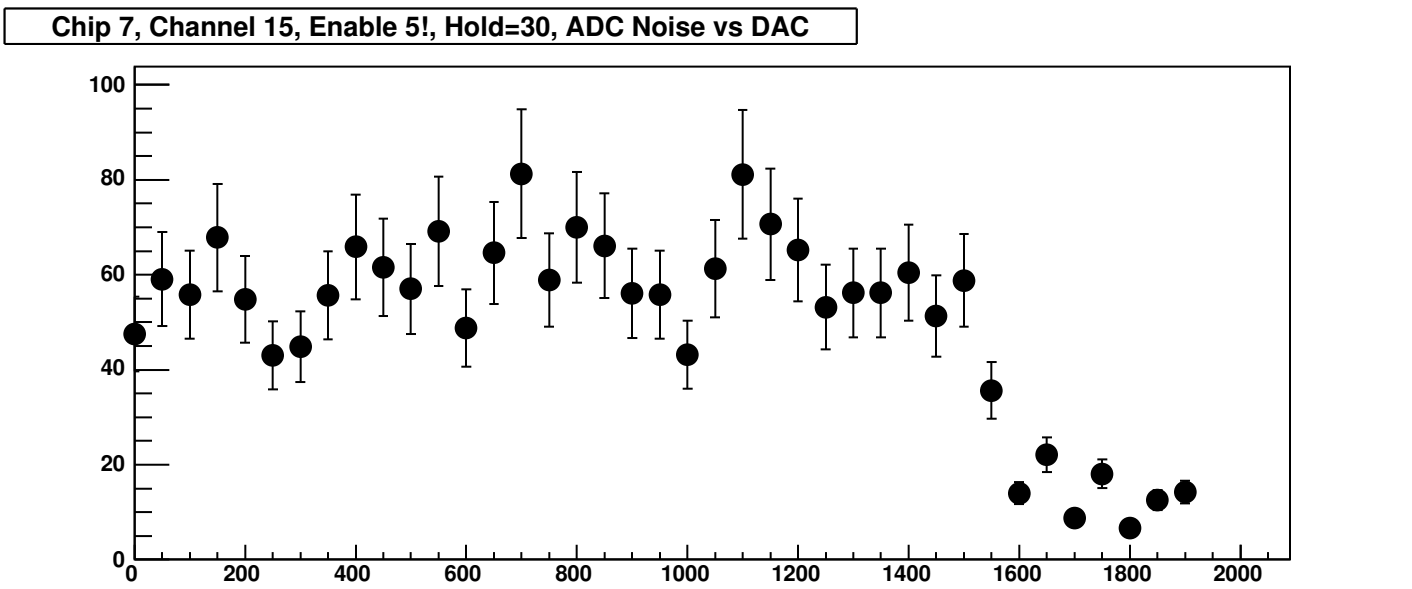

Chip 7, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

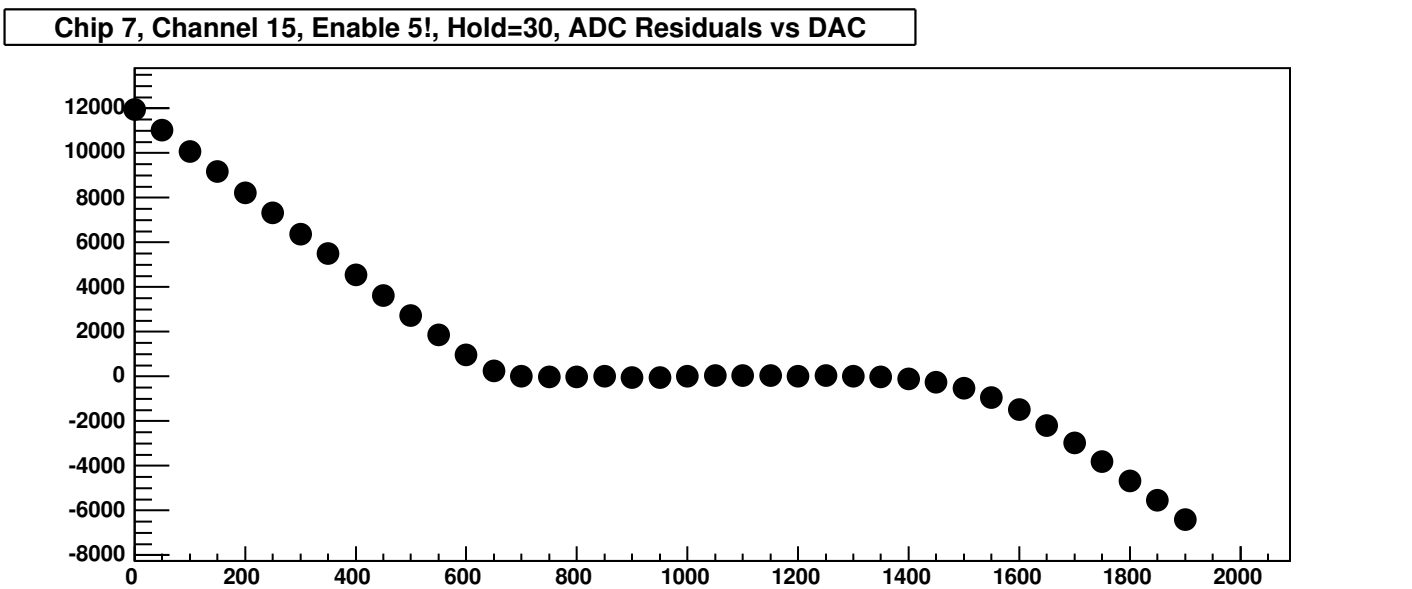

Chip 7, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 9.849 / 11
p0 -1483 ± 20.42
p1 0.08248 ± 0.01809
-1260
-1280
-1300
-1320
-1340
-1360
-1380
-1400
-1420
-1440
-1460
0
200
400
600
800
1000
1200
1400
1600
1800
2000

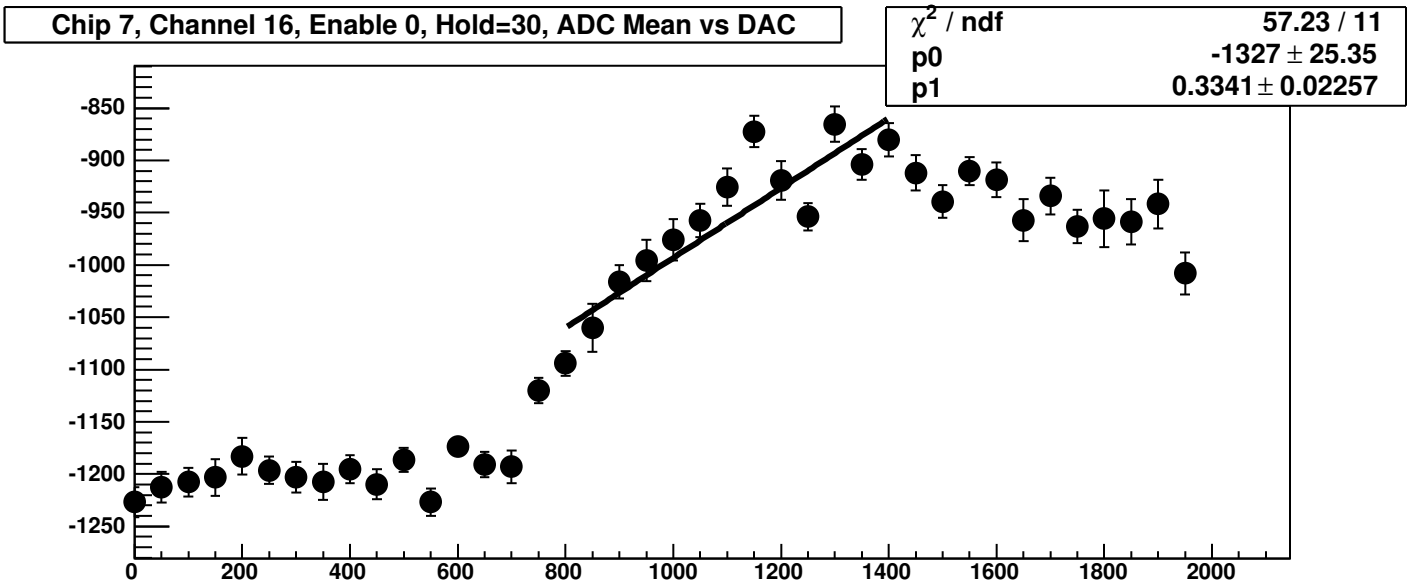

Chip 7, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

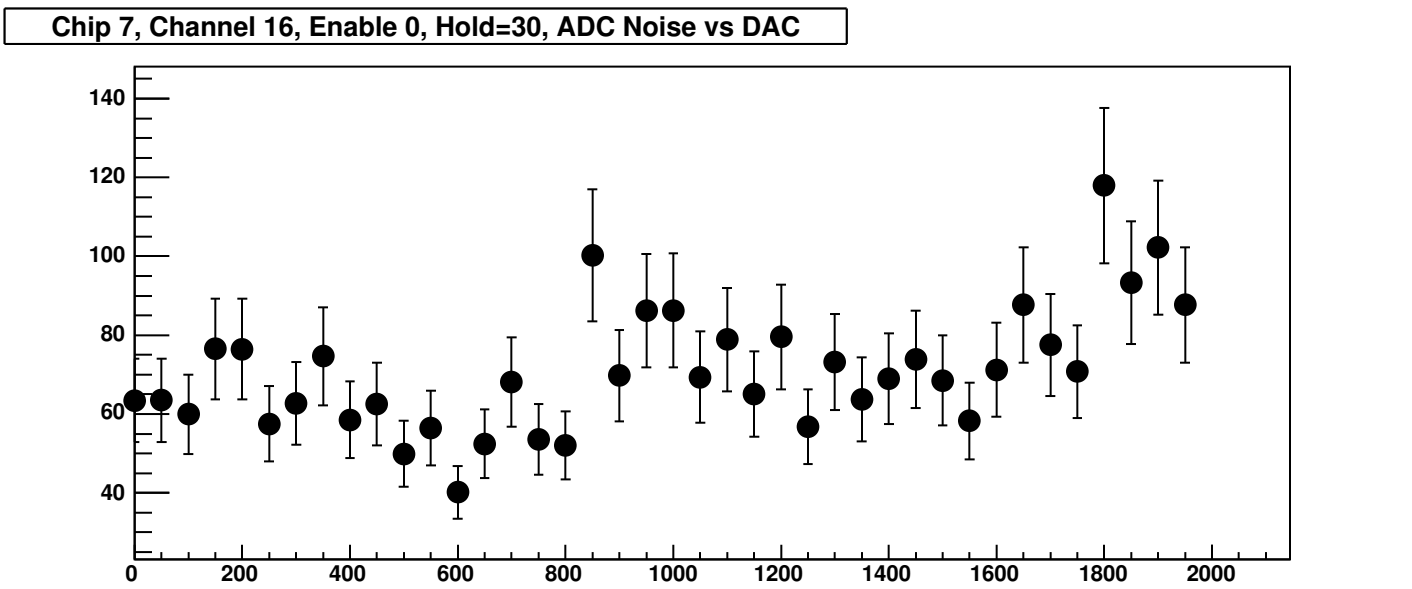

Chip 7, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

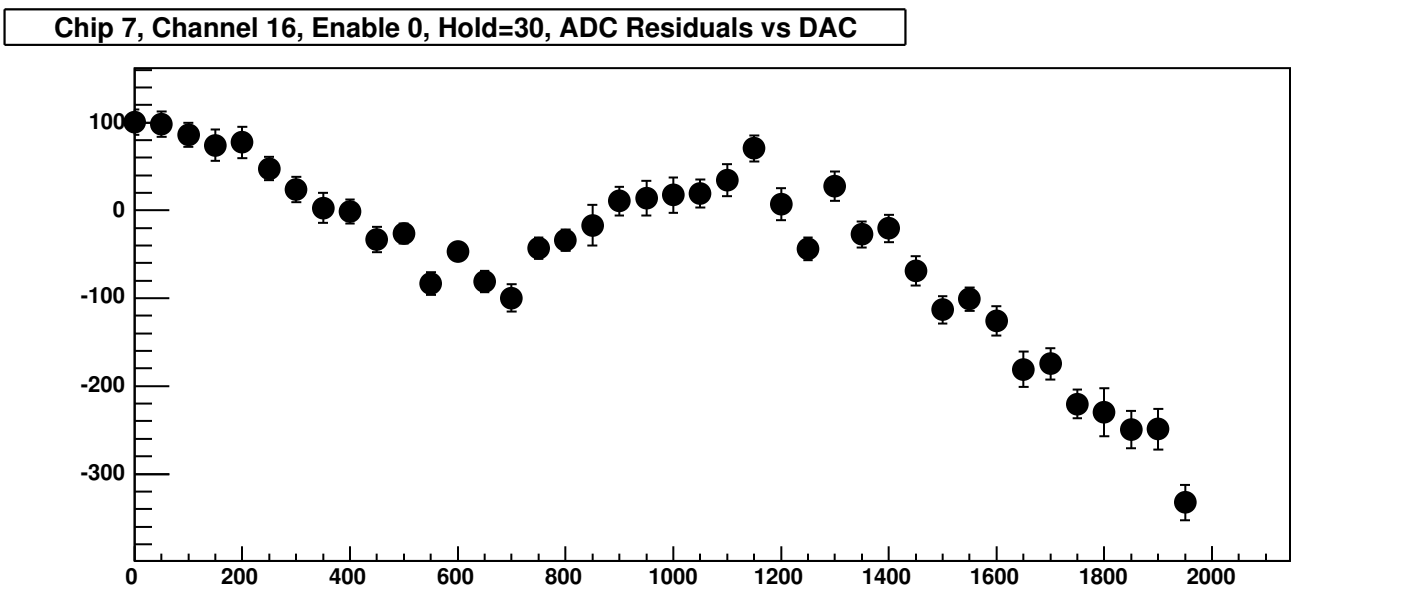

Chip 7, Channel 15, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 116.4 / 11
p0 -1315 ± 22.83
p1 18.8 ± 0.0204
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

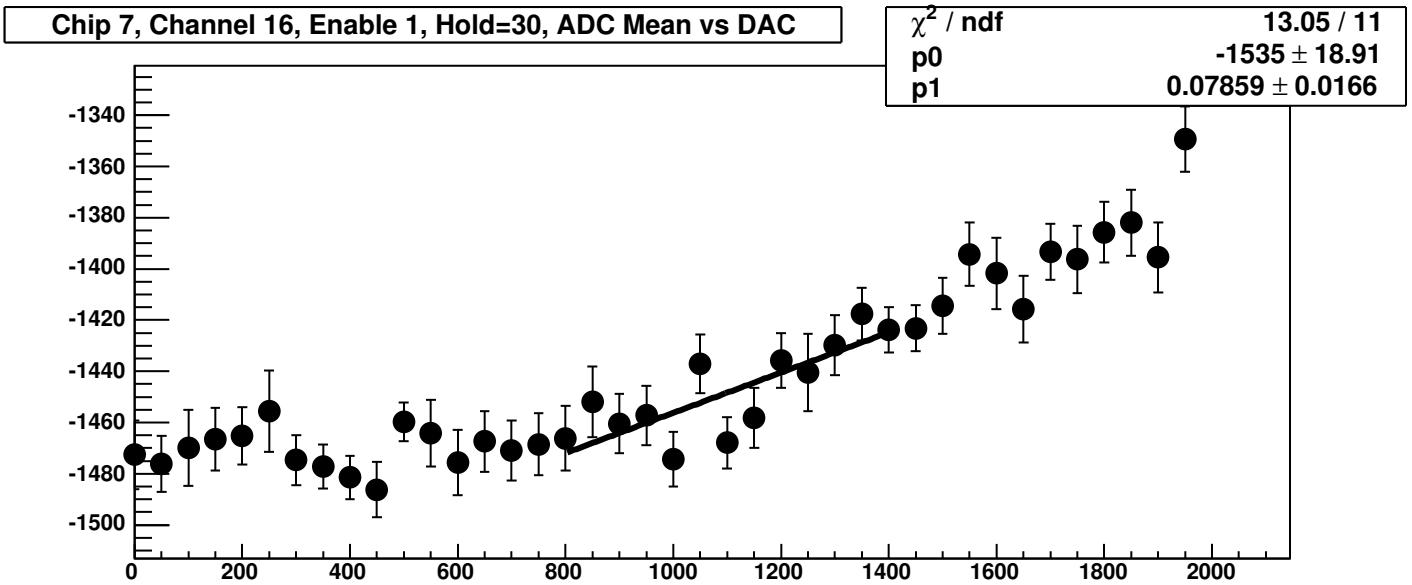

Chip 7, Channel 15, Enable 5!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

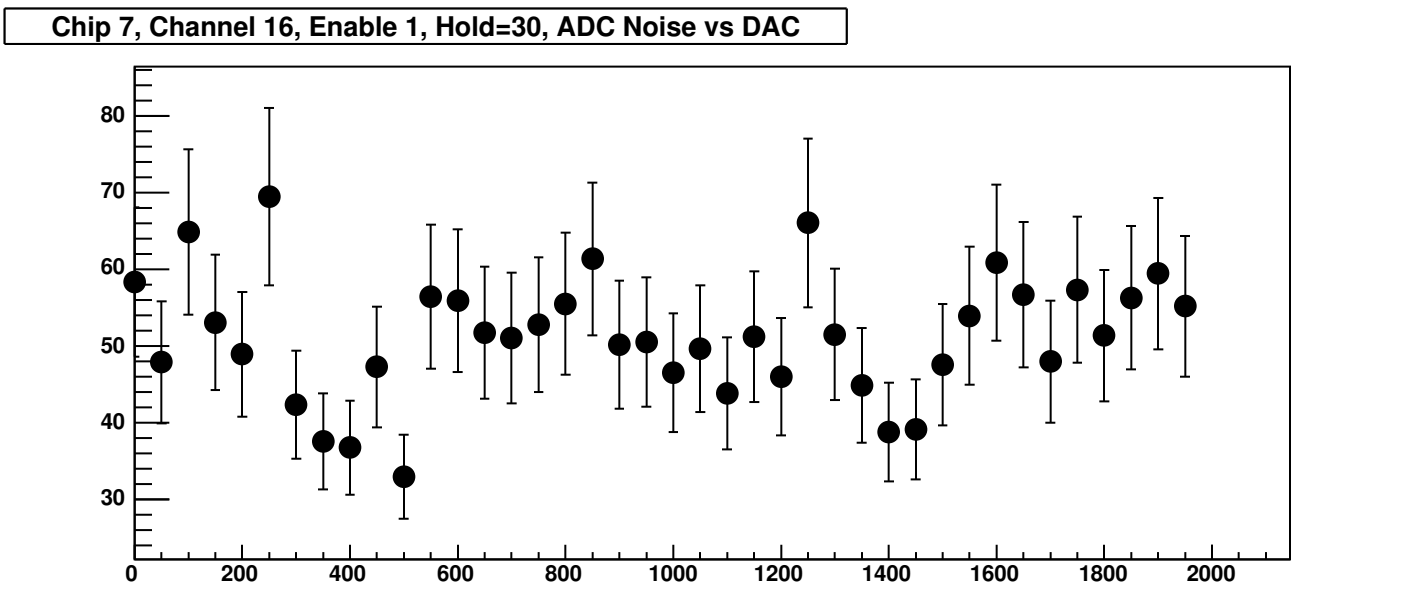

Chip 7, Channel 15, Enable 5!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

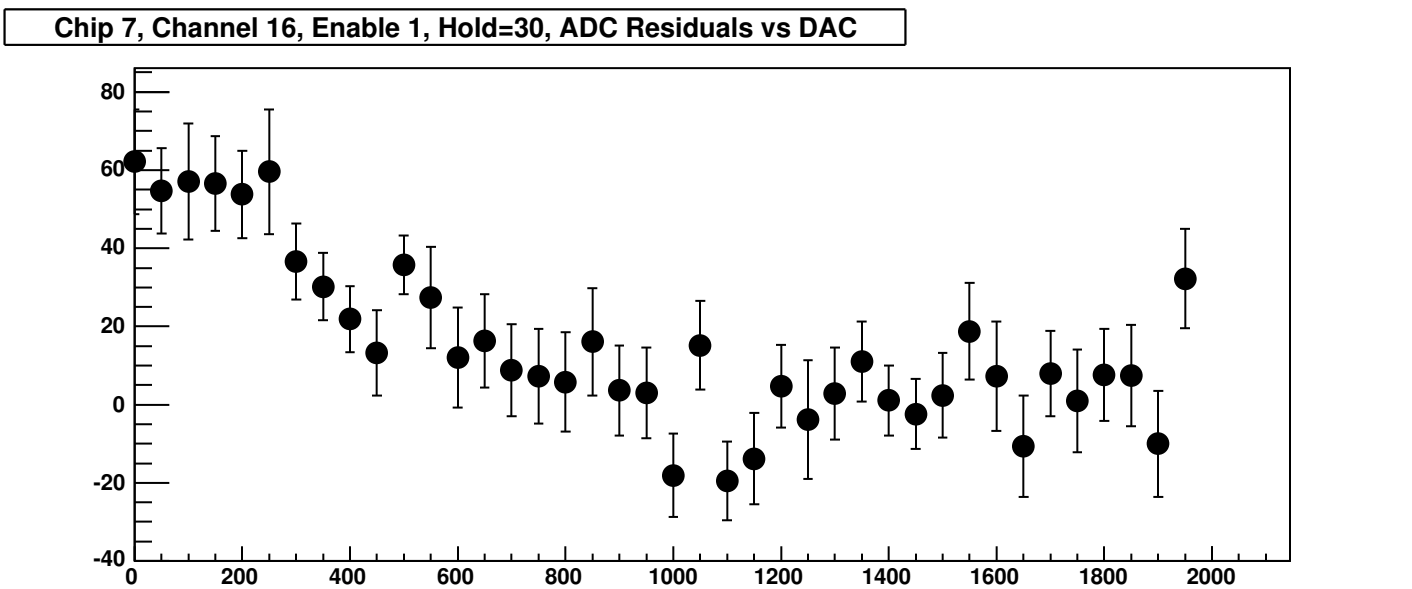

Chip 7, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 57.23 / 11
p0 -1327 ± 25.35
p1 0.3341 ± 0.02257
-850
-900
-950
-1000
-1050
-1100
-1150
-1200
-1250
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

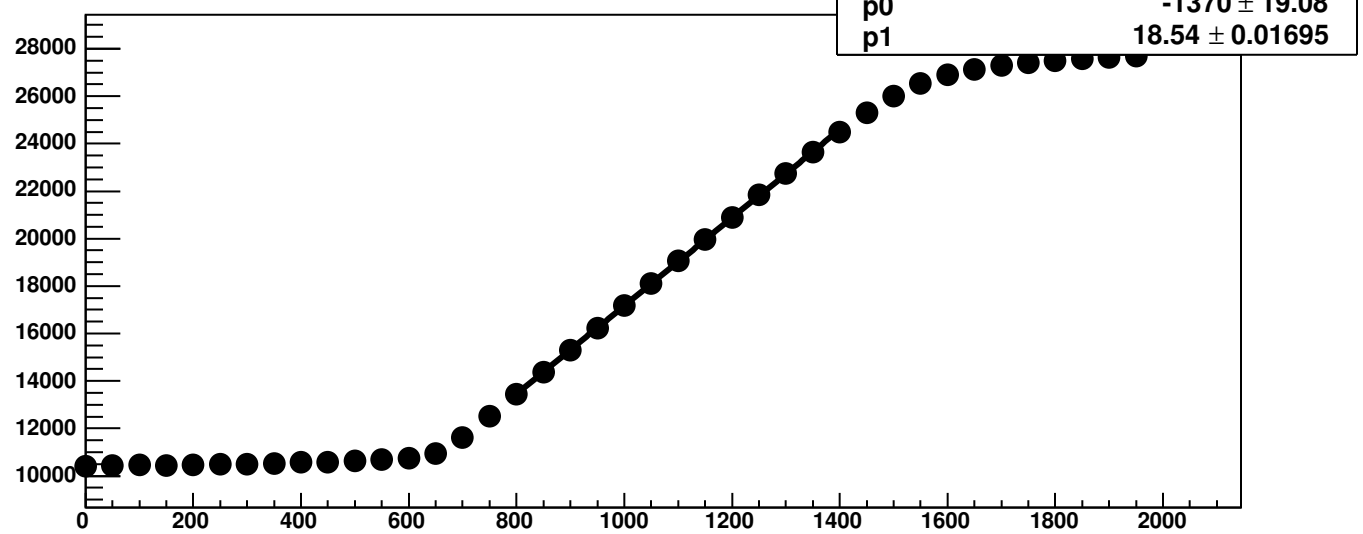

Chip 7, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
100
0
-100
-200
-300
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 13.05 / 11
p0 -1535 ± 18.91
p1 0.07859 ± 0.0166
-1340
-1360
-1380
-1400
-1420
-1440
-1460
-1480
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

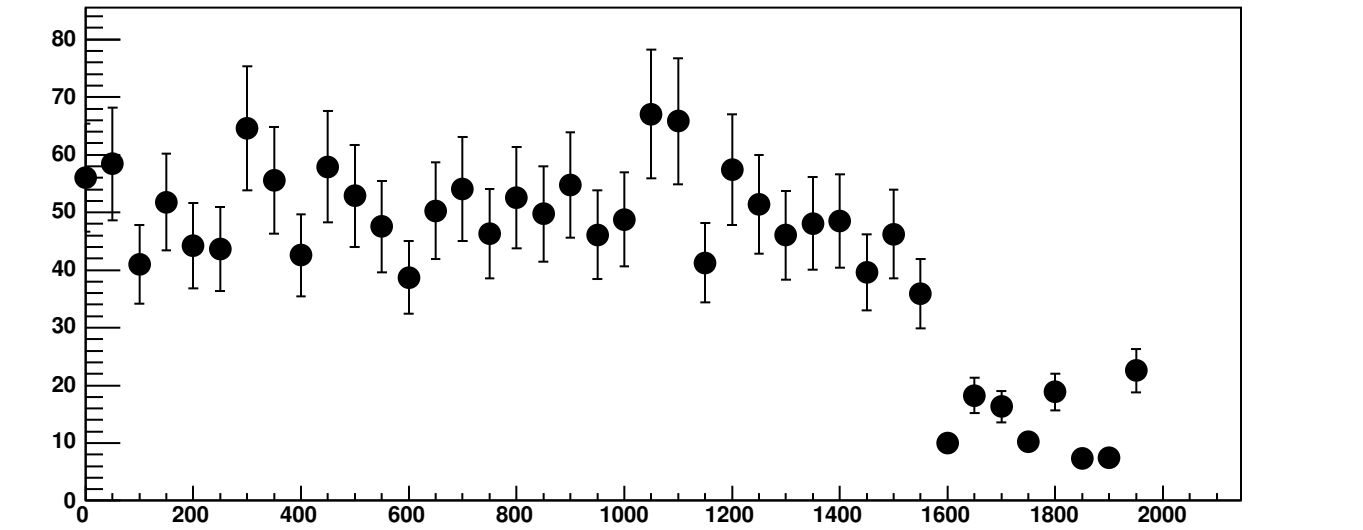

Chip 7, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 7, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

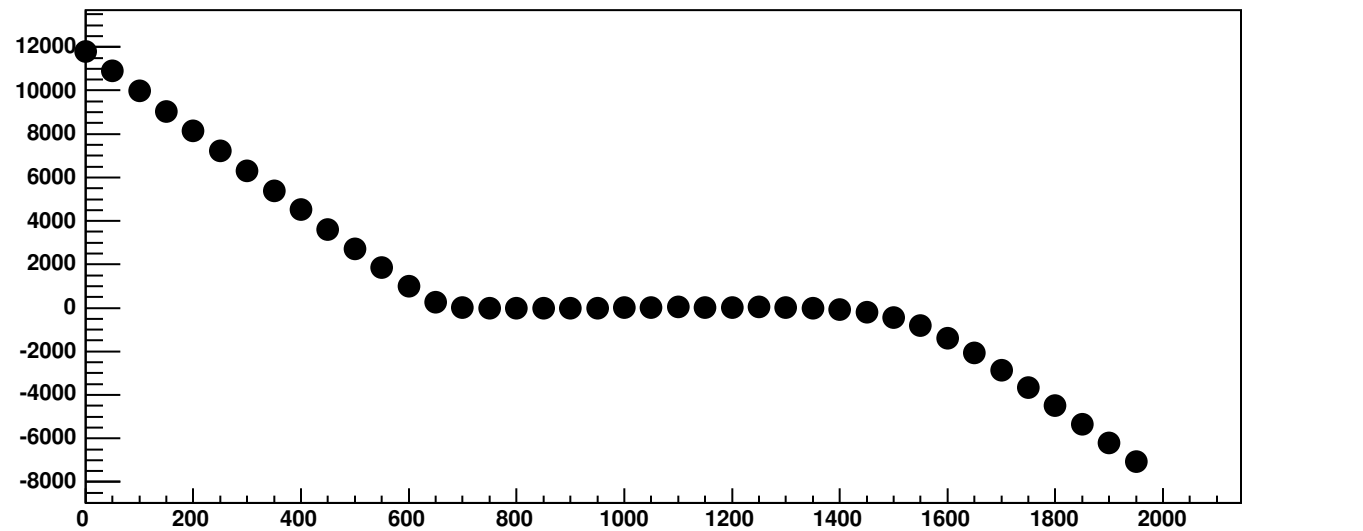
Chip 7, Channel 16, Enable 2!, Hold=30, ADC Mean vs DAC

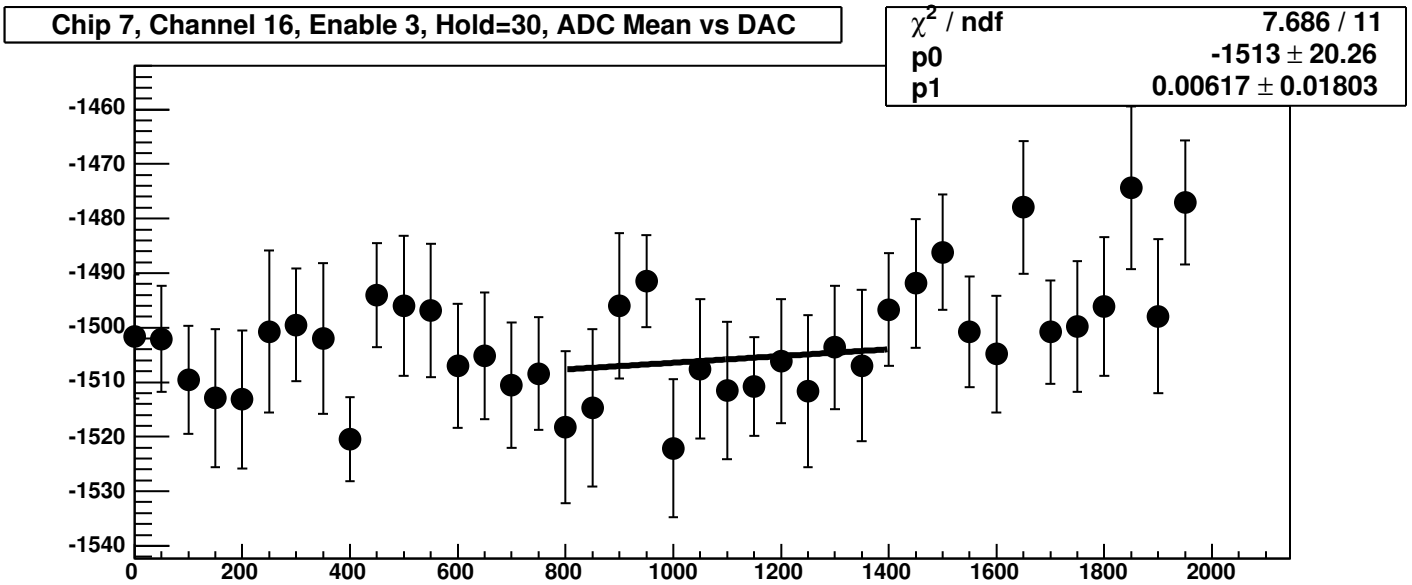


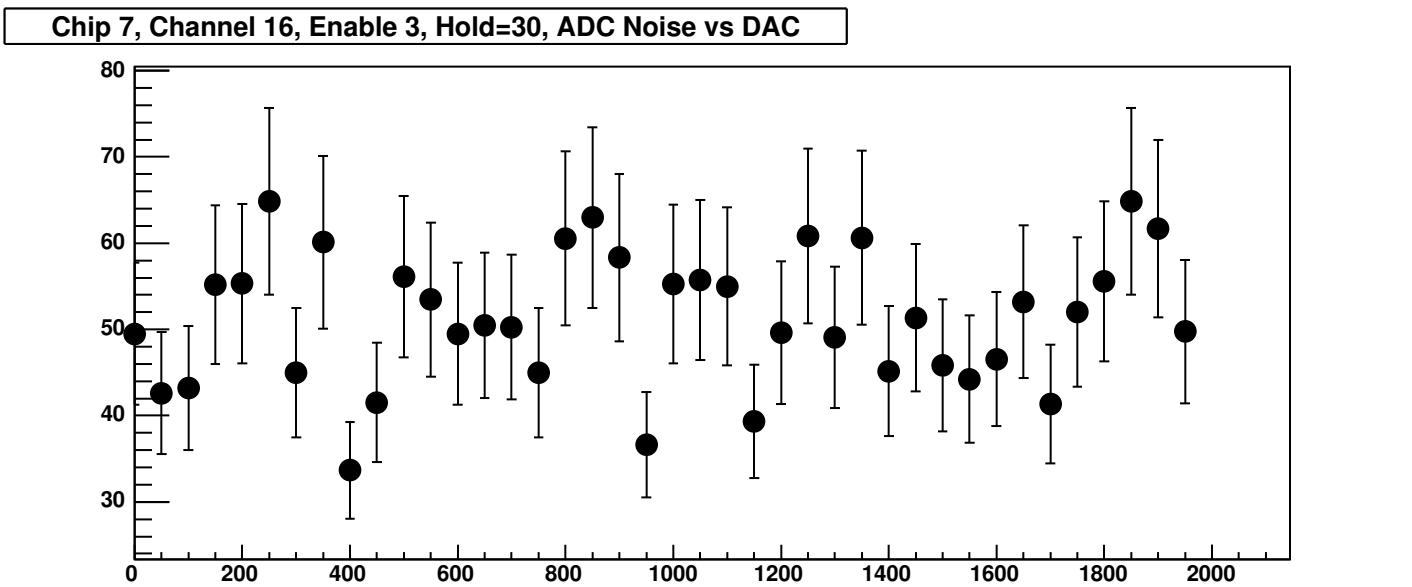
Chip 7, Channel 16, Enable 2!, Hold=30, ADC Noise vs DAC

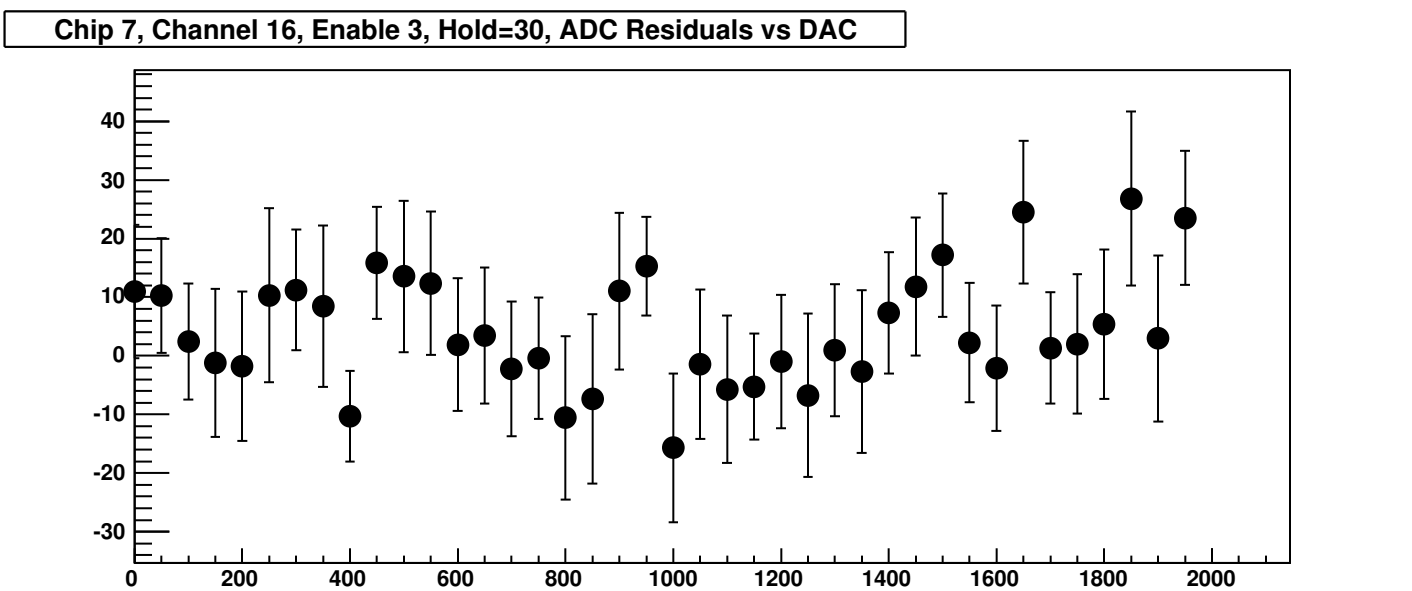


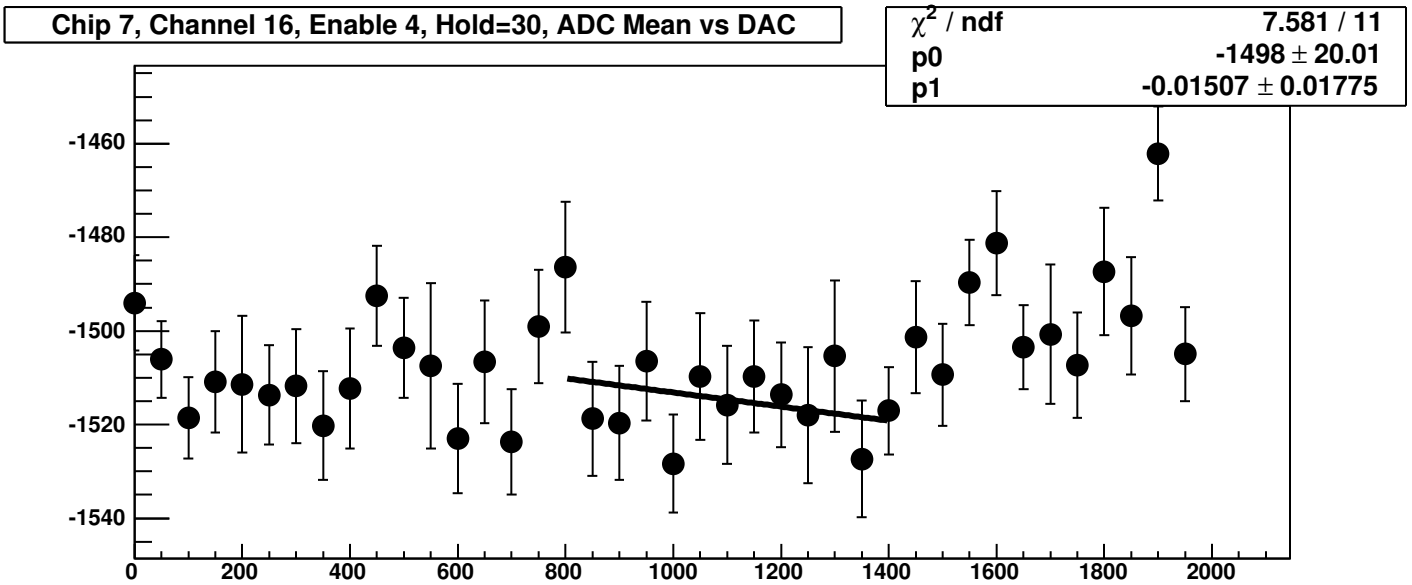
Chip 7, Channel 16, Enable 2!, Hold=30, ADC Residuals vs DAC

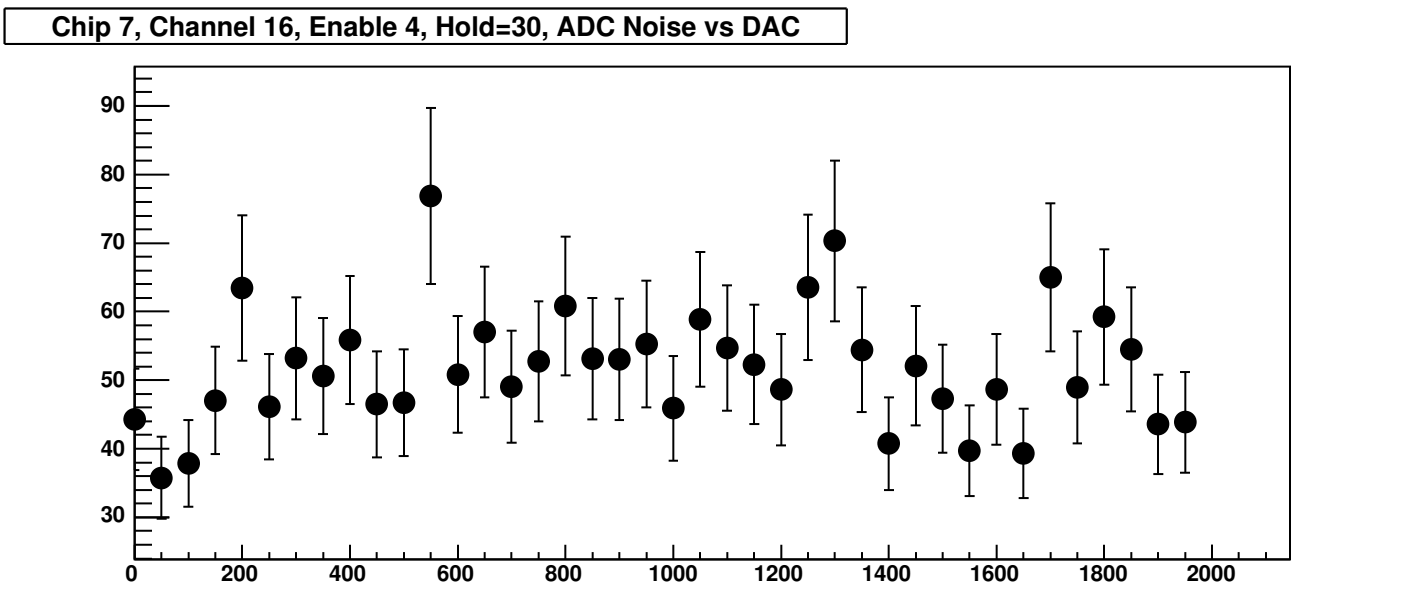


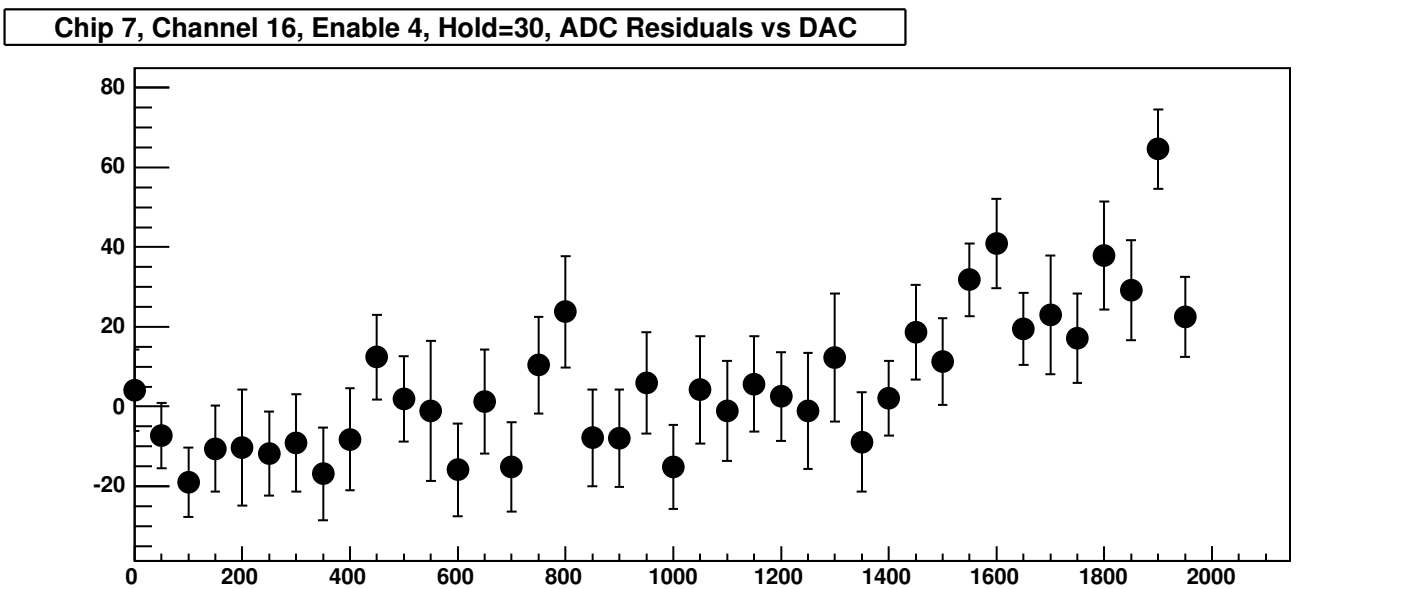

Chip 7, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7.686 / 11
p0 -1513 ± 20.26
p1 0.00617 ± 0.01803
-1460
-1470
-1480
-1490
-1500
-1510
-1520
-1530
-1540
0
200
400
600
800
1000
1200
1400
1600
1800
2000

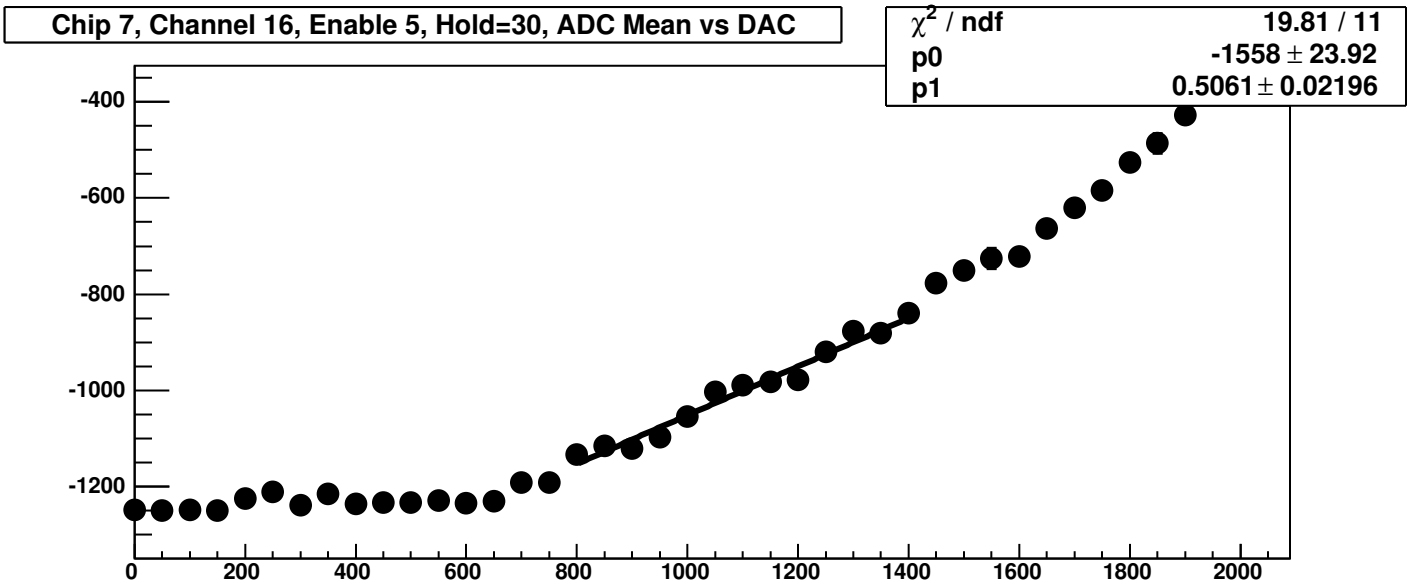

Chip 7, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

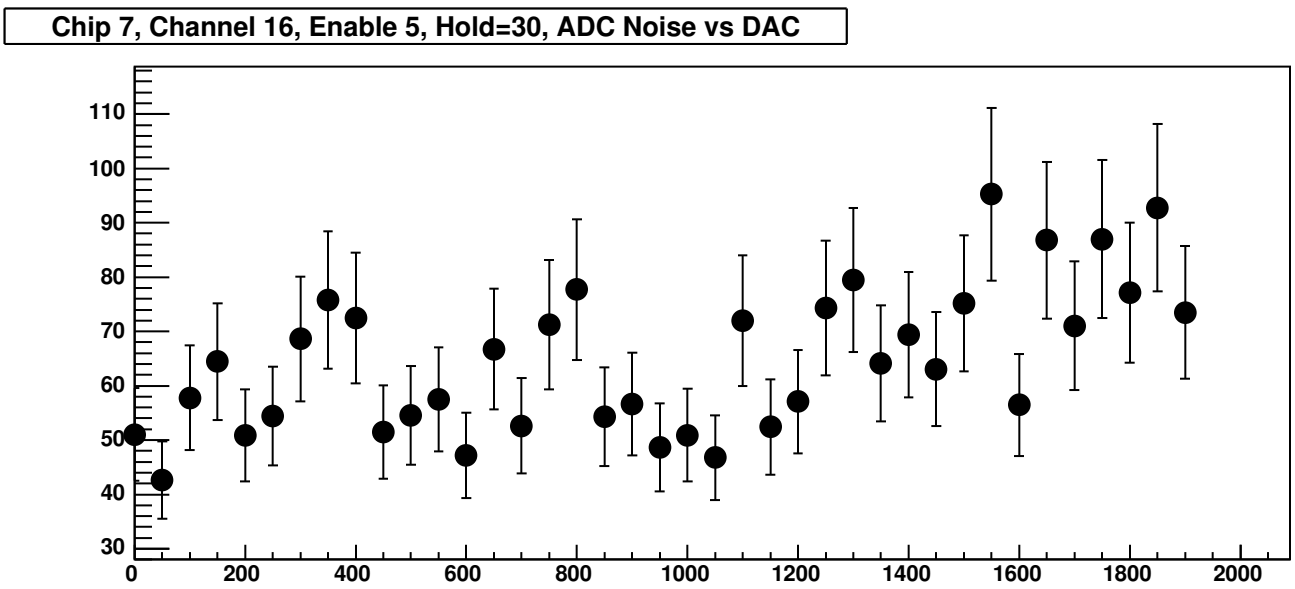

Chip 7, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC
40
30
20
10
0
-10
-20
-30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

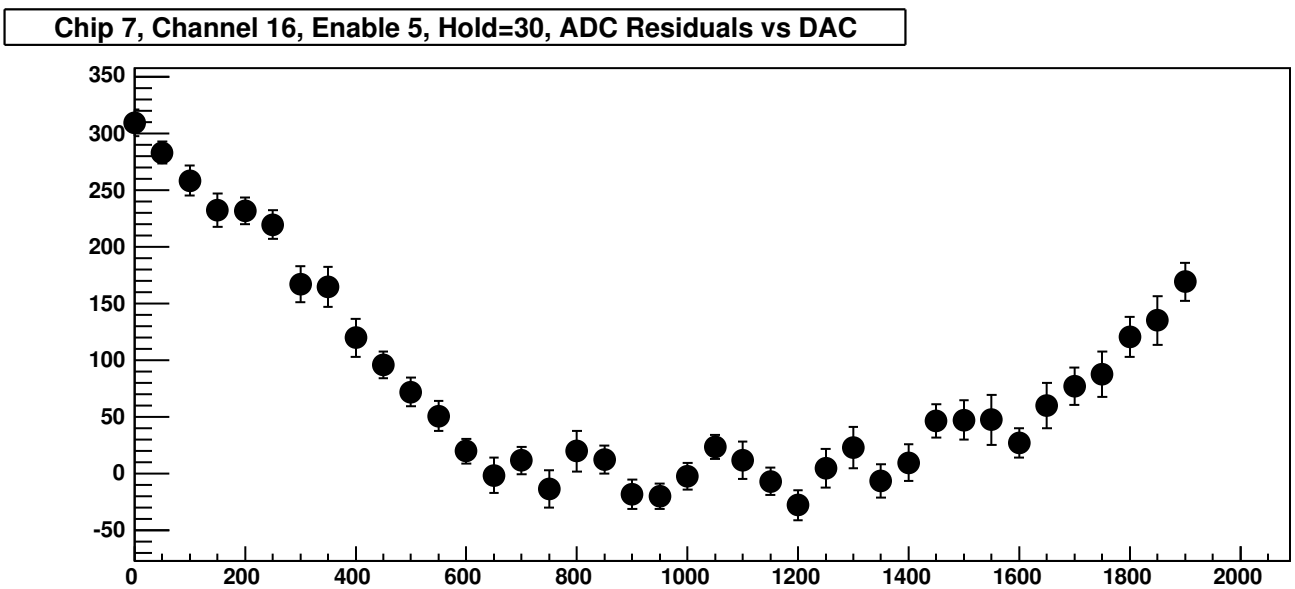

Chip 7, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7.581 / 11
p0 -1498 ± 20.01
p1 -0.01507 ± 0.01775
-1460
-1480
-1500
-1520
-1540
0
200
400
600
800
1000
1200
1400
1600
1800
2000

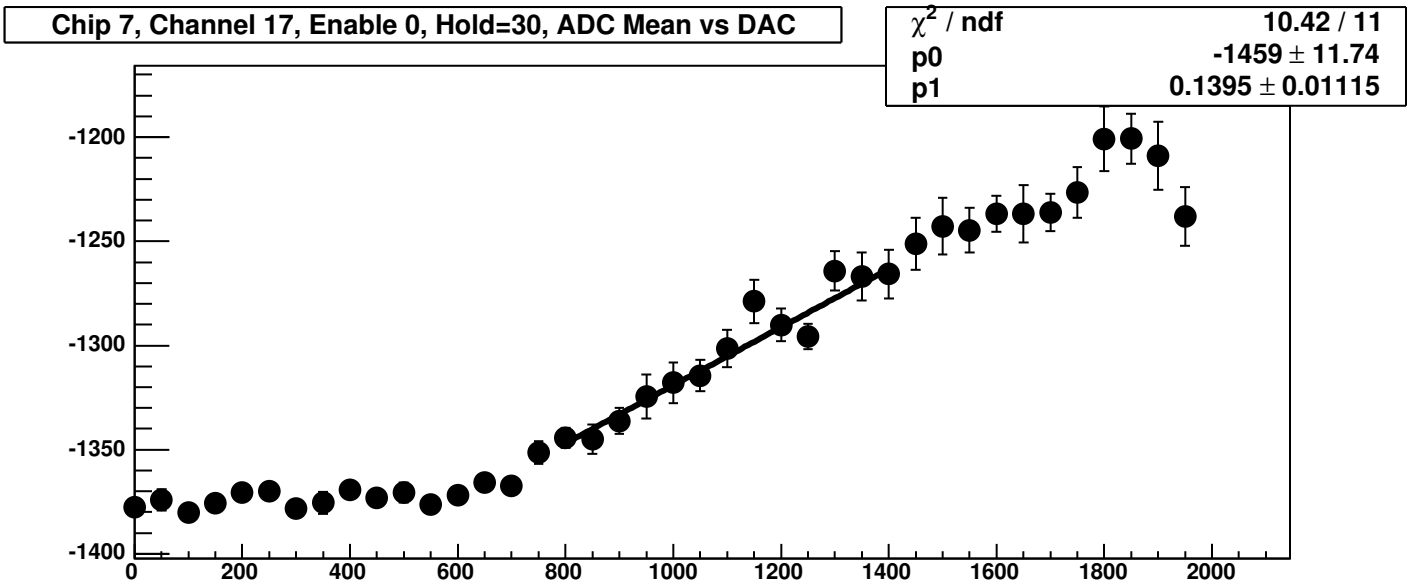

Chip 7, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

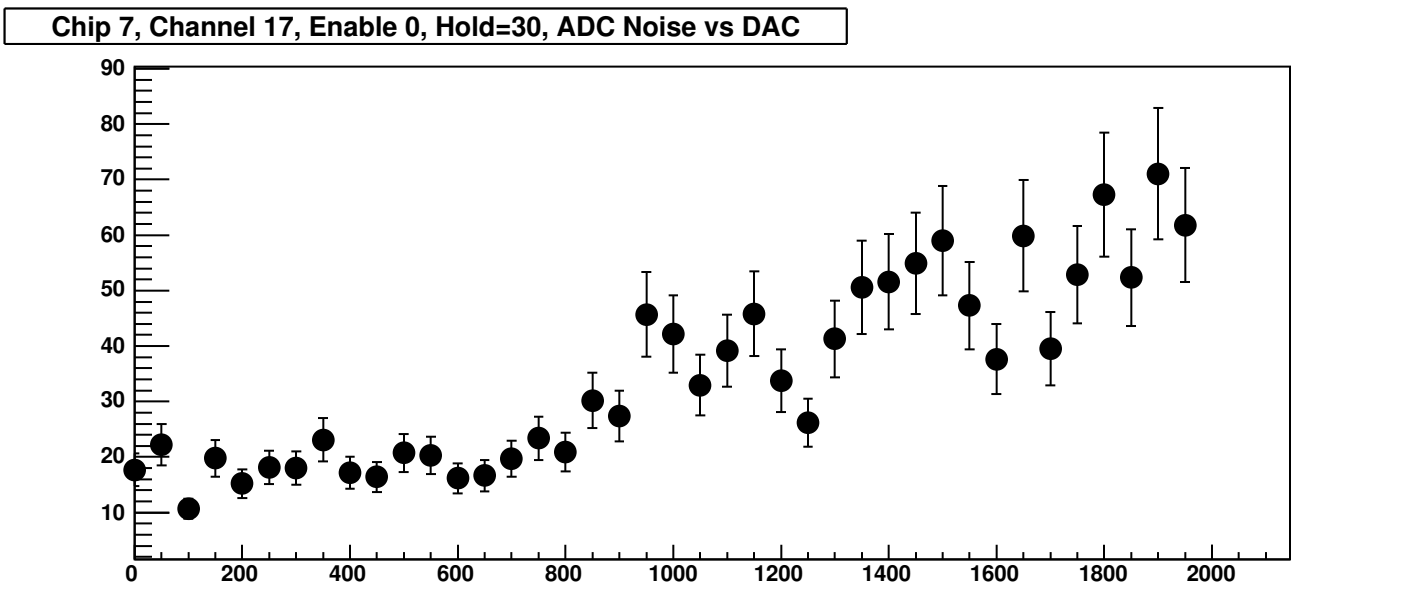

Chip 7, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

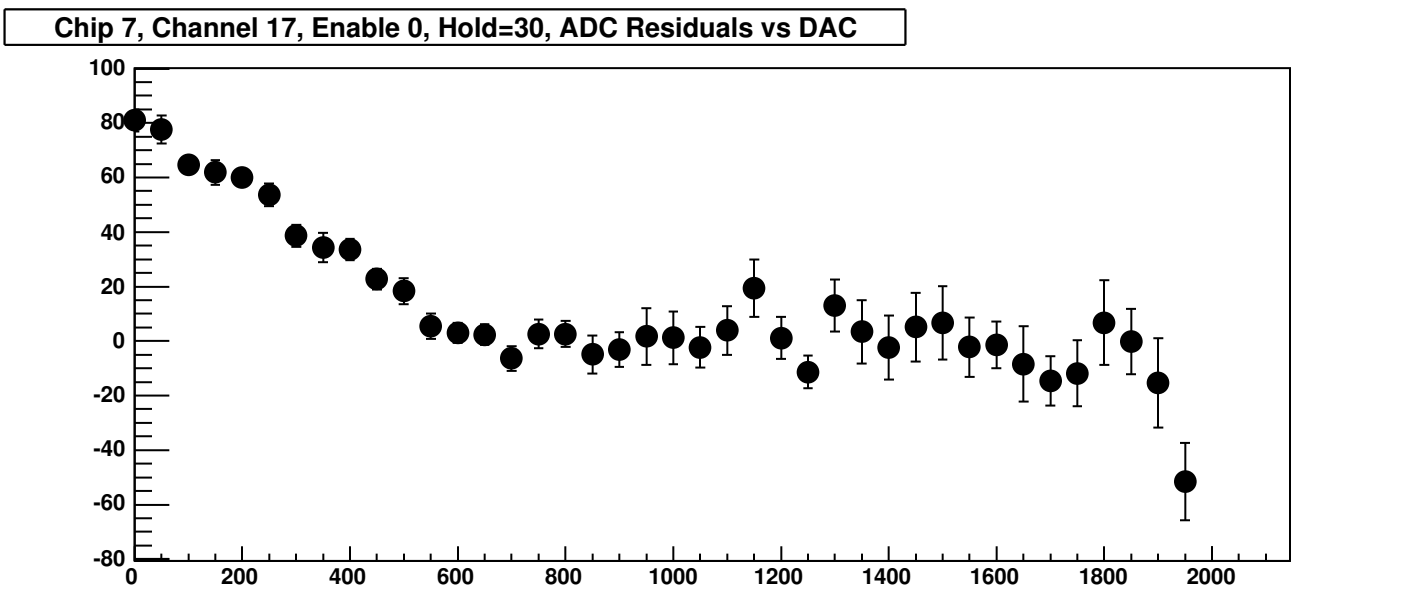

Chip 7, Channel 16, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 19.81 / 11
p0 -1558 ± 23.92
p1 0.5061 ± 0.02196
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

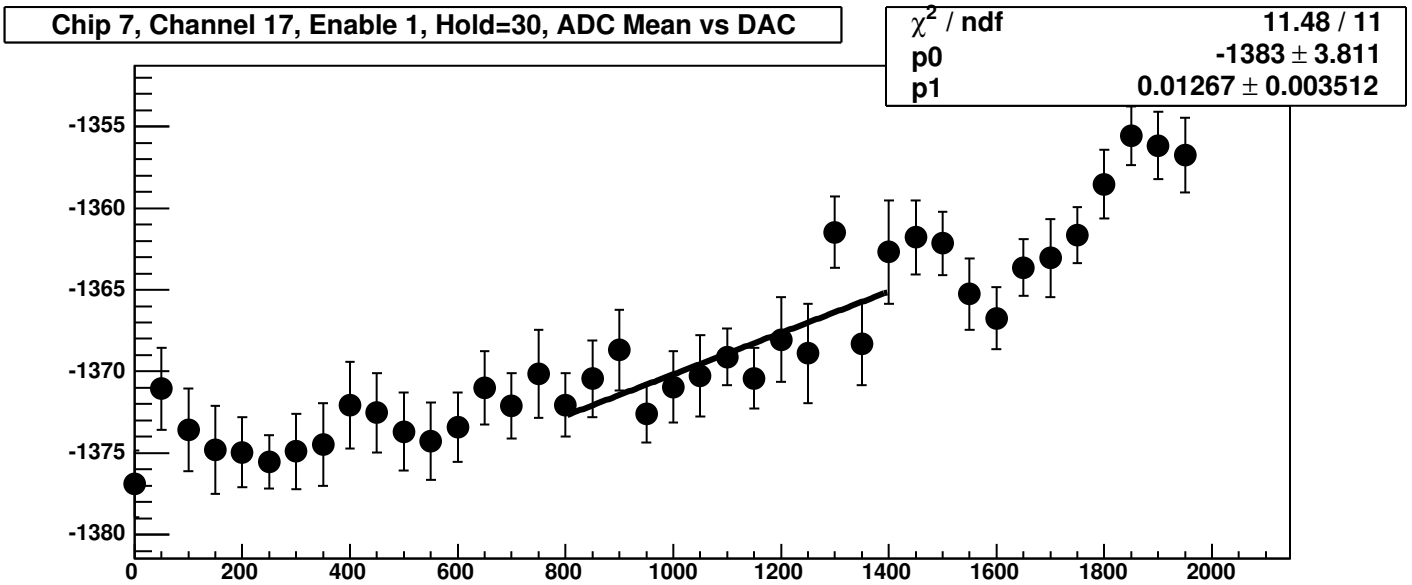

Chip 7, Channel 16, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

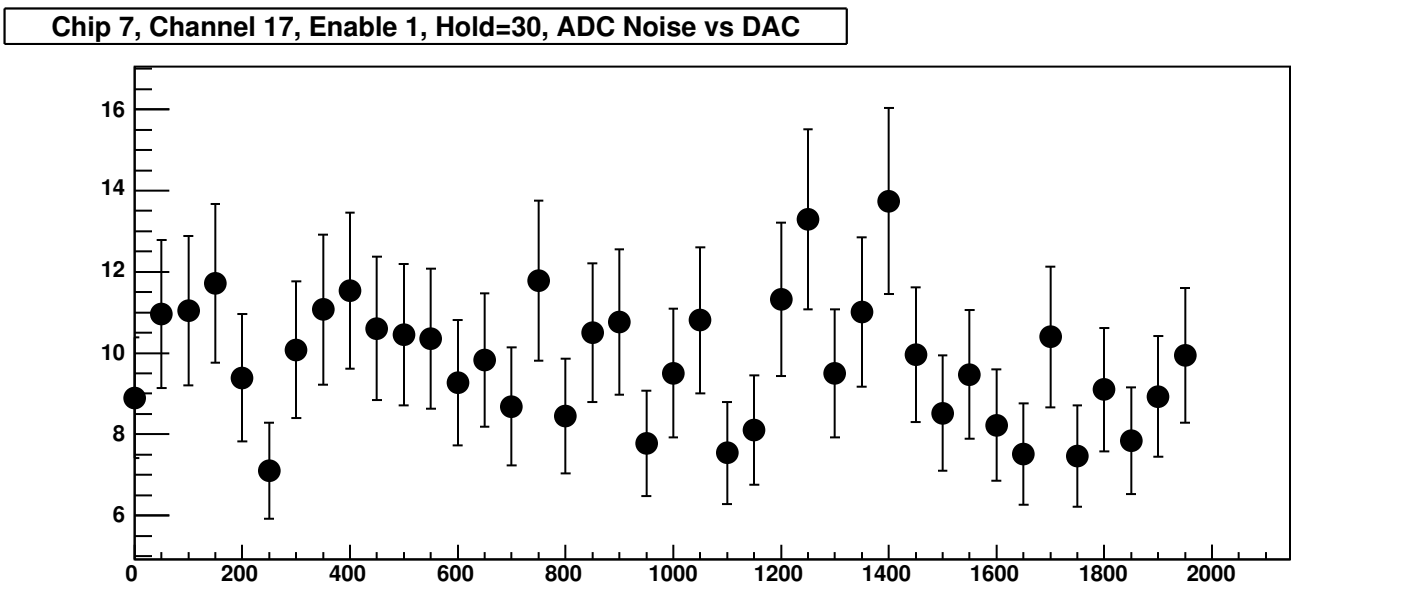

Chip 7, Channel 16, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

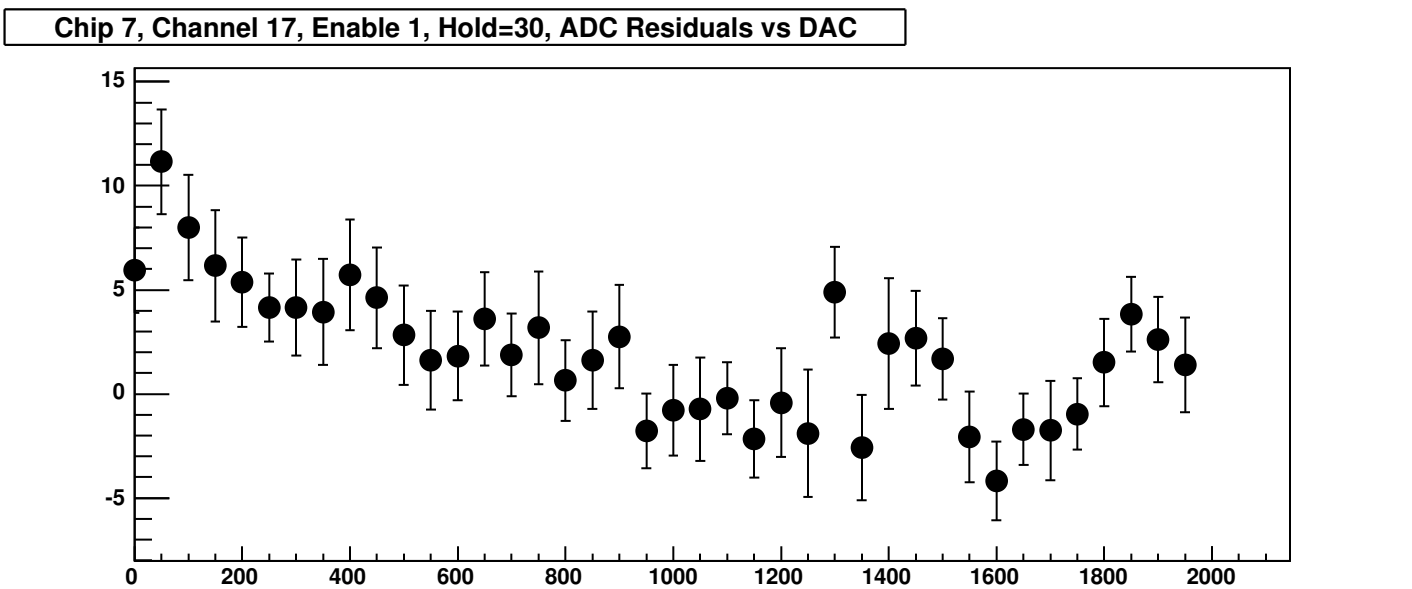

Chip 7, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 10.42 / 11
p0 -1459 ± 11.74
p1 0.1395 ± 0.01115
-1200
-1250
-1300
-1350
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

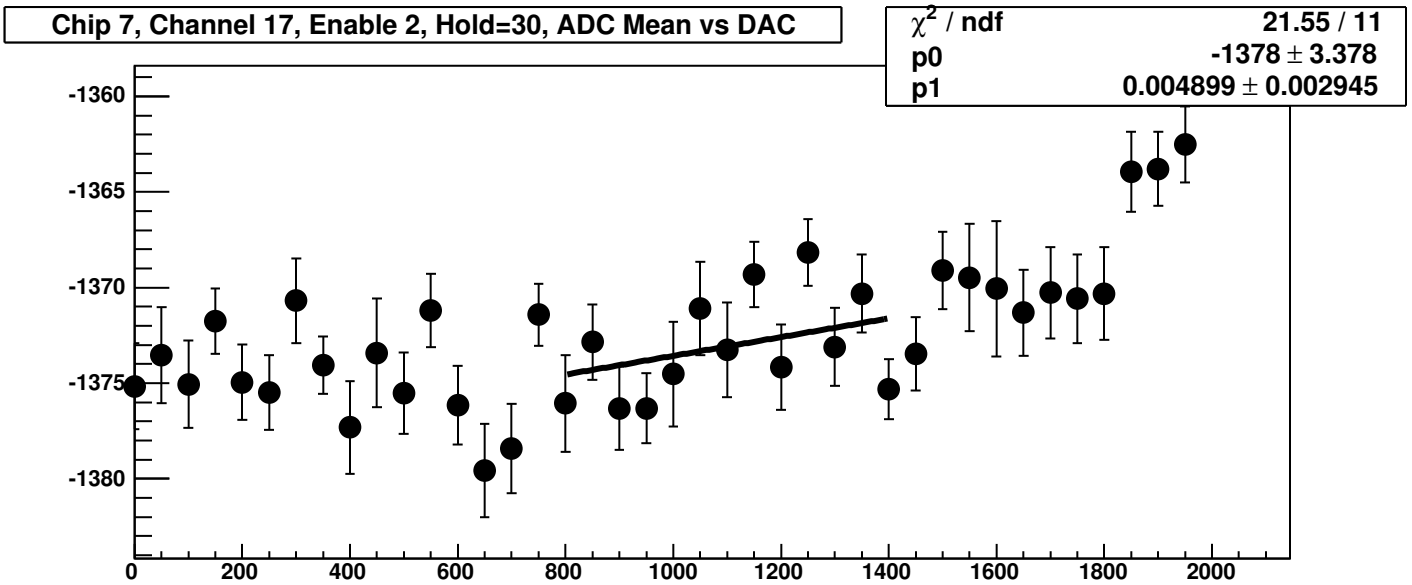

Chip 7, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

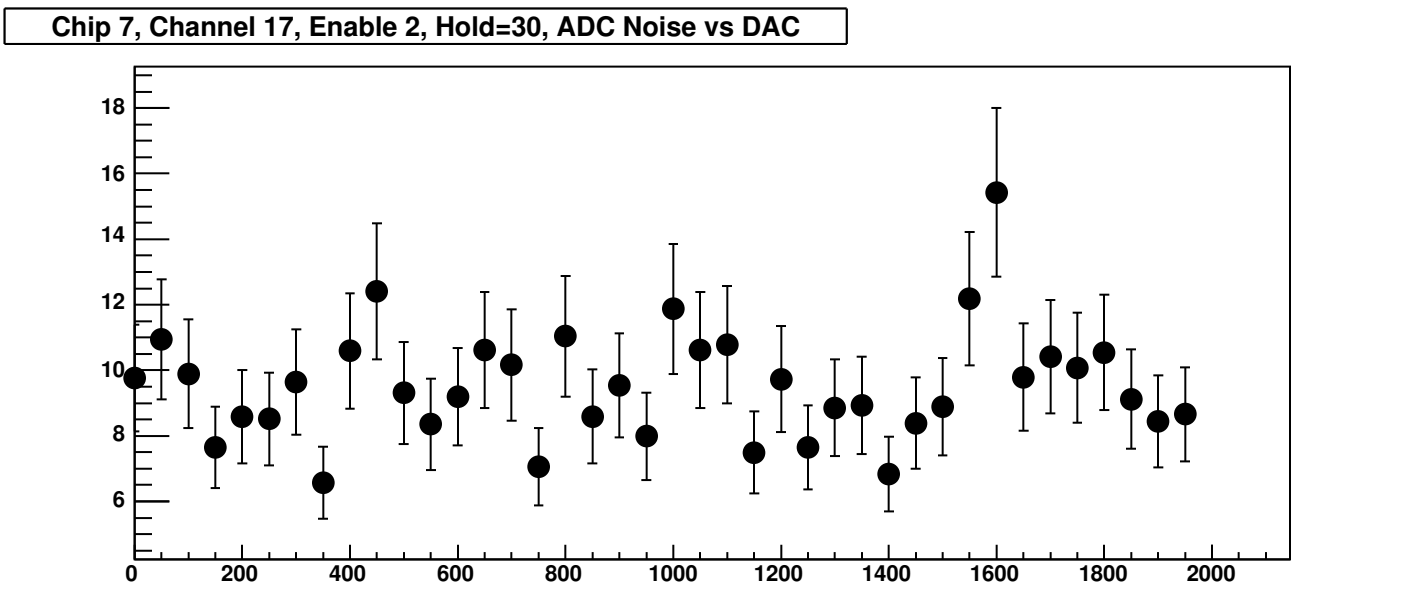

Chip 7, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
-60
-80
0
200
400
600
800
1000
1200
1400
1600
1800
2000

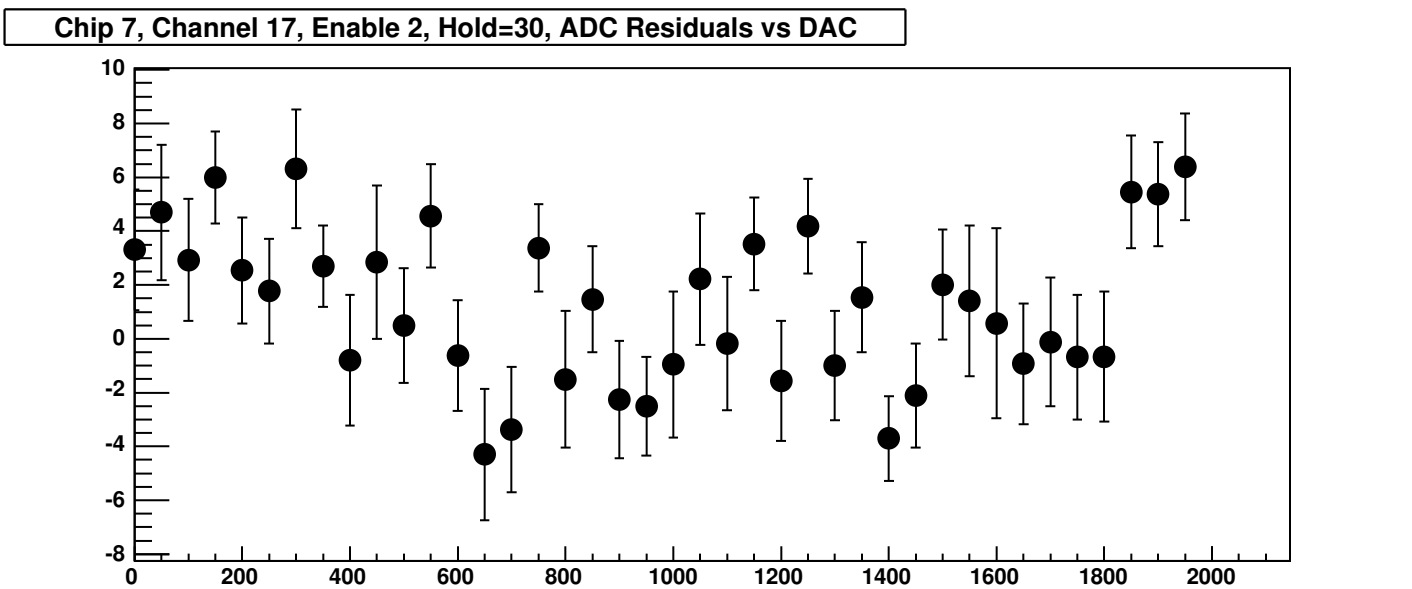

Chip 7, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 11.48 / 11
p0 -1383 ± 3.811
p1 0.01267 ± 0.003512
-1355
-1360
-1365
-1370
-1375
-1380
0
200
400
600
800
1000
1200
1400
1600
1800
2000

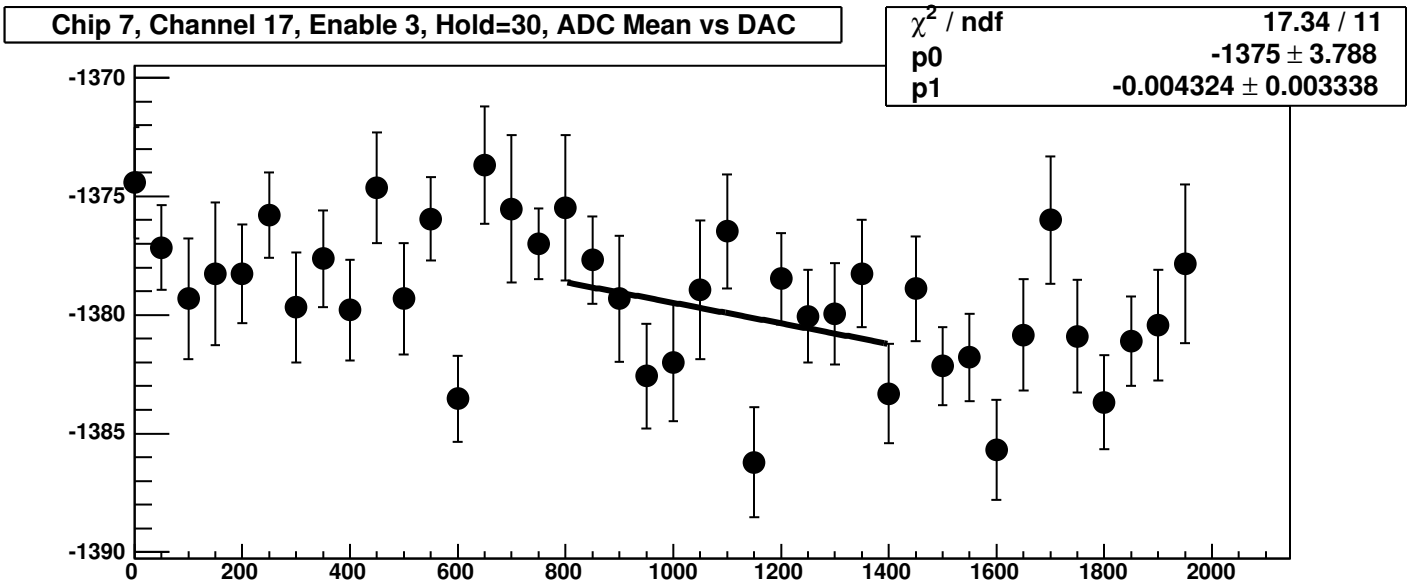

Chip 7, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

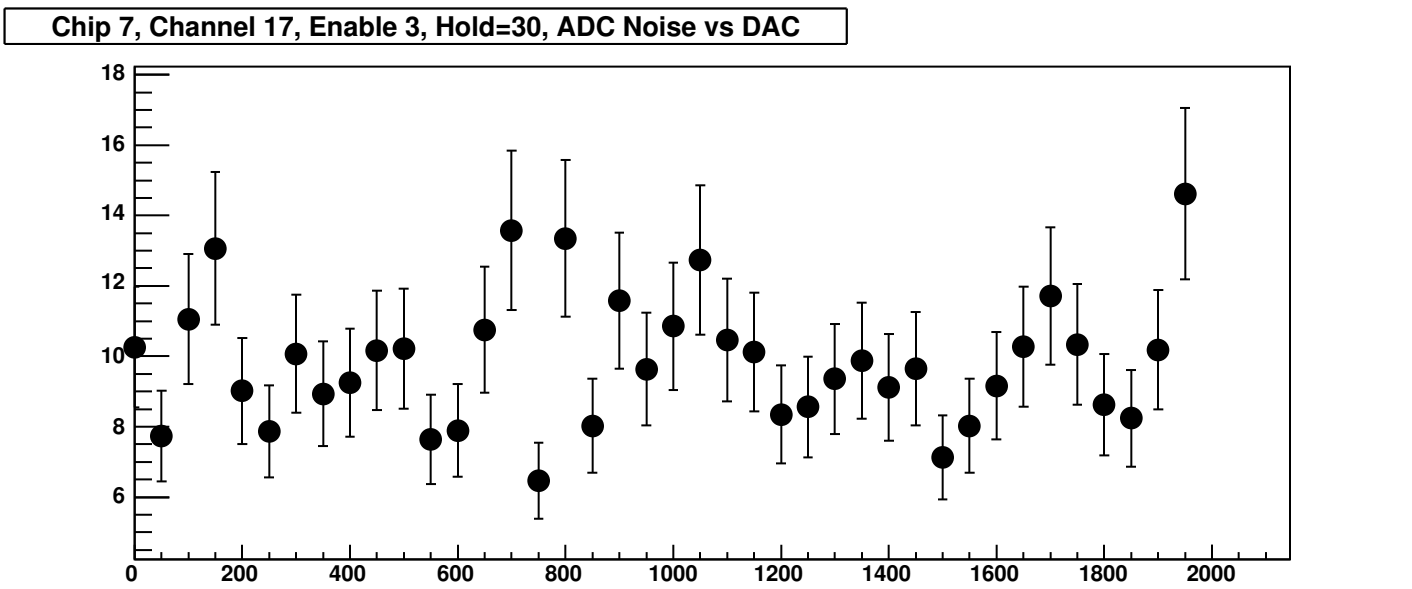

Chip 7, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
15
10
5
0
-5
0
200
400
600
800
1000
1200
1400
1600
1800
2000

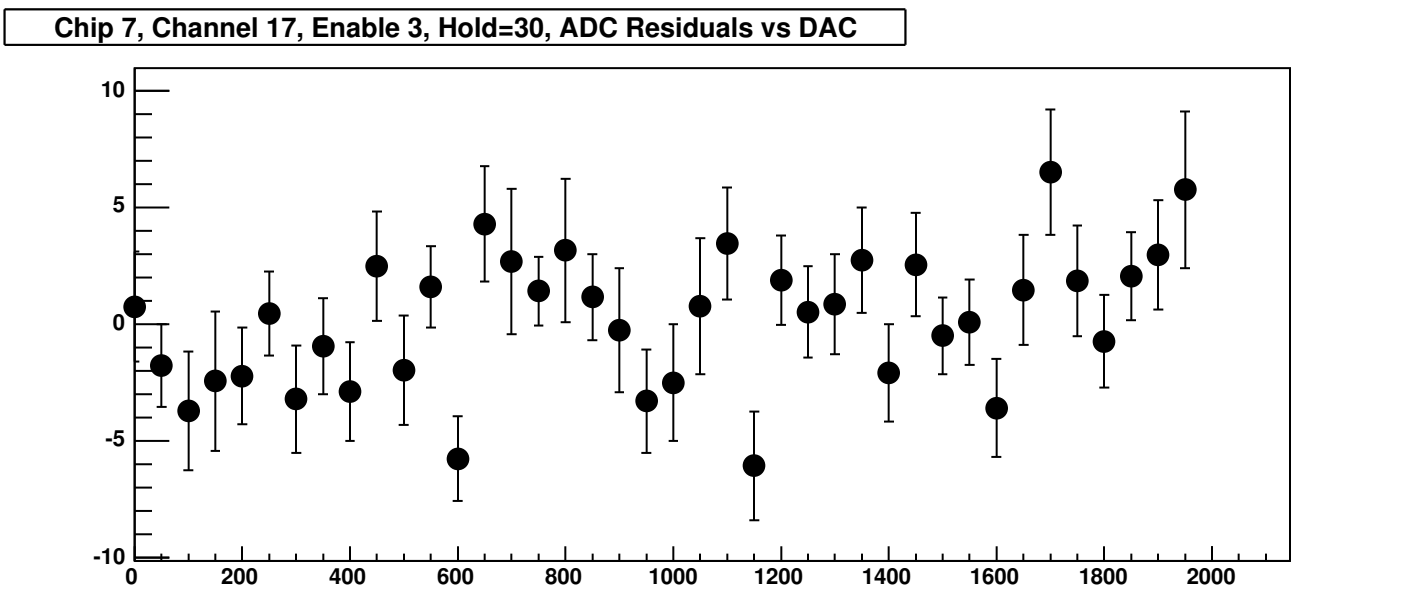

Chip 7, Channel 17, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 21.55 / 11
p0 -1378 ± 3.378
p1 0.004899 ± 0.002945
-1360
-1365
-1370
-1375
-1380
0
200
400
600
800
1000
1200
1400
1600
1800
2000

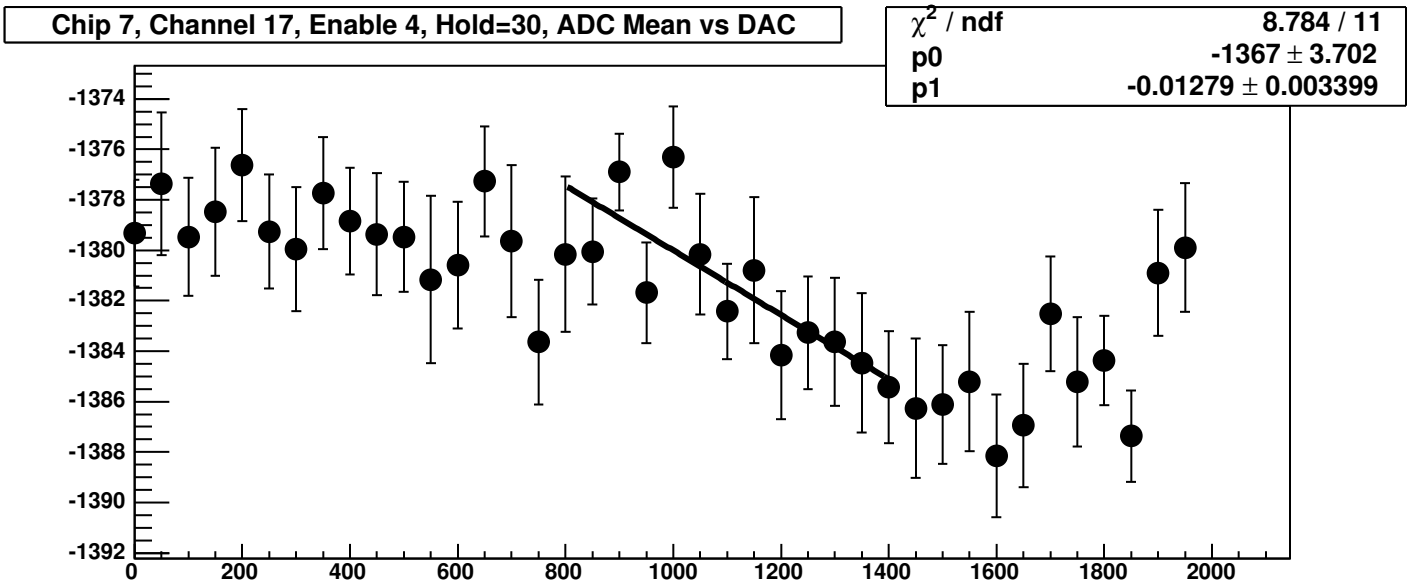

Chip 7, Channel 17, Enable 2, Hold=30, ADC Noise vs DAC
18
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

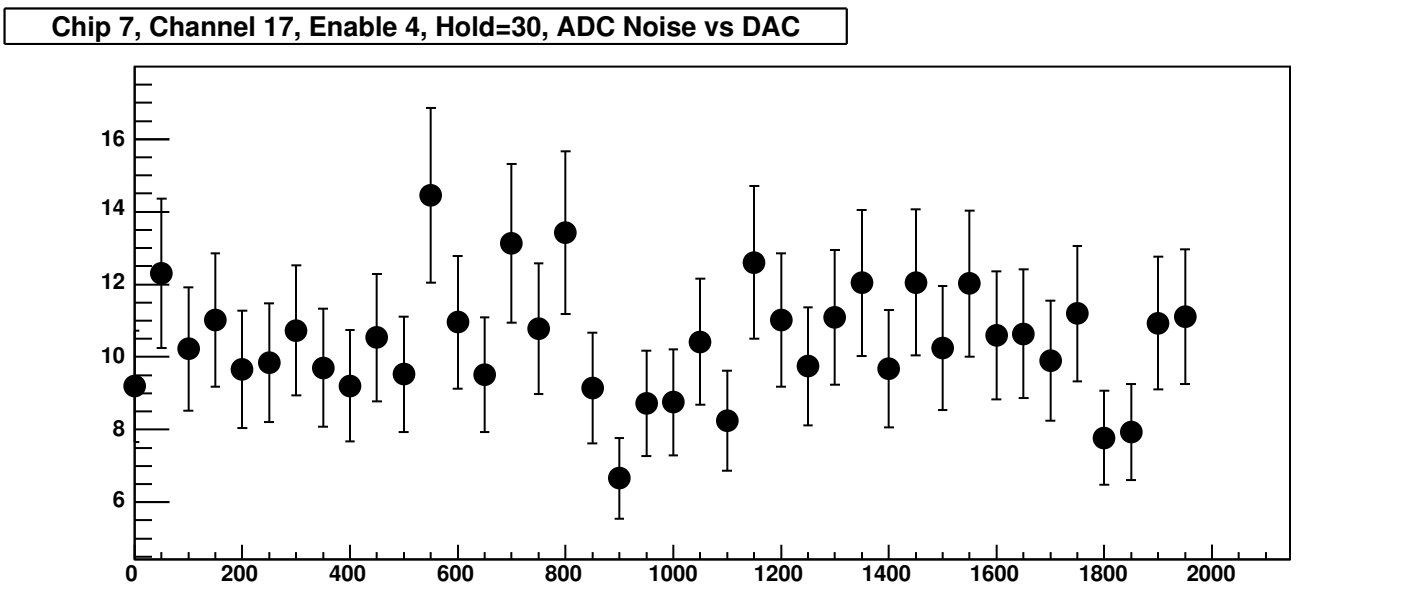

Chip 7, Channel 17, Enable 2, Hold=30, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
200
400
600
800
1000
1200
1400
1600
1800
2000

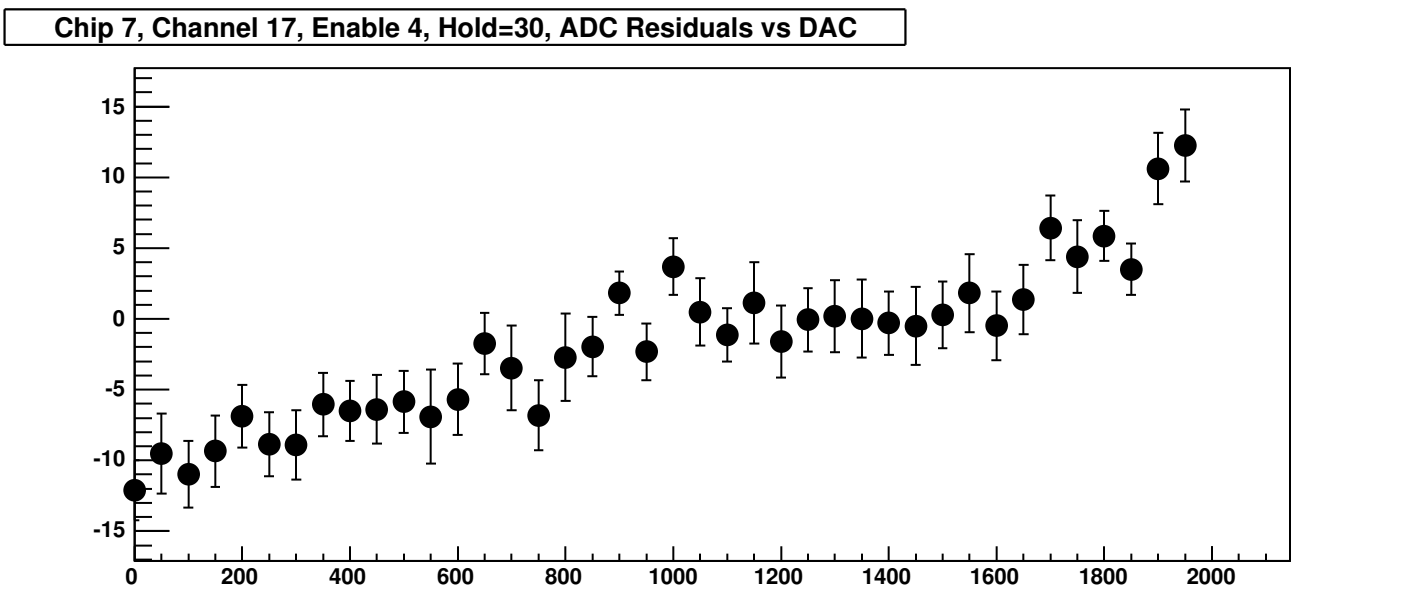

Chip 7, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 17.34 / 11
p0 -1375 ± 3.788
p1 -0.004324 ± 0.003338
-1370
-1375
-1380
-1385
-1390
0
200
400
600
800
1000
1200
1400
1600
1800
2000

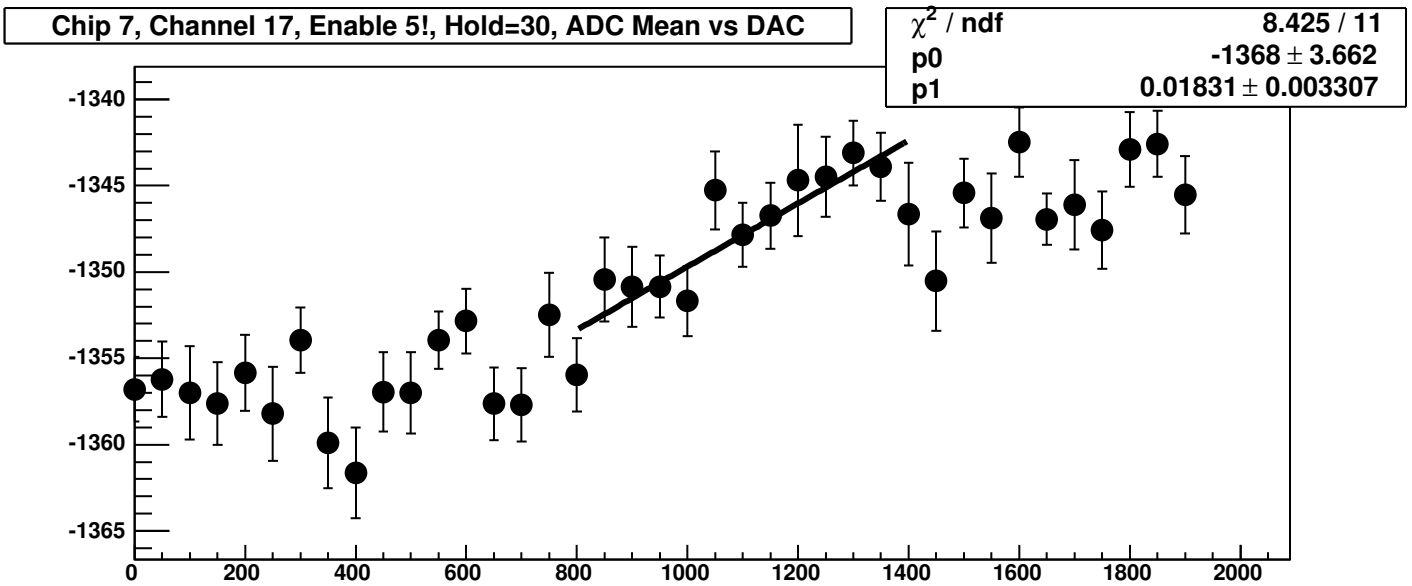

Chip 7, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
18
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

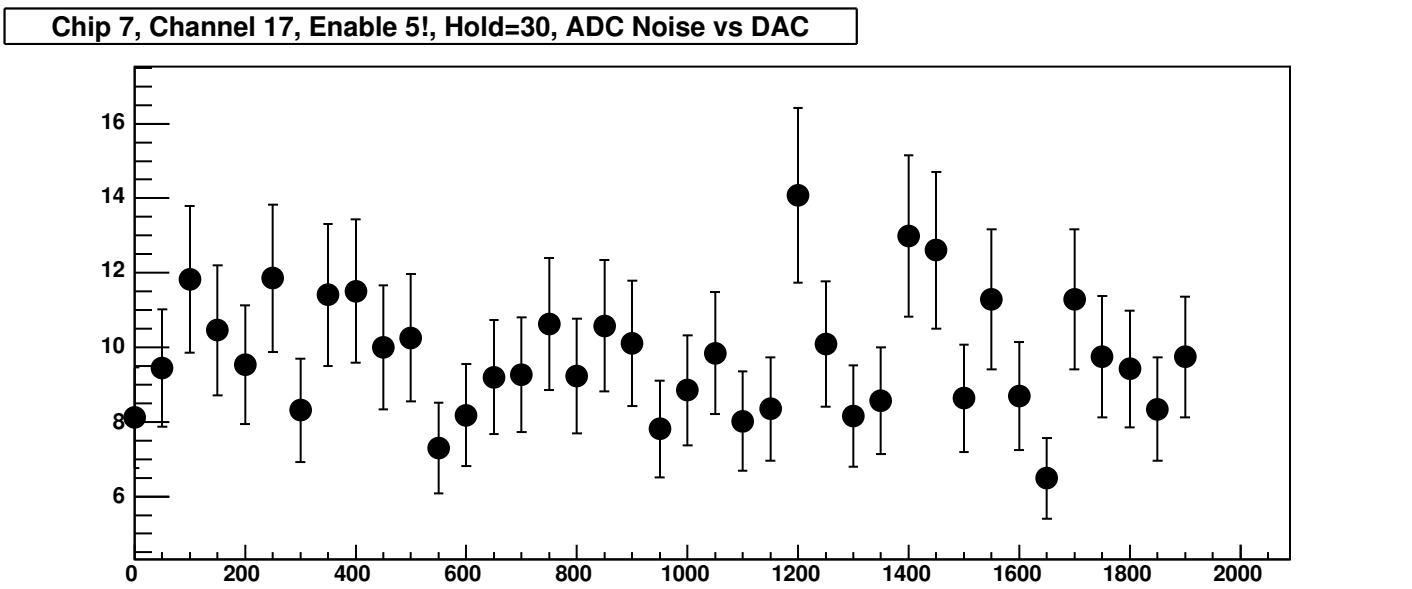

Chip 7, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

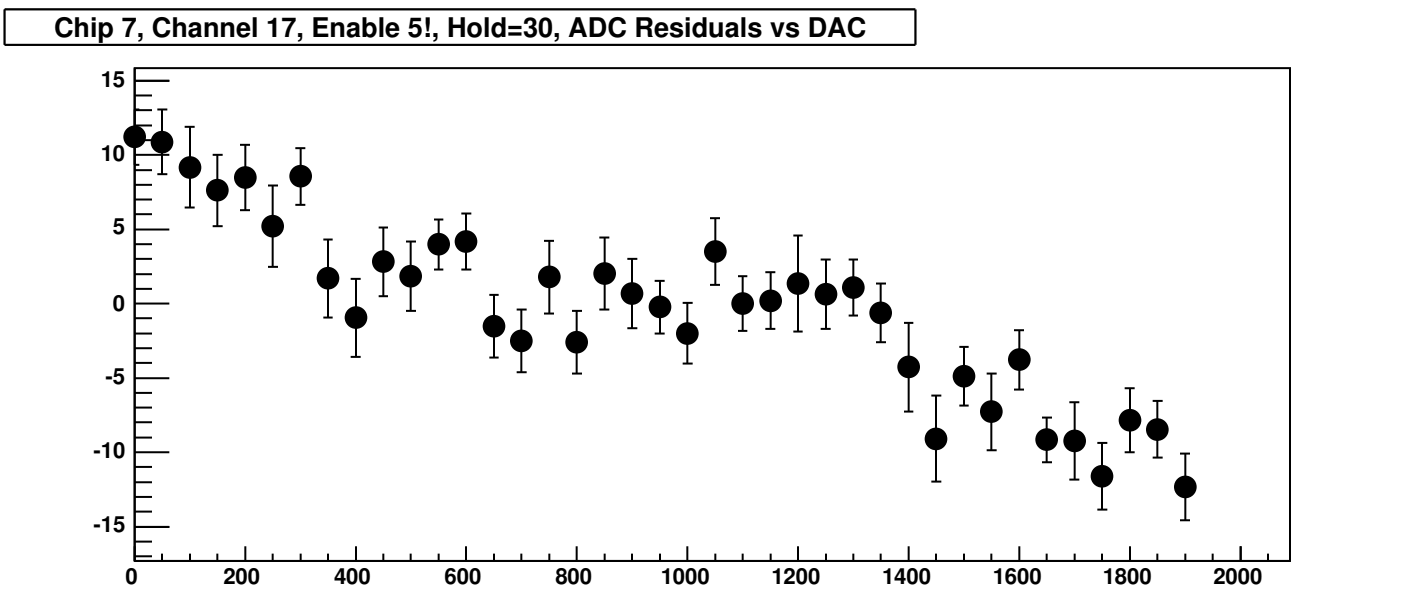

Chip 7, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 8.784 / 11
p0 -1367 ± 3.702
p1 -0.01279 ± 0.003399
-1374
-1376
-1378
-1380
-1382
-1384
-1386
-1388
-1390
-1392
0
200
400
600
800
1000
1200
1400
1600
1800
2000

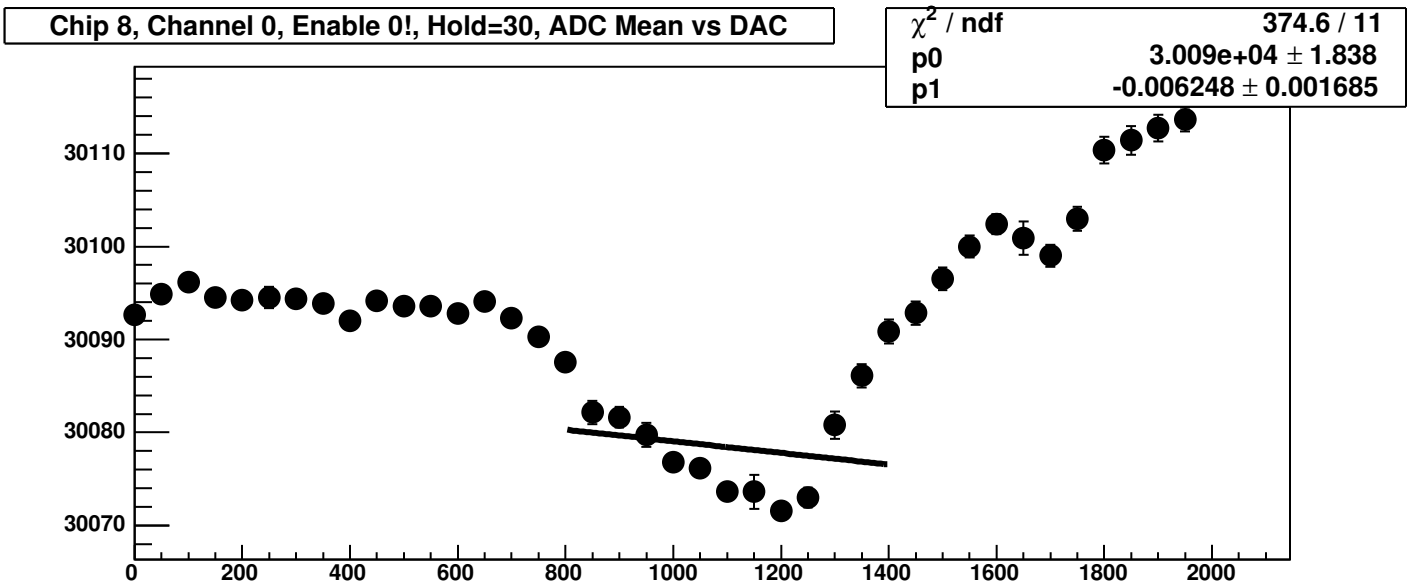

Chip 7, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

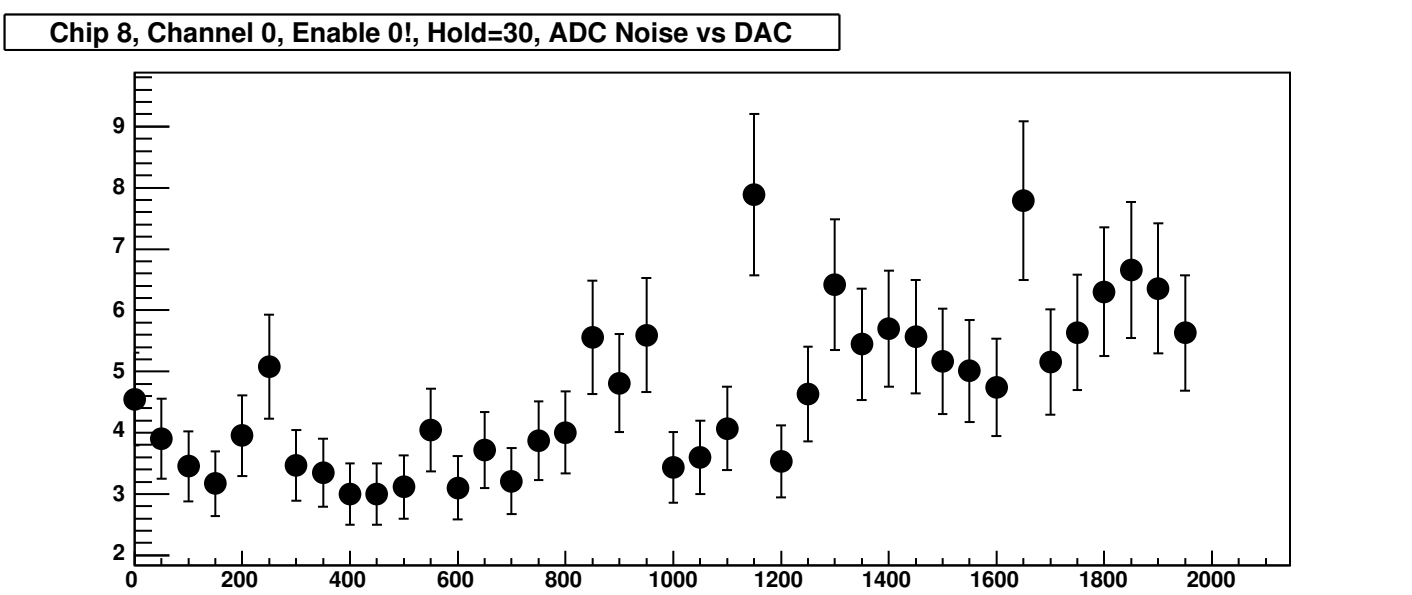

Chip 7, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
200
400
600
800
1000
1200
1400
1600
1800
2000

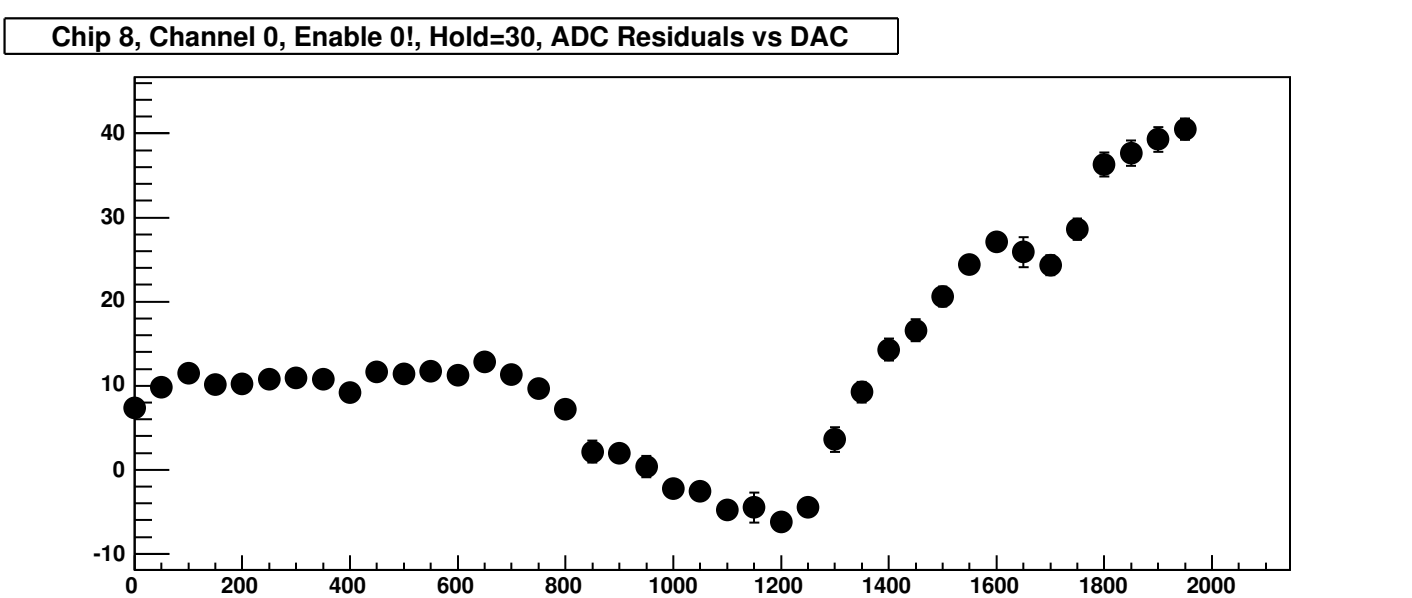

Chip 7, Channel 17, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 8.425 / 11
p0 -1368 ± 3.662
p1 0.01831 ± 0.003307
-1340
-1345
-1350
-1355
-1360
-1365
0
200
400
600
800
1000
1200
1400
1600
1800
2000

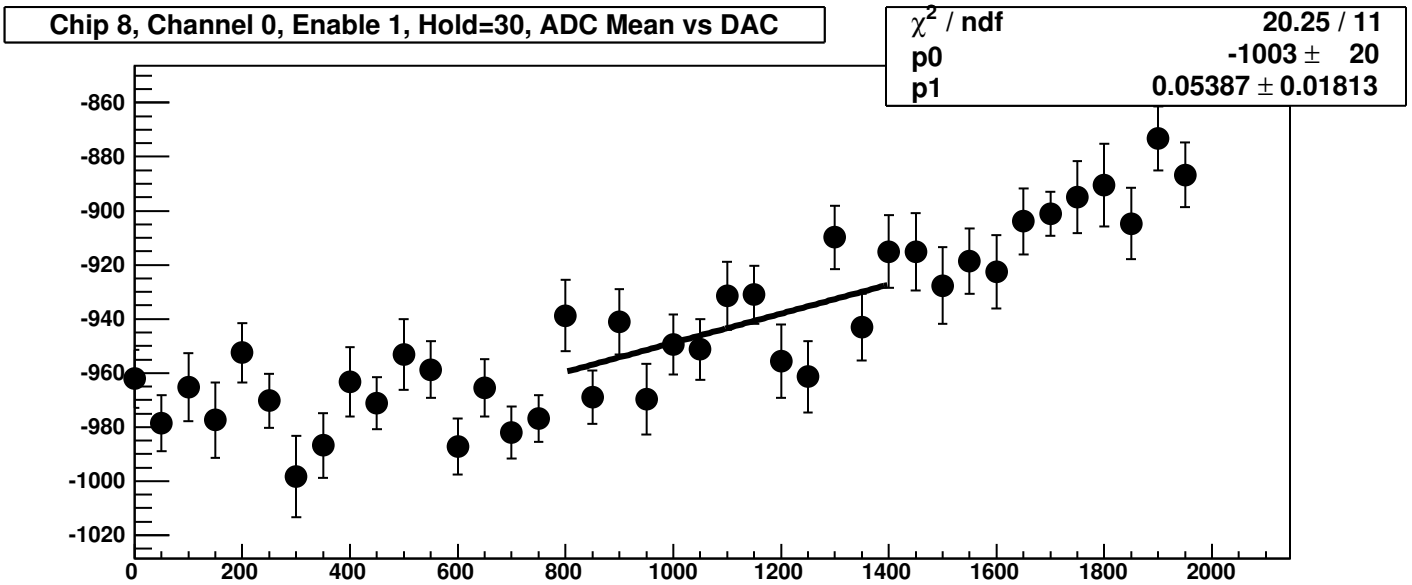

Chip 7, Channel 17, Enable 5!, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

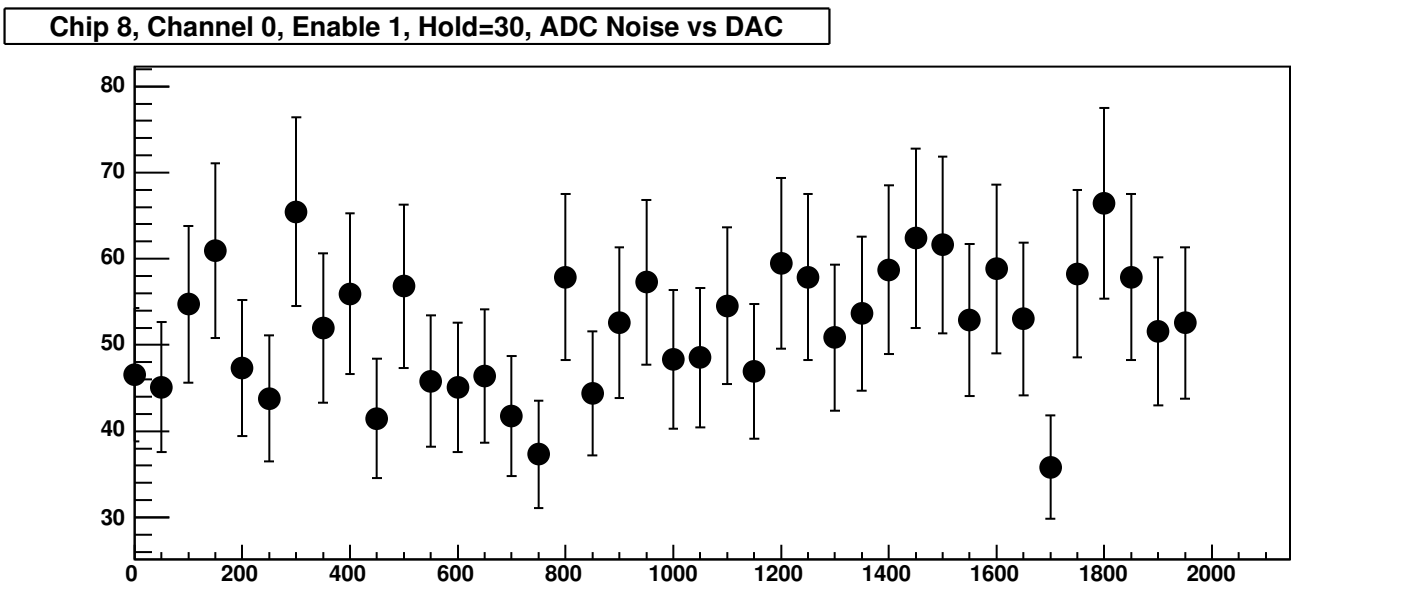

Chip 7, Channel 17, Enable 5!, Hold=30, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
200
400
600
800
1000
1200
1400
1600
1800
2000

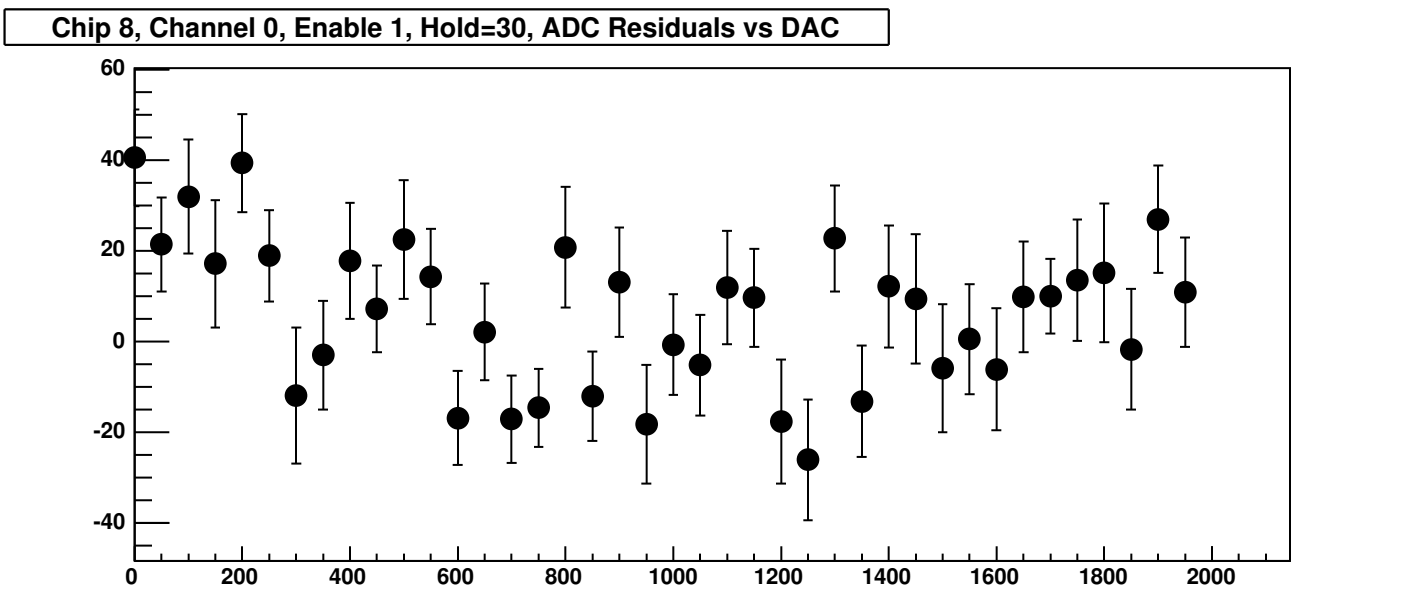

Chip 8, Channel 0, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 374.6 / 11
p0 3.009e+04 ± 1.838
p1 -0.006248 ± 0.001685
30110
30100
30090
30080
30070
0
200
400
600
800
1000
1200
1400
1600
1800
2000

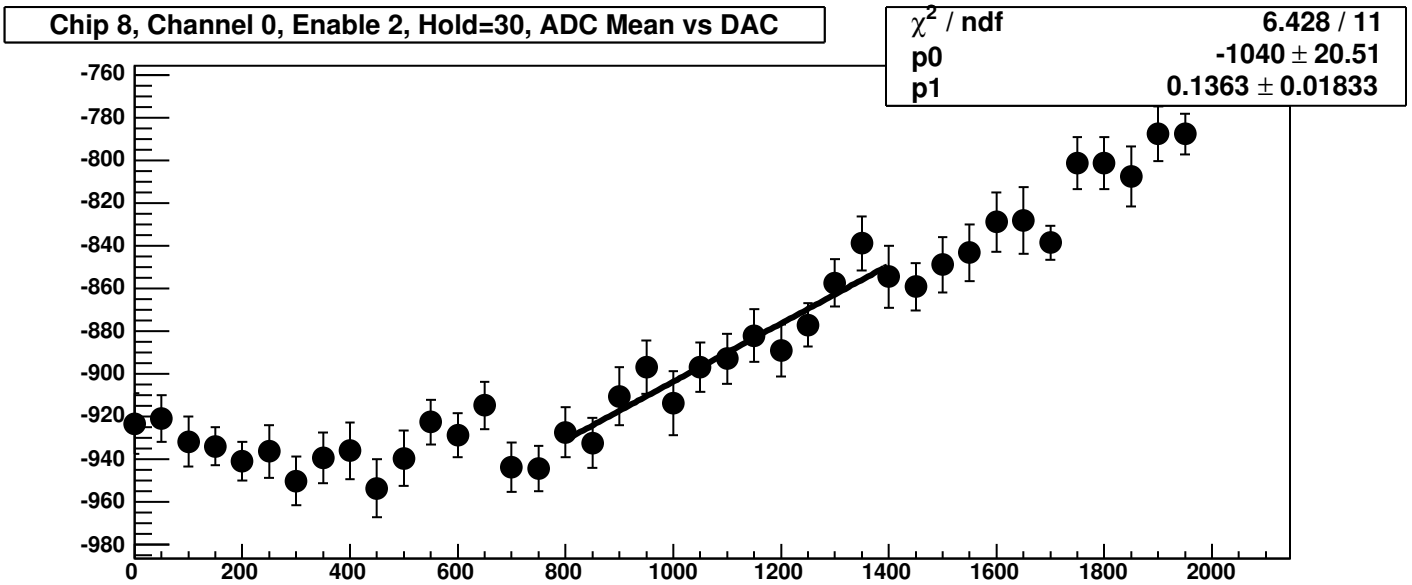

Chip 8, Channel 0, Enable 0!, Hold=30, ADC Noise vs DAC
9
8
7
6
5
4
3
2
0
200
400
600
800
1000
1200
1400
1600
1800
2000

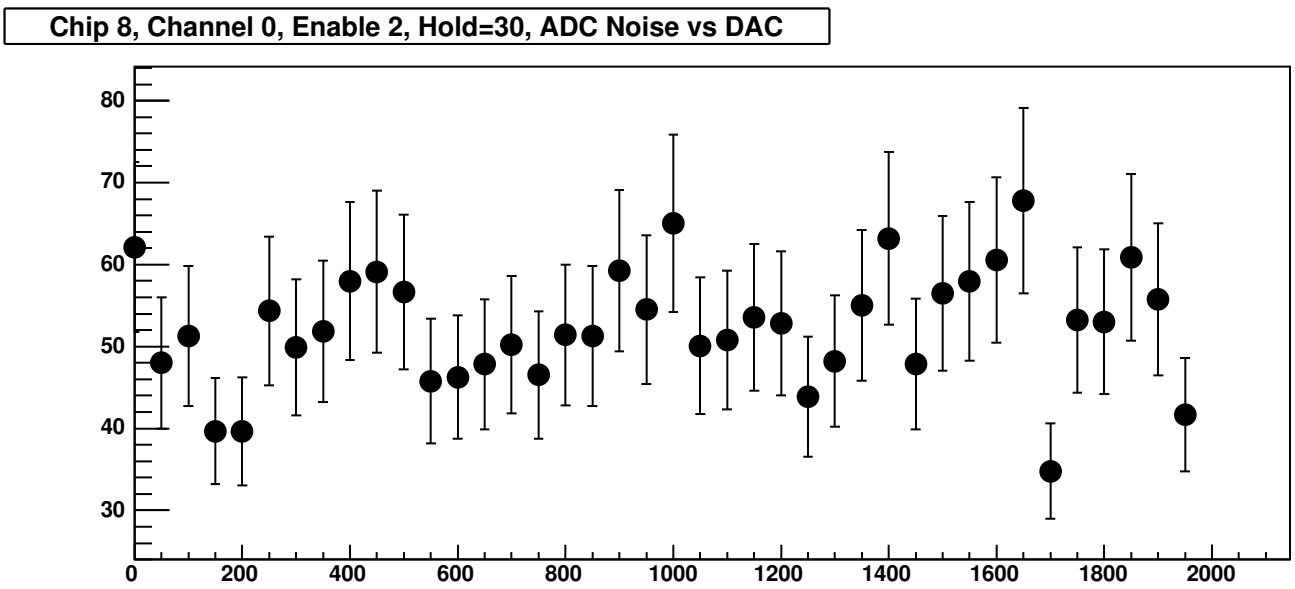

Chip 8, Channel 0, Enable 0!, Hold=30, ADC Residuals vs DAC
40
30
20
10
0
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

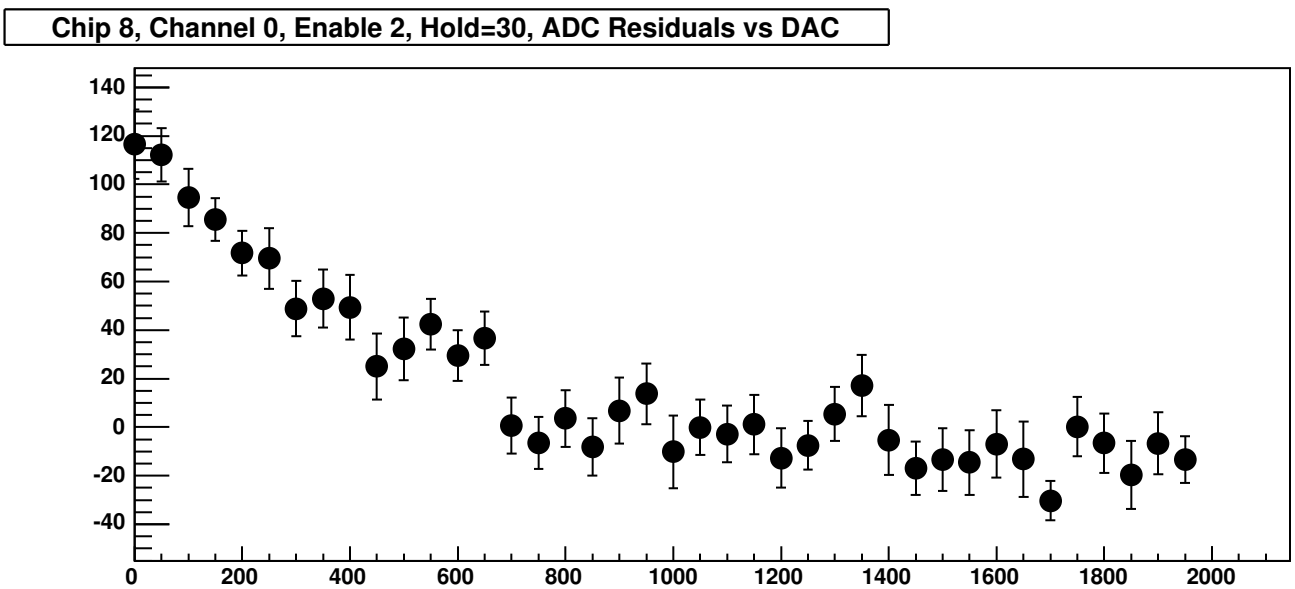

Chip 8, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 20.25 / 11
p0 -1003 ± 20
p1 0.05387 ± 0.01813
-860
-880
-900
-920
-940
-960
-980
-1000
-1020
0
200
400
600
800
1000
1200
1400
1600
1800
2000

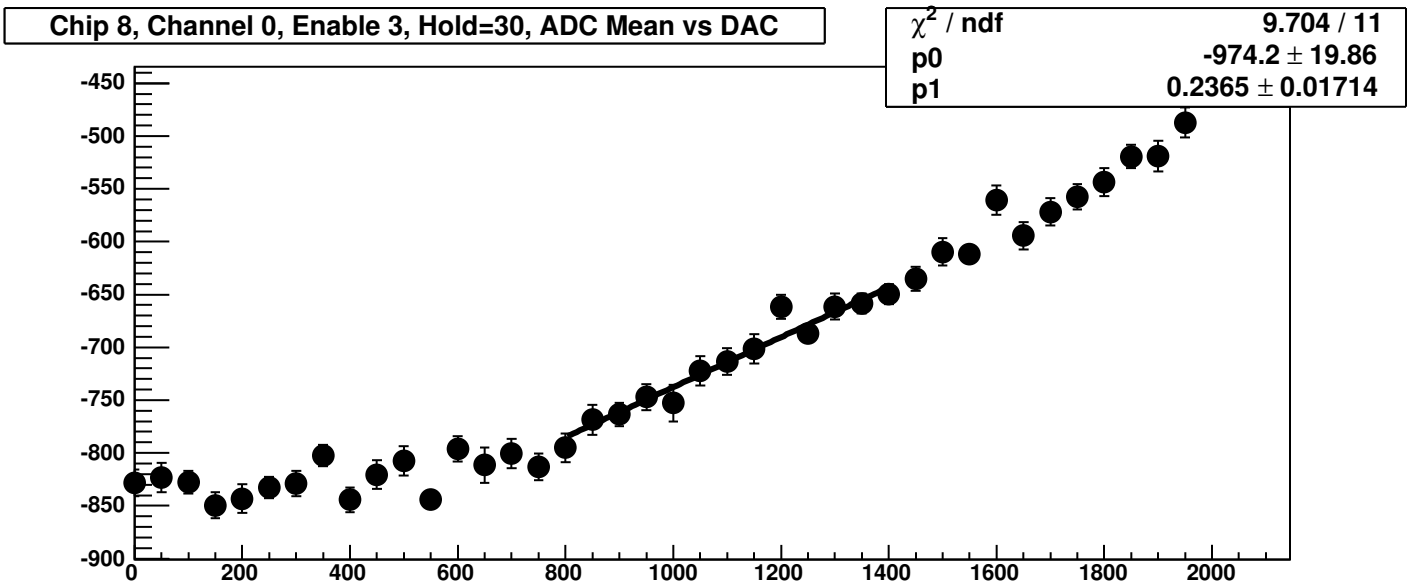

Chip 8, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

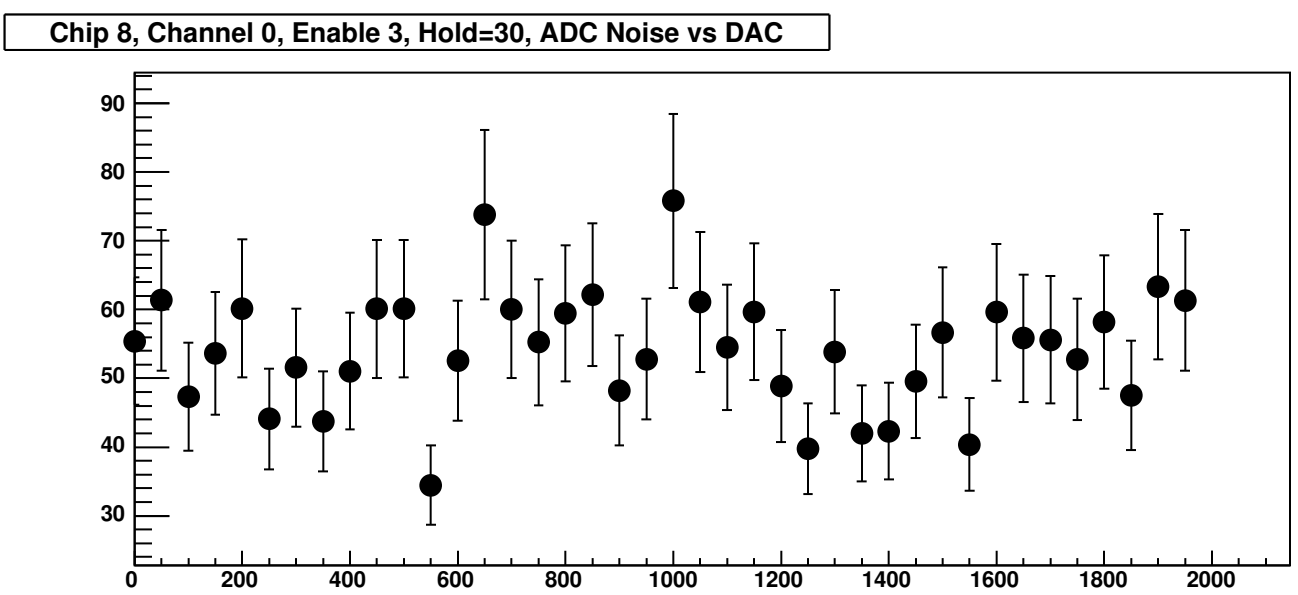

Chip 8, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

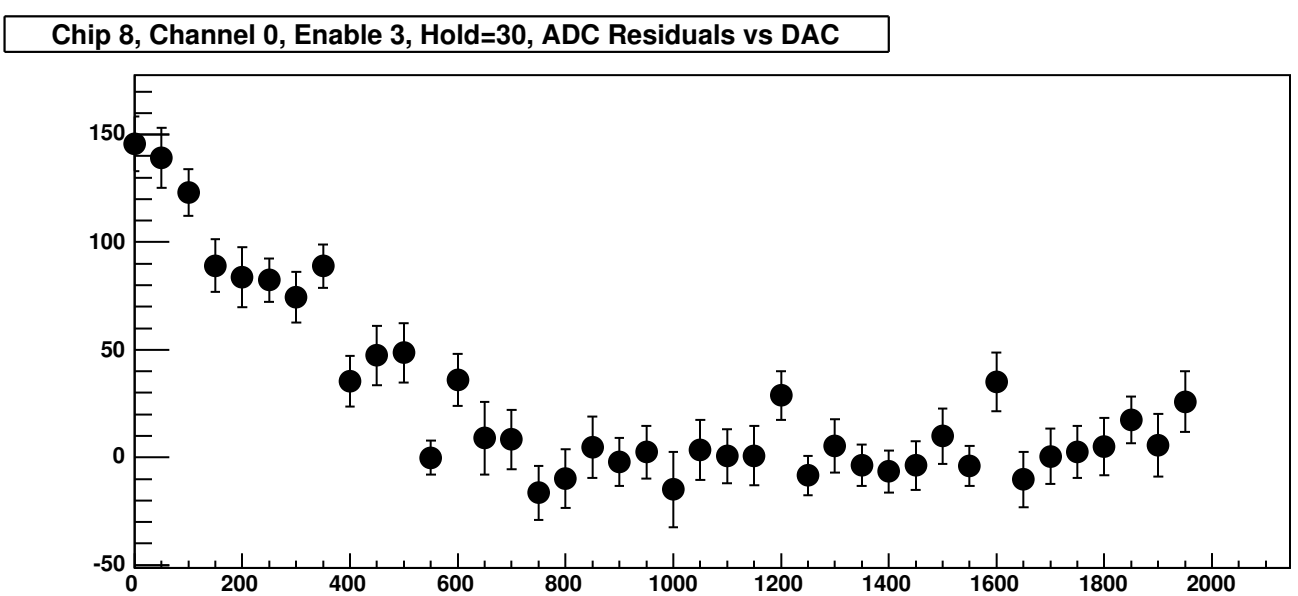

Chip 8, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 6.428 / 11
p0 -1040 ± 20.51
p1 0.1363 ± 0.01833
-760
-780
-800
-820
-840
-860
-880
-900
-920
-940
-960
-980
0
200
400
600
800
1000
1200
1400
1600
1800
2000

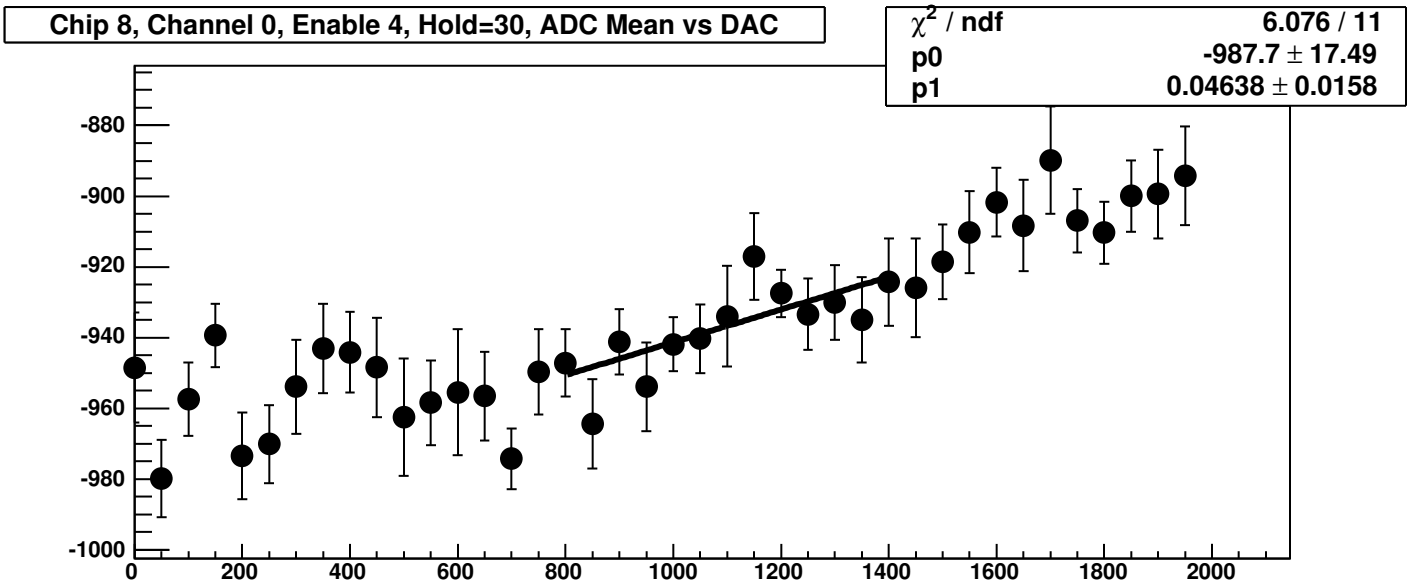

Chip 8, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

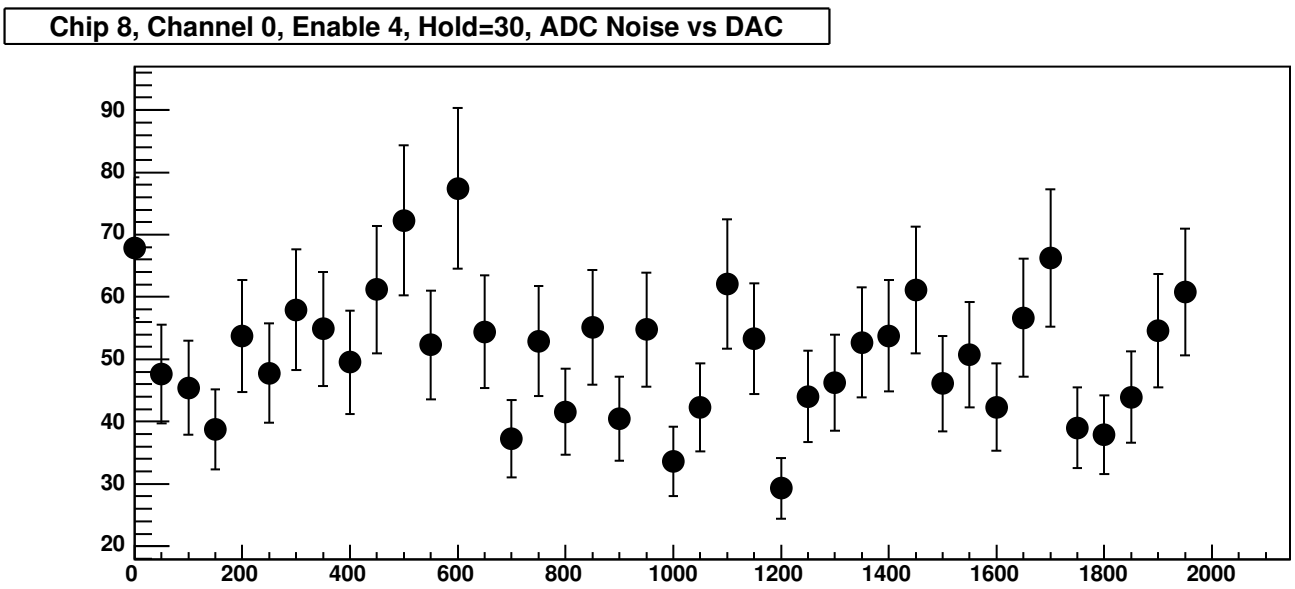

Chip 8, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

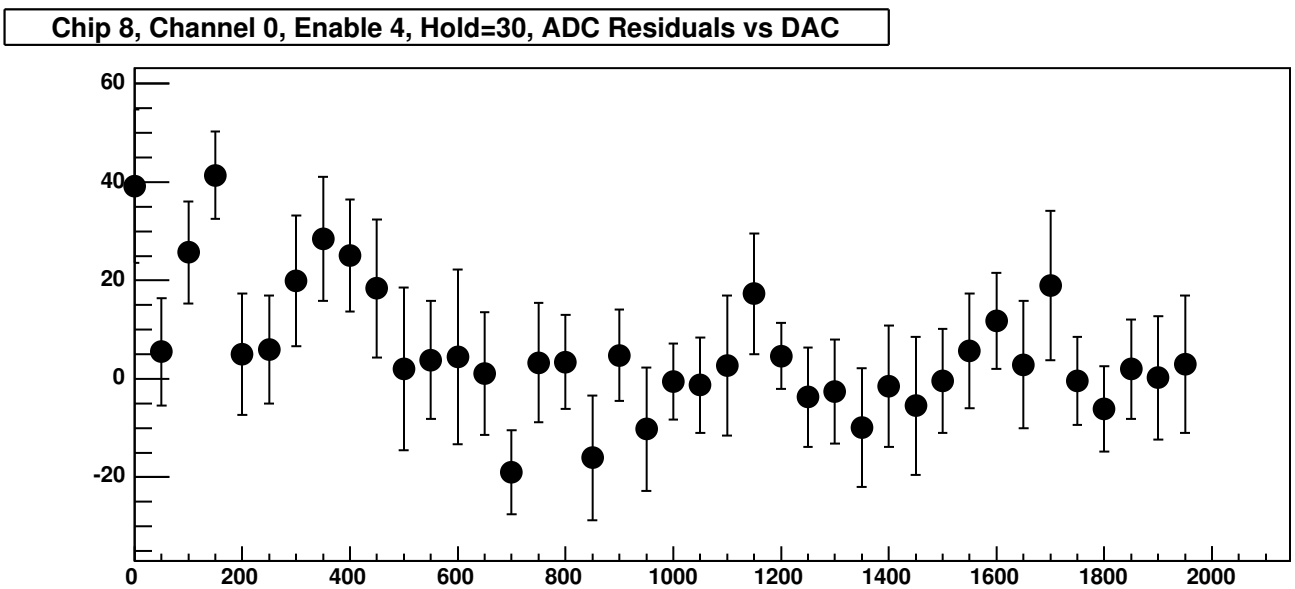

Chip 8, Channel 0, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 9.704 / 11
p0 -974.2 ± 19.86
p1 0.2365 ± 0.01714
-450
-500
-550
-600
-650
-700
-750
-800
-850
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

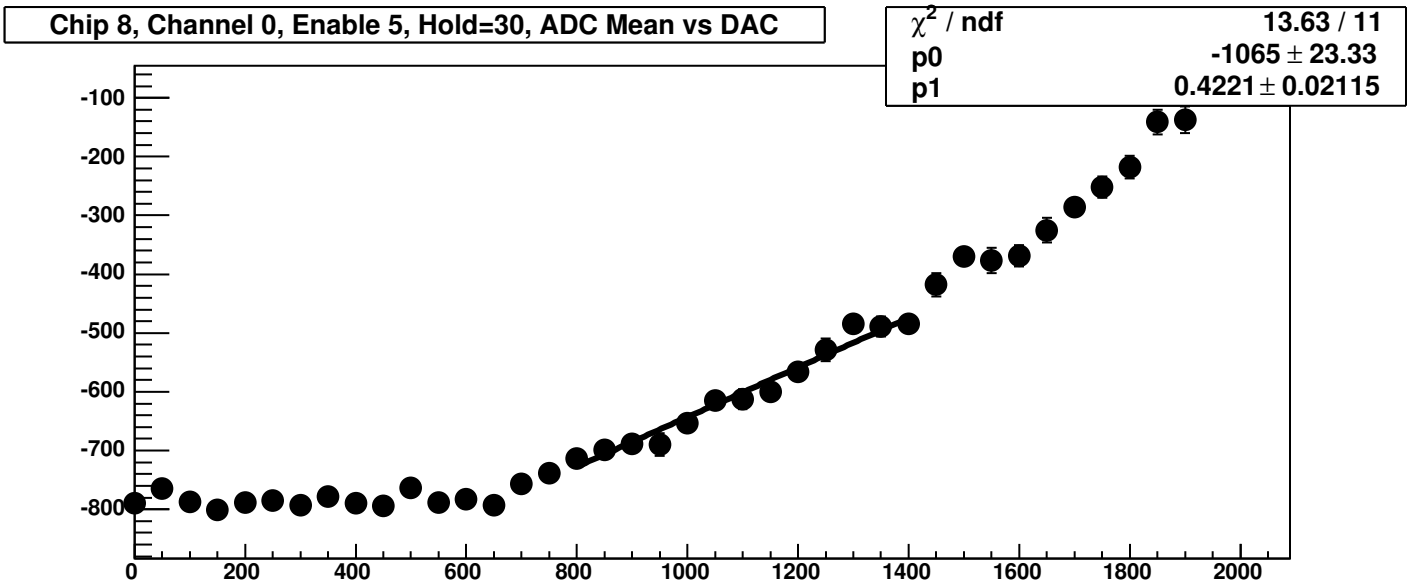

Chip 8, Channel 0, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

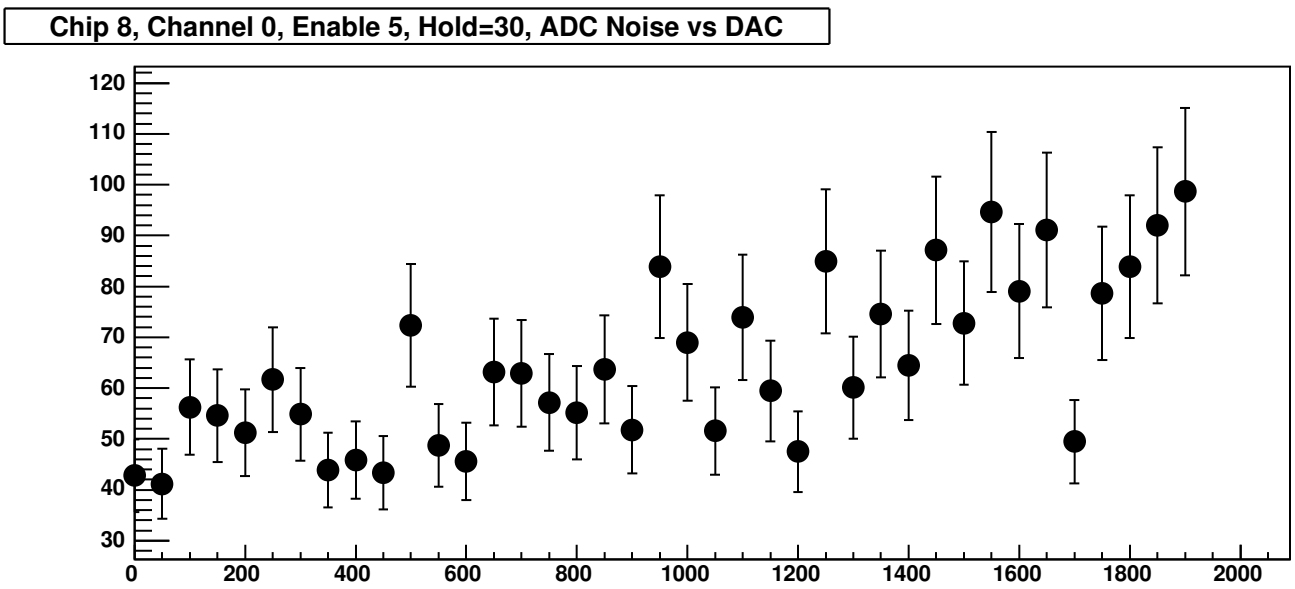

Chip 8, Channel 0, Enable 3, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

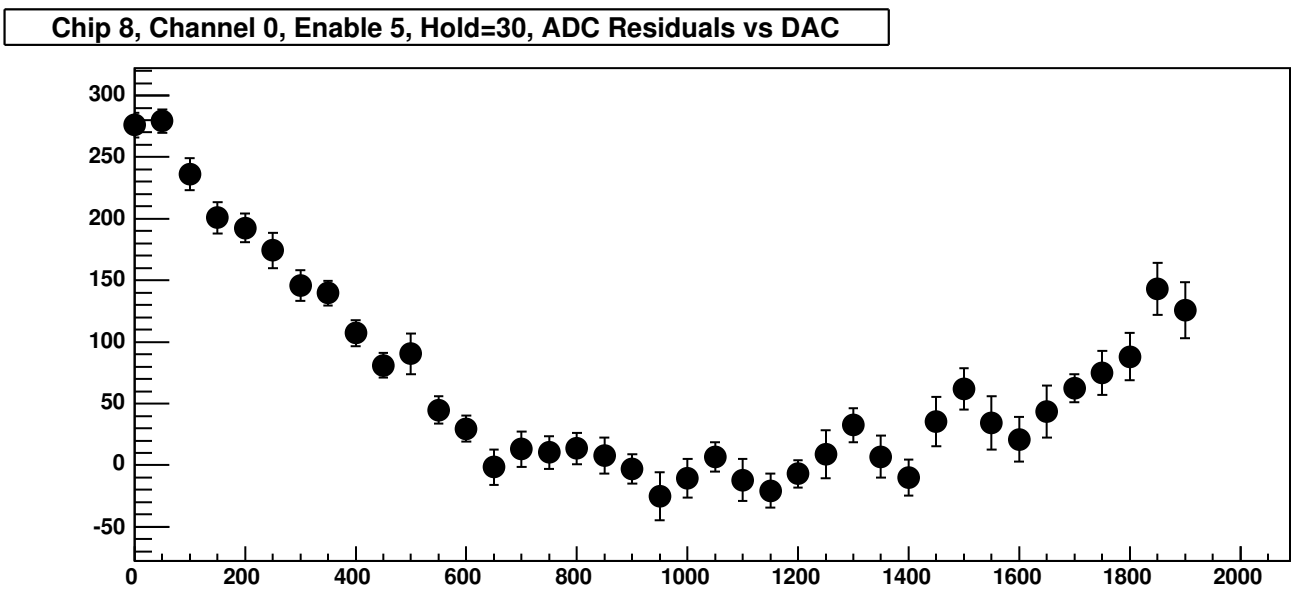

Chip 8, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6.076 / 11
p0 -987.7 ± 17.49
p1 0.04638 ± 0.0158
-880
-900
-920
-940
-960
-980
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

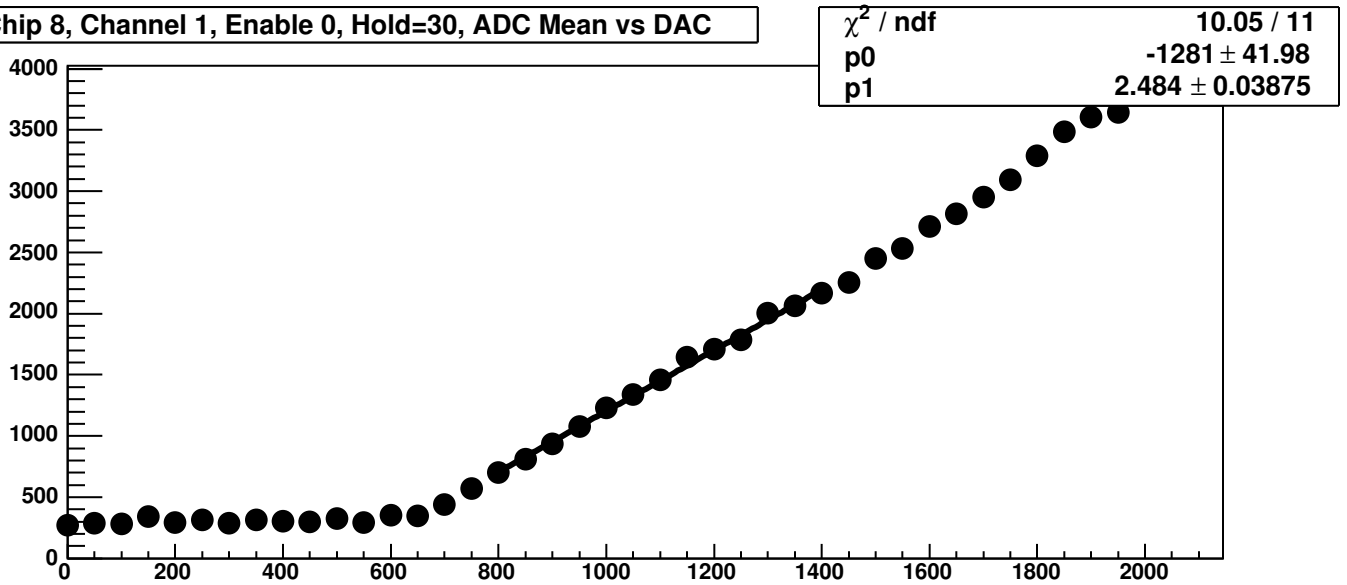

Chip 8, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 13.63 / 11
p0 -1065 ± 23.33
p1 0.4221 ± 0.02115
-100
-200
-300
-400
-500
-600
-700
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

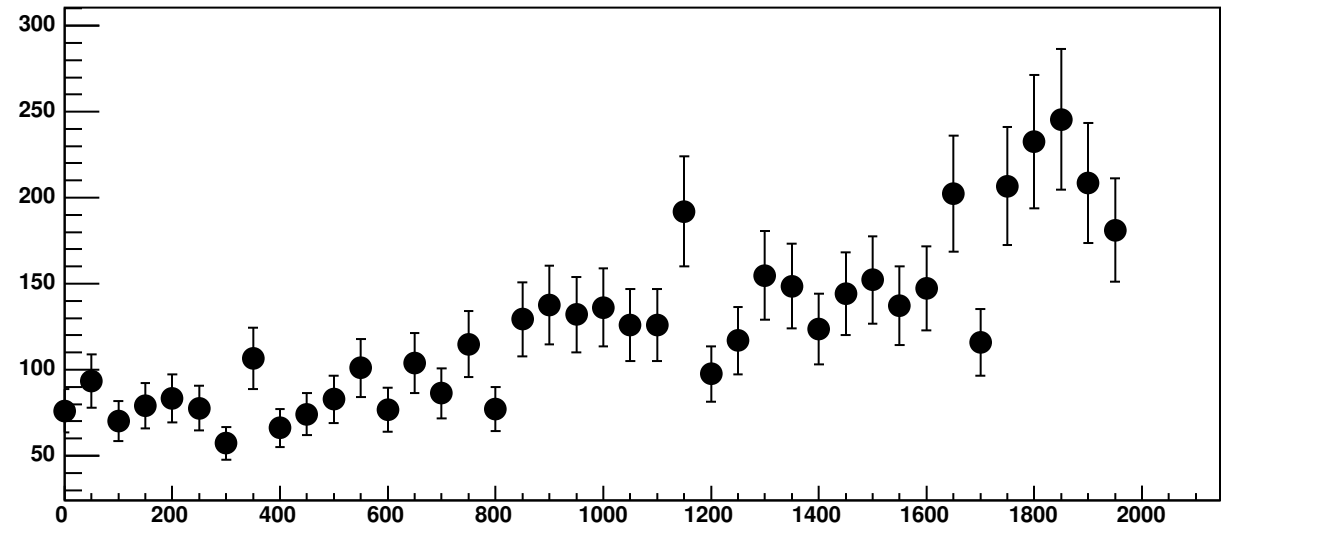

Chip 8, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
120
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

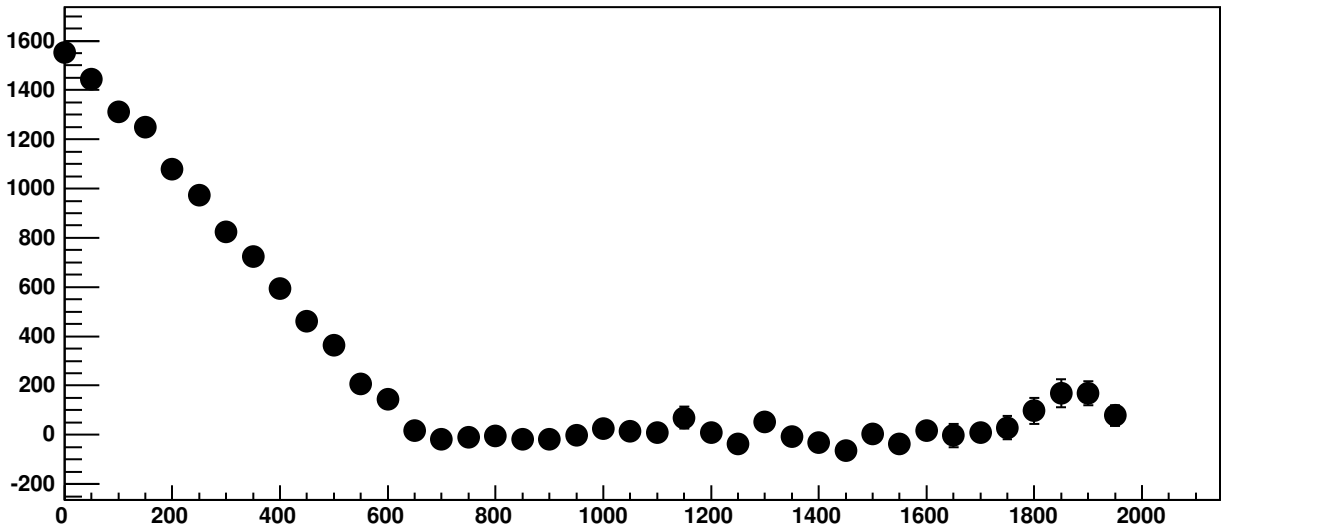
Chip 8, Channel 1, Enable 0, Hold=30, ADC Mean vs DAC

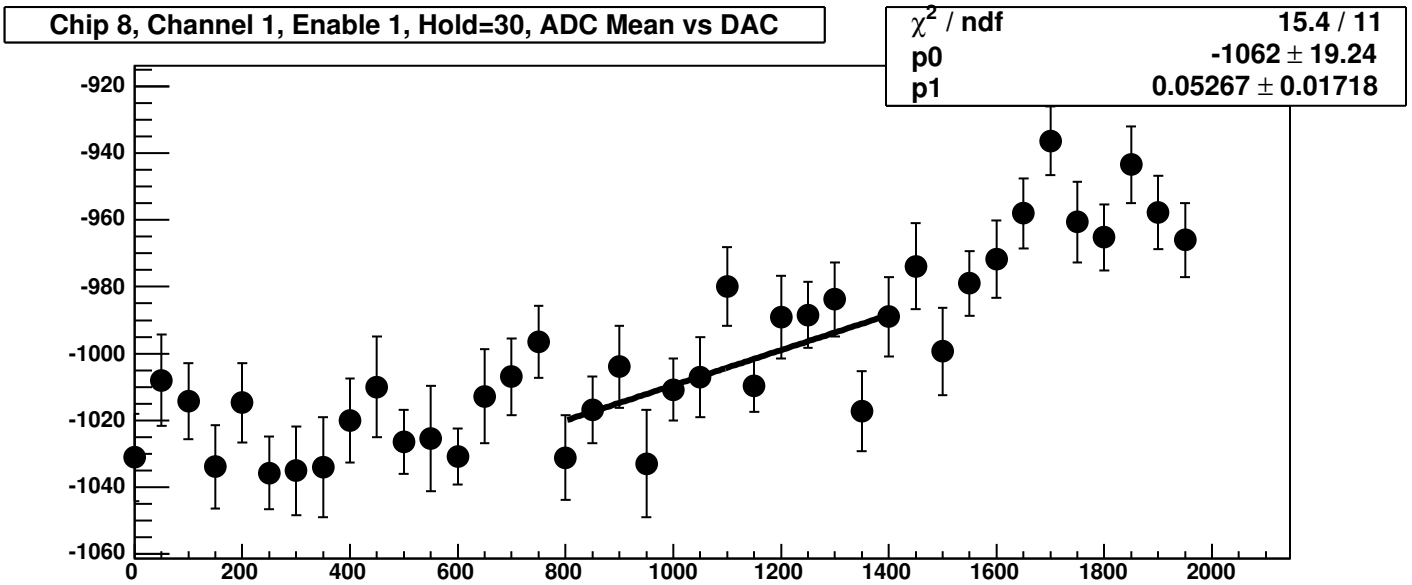


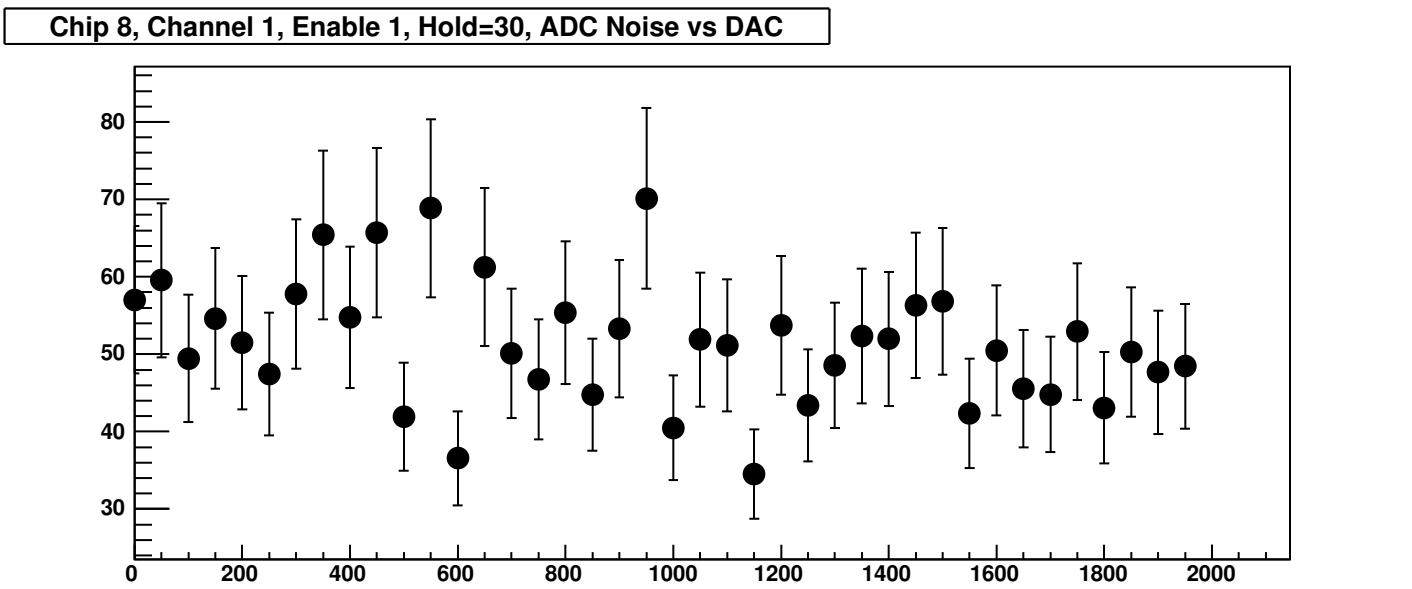
Chip 8, Channel 1, Enable 0, Hold=30, ADC Noise vs DAC

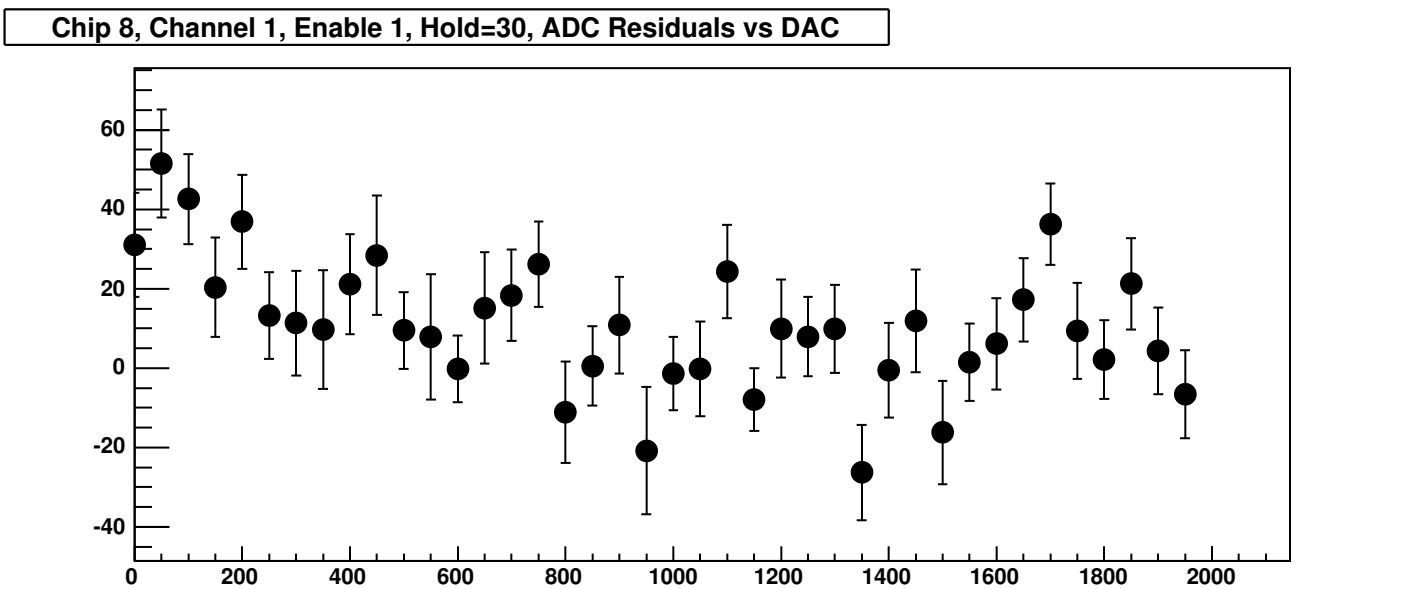


Chip 8, Channel 1, Enable 0, Hold=30, ADC Residuals vs DAC

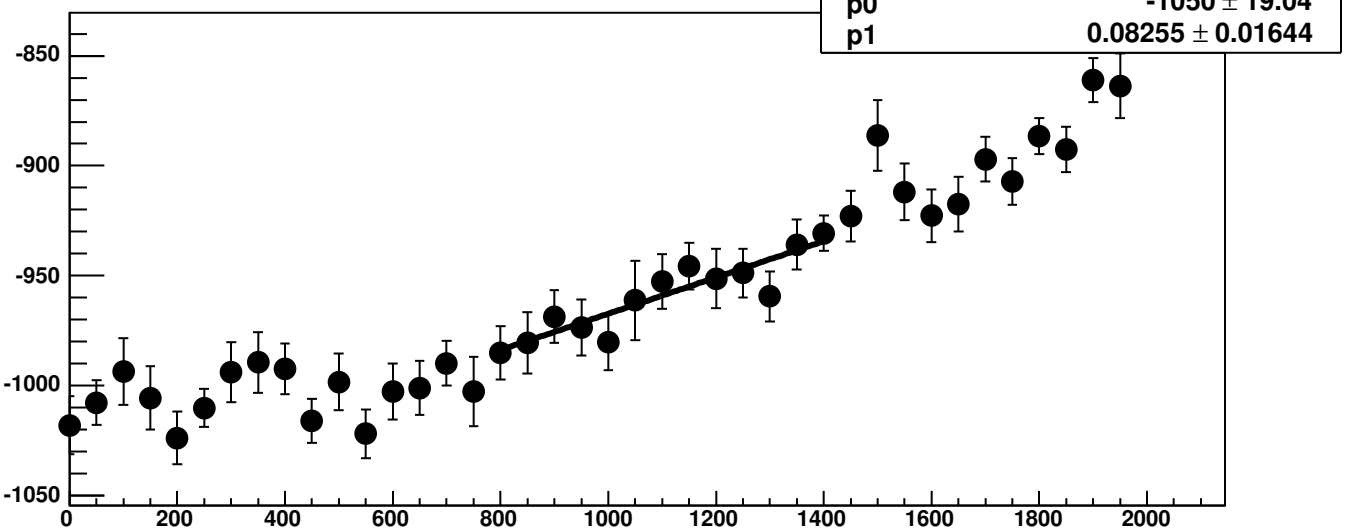



Chip 8, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 15.4 / 11
p0 -1062 ± 19.24
p1 0.05267 ± 0.01718
-920
-940
-960
-980
-1000
-1020
-1040
-1060
0
200
400
600
800
1000
1200
1400
1600
1800
2000

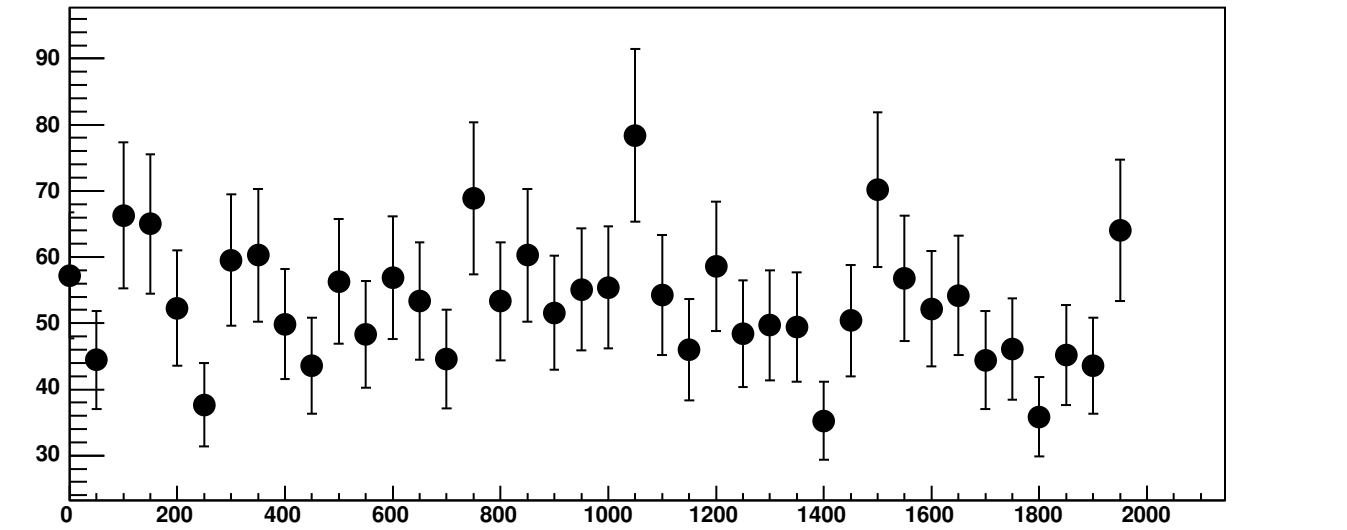

Chip 8, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

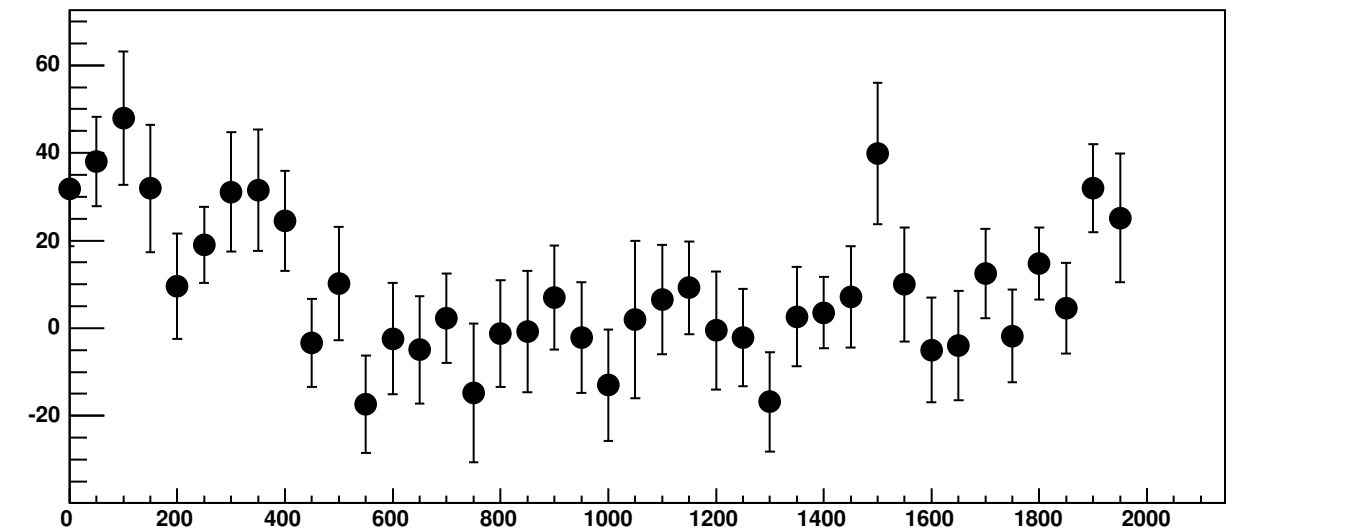
Chip 8, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC

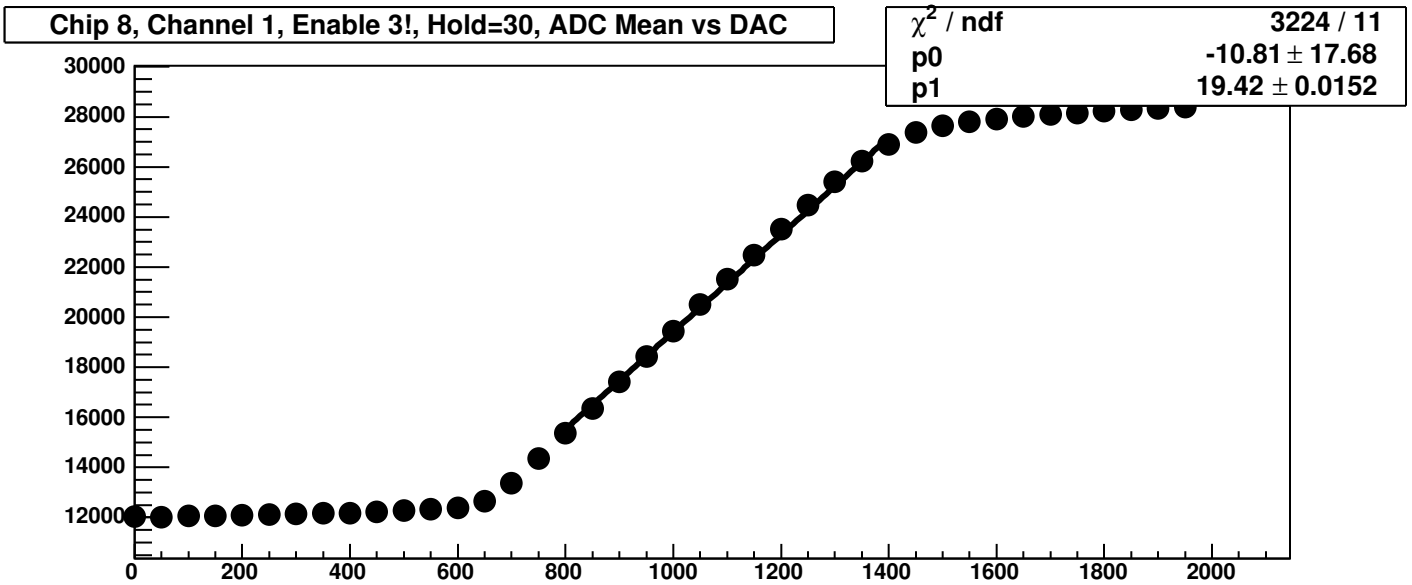


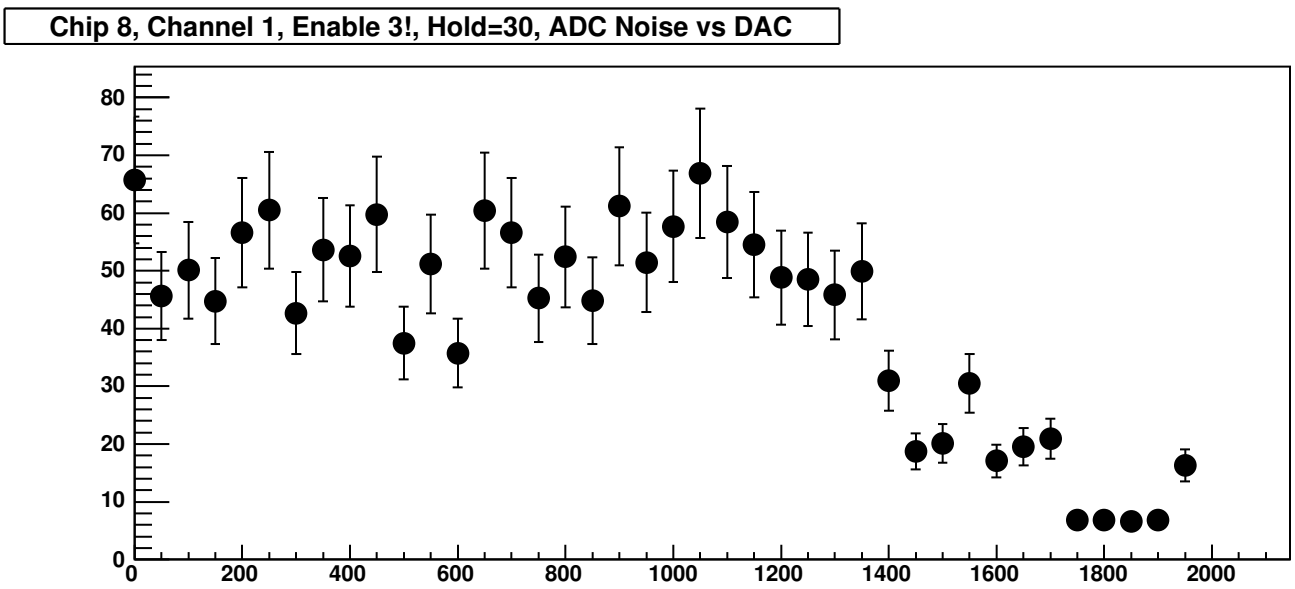
Chip 8, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC

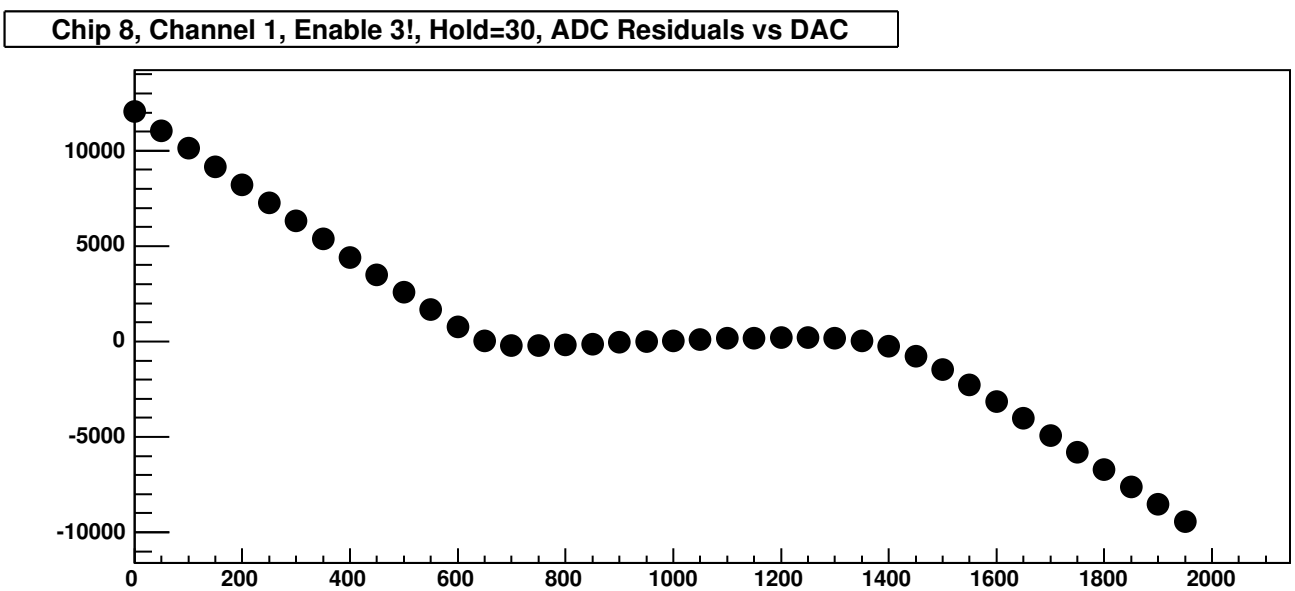


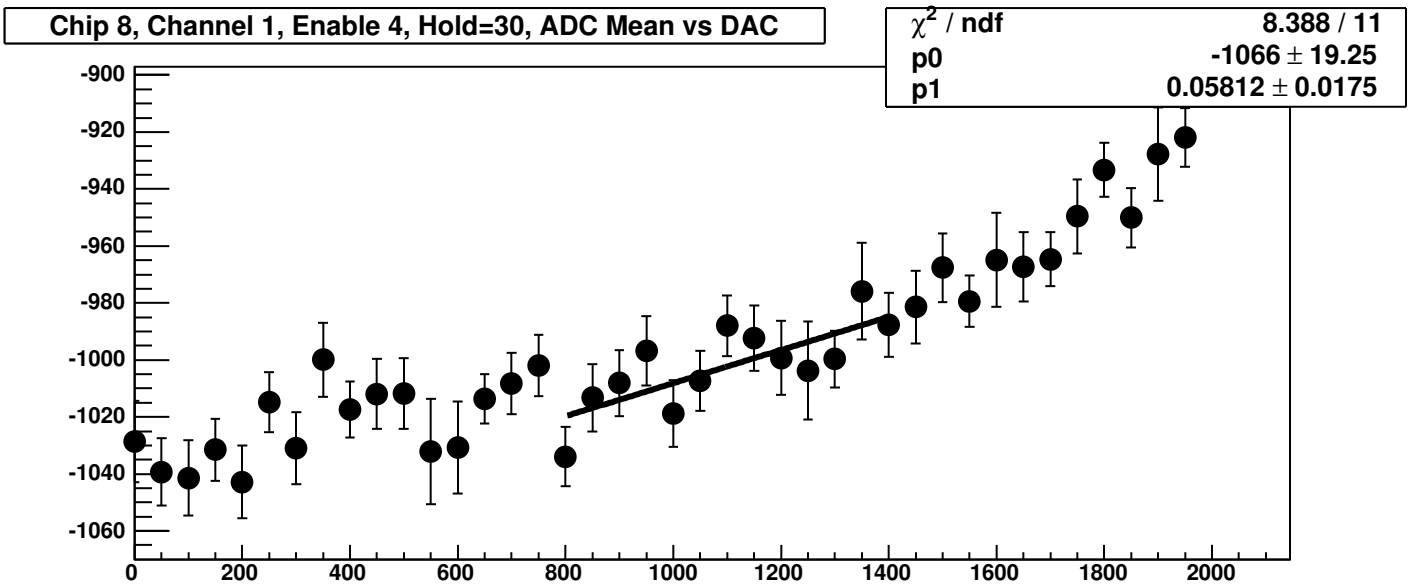
Chip 8, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC

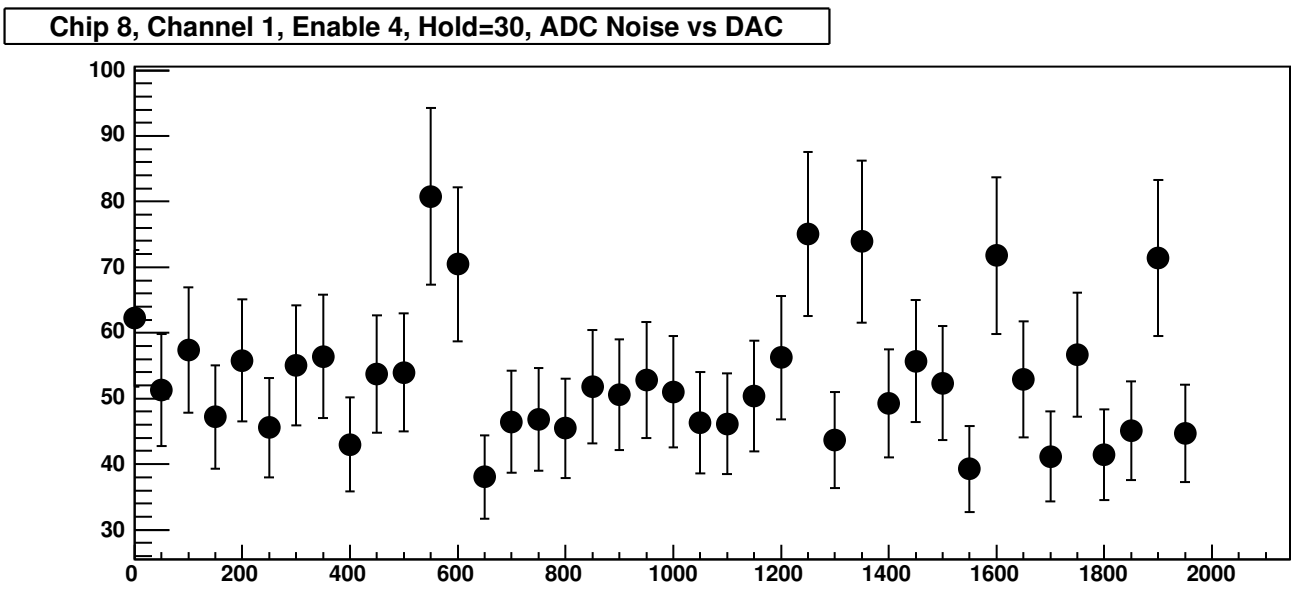


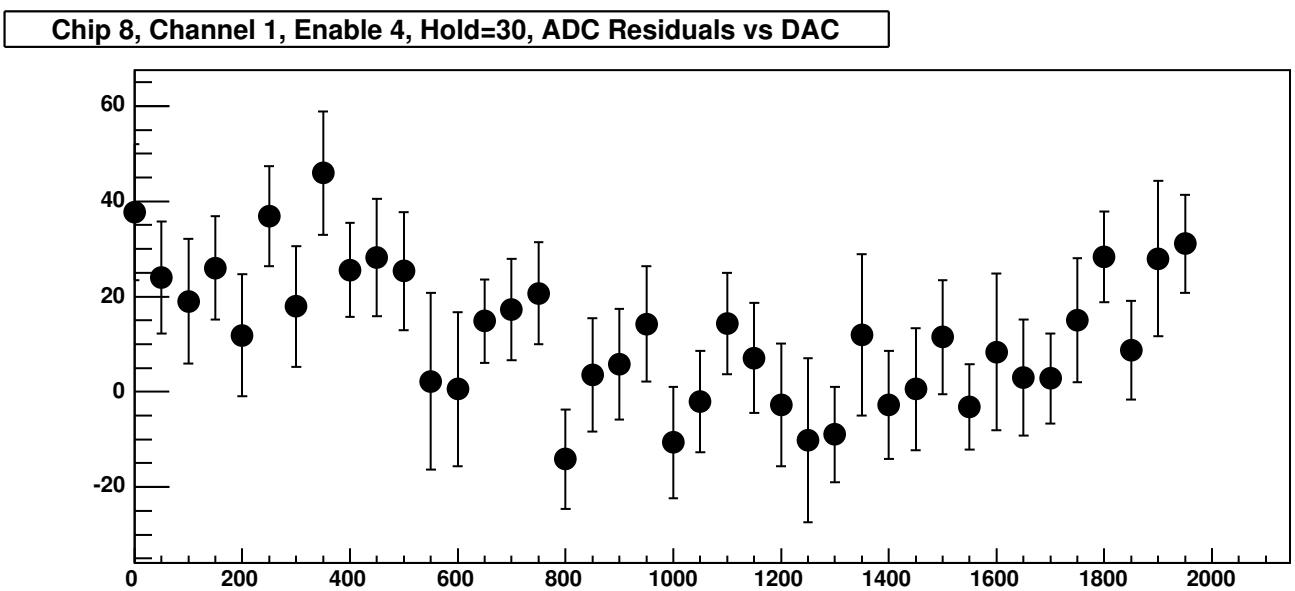

Chip 8, Channel 1, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 3224 / 11
p0 -10.81 ± 17.68
p1 19.42 ± 0.0152
30000
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

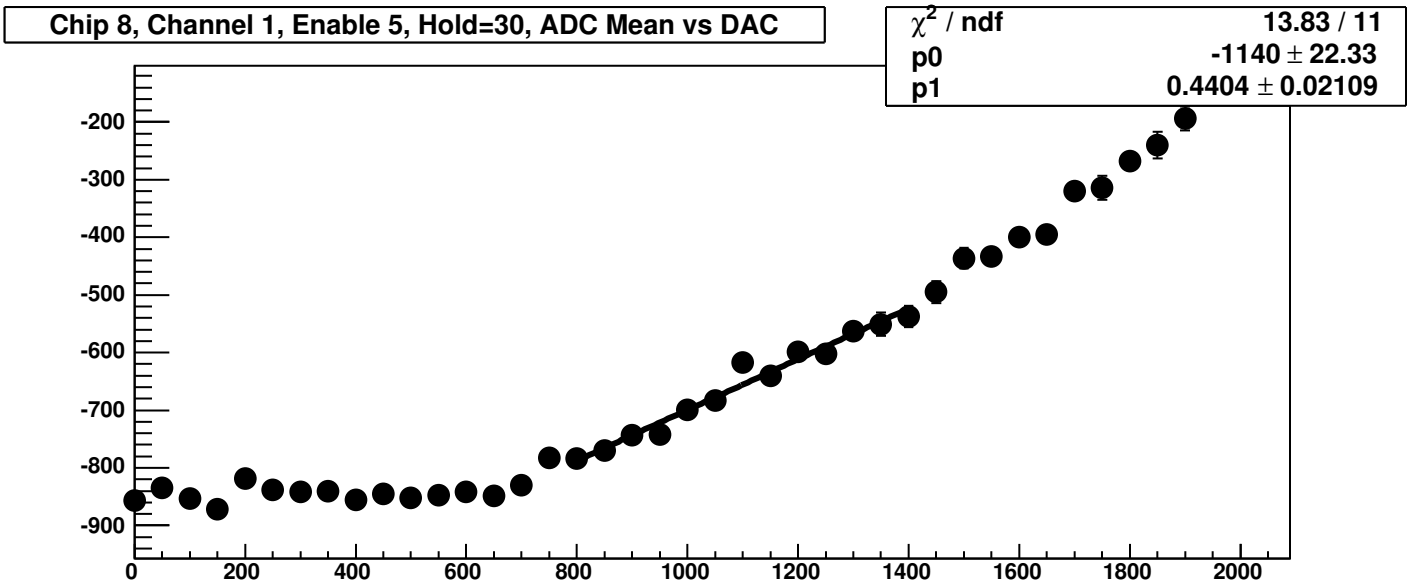

Chip 8, Channel 1, Enable 3!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

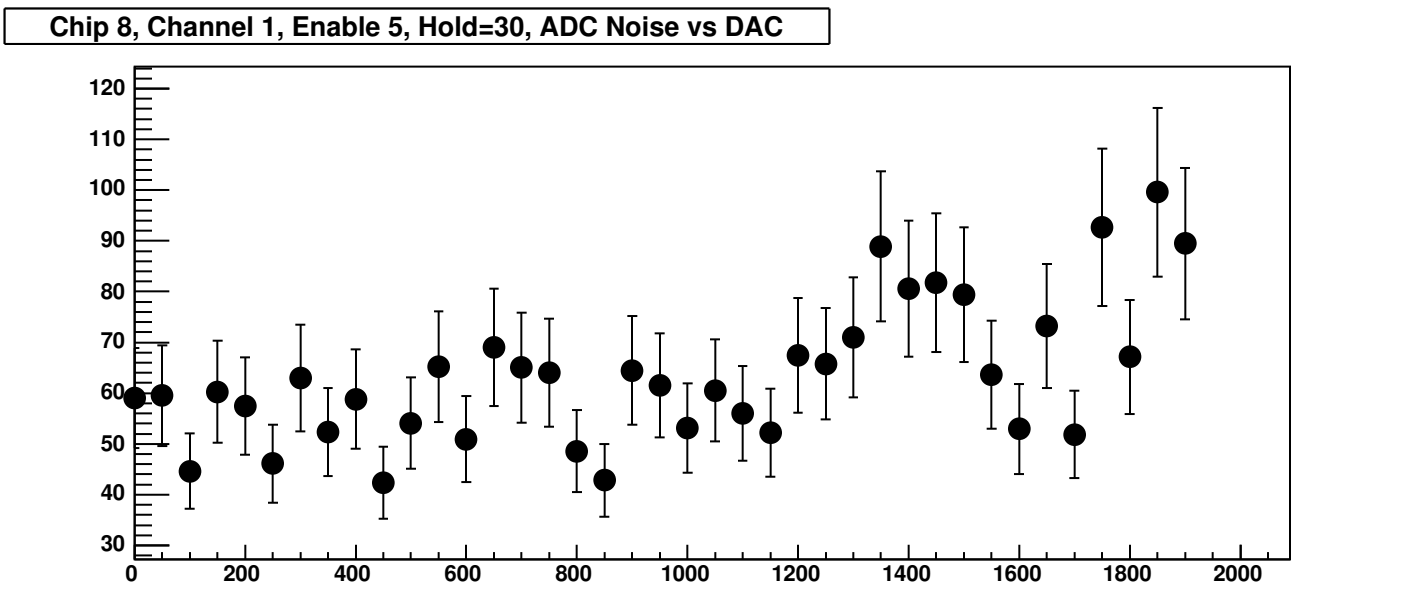

Chip 8, Channel 1, Enable 3!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

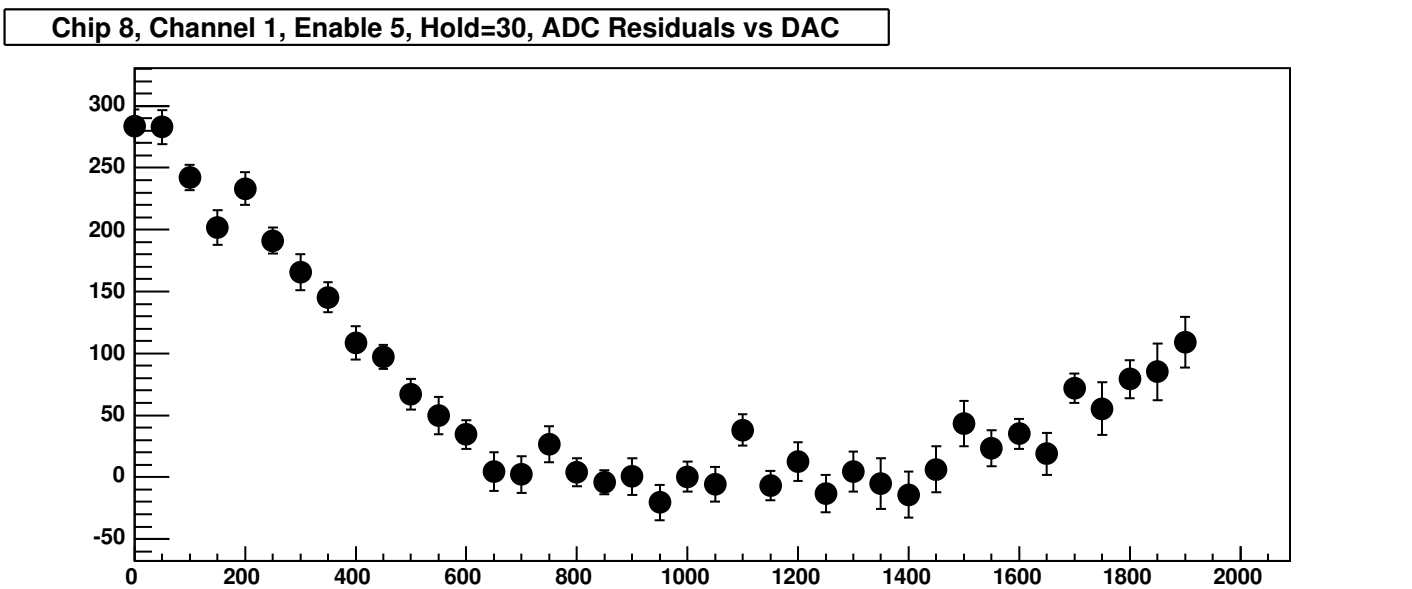

Chip 8, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 8.388 / 11
p0 -1066 ± 19.25
p1 0.05812 ± 0.0175
-900
-920
-940
-960
-980
-1000
-1020
-1040
-1060
0
200
400
600
800
1000
1200
1400
1600
1800
2000

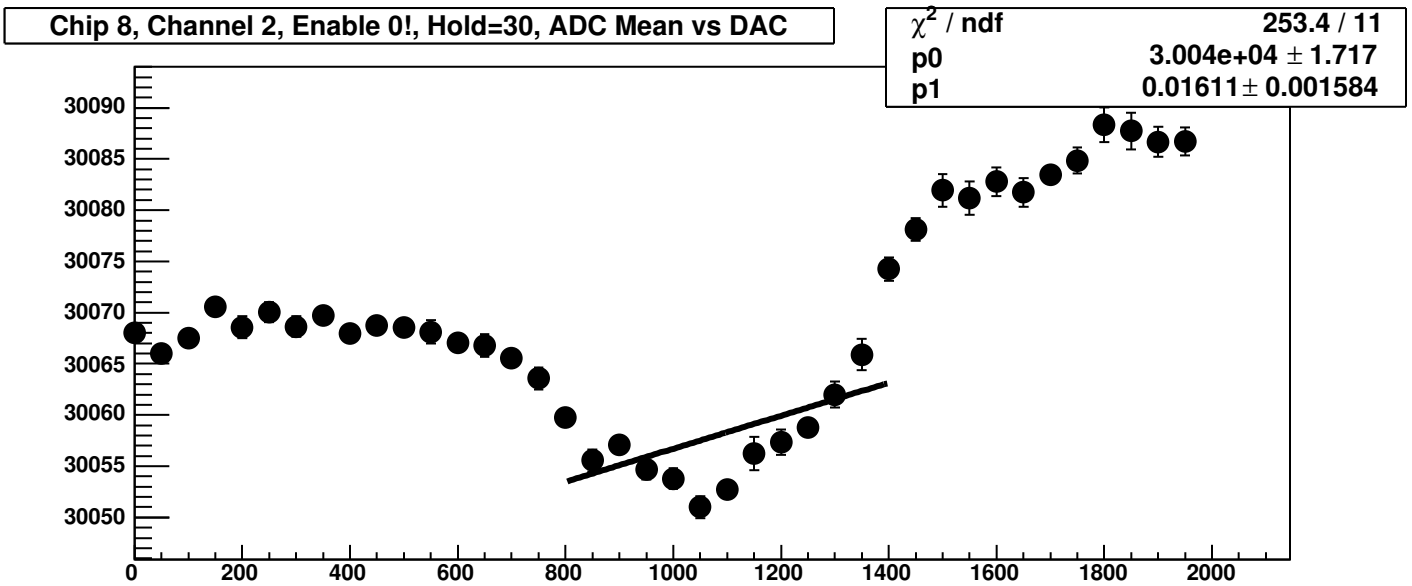

Chip 8, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

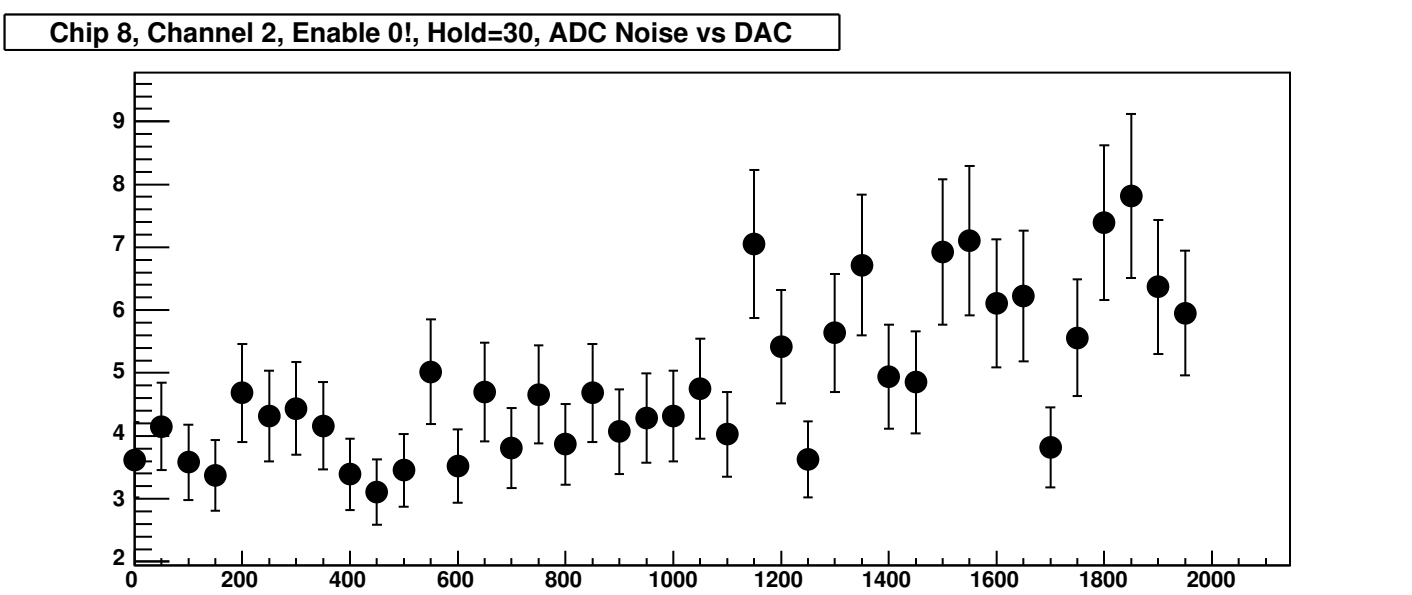

Chip 8, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

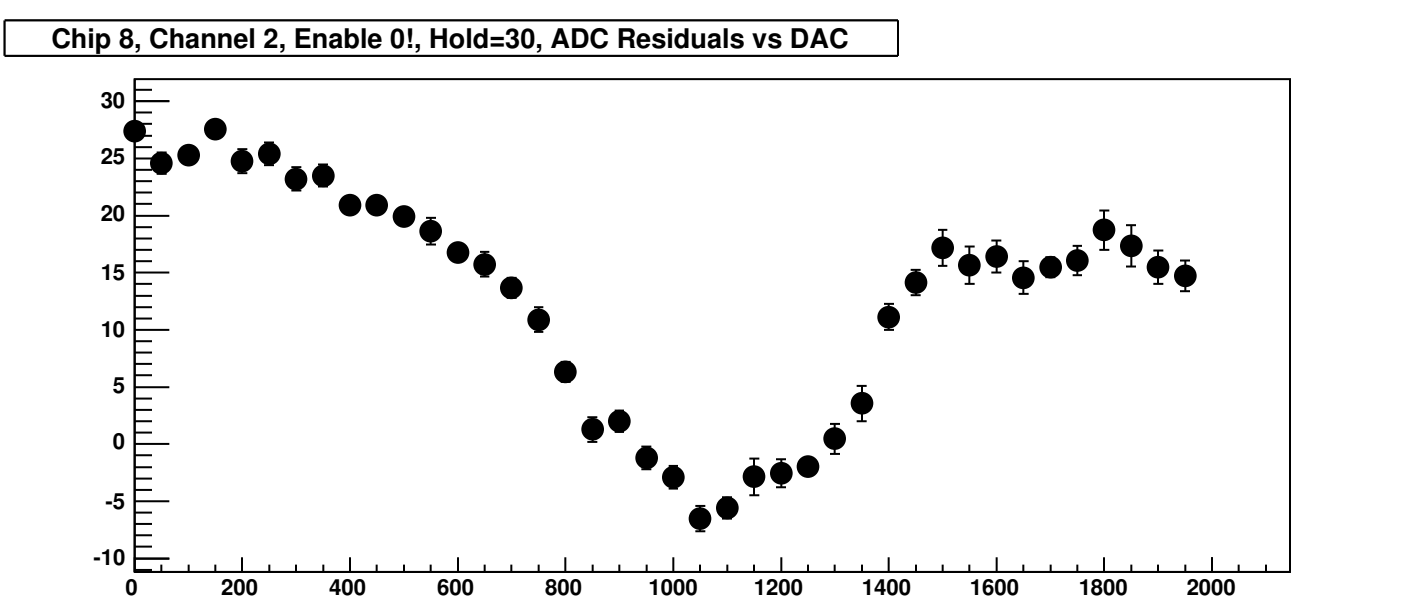

Chip 8, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 13.83 / 11
p0 -1140 ± 22.33
p1 0.4404 ± 0.02109
-200
-300
-400
-500
-600
-700
-800
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

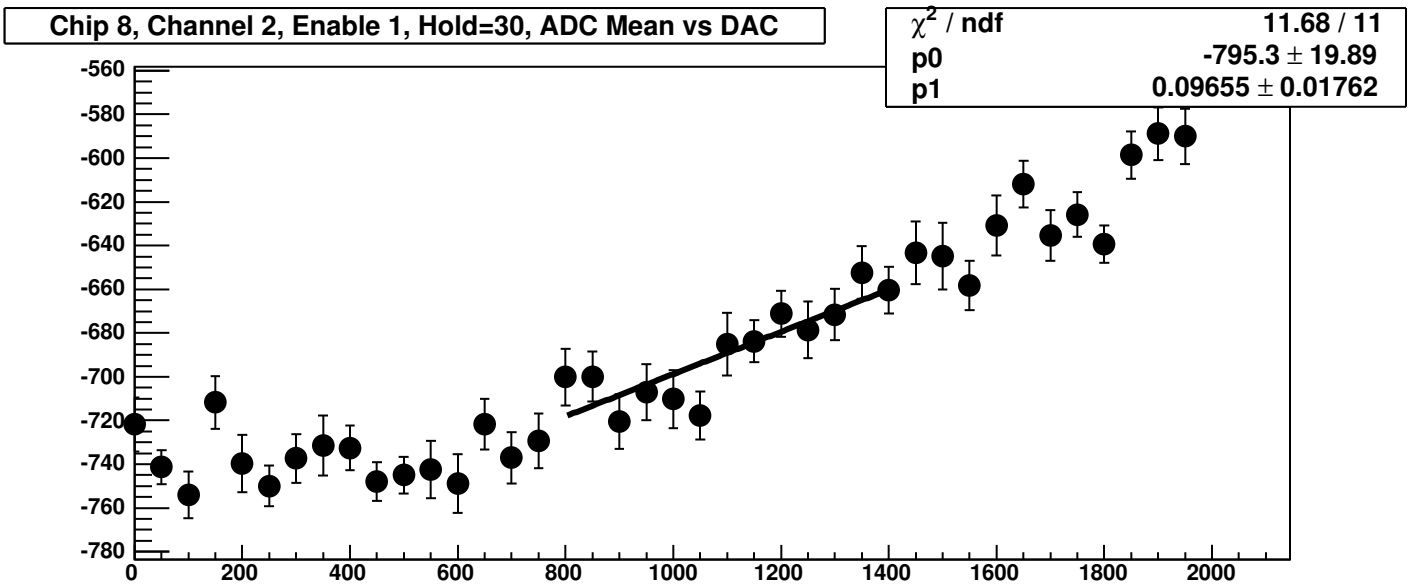

Chip 8, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
120
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

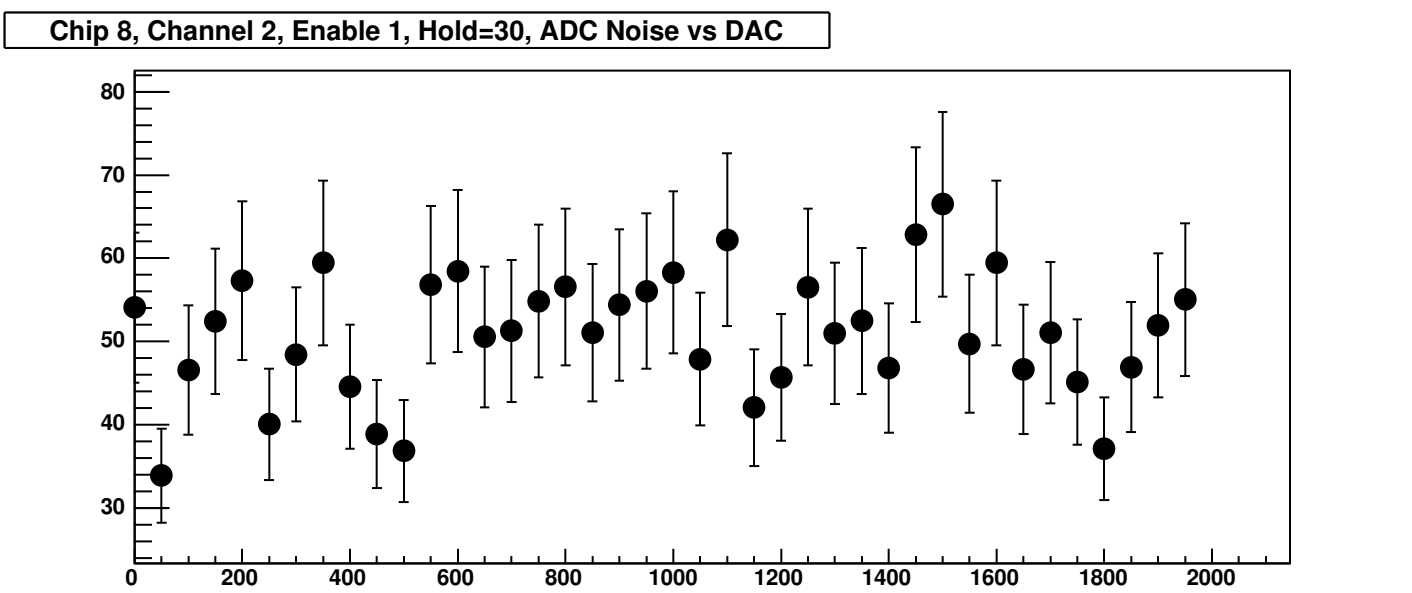

Chip 8, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

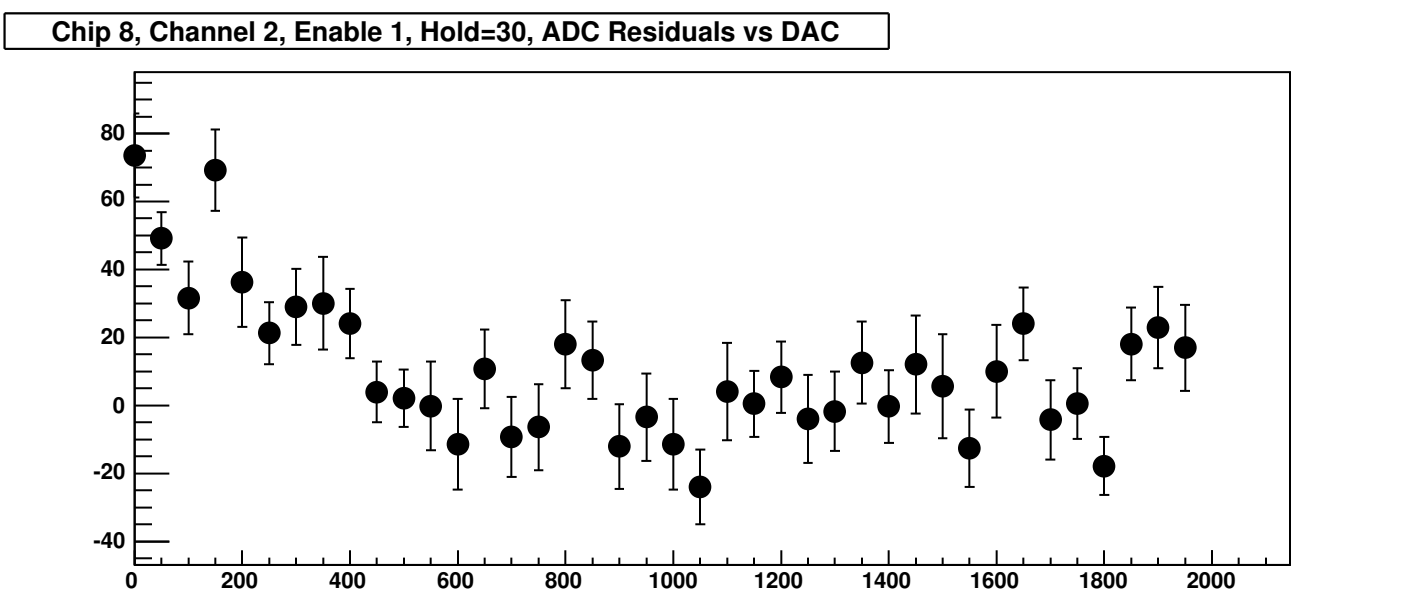

Chip 8, Channel 2, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 253.4 / 11
p0 3.004e+04 ± 1.717
p1 0.01611 ± 0.001584
30090
30085
30080
30075
30070
30065
30060
30055
30050
0
200
400
600
800
1000
1200
1400
1600
1800
2000

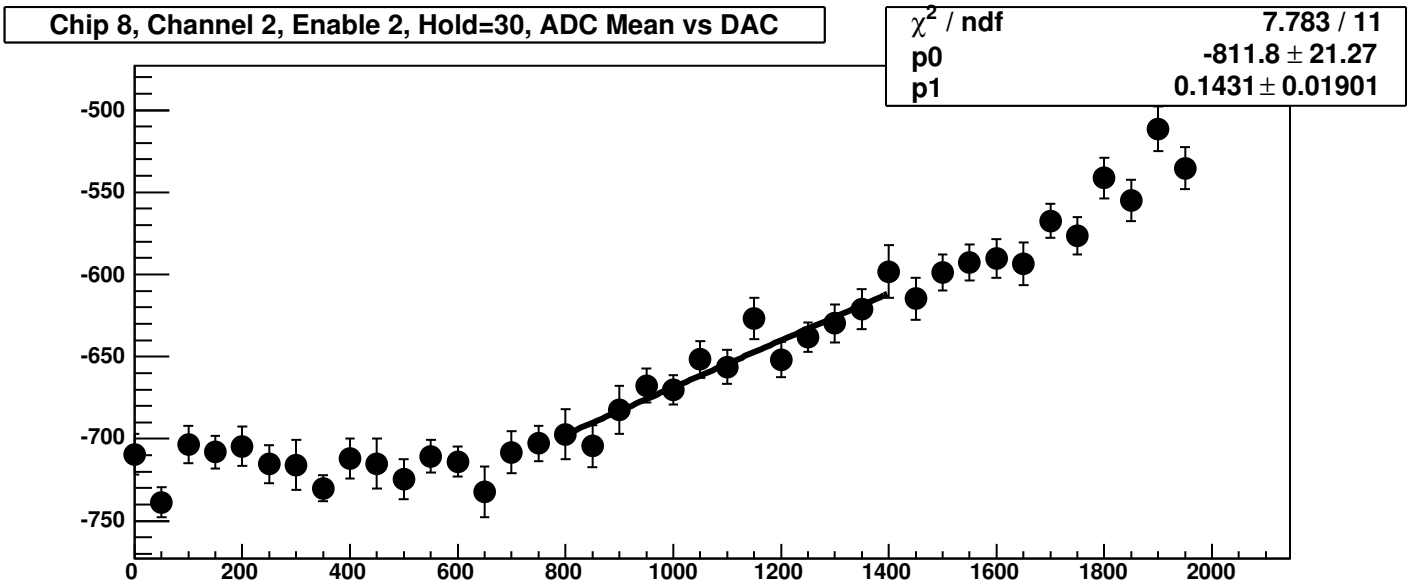

Chip 8, Channel 2, Enable 0!, Hold=30, ADC Noise vs DAC
9
8
7
6
5
4
3
2
0
200
400
600
800
1000
1200
1400
1600
1800
2000

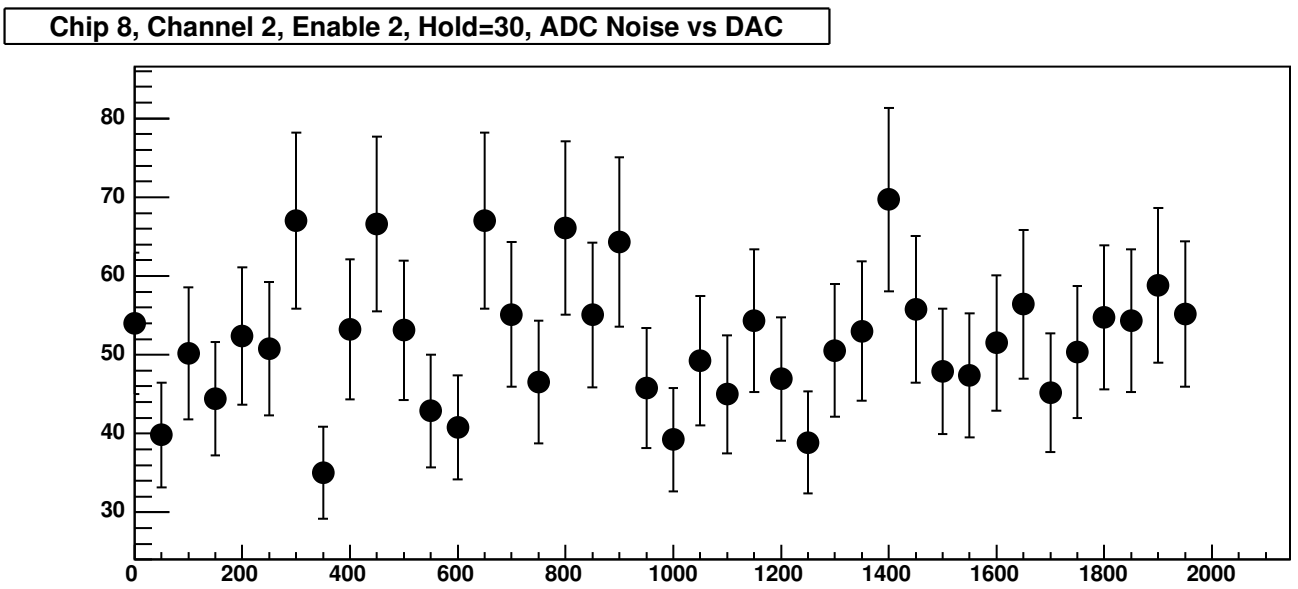

Chip 8, Channel 2, Enable 0!, Hold=30, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

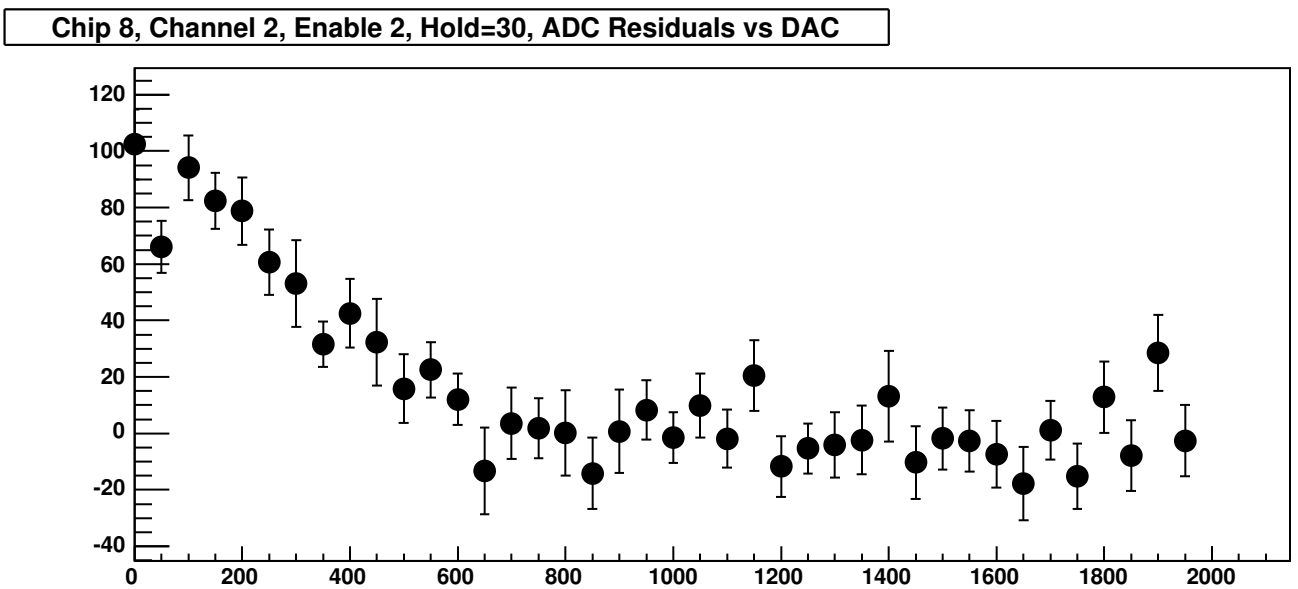

Chip 8, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 11.68 / 11
p0 -795.3 ± 19.89
p1 0.09655 ± 0.01762
-560
-580
-600
-620
-640
-660
-680
-700
-720
-740
-760
-780
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

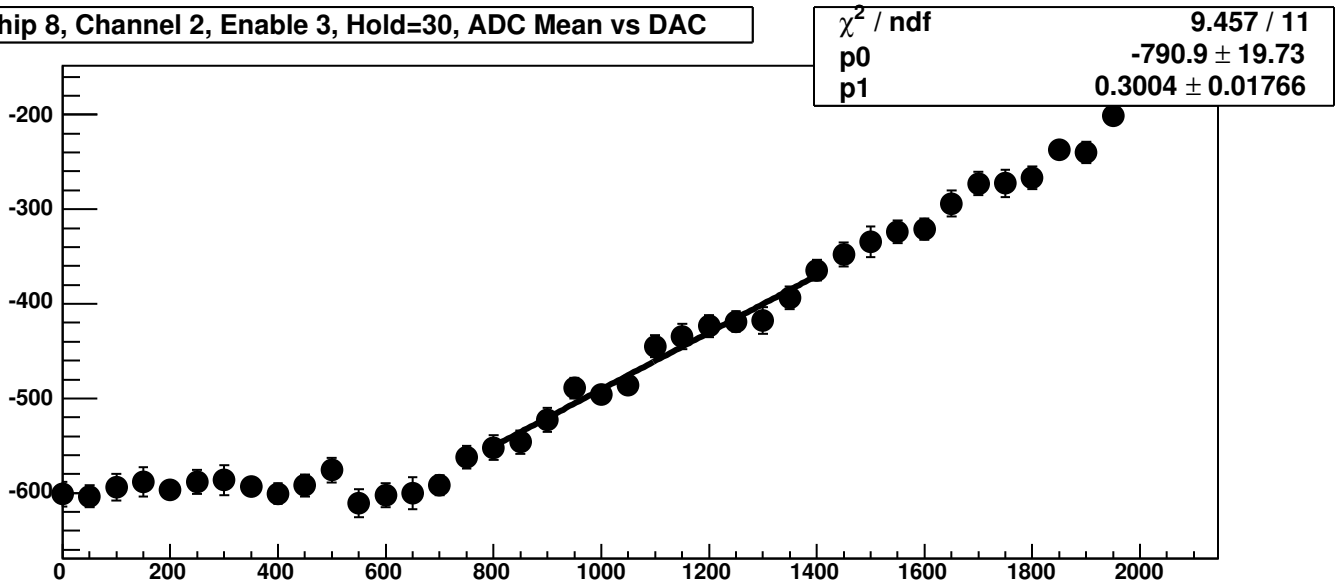

Chip 8, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 7.783 / 11
p0 -811.8 ± 21.27
p1 0.1431 ± 0.01901
-500
-550
-600
-650
-700
-750
0
200
400
600
800
1000
1200
1400
1600
1800
2000

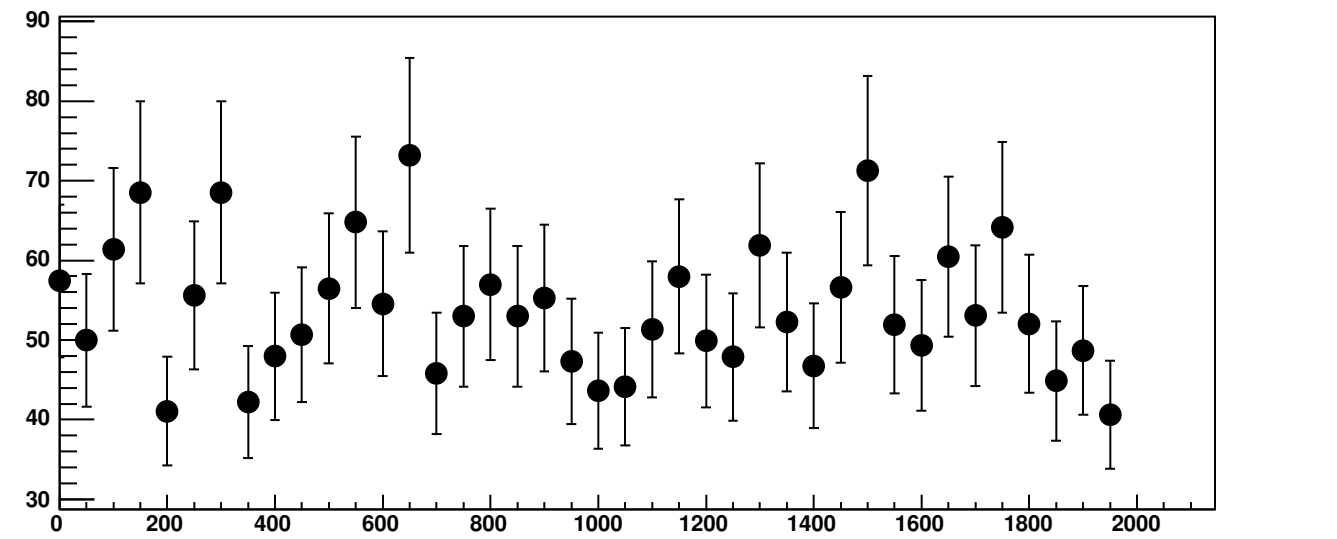

Chip 8, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

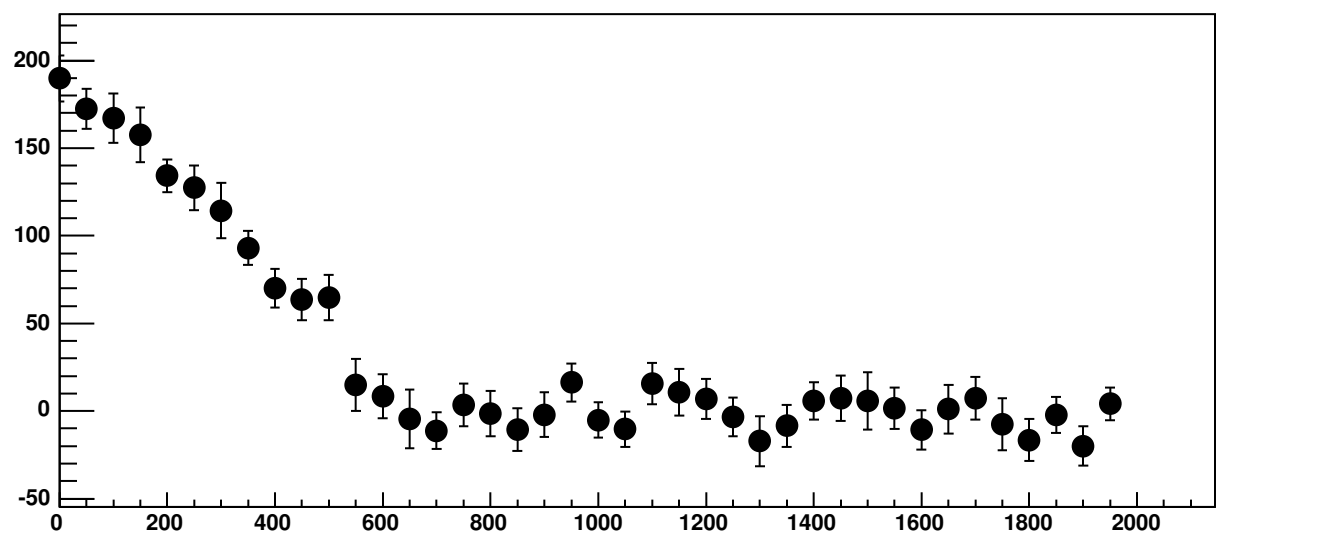
Chip 8, Channel 2, Enable 3, Hold=30, ADC Mean vs DAC

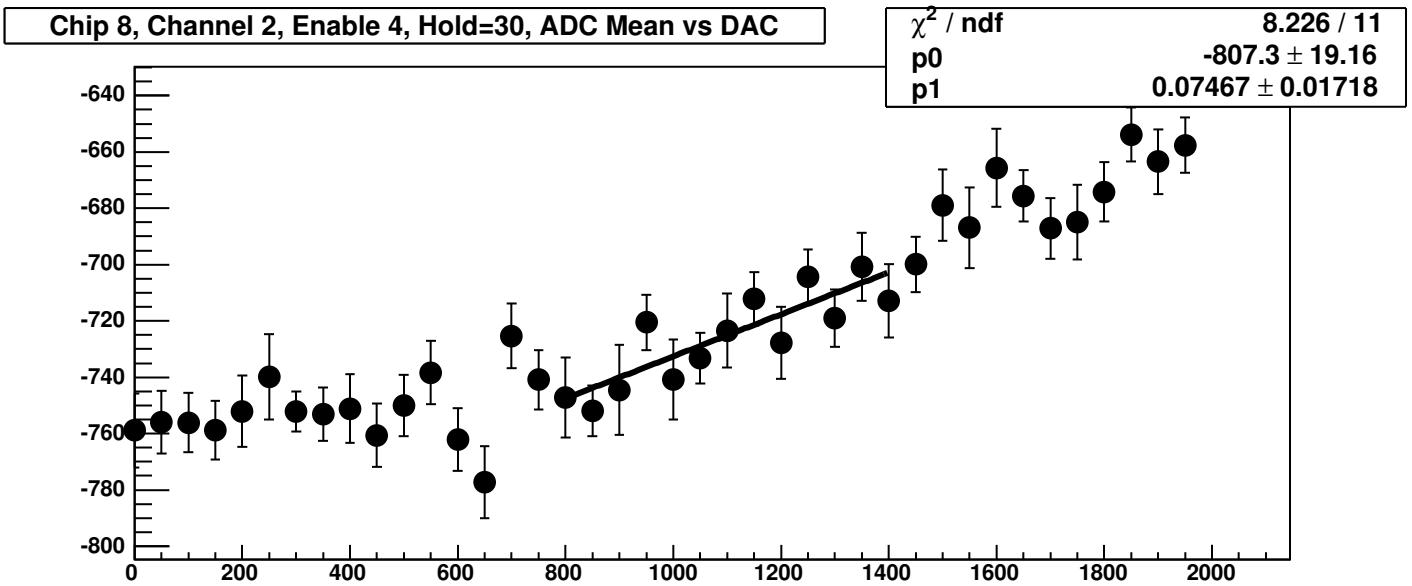


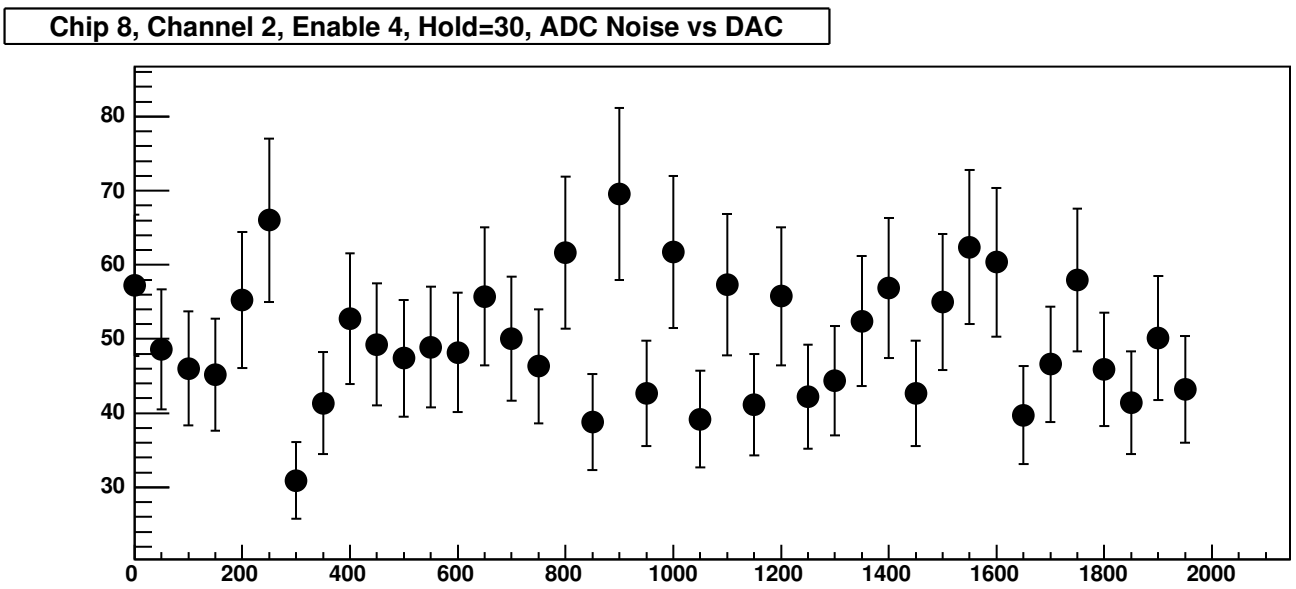
Chip 8, Channel 2, Enable 3, Hold=30, ADC Noise vs DAC

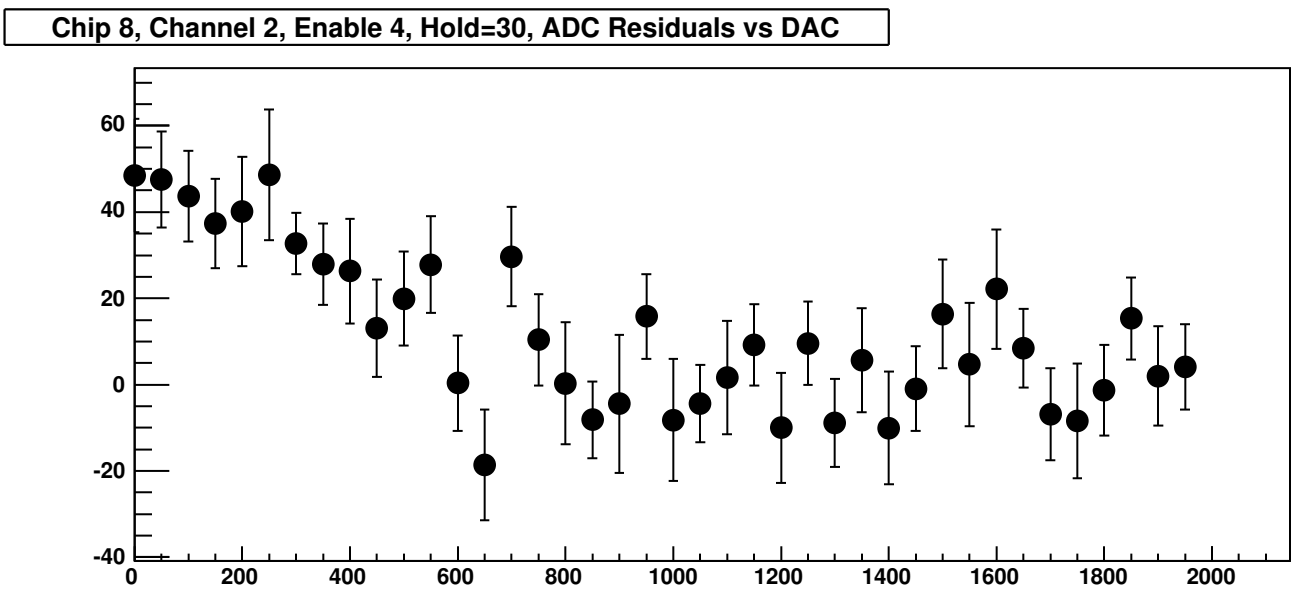


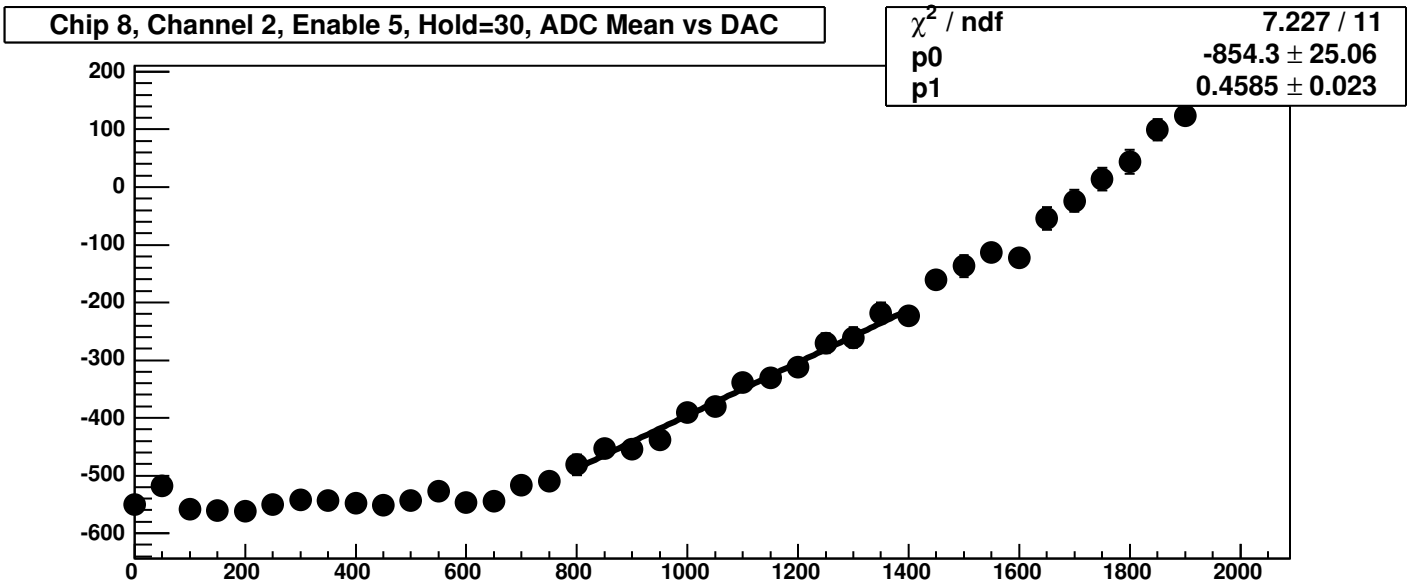
Chip 8, Channel 2, Enable 3, Hold=30, ADC Residuals vs DAC

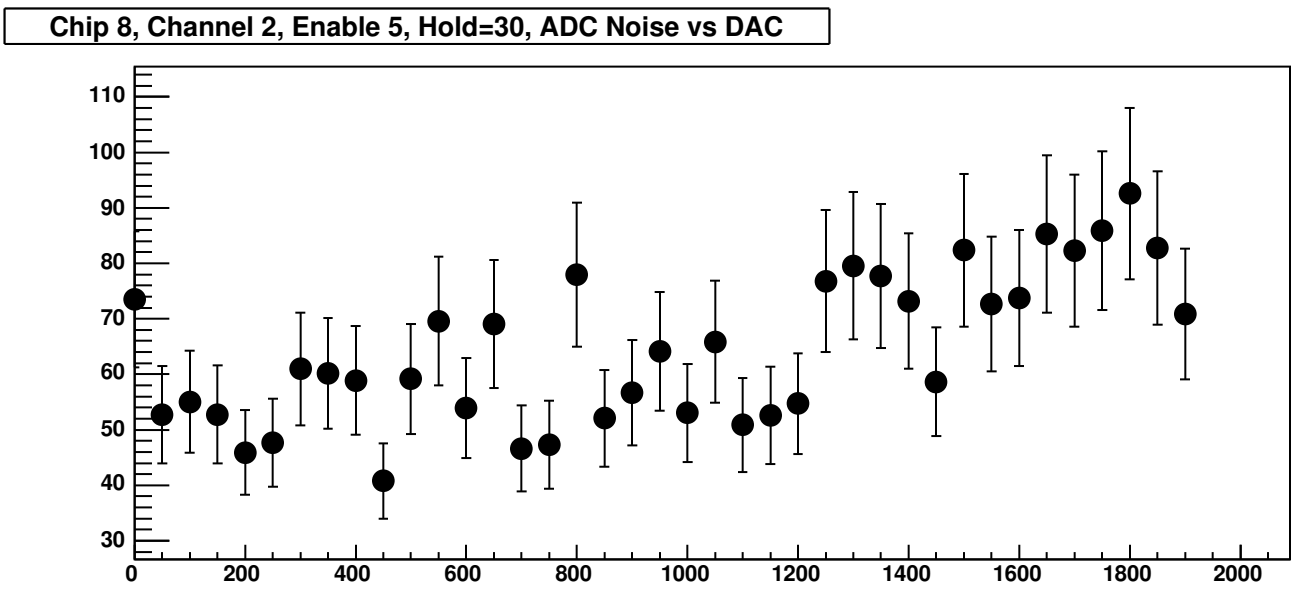


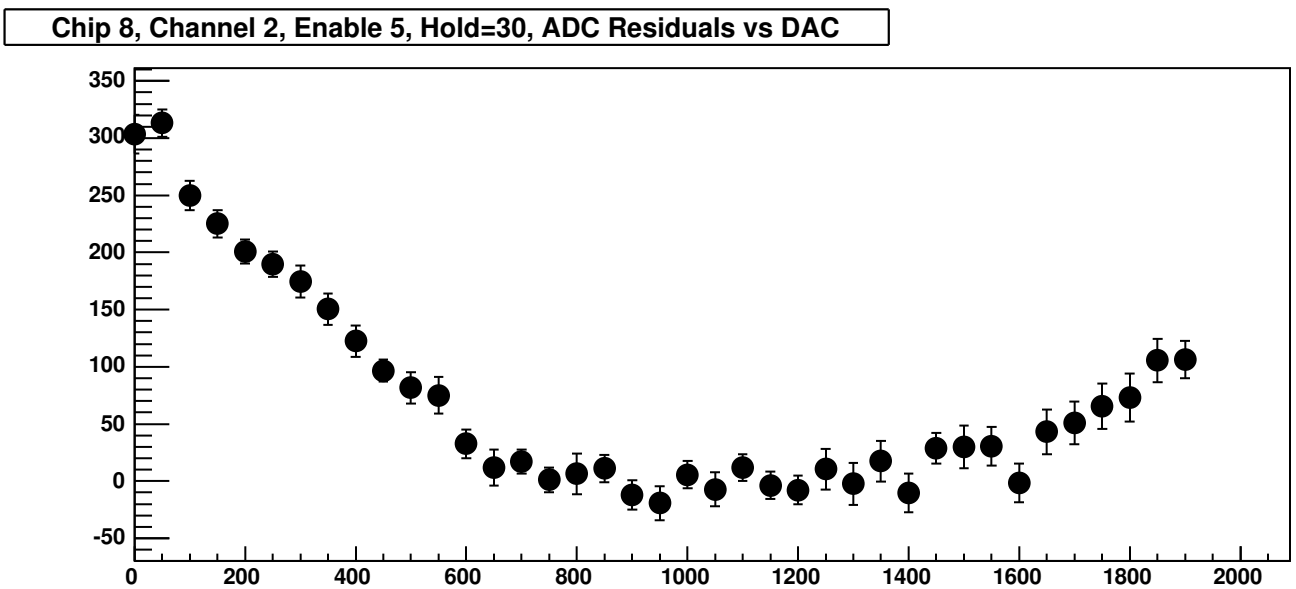

Chip 8, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 8.226 / 11
p0 -807.3 ± 19.16
p1 0.07467 ± 0.01718
-640
-660
-680
-700
-720
-740
-760
-780
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

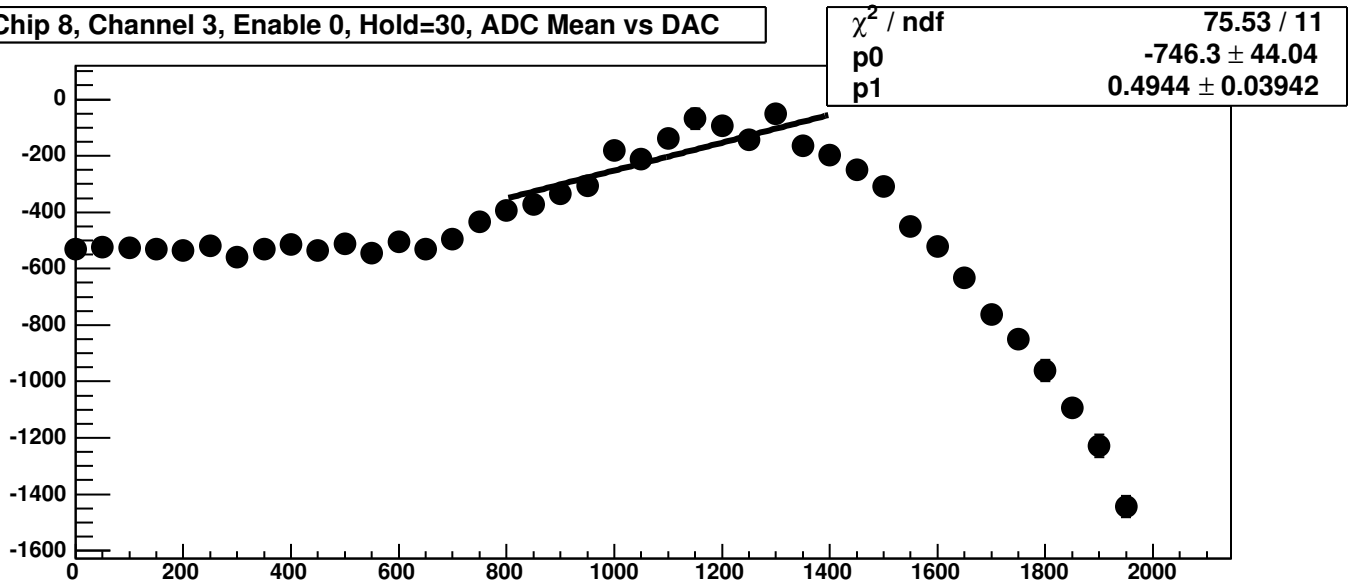

Chip 8, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7.227 / 11
p0 -854.3 ± 25.06
p1 0.4585 ± 0.023
200
100
0
-100
-200
-300
-400
-500
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

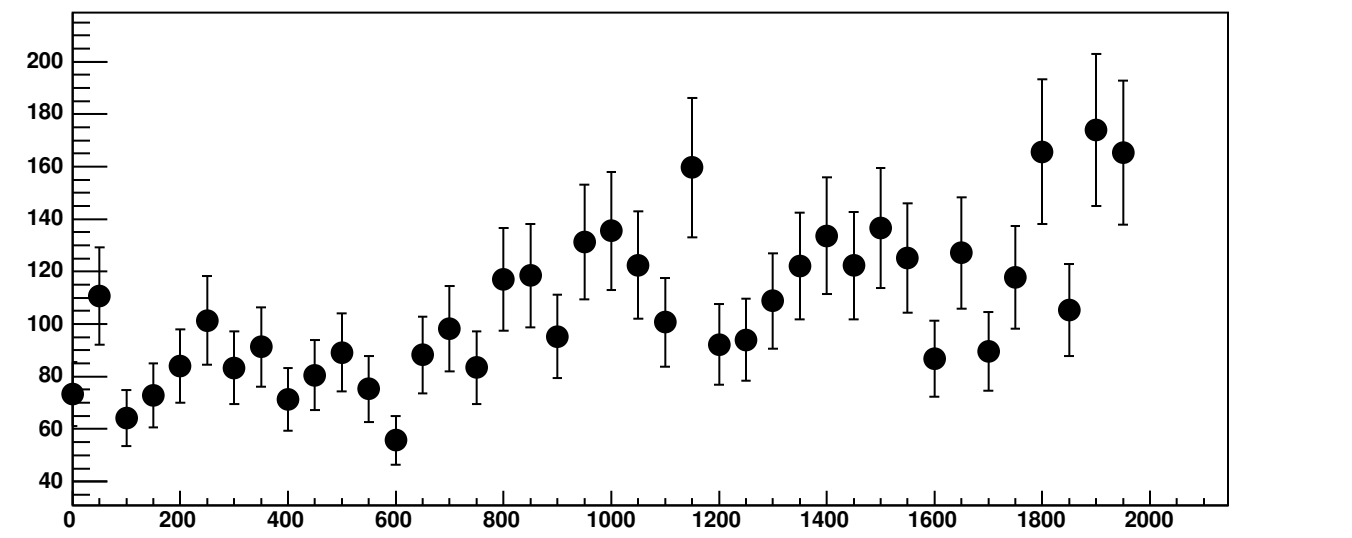

Chip 8, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

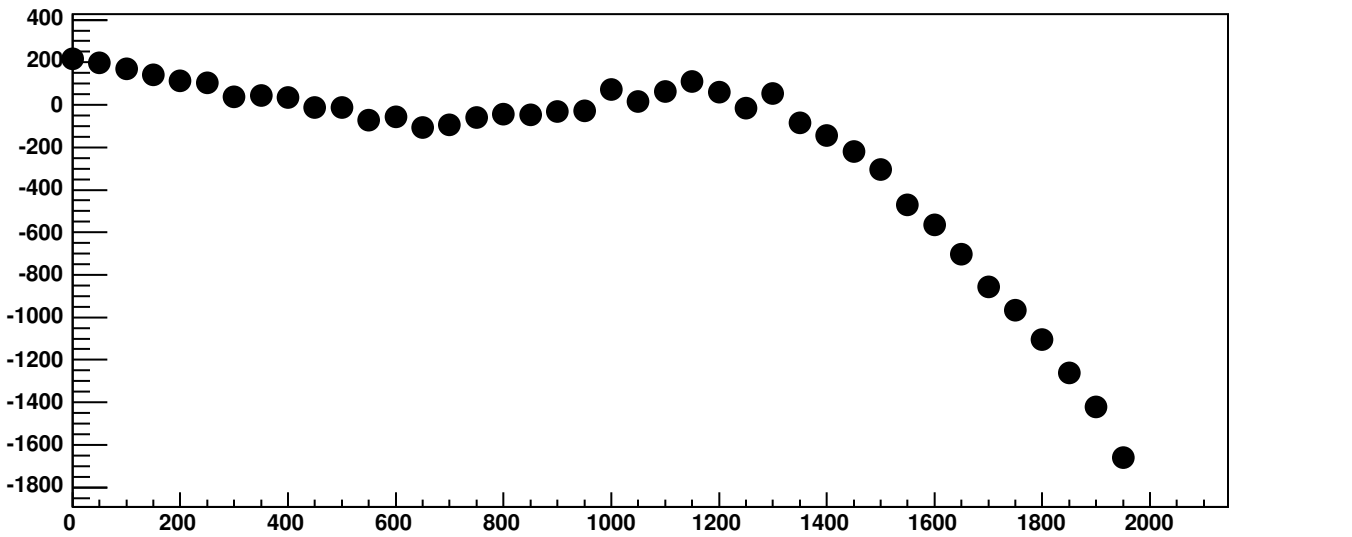
Chip 8, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC

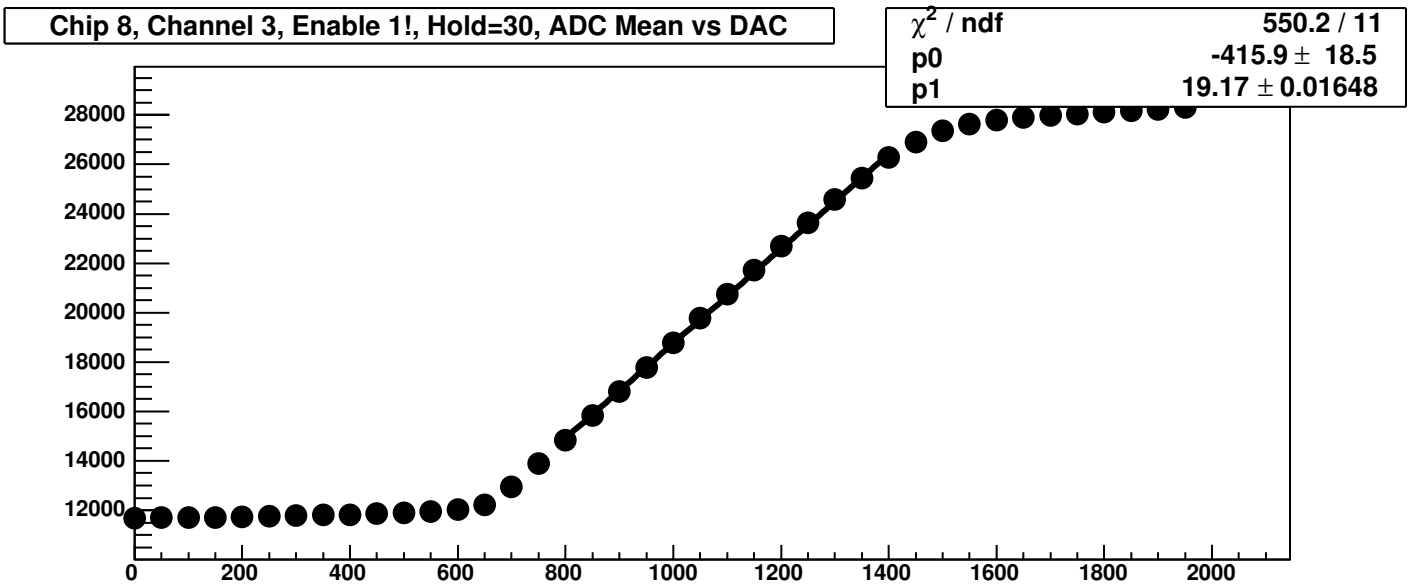


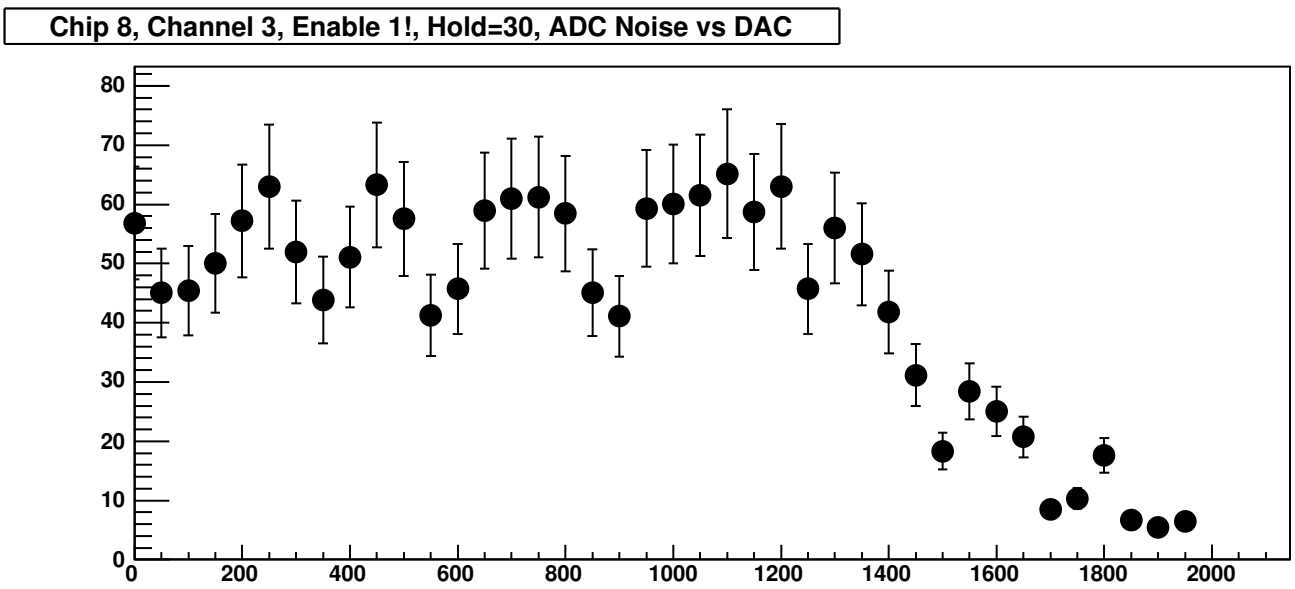
Chip 8, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC

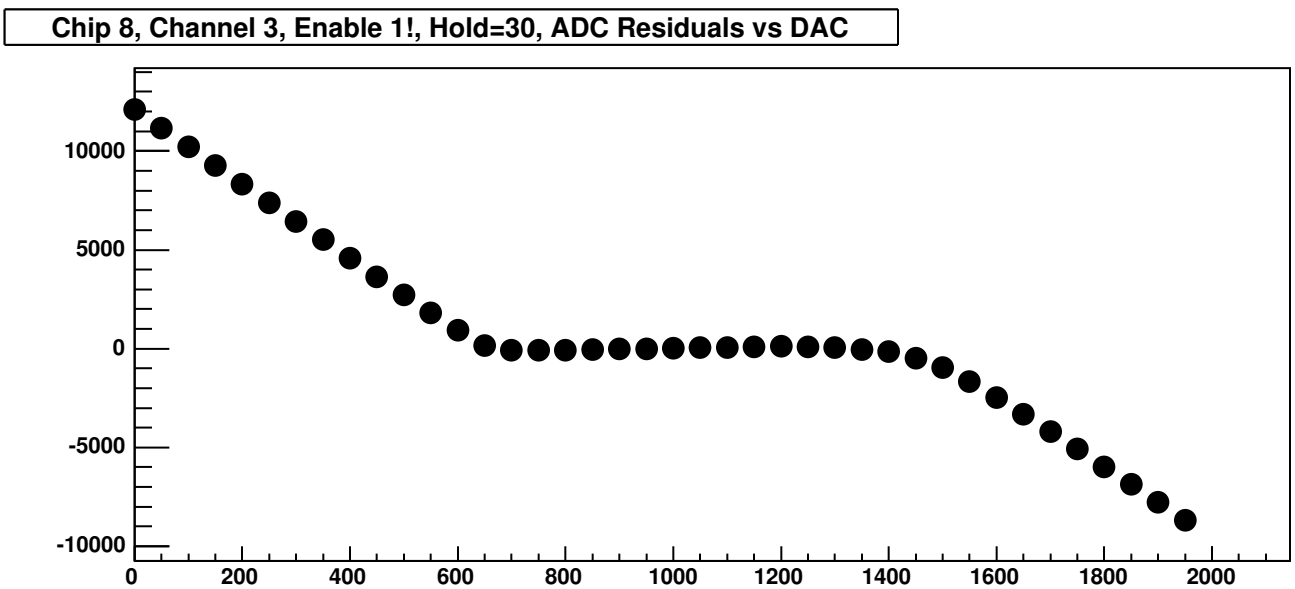


Chip 8, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC

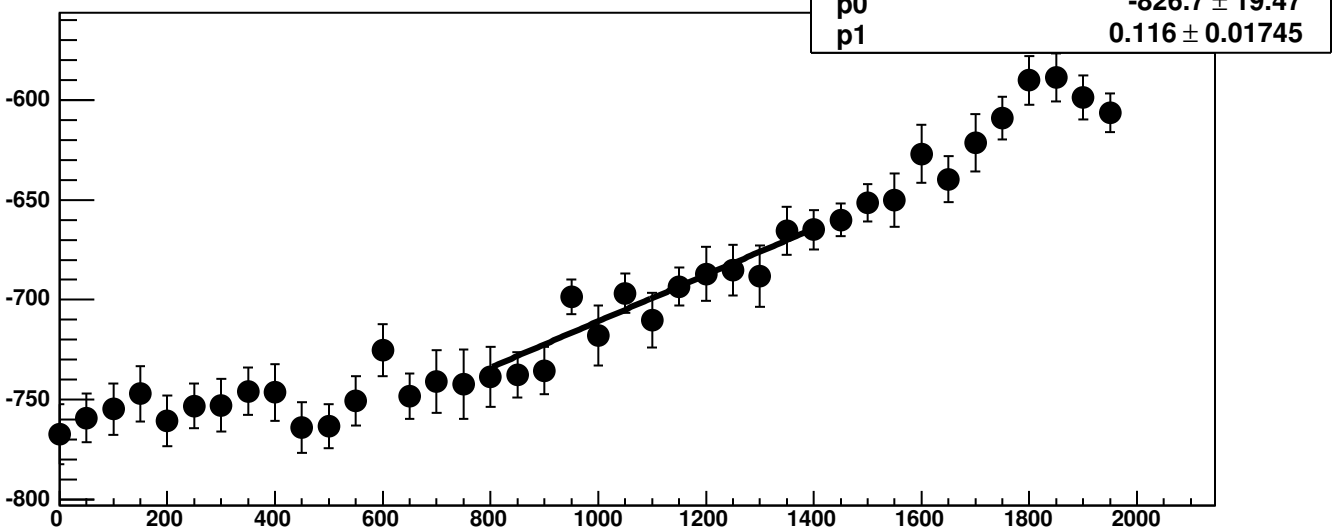



Chip 8, Channel 3, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 550.2 / 11
p0 -415.9 ± 18.5
p1 19.17 ± 0.01648
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

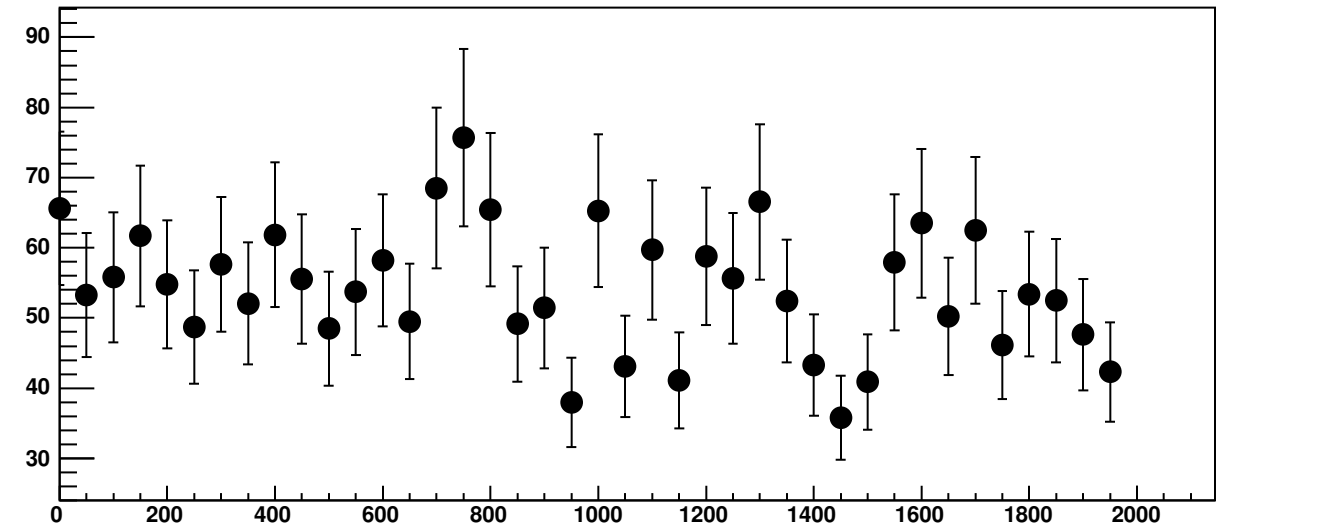

Chip 8, Channel 3, Enable 1!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 3, Enable 1!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

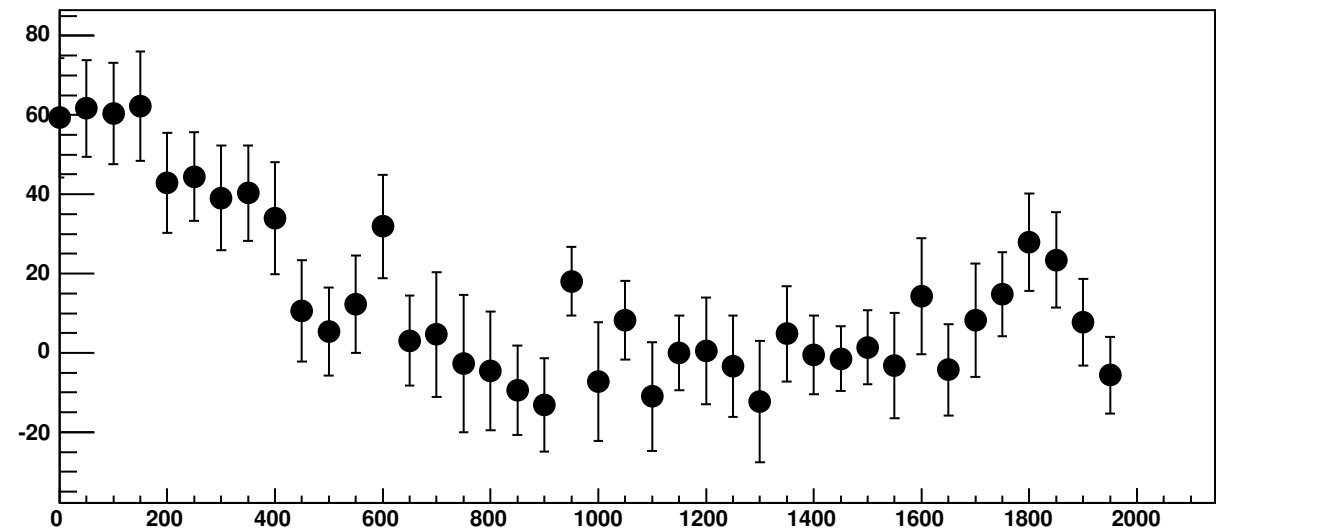
Chip 8, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC

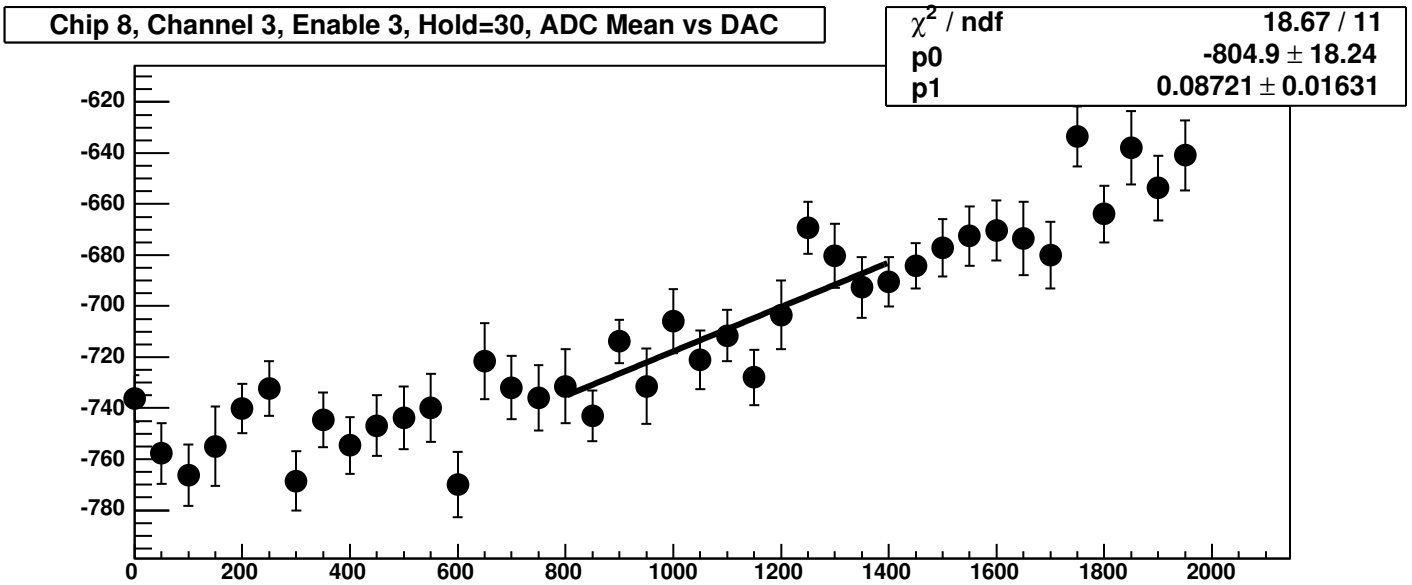


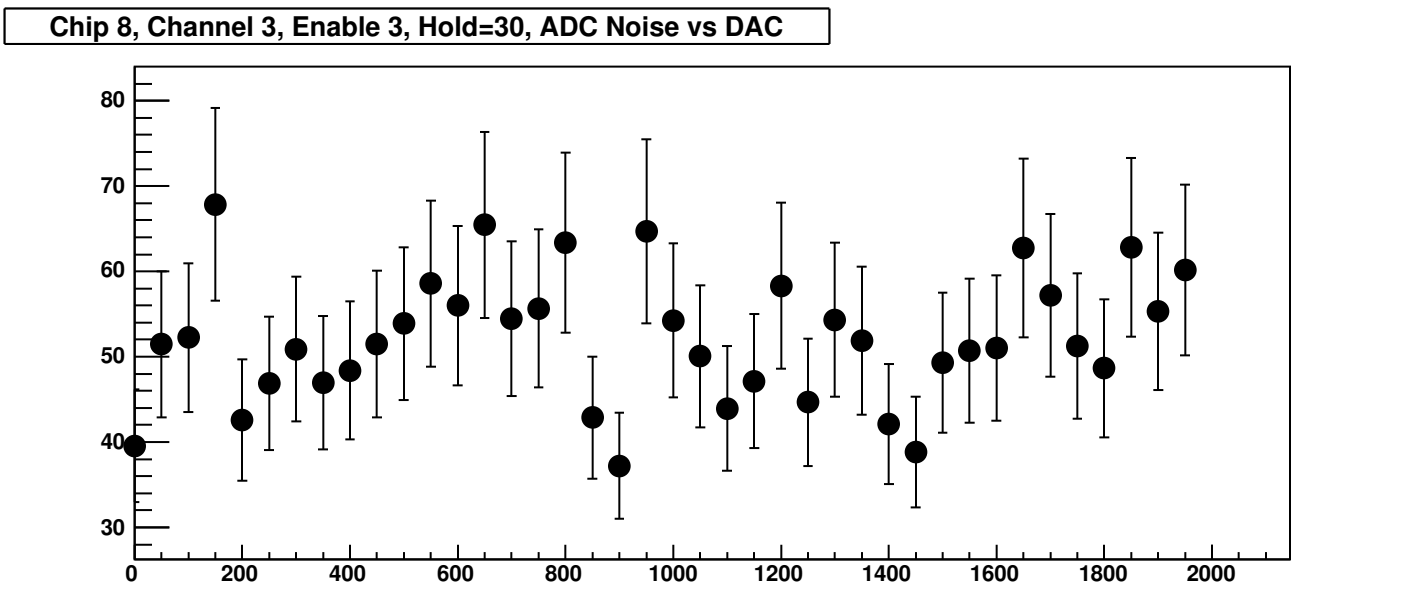
Chip 8, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC

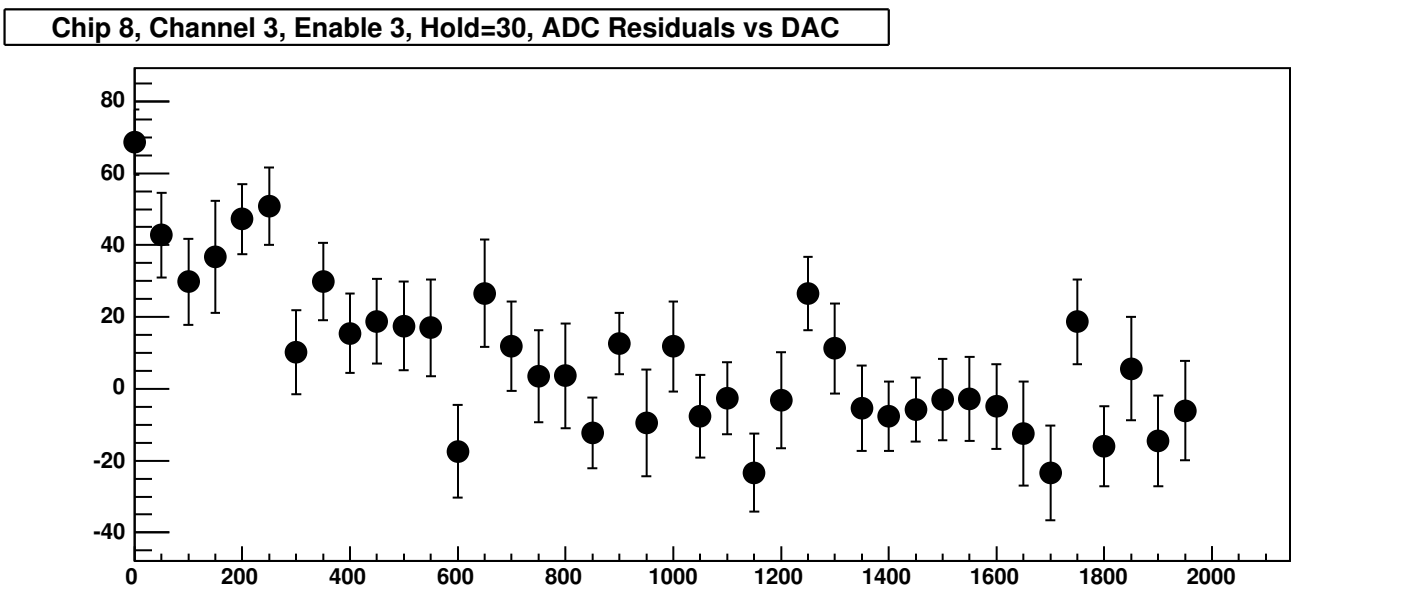


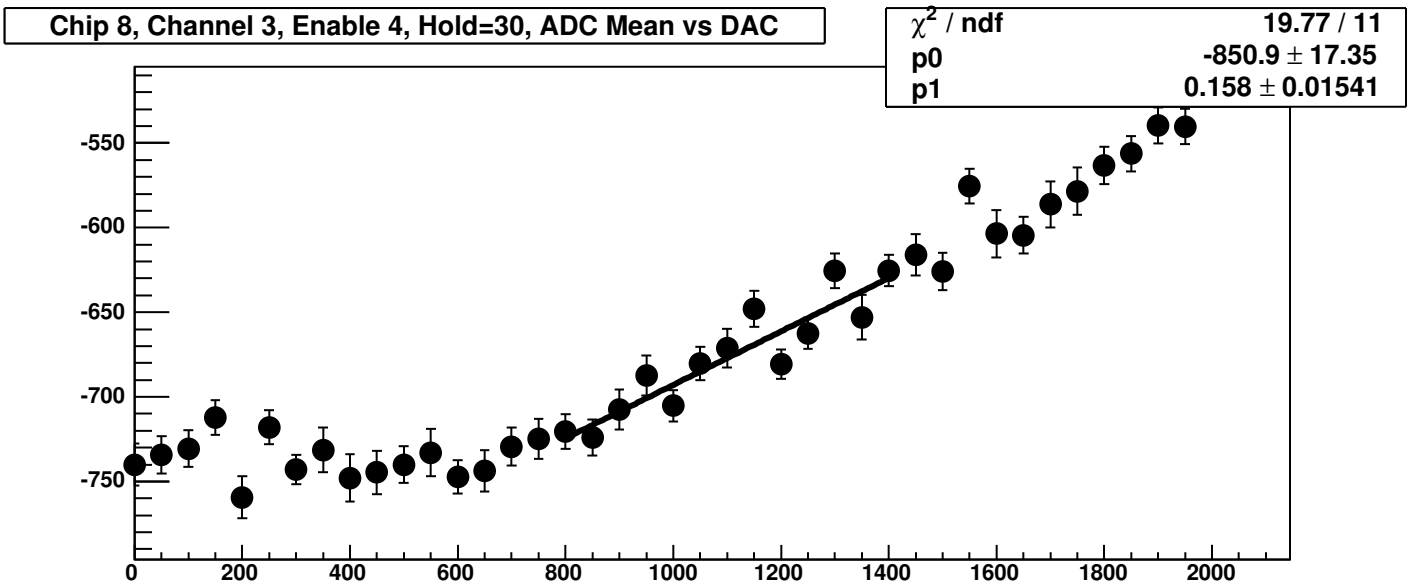
Chip 8, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC

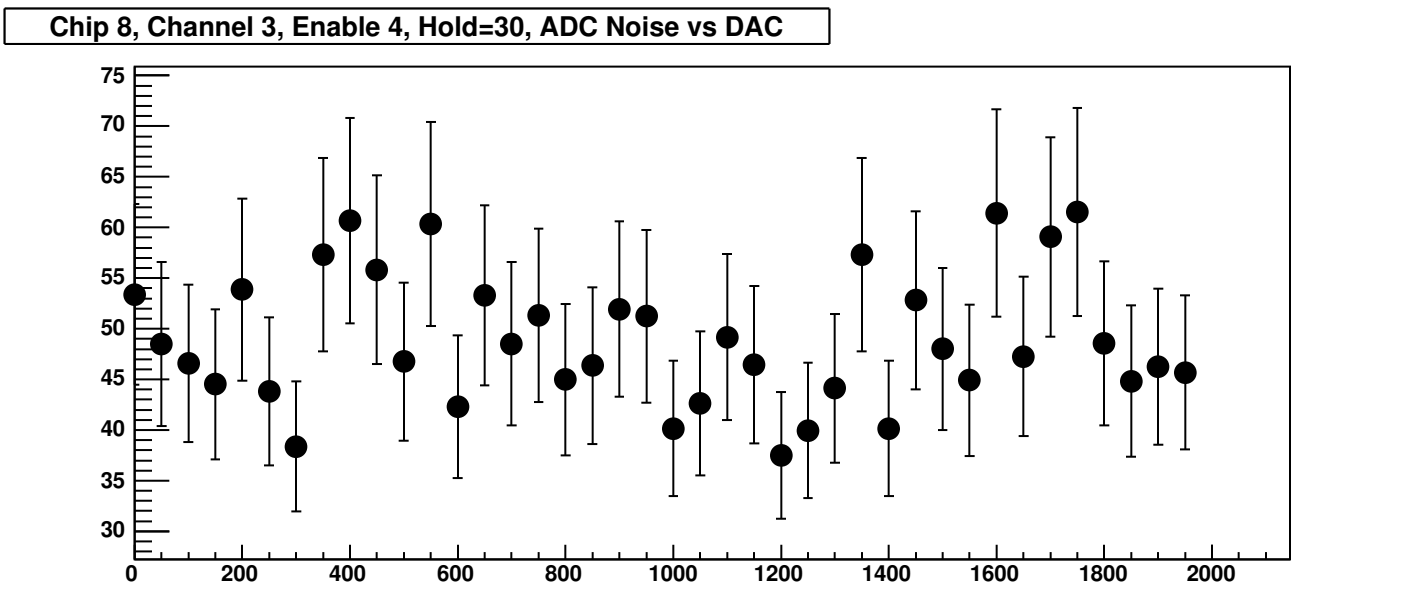


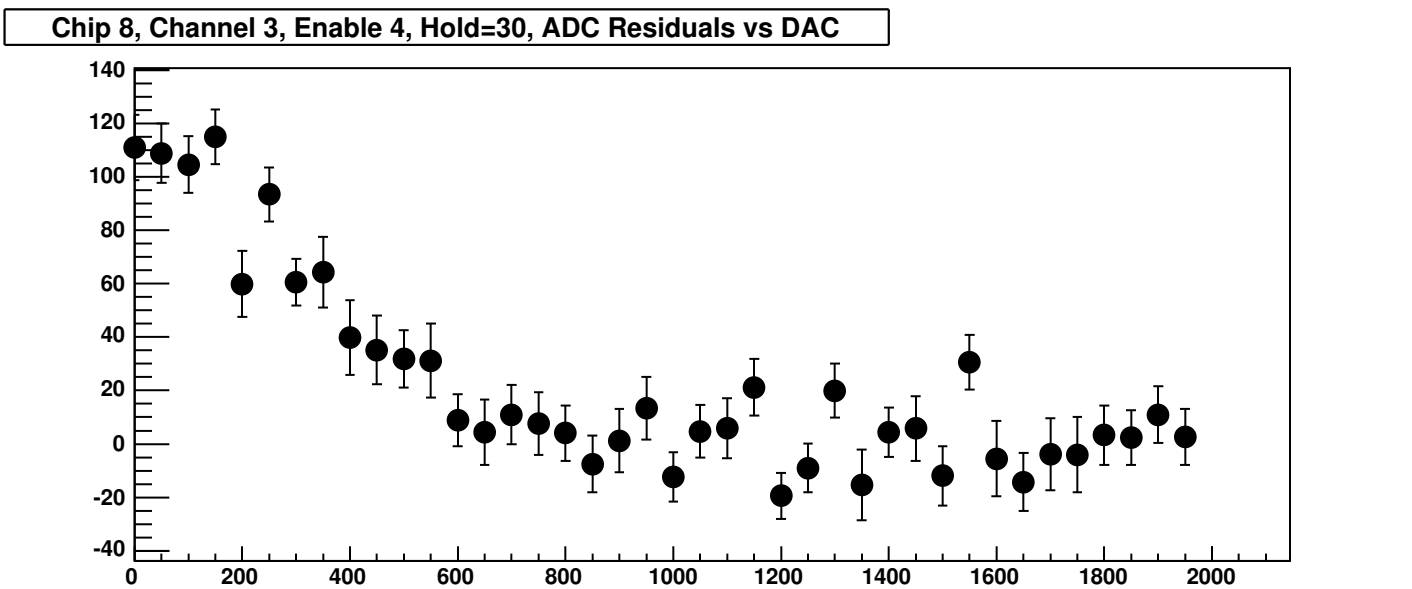

Chip 8, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 18.67 / 11
p0 -804.9 ± 18.24
p1 0.08721 ± 0.01631
-620
-640
-660
-680
-700
-720
-740
-760
-780
0
200
400
600
800
1000
1200
1400
1600
1800
2000

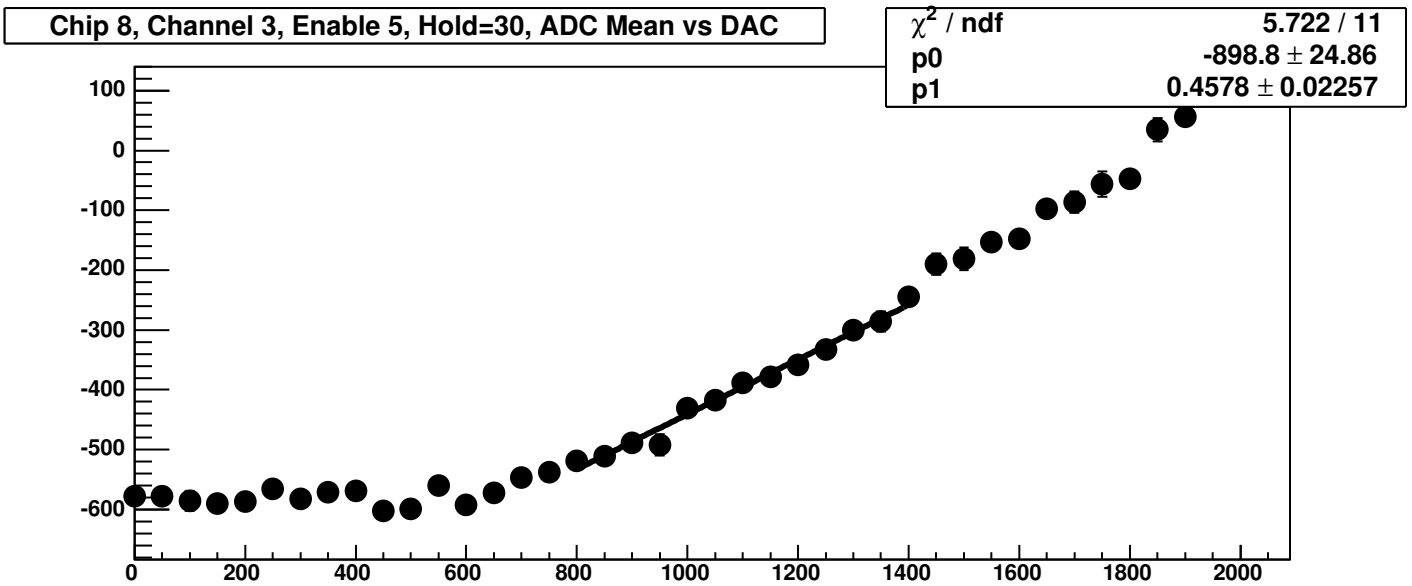

Chip 8, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

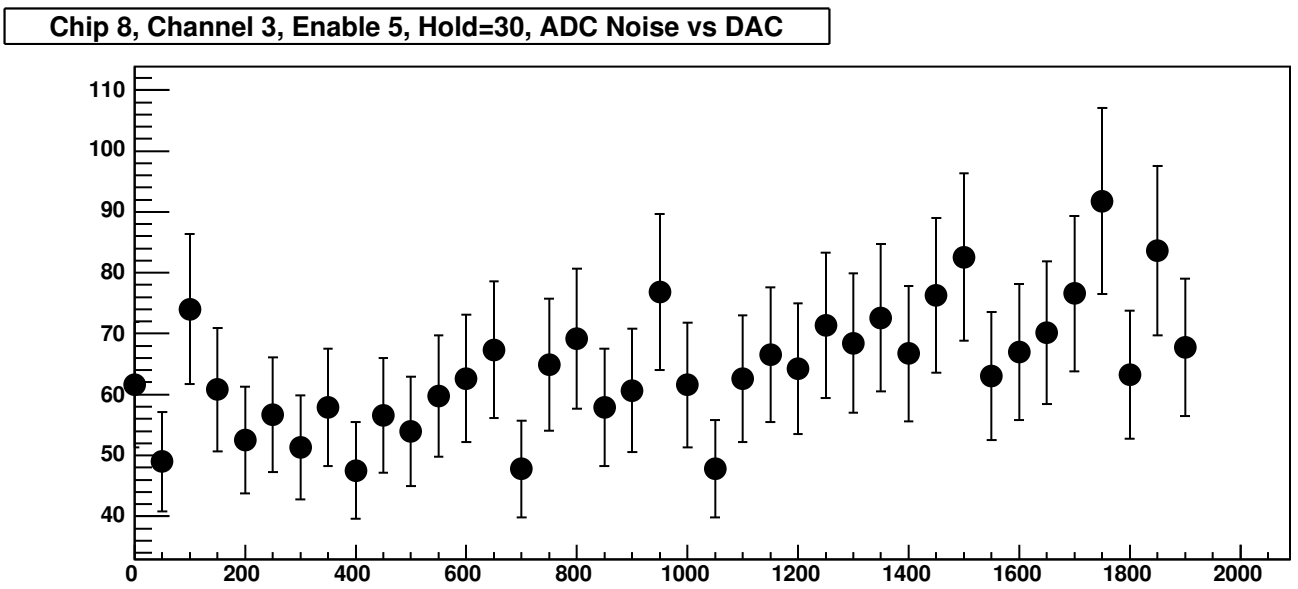

Chip 8, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

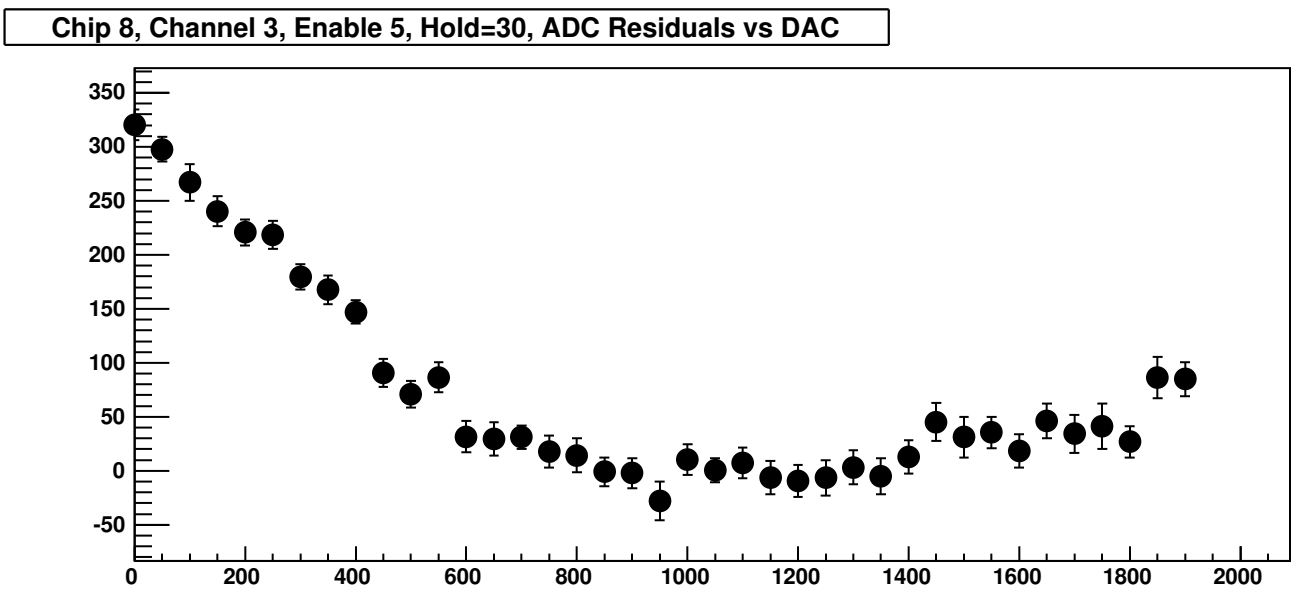

Chip 8, Channel 3, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 19.77 / 11
p0 -850.9 ± 17.35
p1 0.158 ± 0.01541
-550
-600
-650
-700
-750
0
200
400
600
800
1000
1200
1400
1600
1800
2000

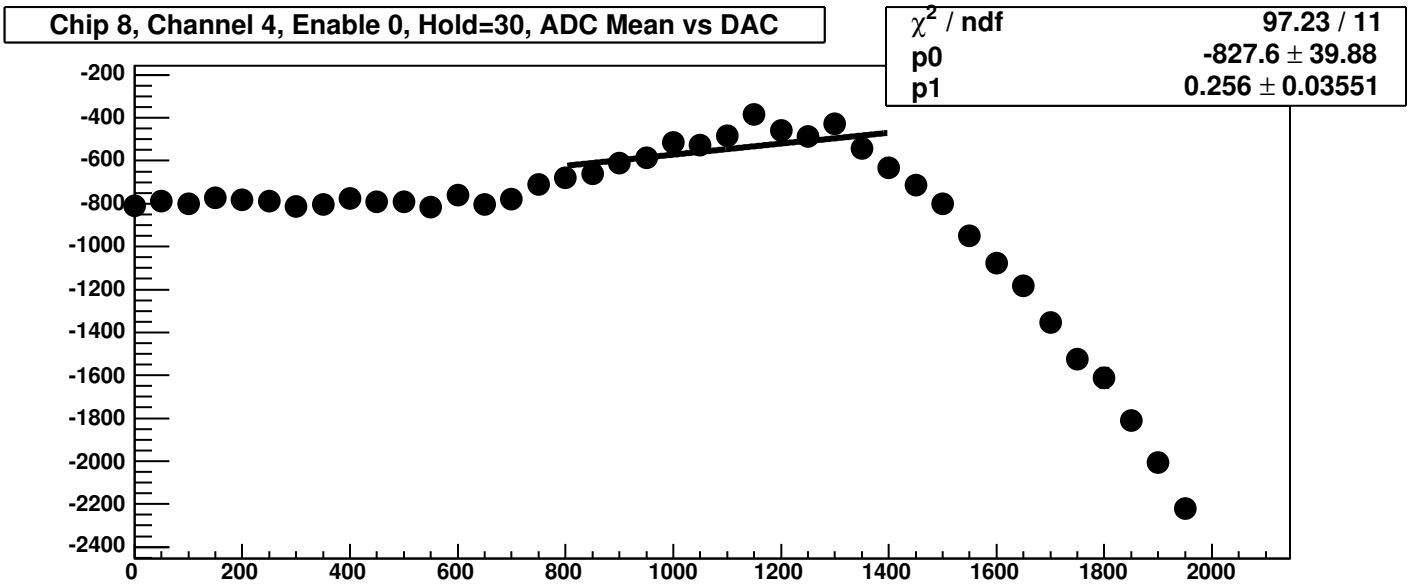

Chip 8, Channel 3, Enable 4, Hold=30, ADC Noise vs DAC
75
70
65
60
55
50
45
40
35
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

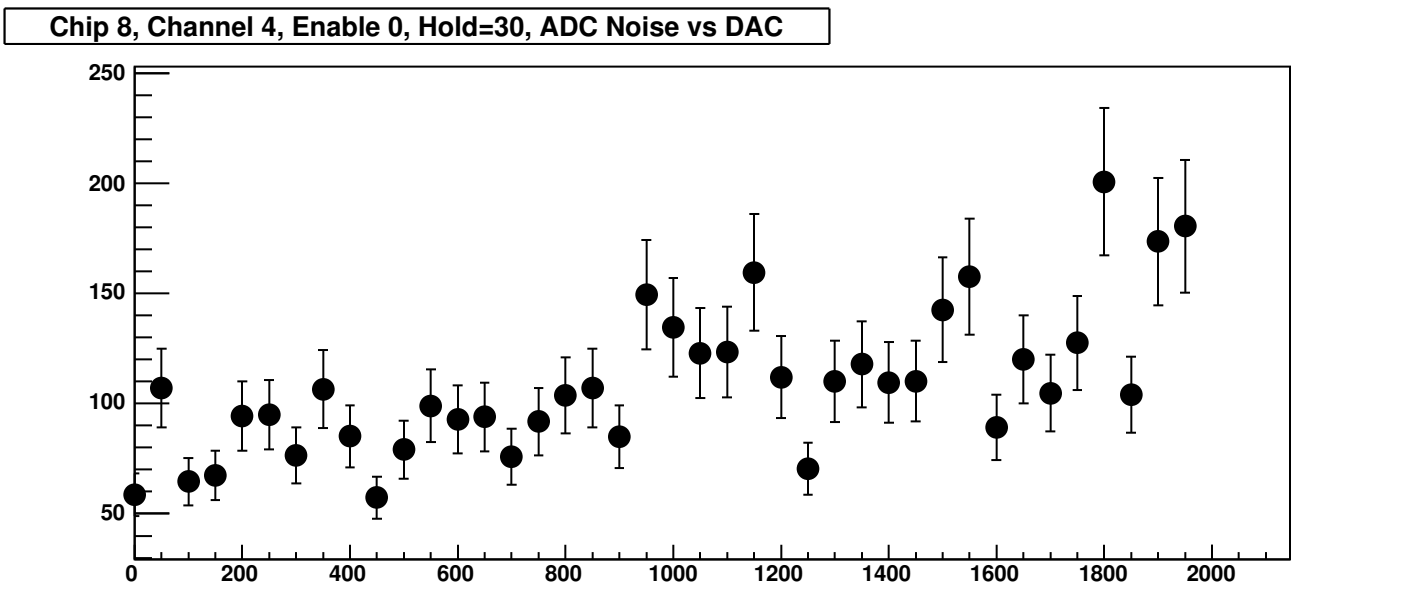

Chip 8, Channel 3, Enable 4, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

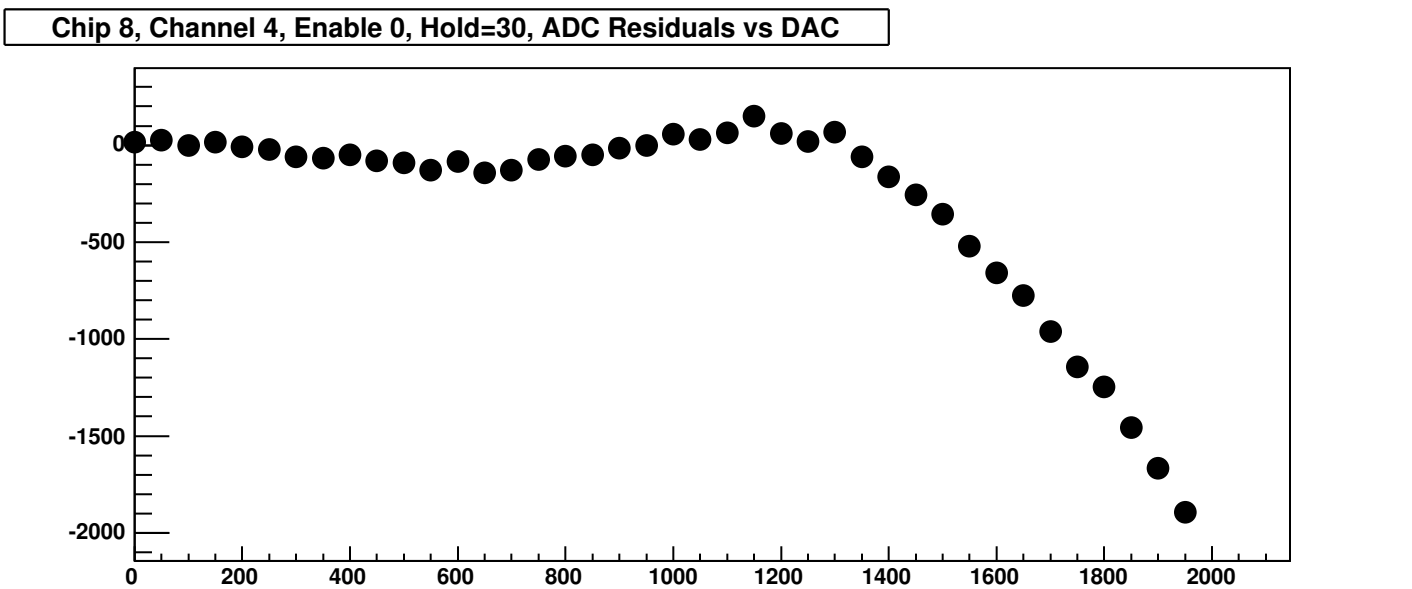

Chip 8, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5.722 / 11
p0 -898.8 ± 24.86
p1 0.4578 ± 0.02257
100
0
-100
-200
-300
-400
-500
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

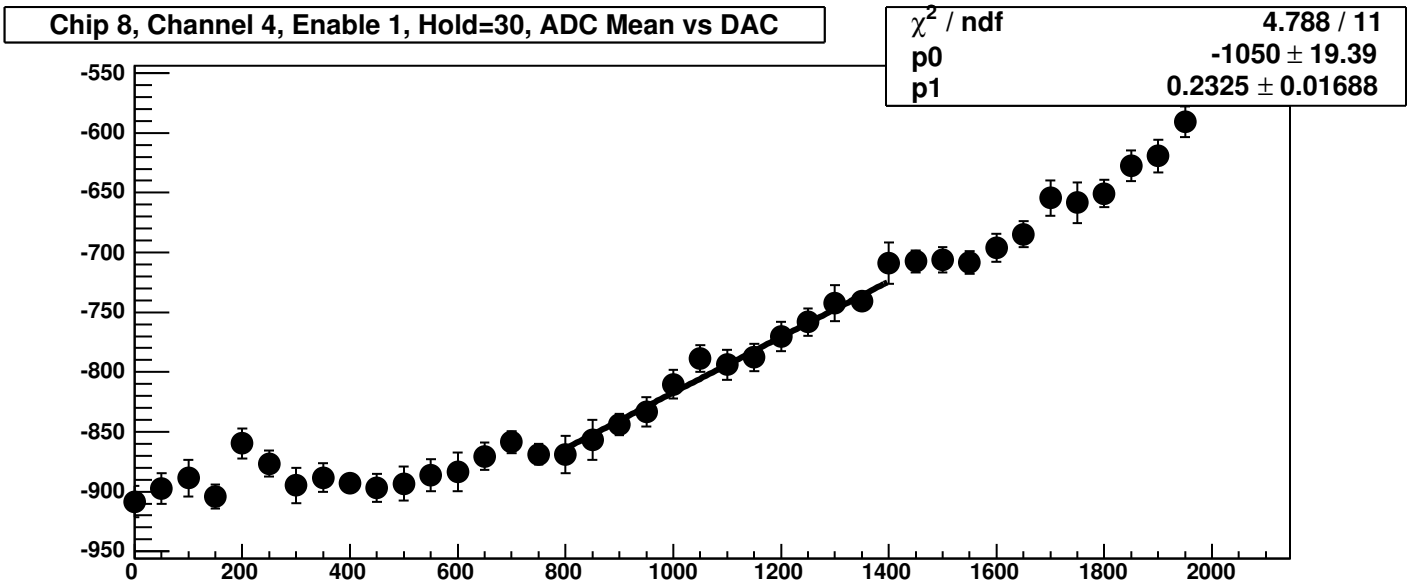

Chip 8, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

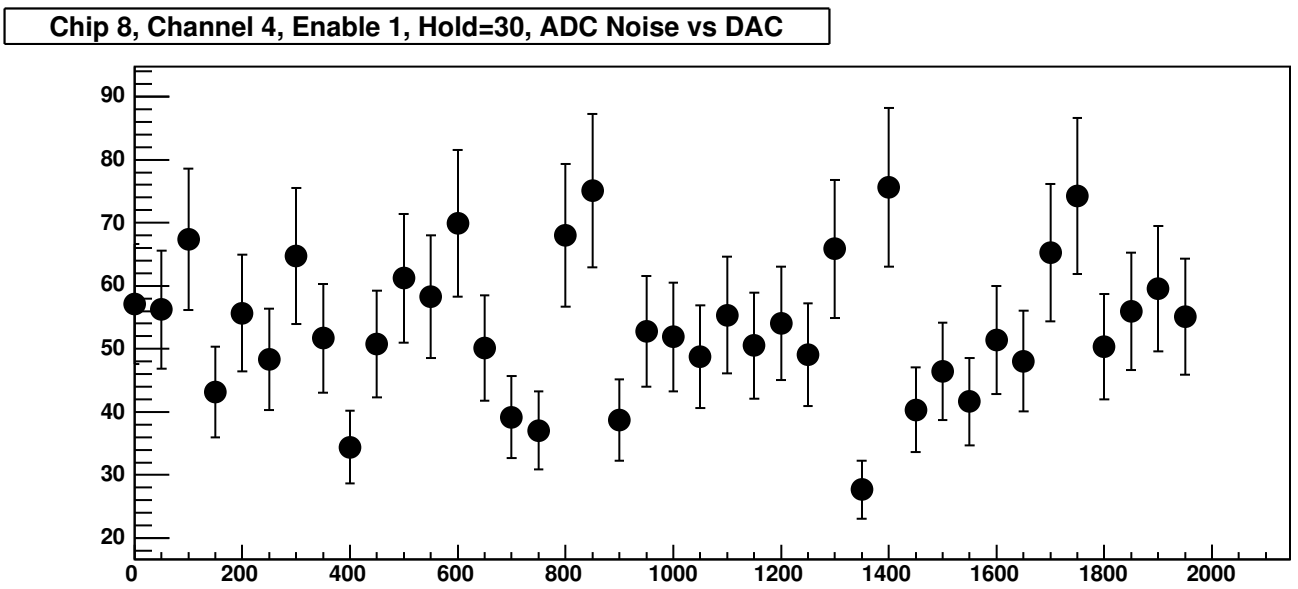

Chip 8, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

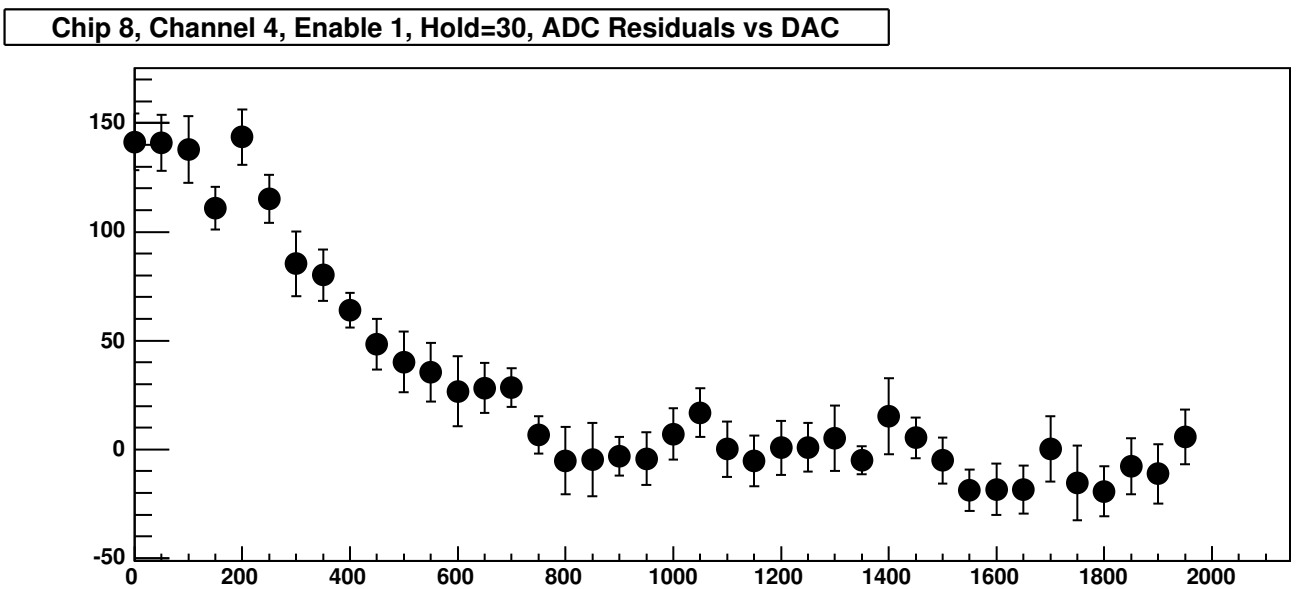

Chip 8, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 97.23 / 11
p0 -827.6 ± 39.88
p1 0.256 ± 0.03551
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
-2000
-2200
-2400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

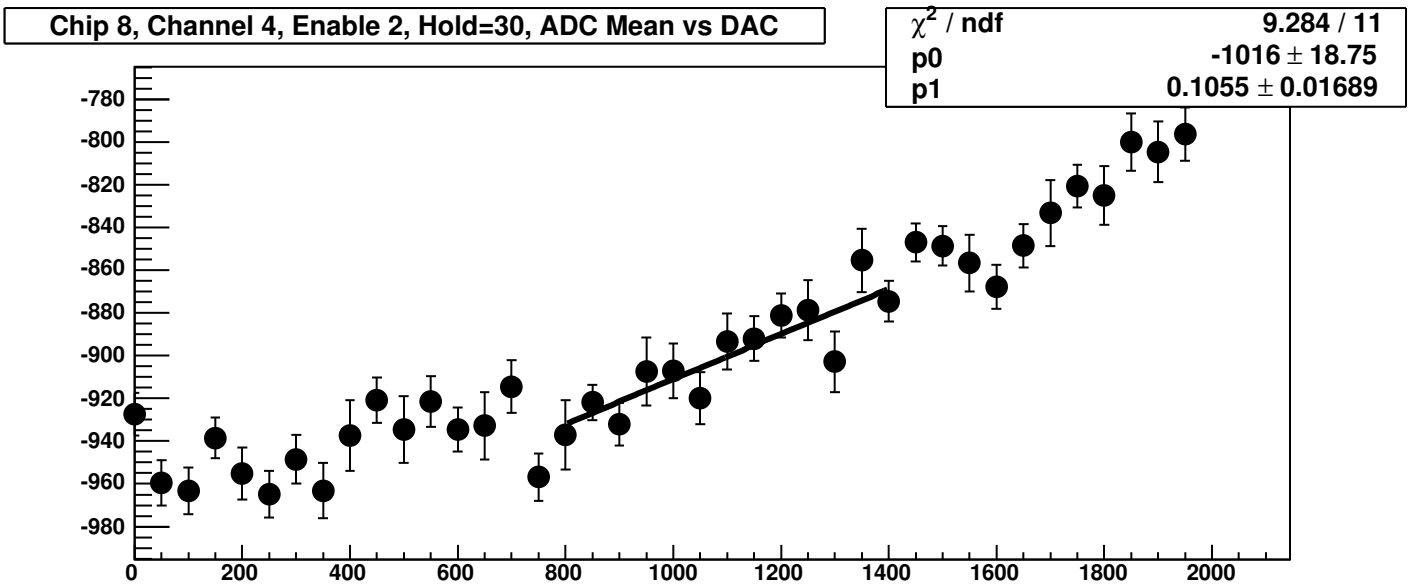

Chip 8, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC
250
200
150
100
50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

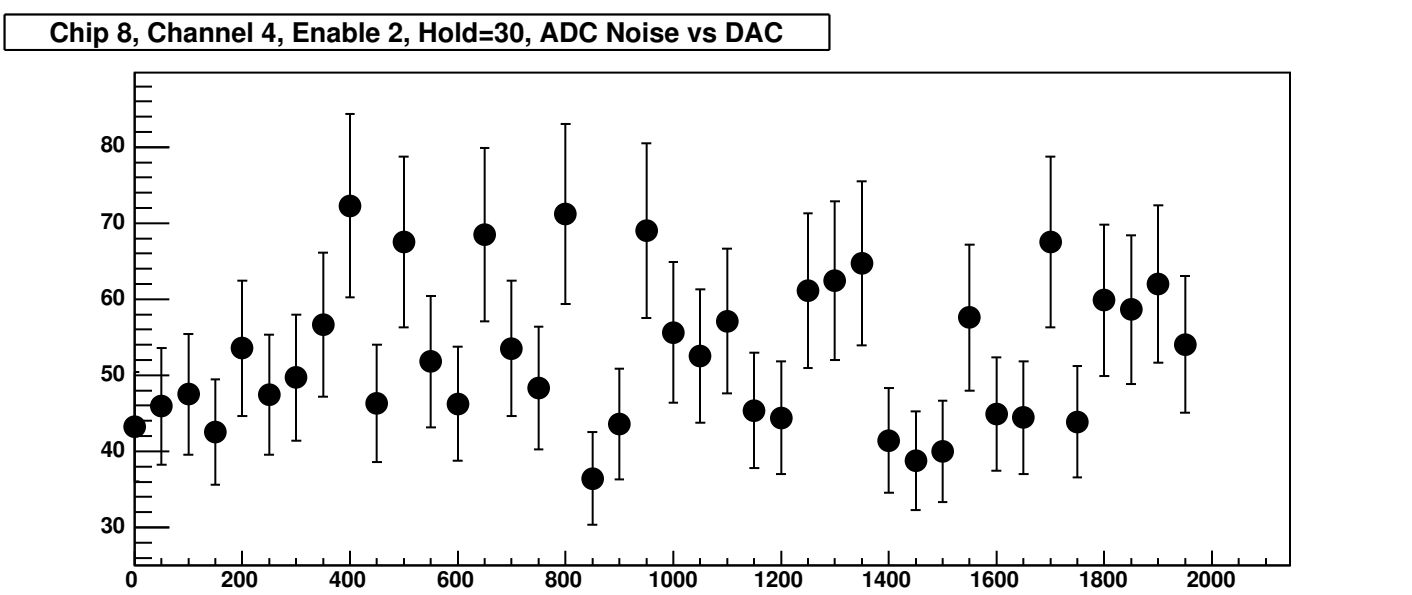

Chip 8, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

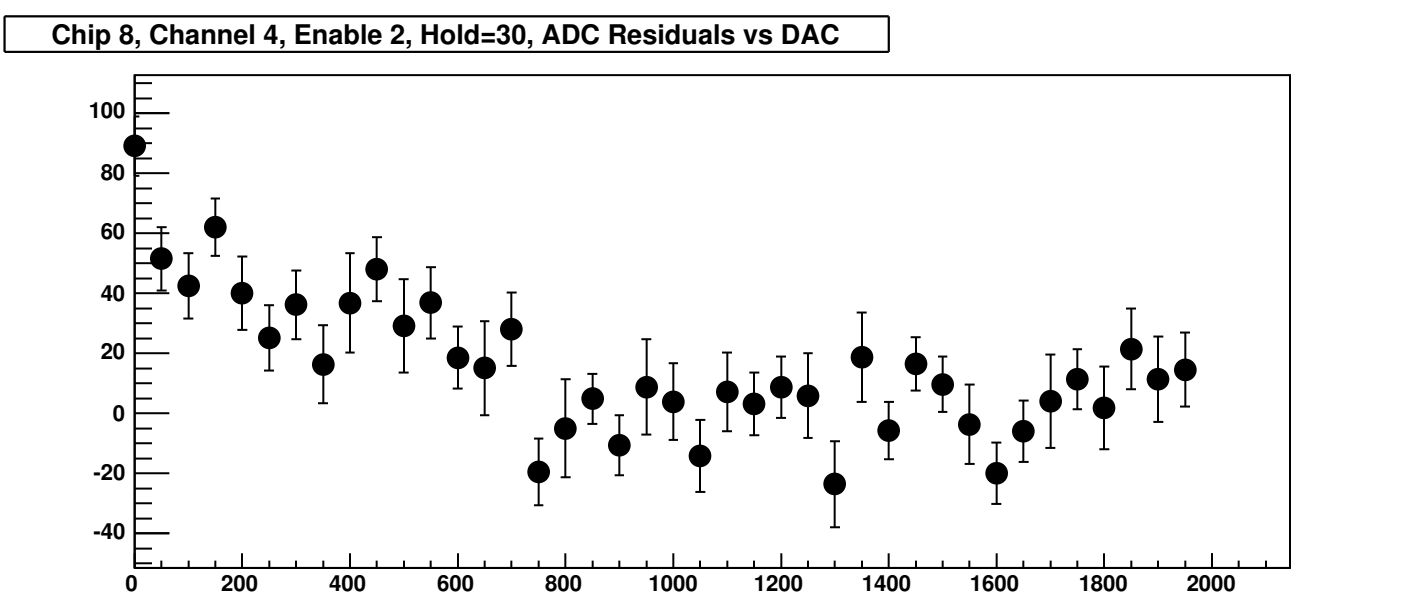

Chip 8, Channel 4, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4.788 / 11
p0 -1050 ± 19.39
p1 0.2325 ± 0.01688
-550
-600
-650
-700
-750
-800
-850
-900
-950
0
200
400
600
800
1000
1200
1400
1600
1800
2000

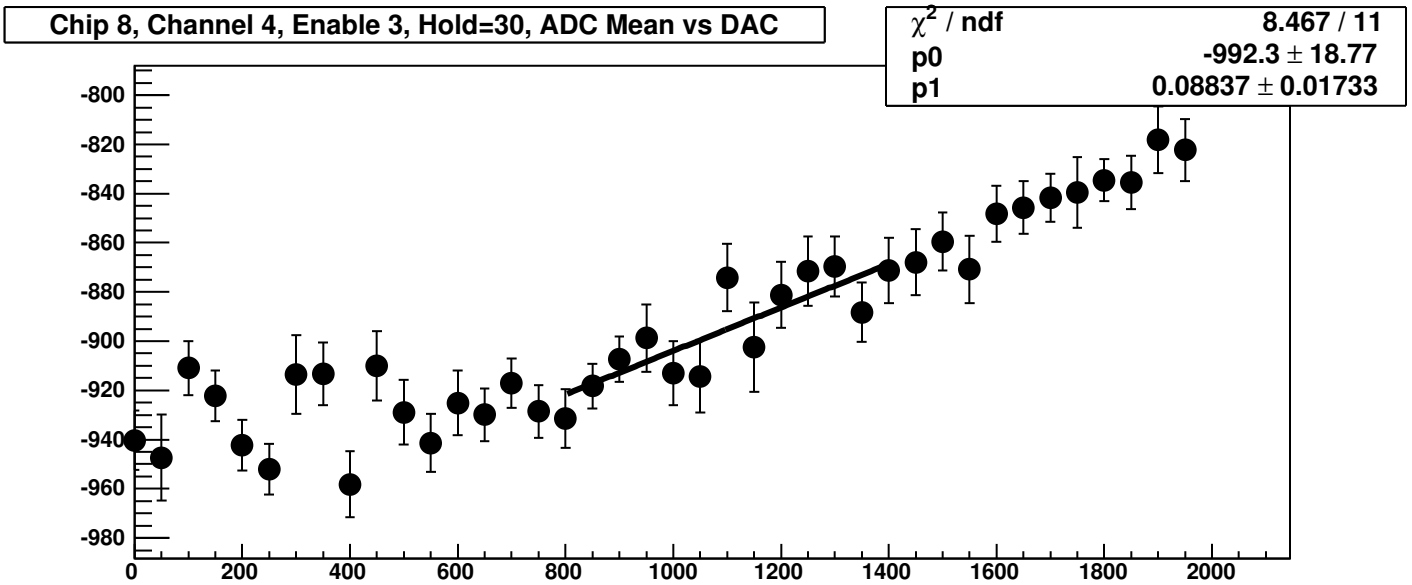

Chip 8, Channel 4, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

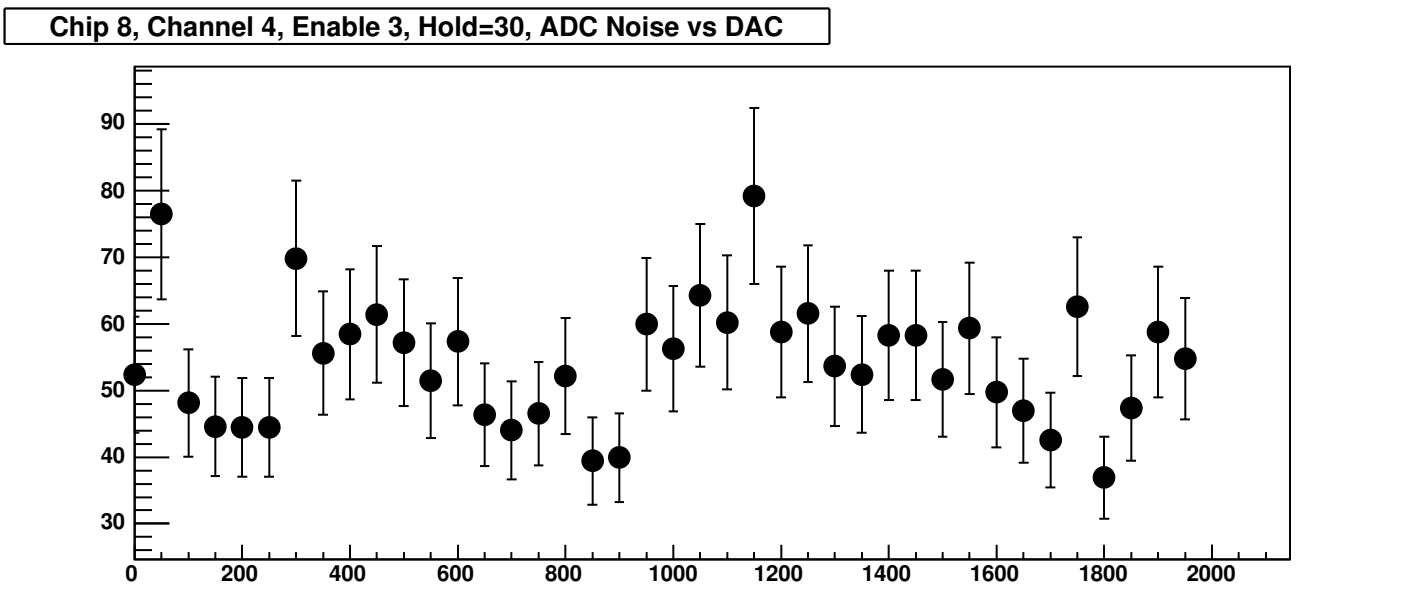

Chip 8, Channel 4, Enable 1, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

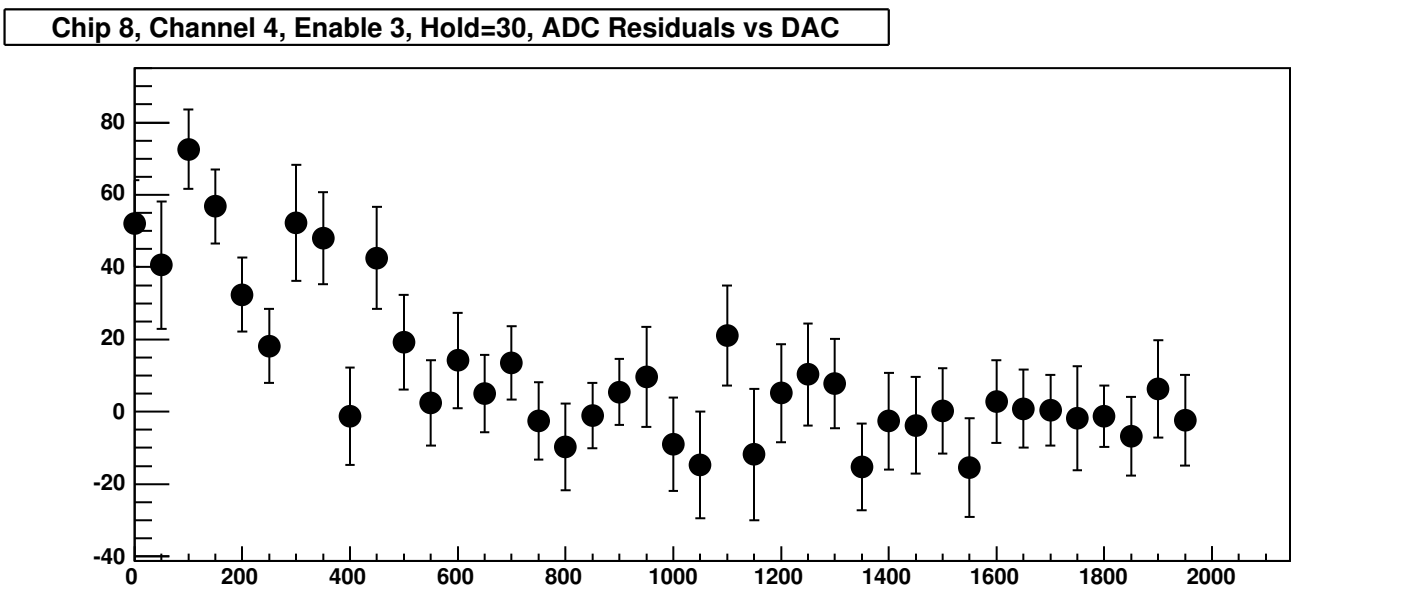

Chip 8, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 9.284 / 11
p0 -1016 ± 18.75
p1 0.1055 ± 0.01689
-780
-800
-820
-840
-860
-880
-900
-920
-940
-960
-980
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

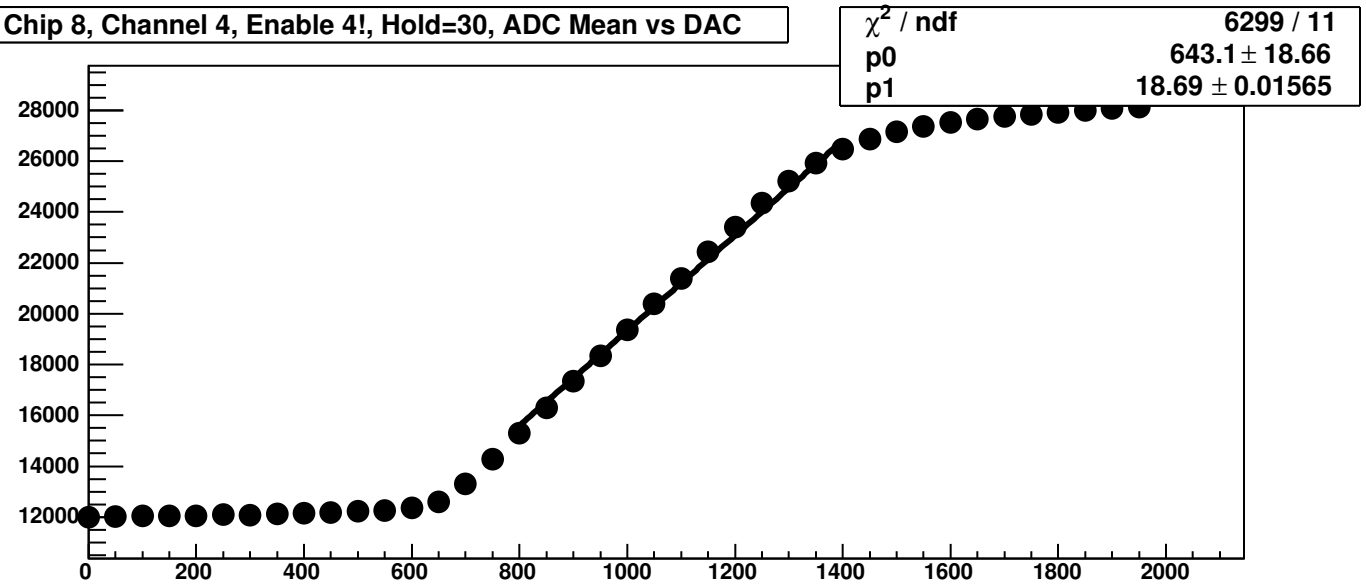

Chip 8, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 8.467 / 11
p0 -992.3 ± 18.77
p1 0.08837 ± 0.01733
-800
-820
-840
-860
-880
-900
-920
-940
-960
-980
0
200
400
600
800
1000
1200
1400
1600
1800
2000

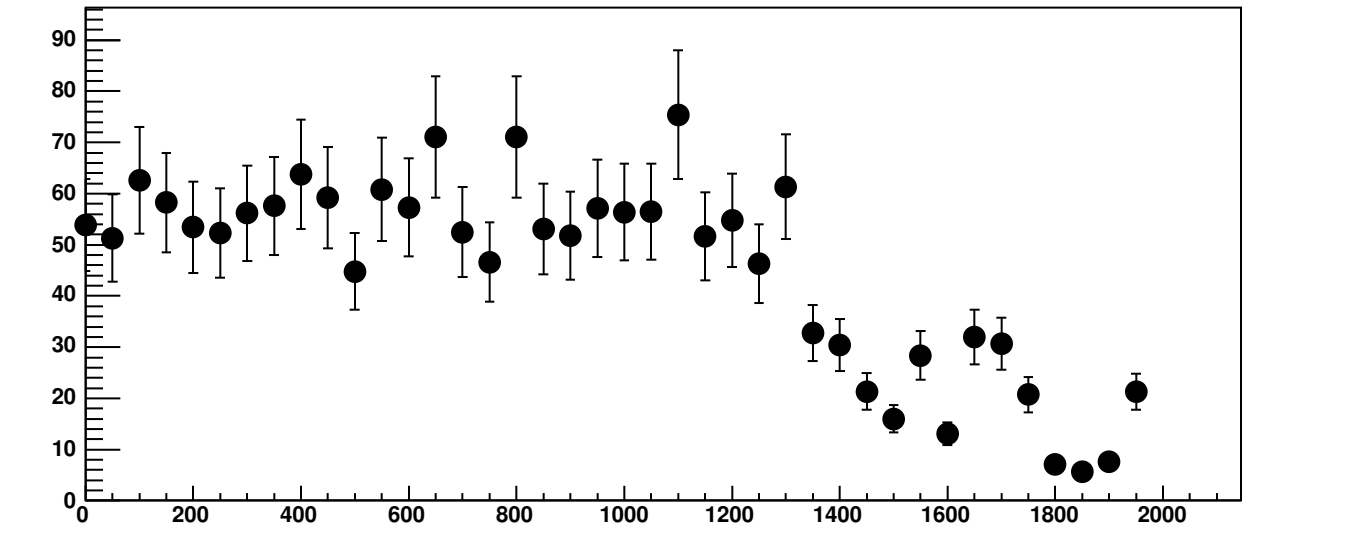

Chip 8, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

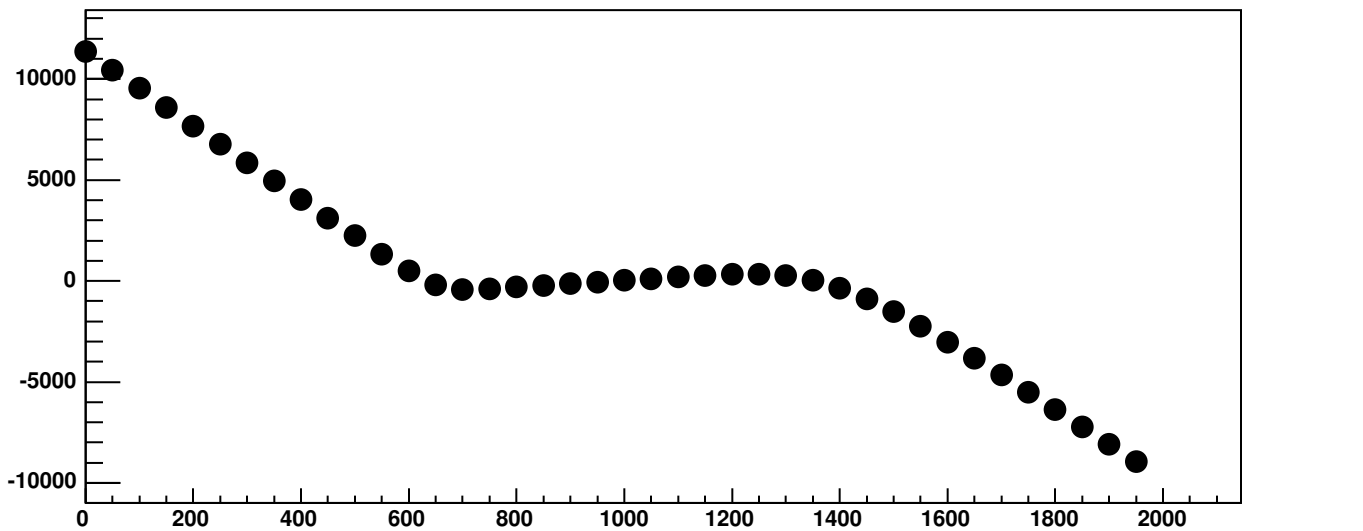
Chip 8, Channel 4, Enable 4!, Hold=30, ADC Mean vs DAC

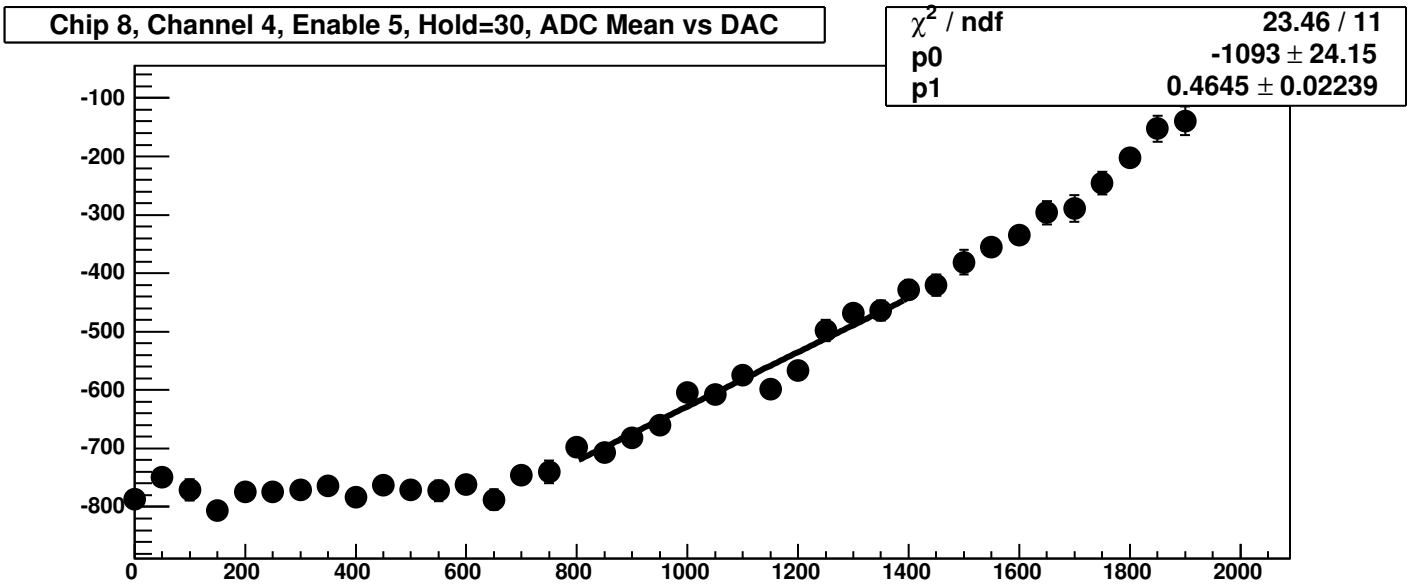


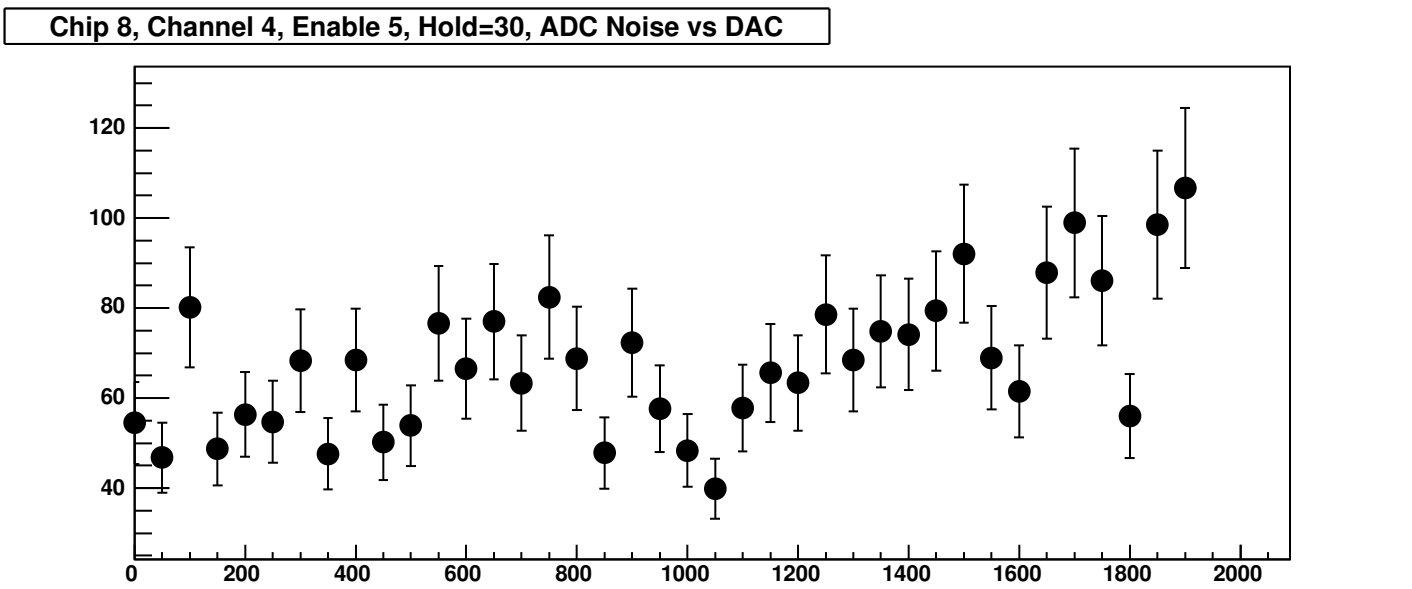
Chip 8, Channel 4, Enable 4!, Hold=30, ADC Noise vs DAC

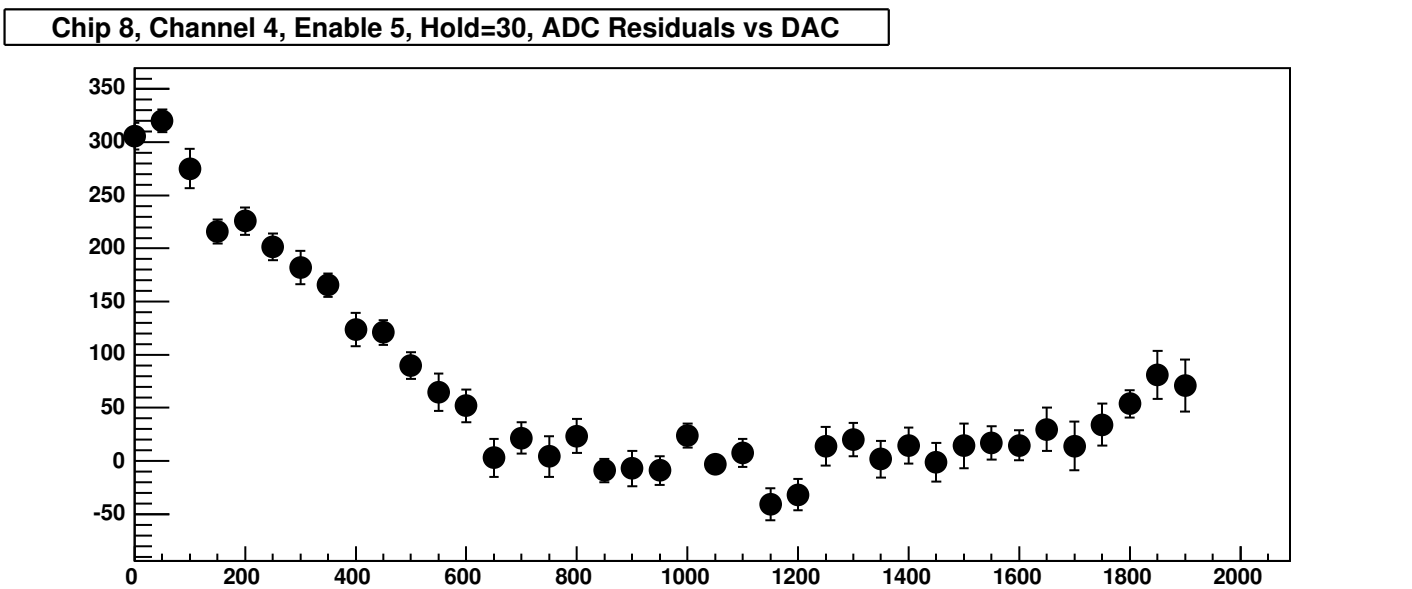


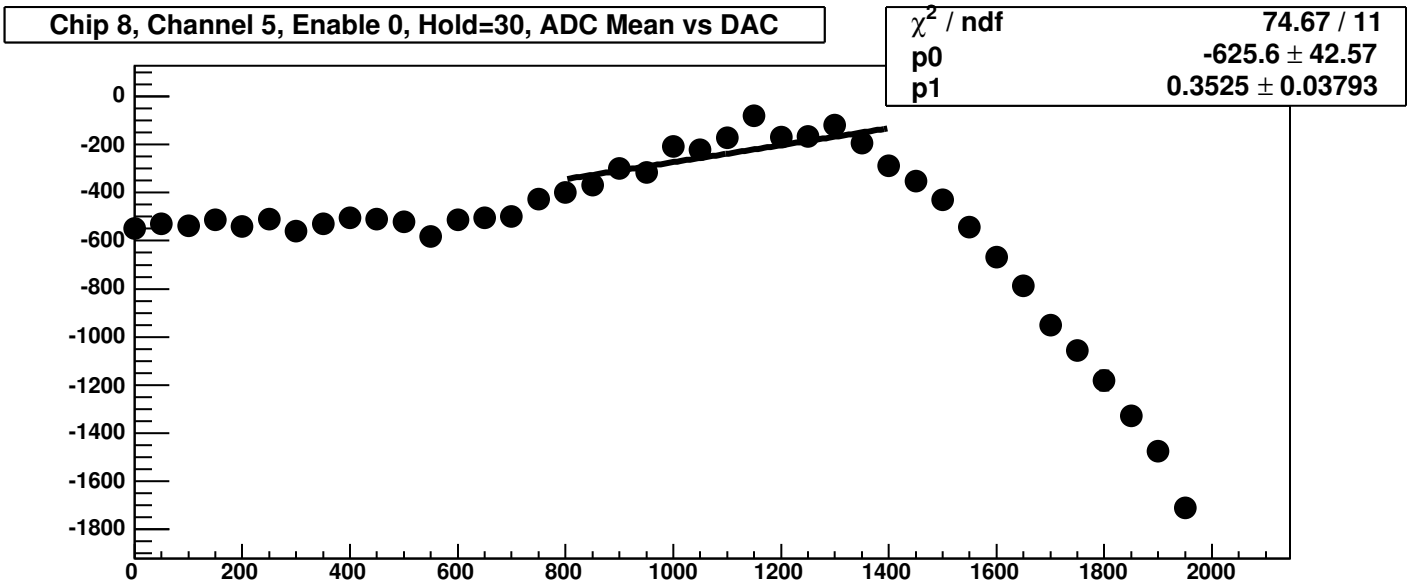
Chip 8, Channel 4, Enable 4!, Hold=30, ADC Residuals vs DAC

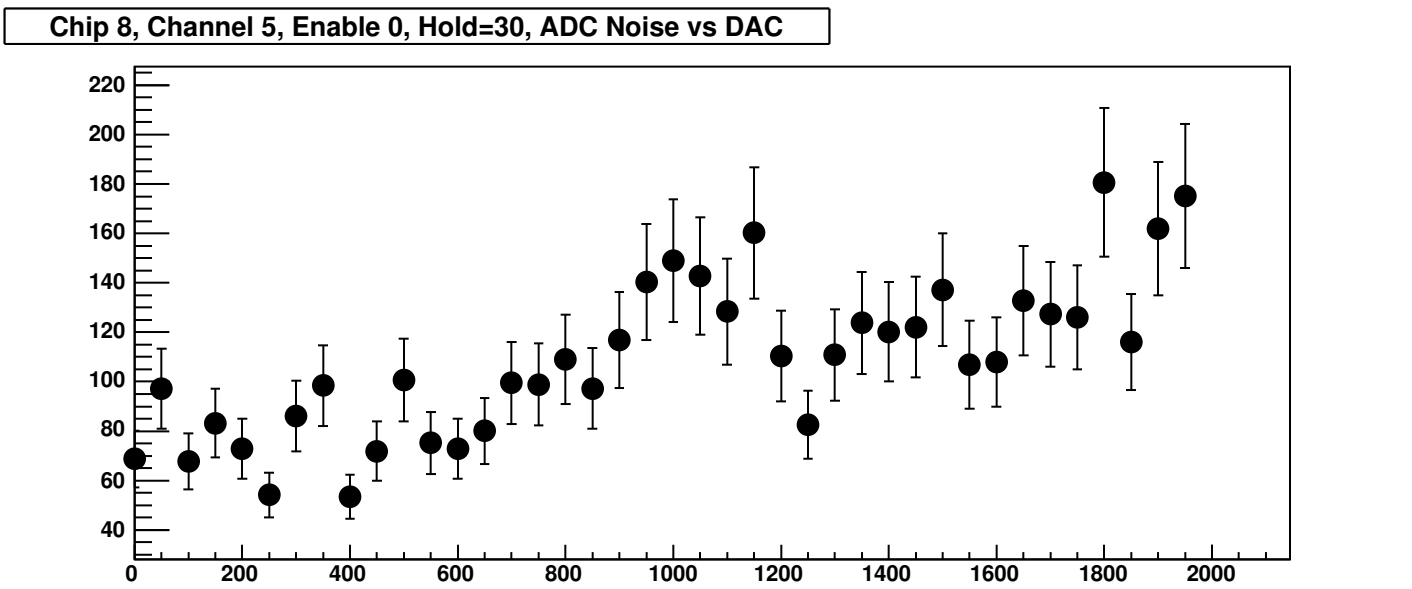


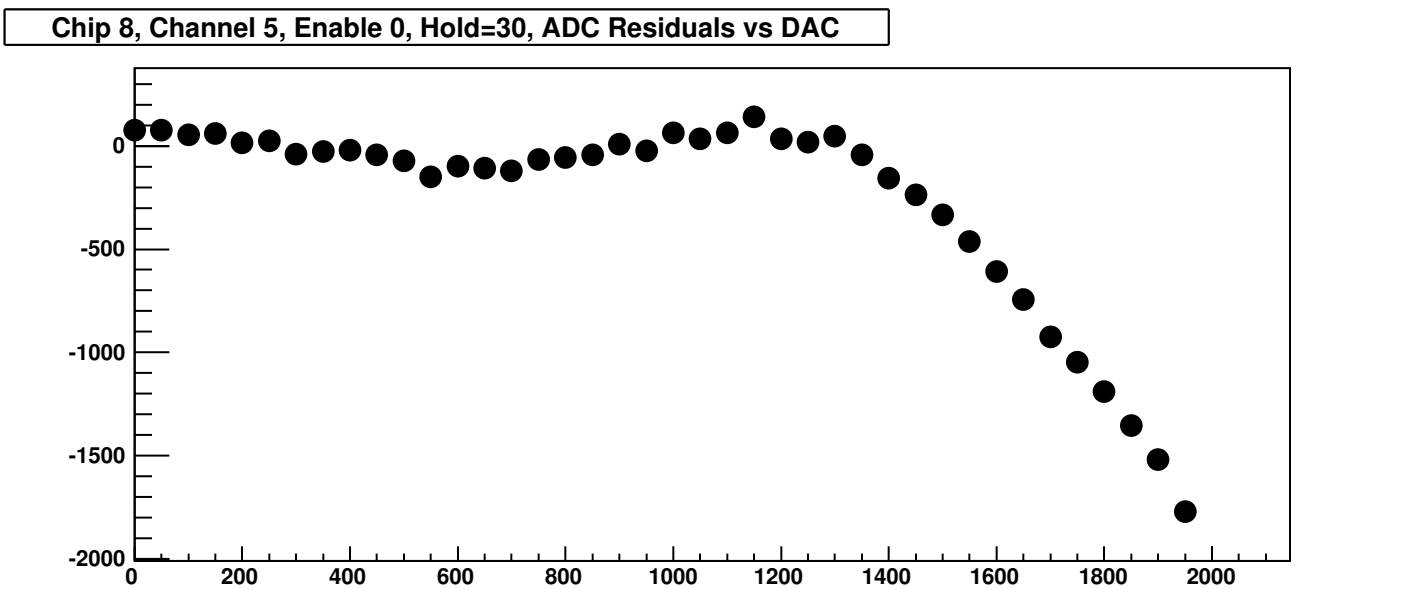

Chip 8, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 23.46 / 11
p0 -1093 ± 24.15
p1 0.4645 ± 0.02239
-100
-200
-300
-400
-500
-600
-700
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

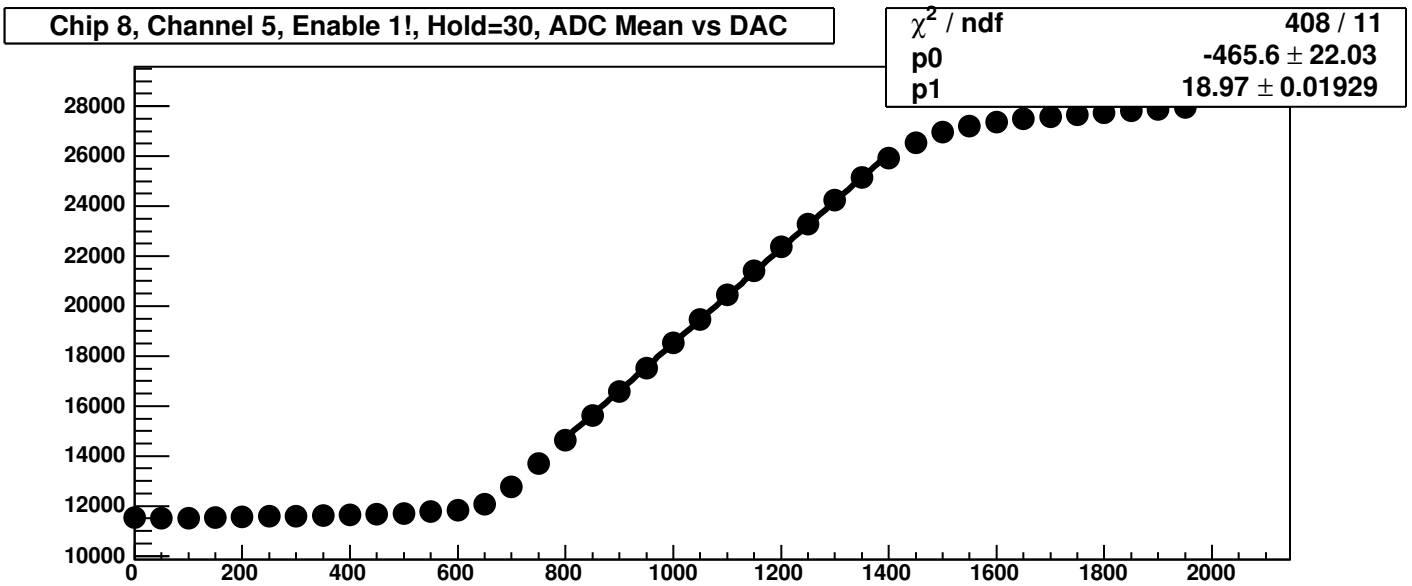

Chip 8, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

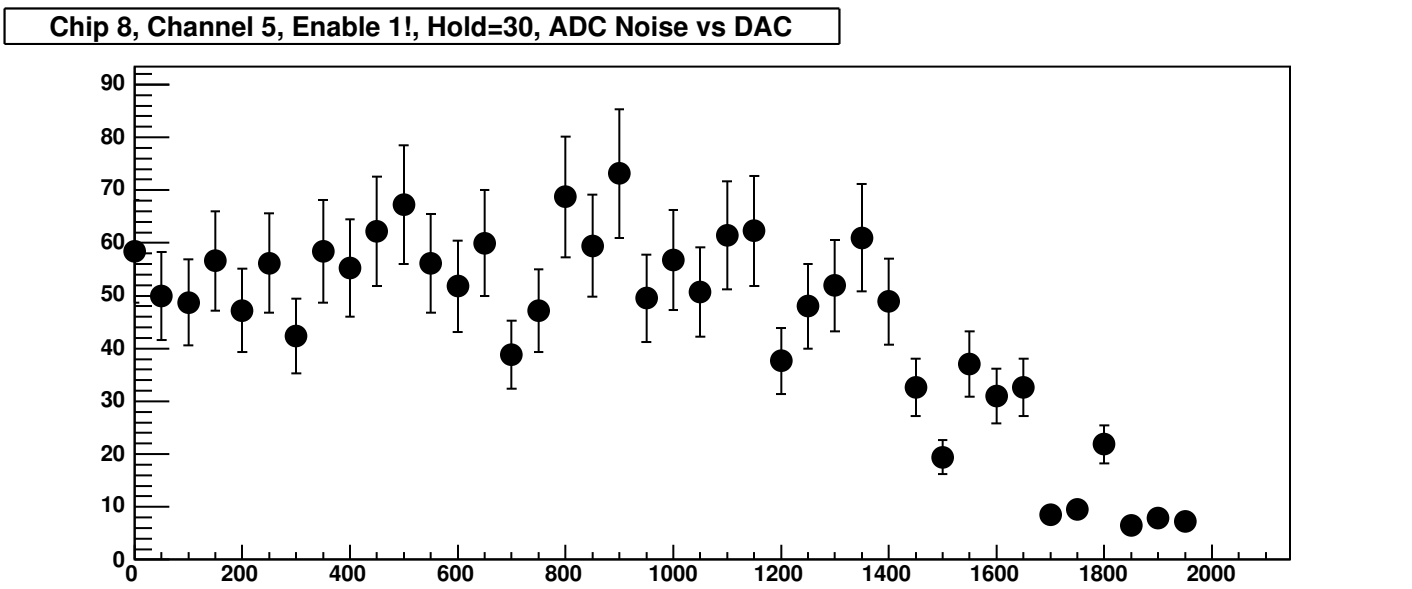

Chip 8, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

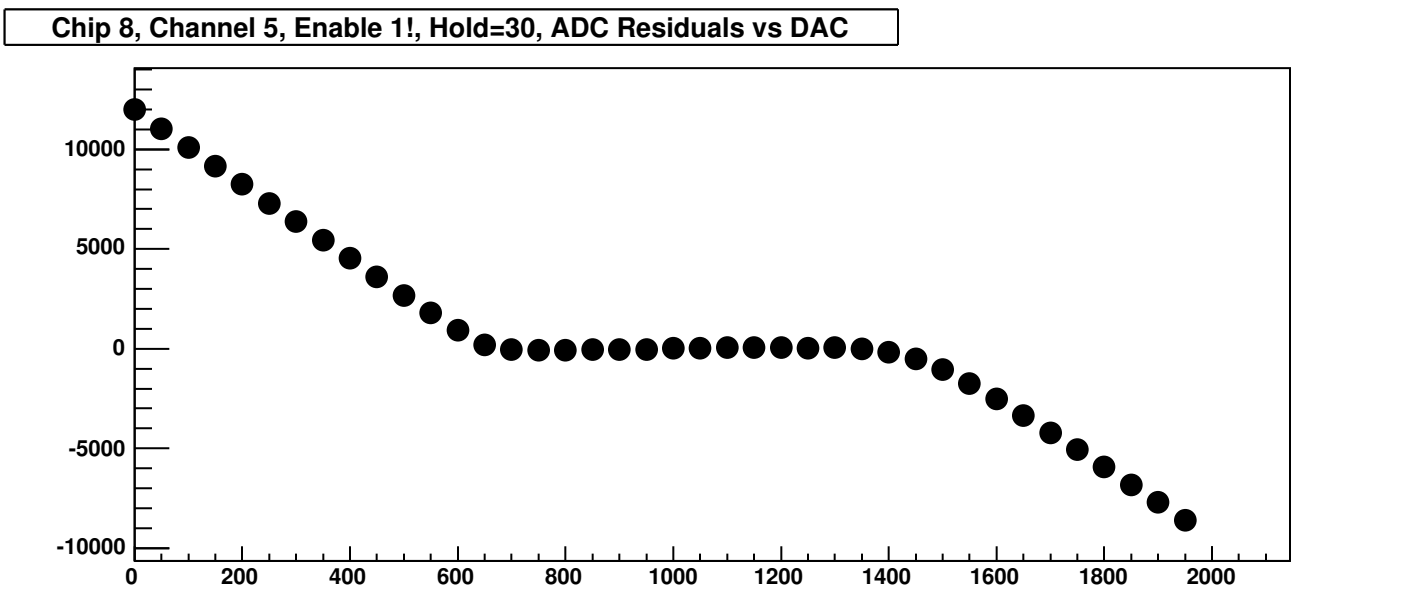

Chip 8, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 74.67 / 11
p0 -625.6 ± 42.57
p1 0.3525 ± 0.03793
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

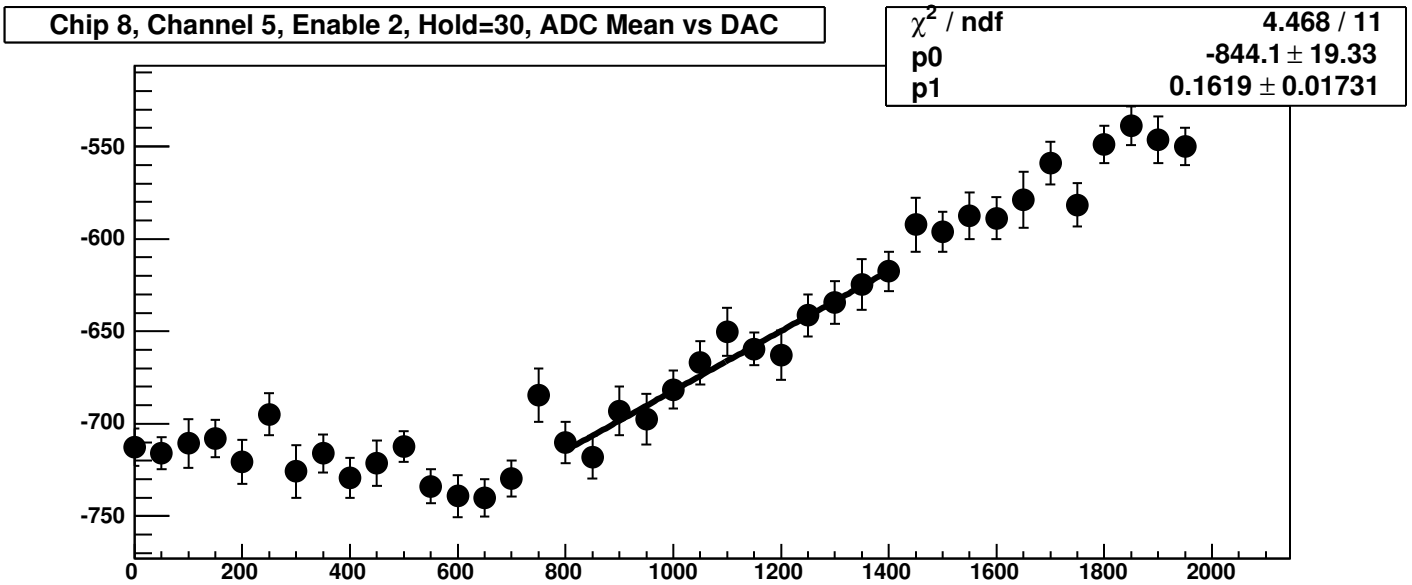

Chip 8, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

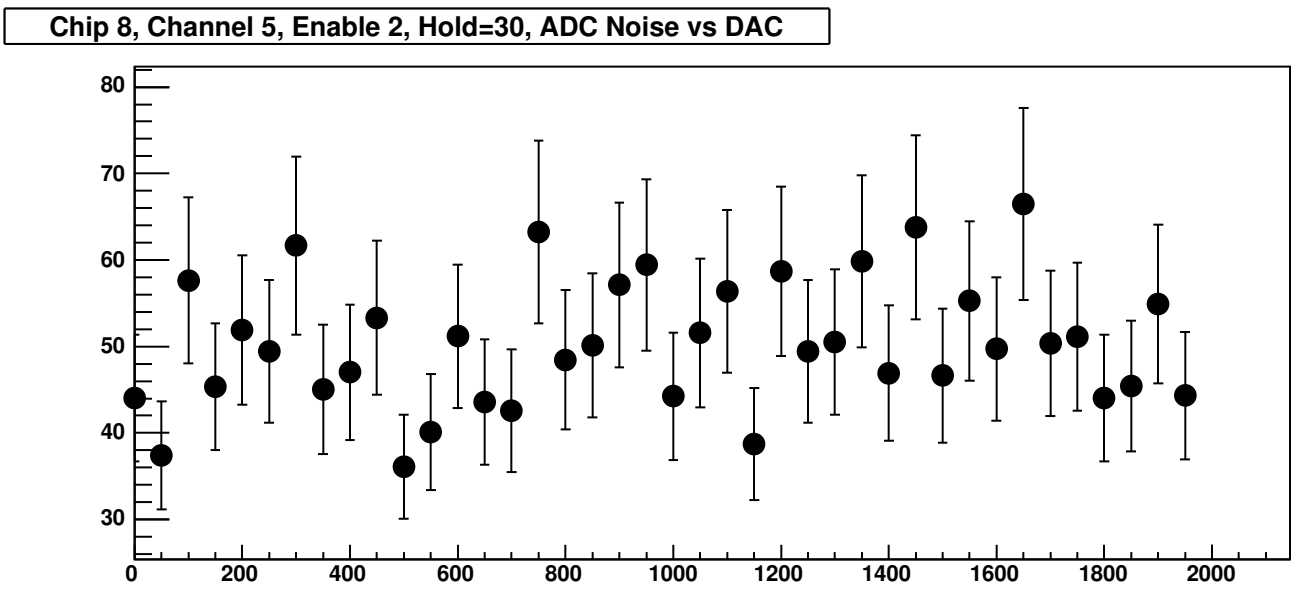

Chip 8, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

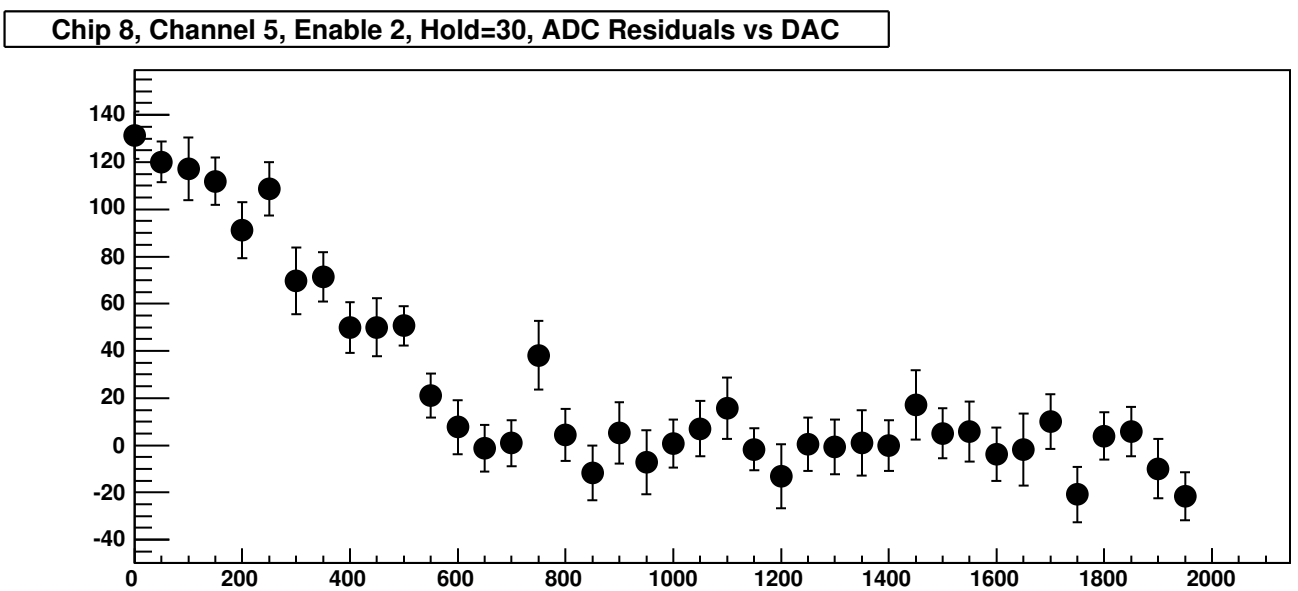

Chip 8, Channel 5, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 408 / 11
p0 -465.6 ± 22.03
p1 18.97 ± 0.01929
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 5, Enable 1!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

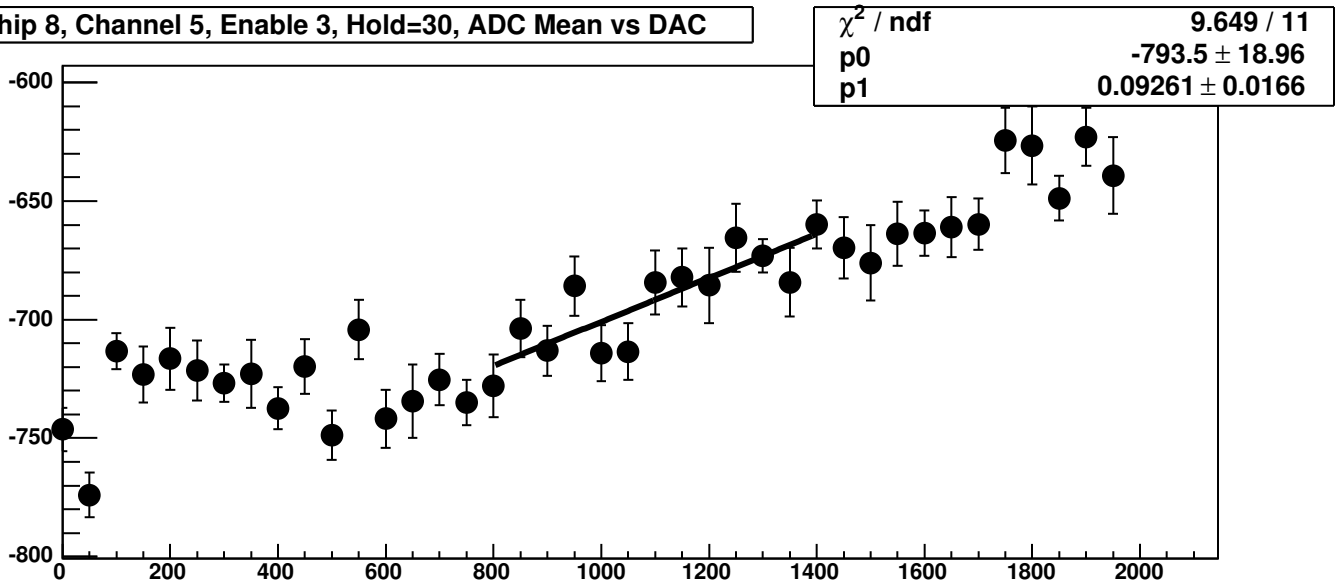

Chip 8, Channel 5, Enable 1!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4.468 / 11
p0 -844.1 ± 19.33
p1 0.1619 ± 0.01731
-550
-600
-650
-700
-750
0
200
400
600
800
1000
1200
1400
1600
1800
2000

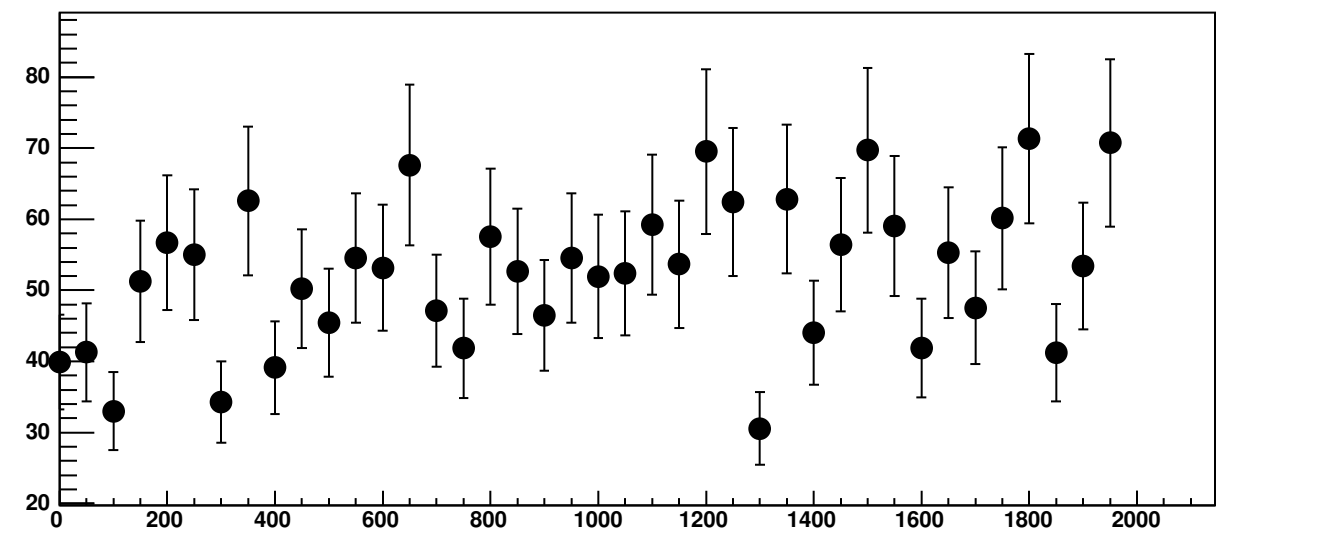

Chip 8, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

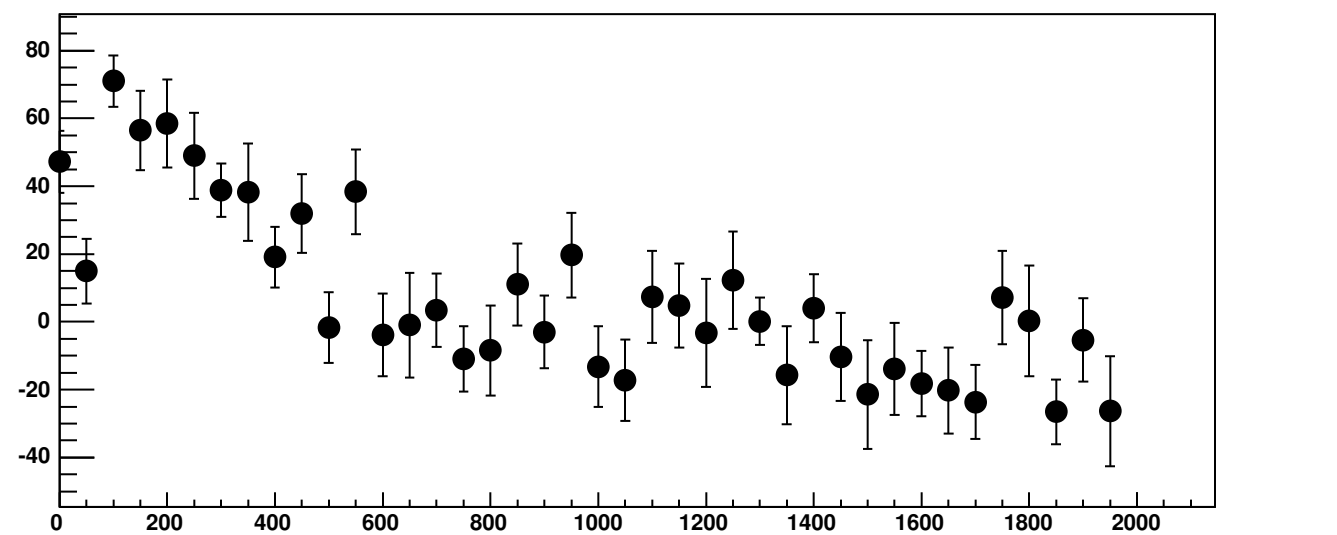
Chip 8, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC

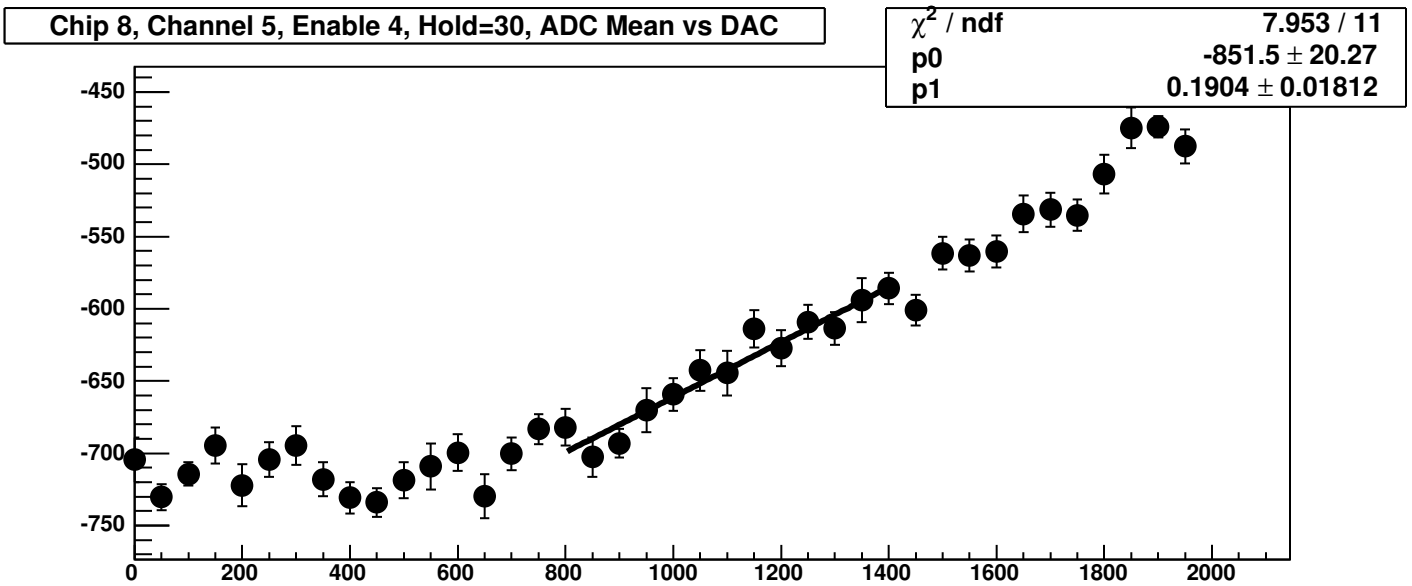


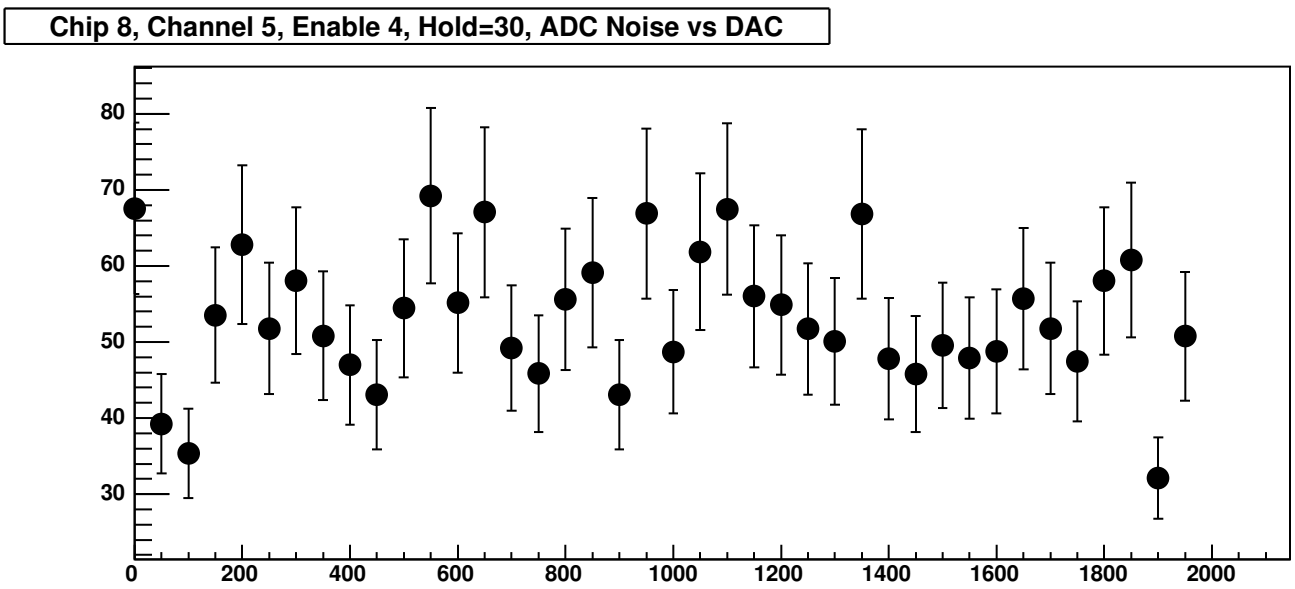
Chip 8, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC

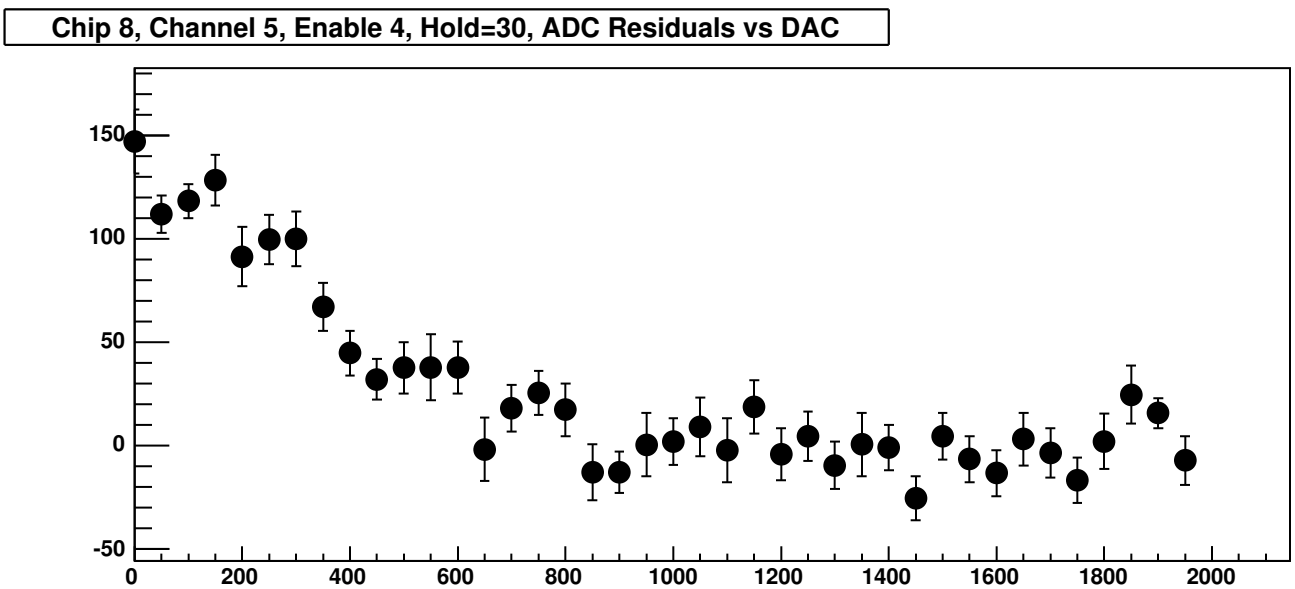


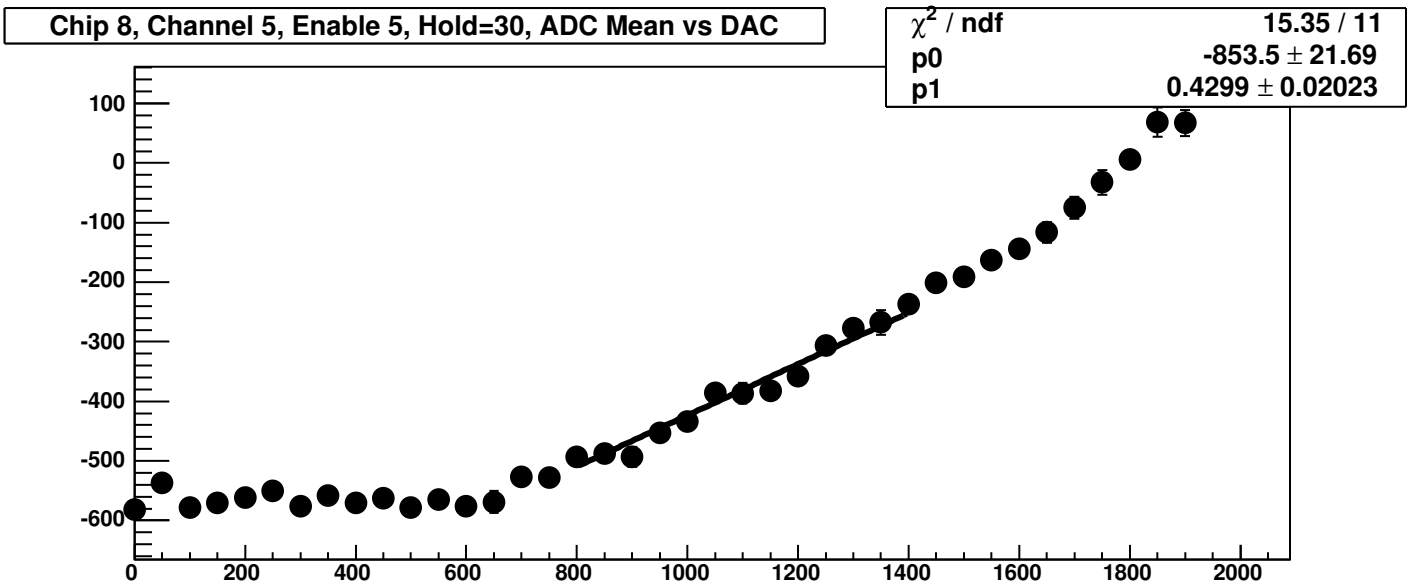
Chip 8, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC

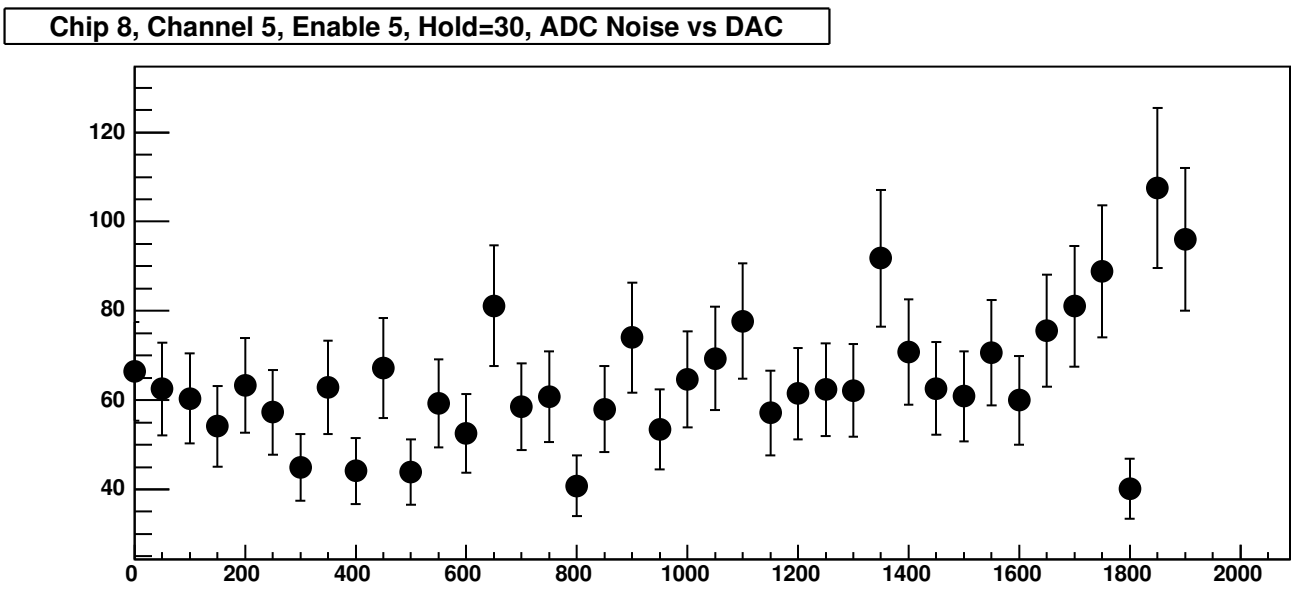


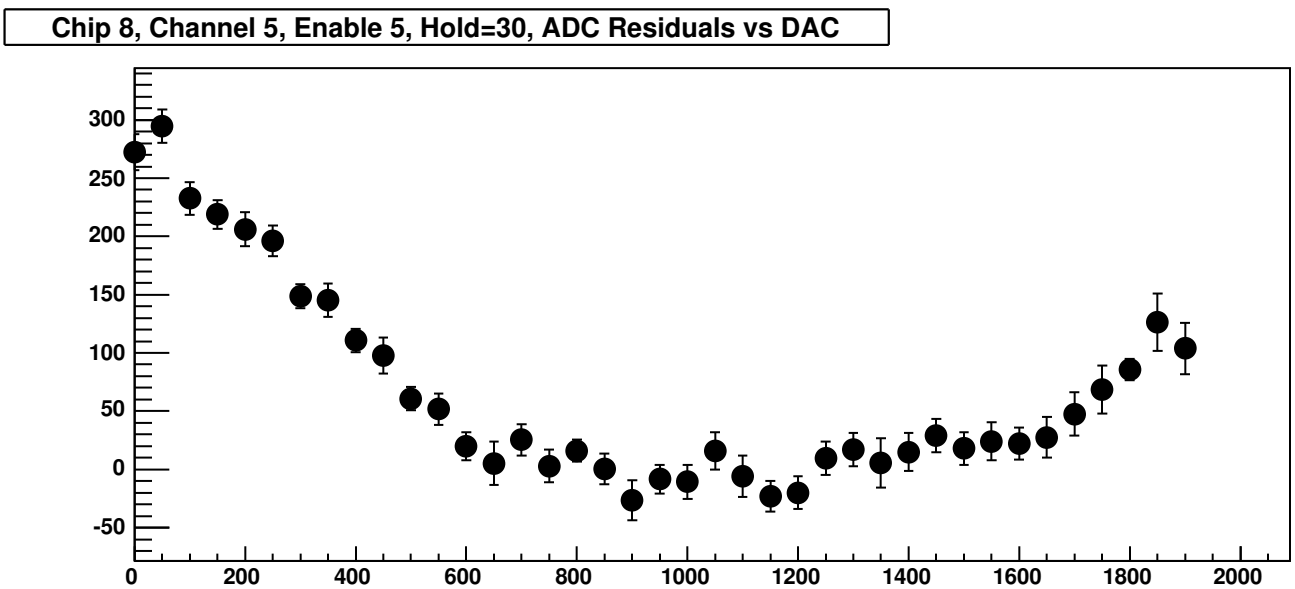

Chip 8, Channel 5, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7.953 / 11
p0 -851.5 ± 20.27
p1 0.1904 ± 0.01812
-450
-500
-550
-600
-650
-700
-750
0
200
400
600
800
1000
1200
1400
1600
1800
2000

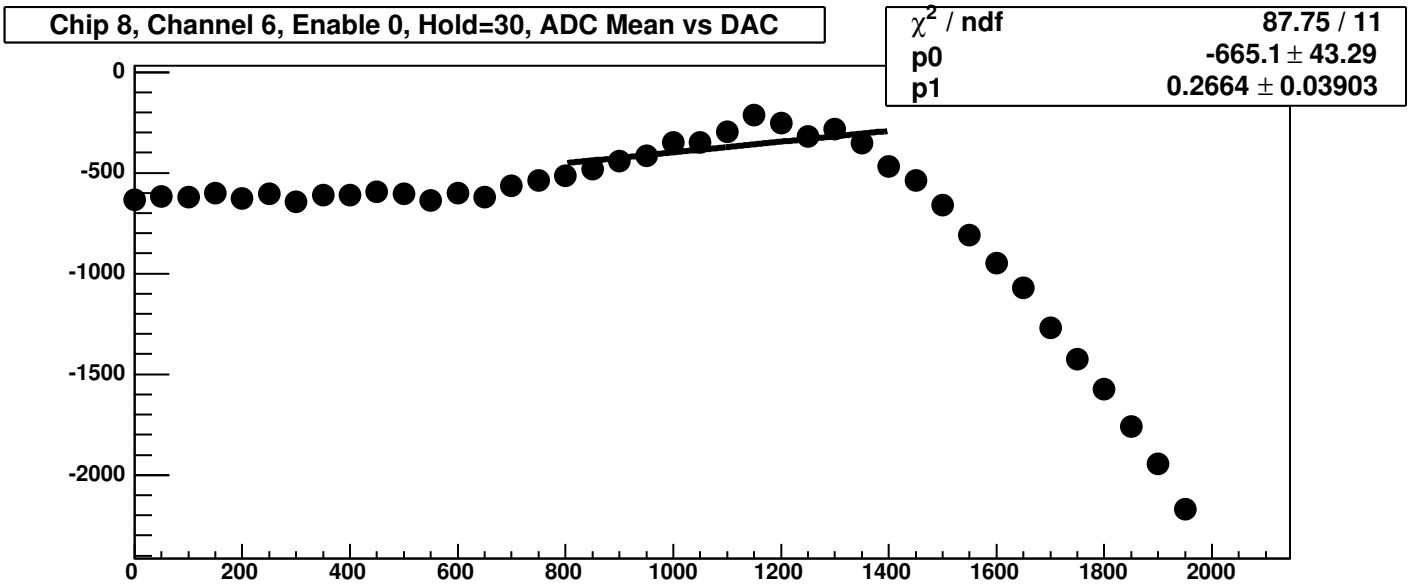

Chip 8, Channel 5, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

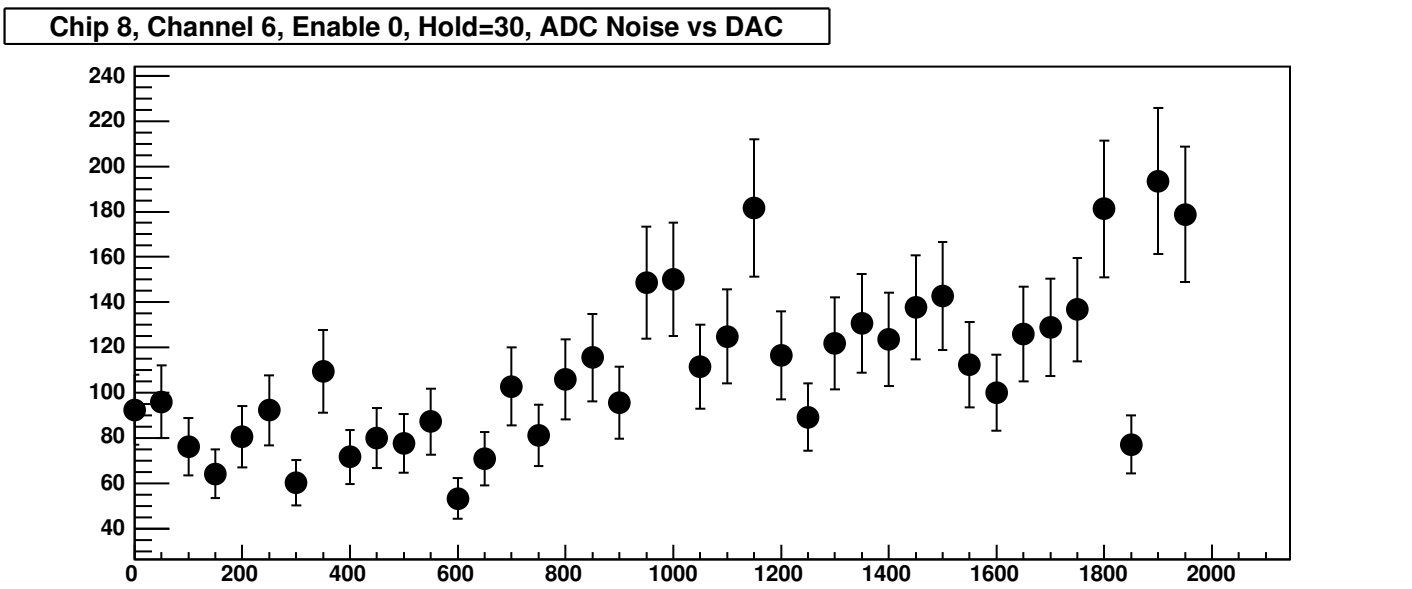

Chip 8, Channel 5, Enable 4, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

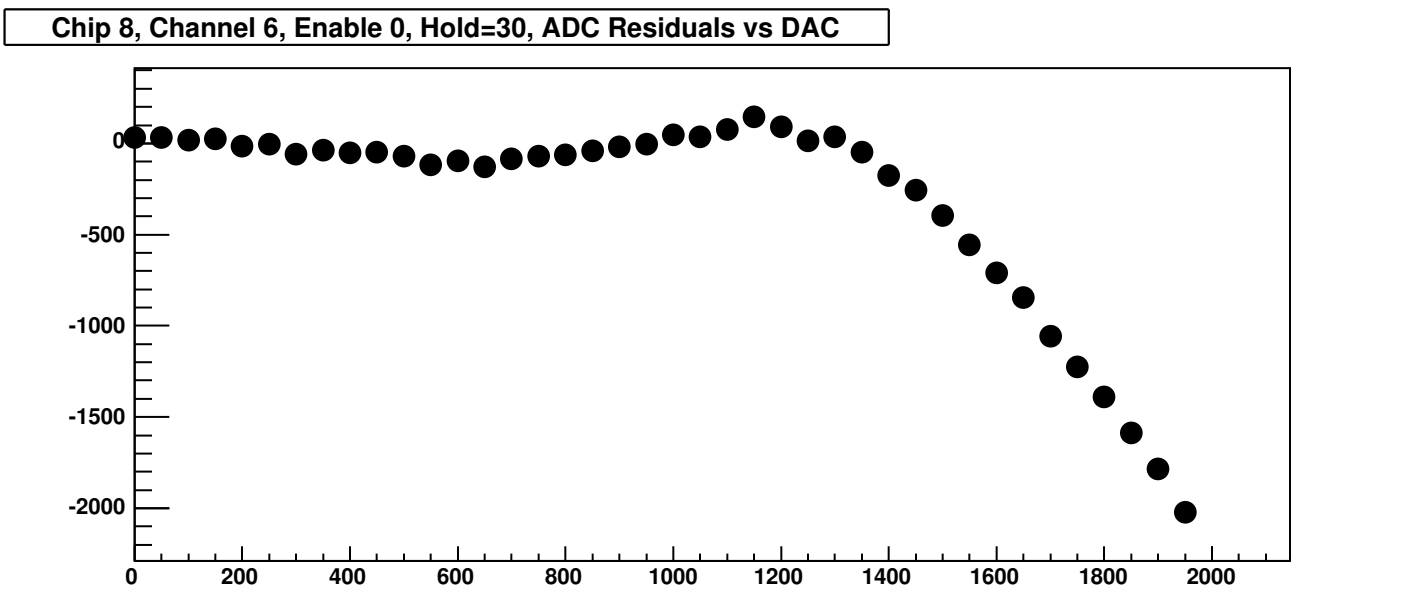

Chip 8, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 15.35 / 11
p0 -853.5 ± 21.69
p1 0.4299 ± 0.02023
100
0
-100
-200
-300
-400
-500
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

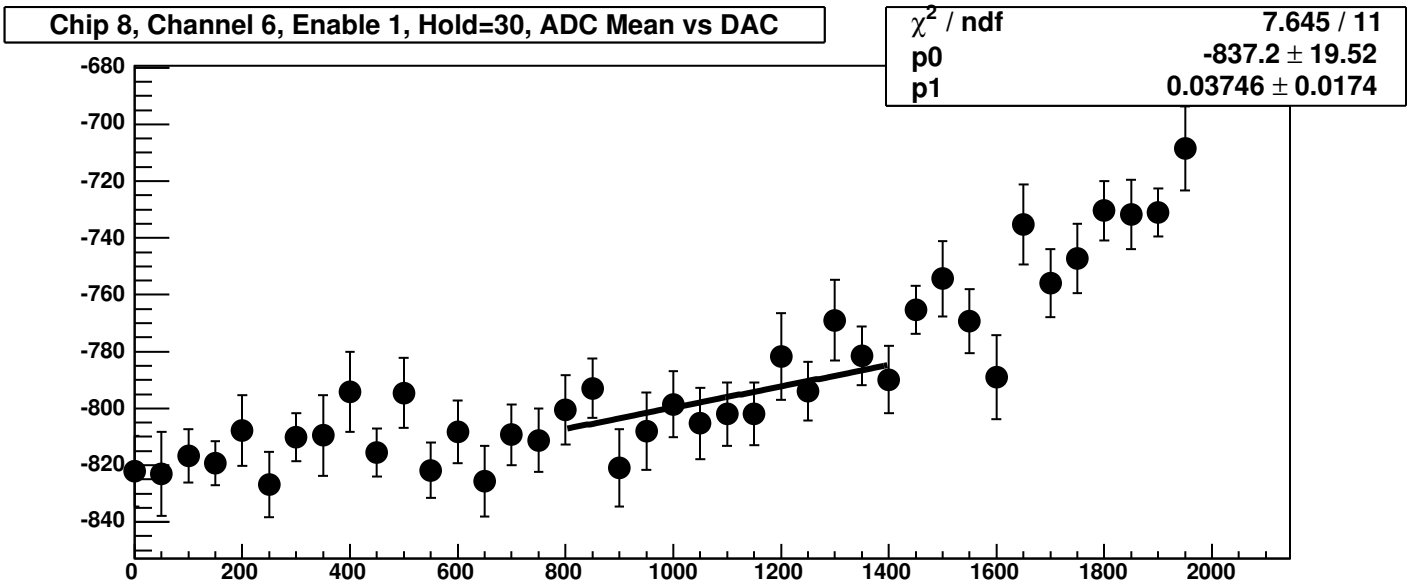

Chip 8, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

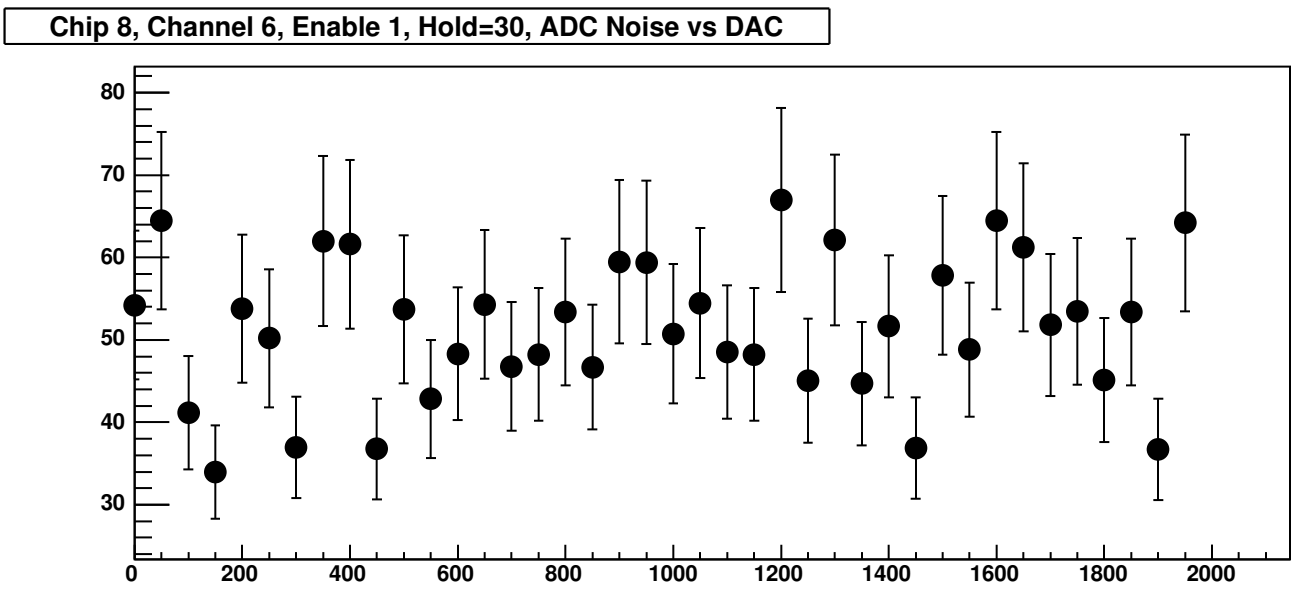

Chip 8, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

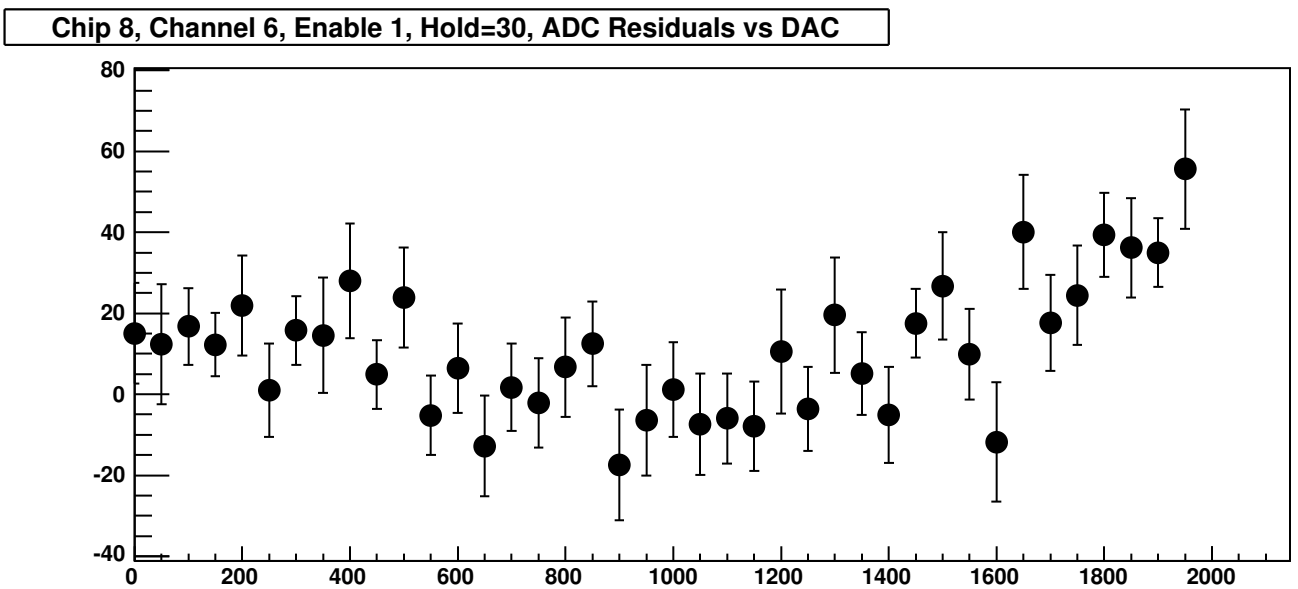

Chip 8, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 87.75 / 11
p0 -665.1 ± 43.29
p1 0.2664 ± 0.03903
0
-500
-1000
-1500
-2000
0 200 400 600 800 1000 1200 1400 1600 1800 2000


Chip 8, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC
240
220
200
180
160
140
120
100
80
60
40
0 200 400 600 800 1000 1200 1400 1600 1800 2000

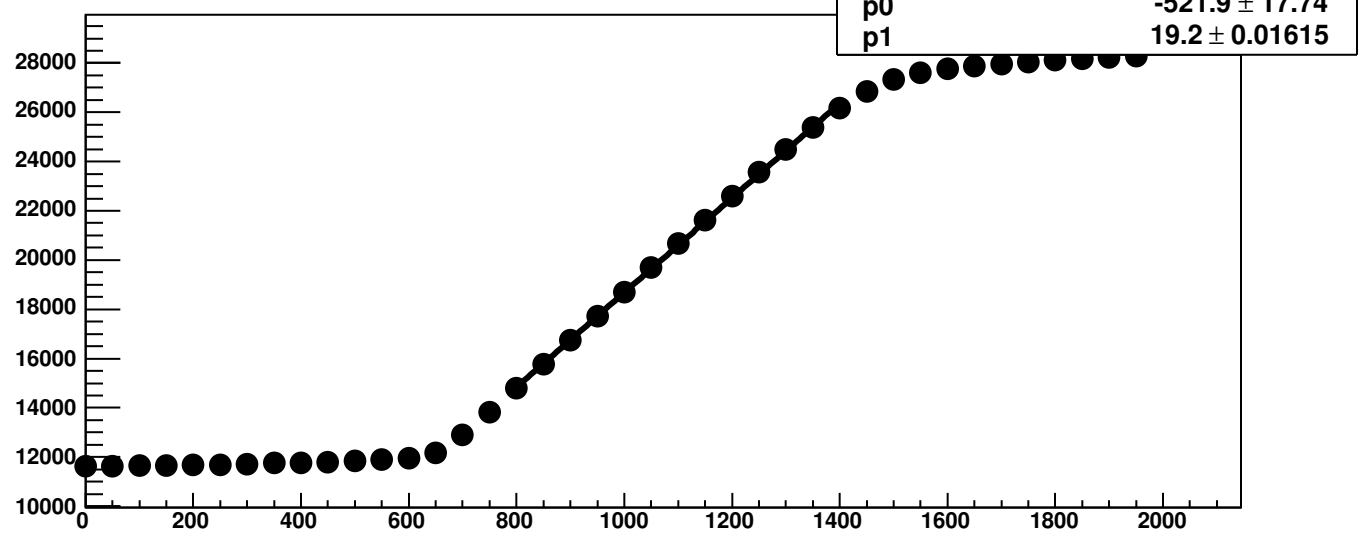

Chip 8, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0 200 400 600 800 1000 1200 1400 1600 1800 2000


Chip 8, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7.645 / 11
p0 -837.2 ± 19.52
p1 0.03746 ± 0.0174
-680
-700
-720
-740
-760
-780
-800
-820
-840
0
200
400
600
800
1000
1200
1400
1600
1800
2000

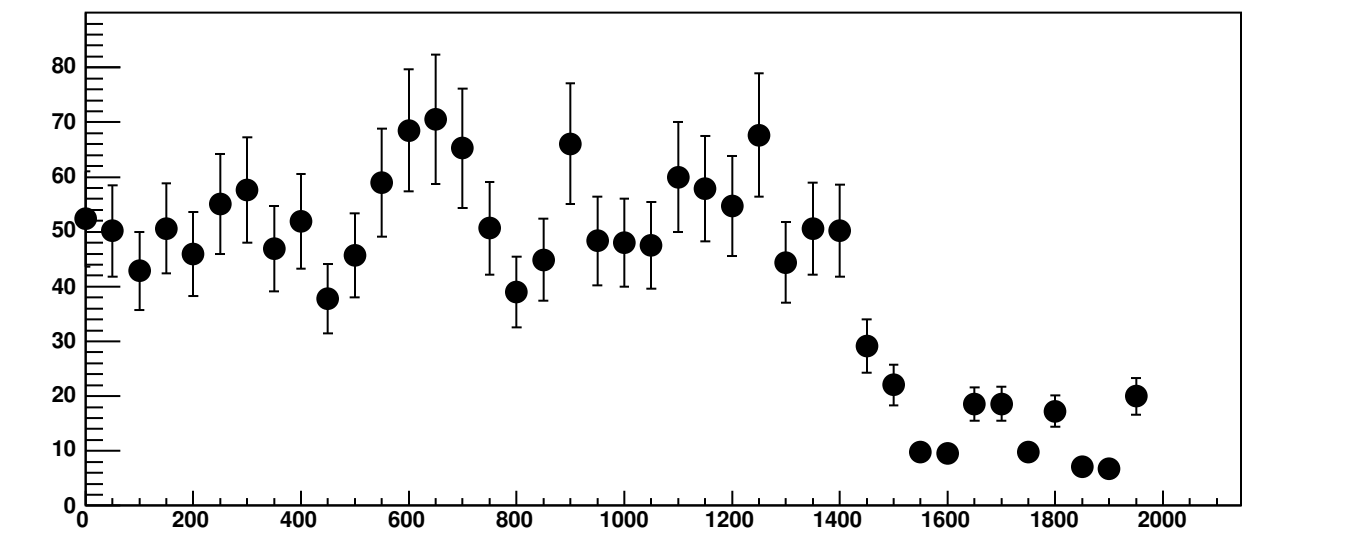

Chip 8, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

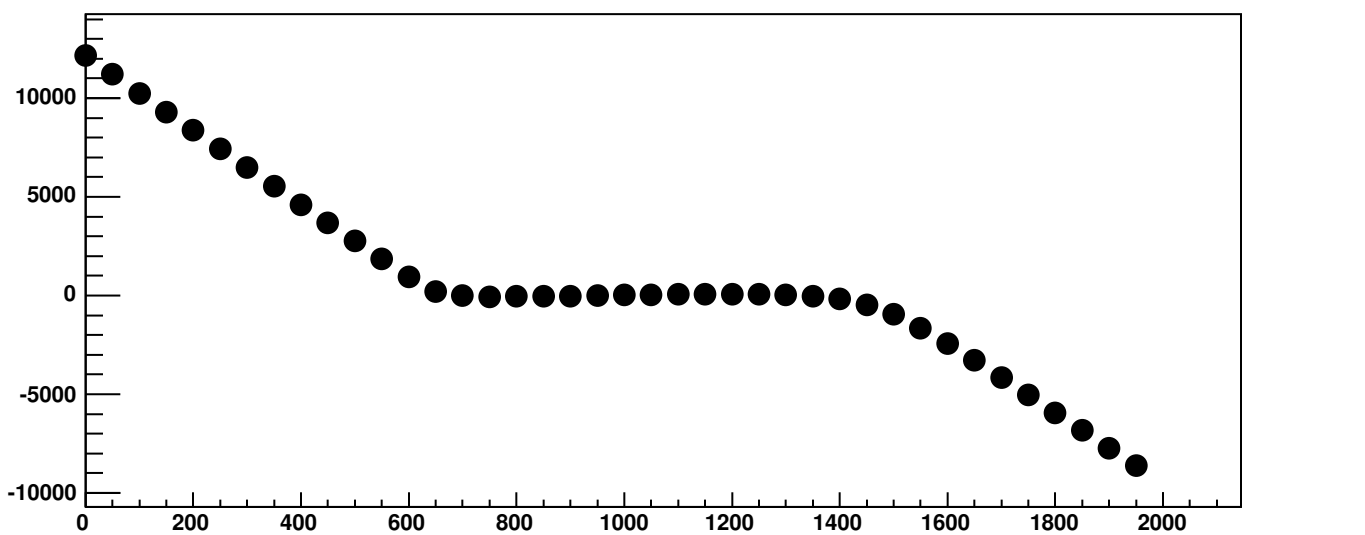
Chip 8, Channel 6, Enable 2!, Hold=30, ADC Mean vs DAC

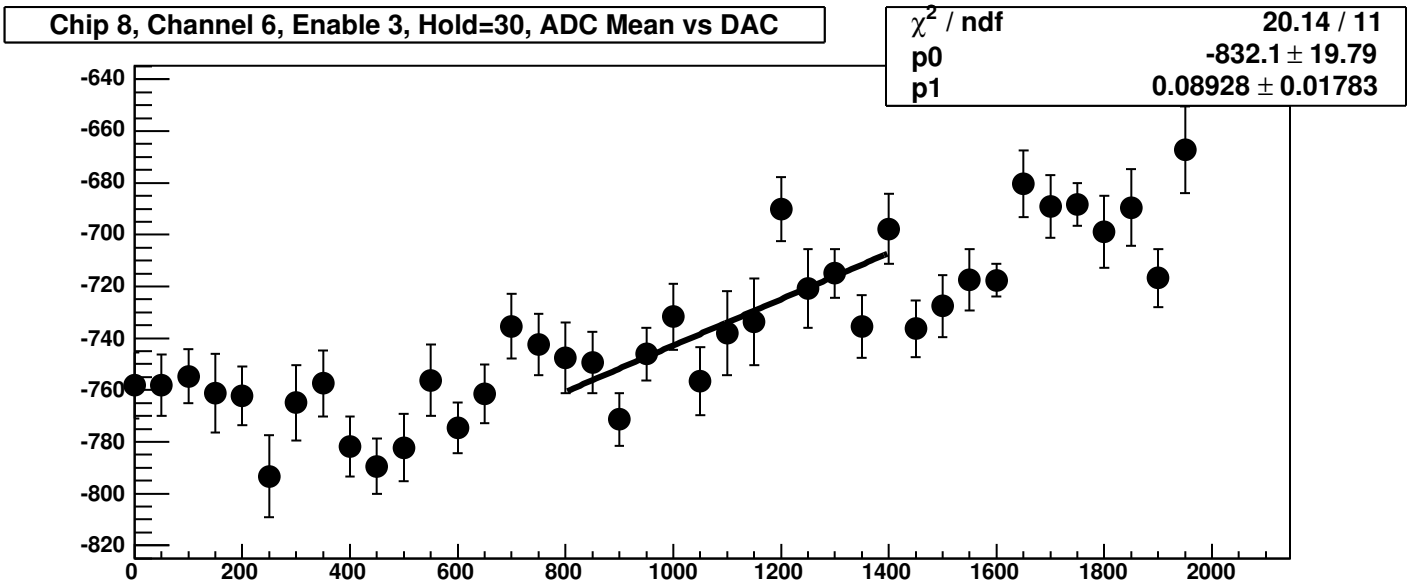


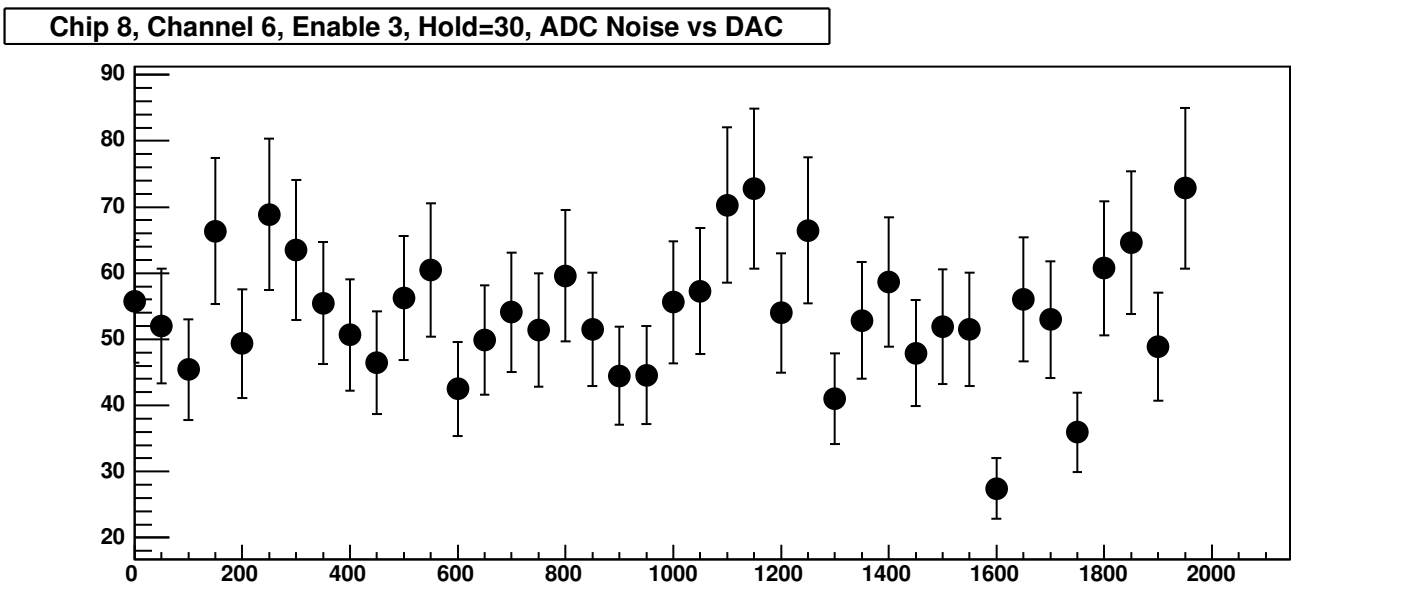
Chip 8, Channel 6, Enable 2!, Hold=30, ADC Noise vs DAC

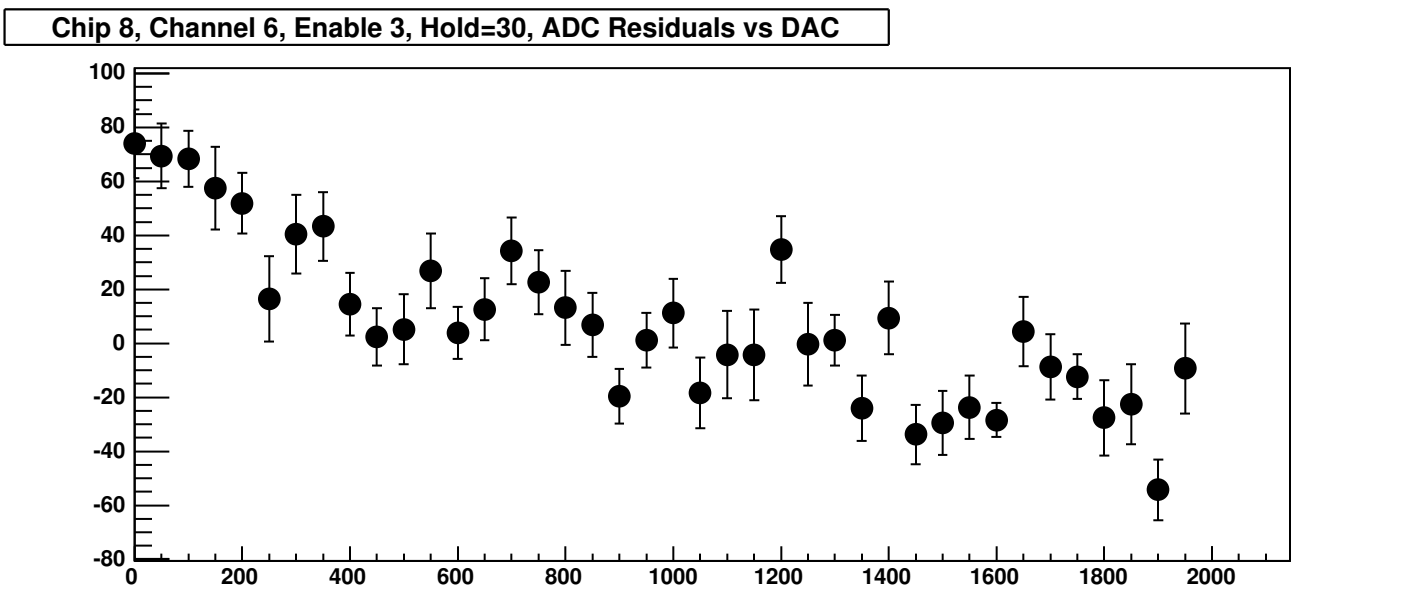


Chip 8, Channel 6, Enable 2!, Hold=30, ADC Residuals vs DAC

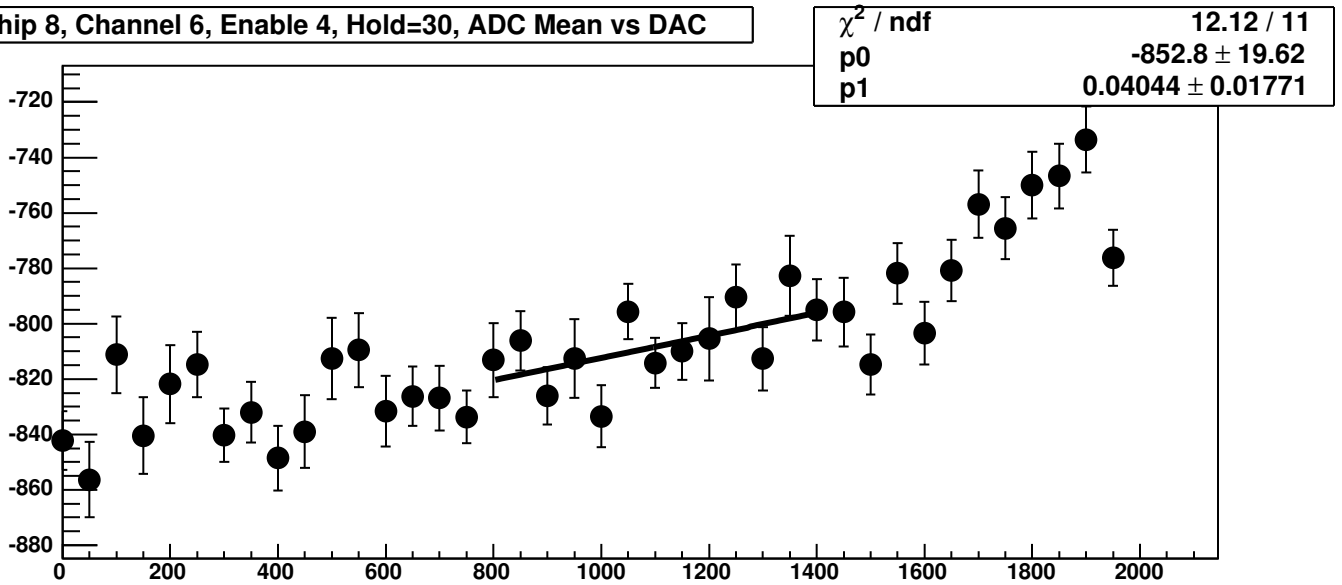



Chip 8, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 20.14 / 11
p0 -832.1 ± 19.79
p1 0.08928 ± 0.01783
-640
-660
-680
-700
-720
-740
-760
-780
-800
-820
0
200
400
600
800
1000
1200
1400
1600
1800
2000

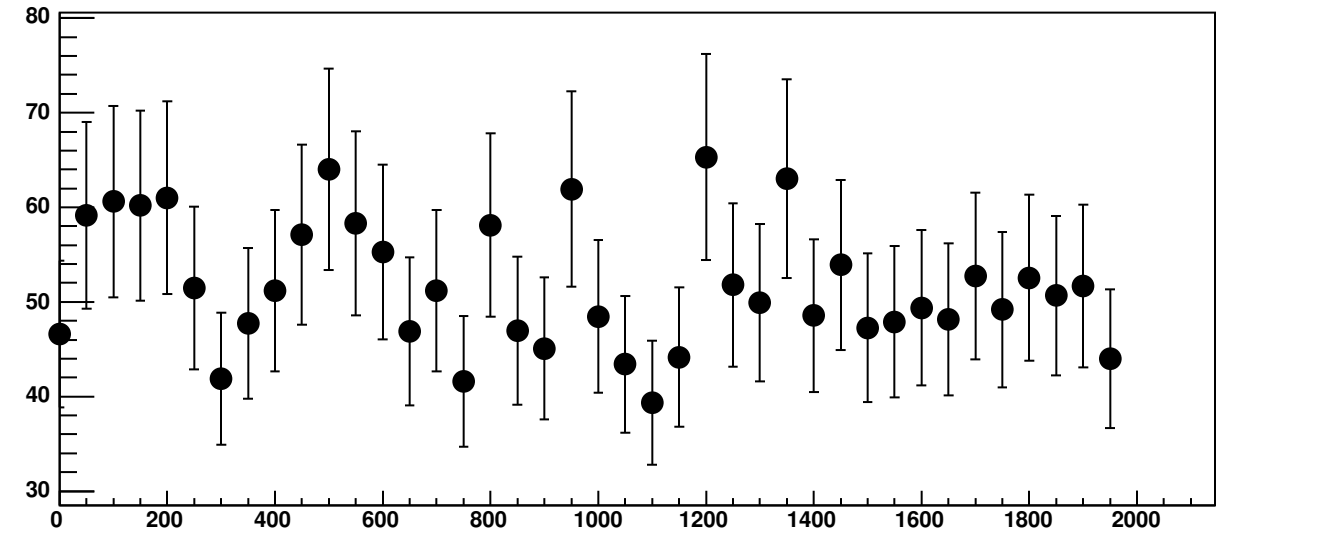

Chip 8, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
-60
-80
0
200
400
600
800
1000
1200
1400
1600
1800
2000

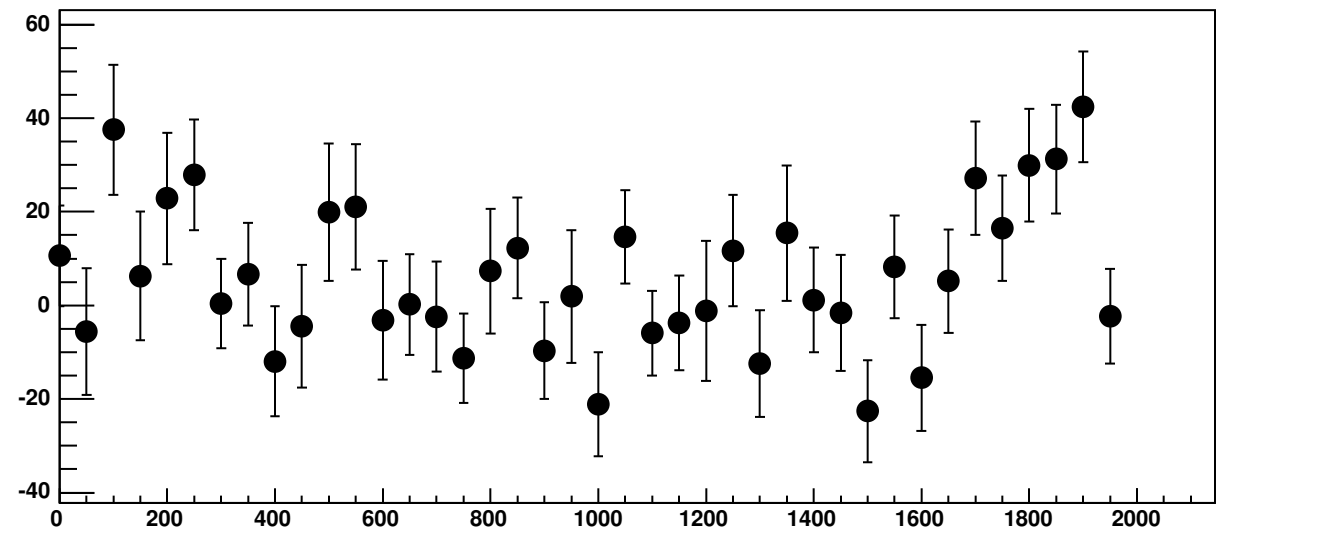
Chip 8, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC

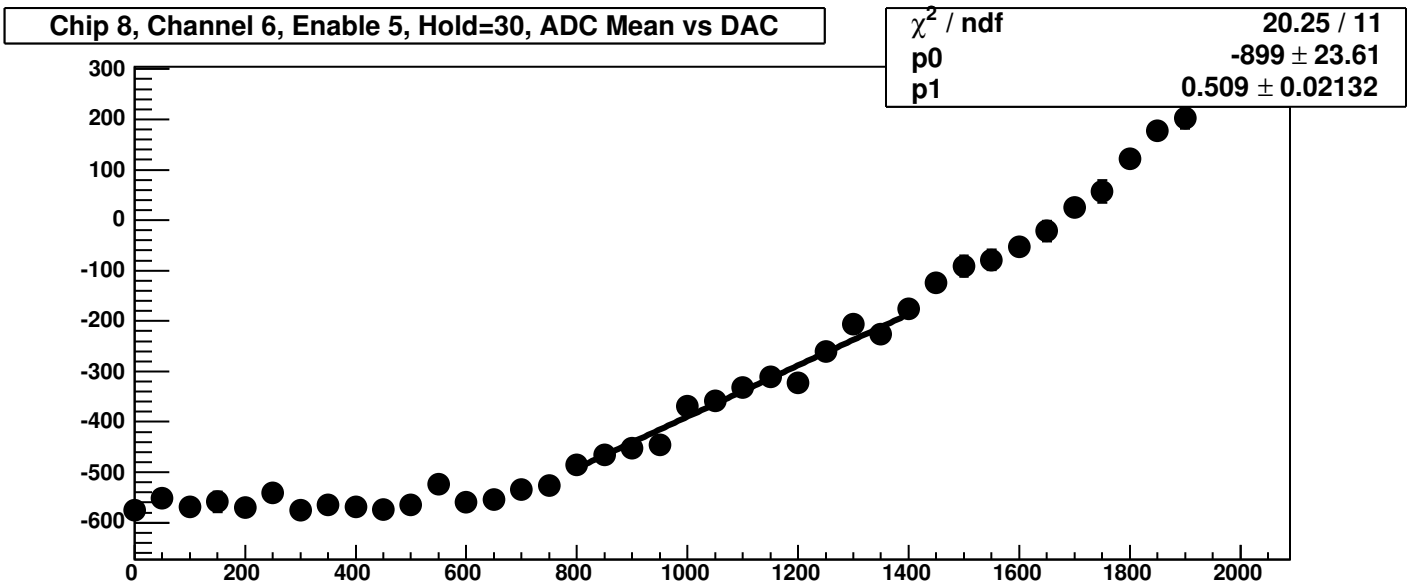


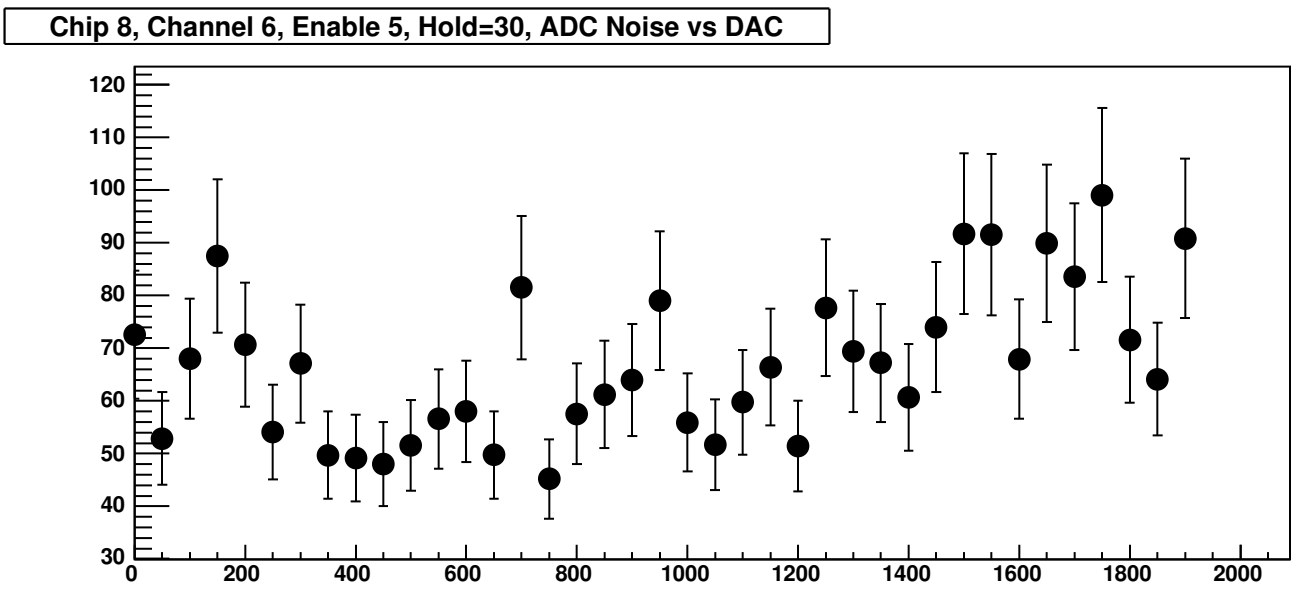
Chip 8, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC

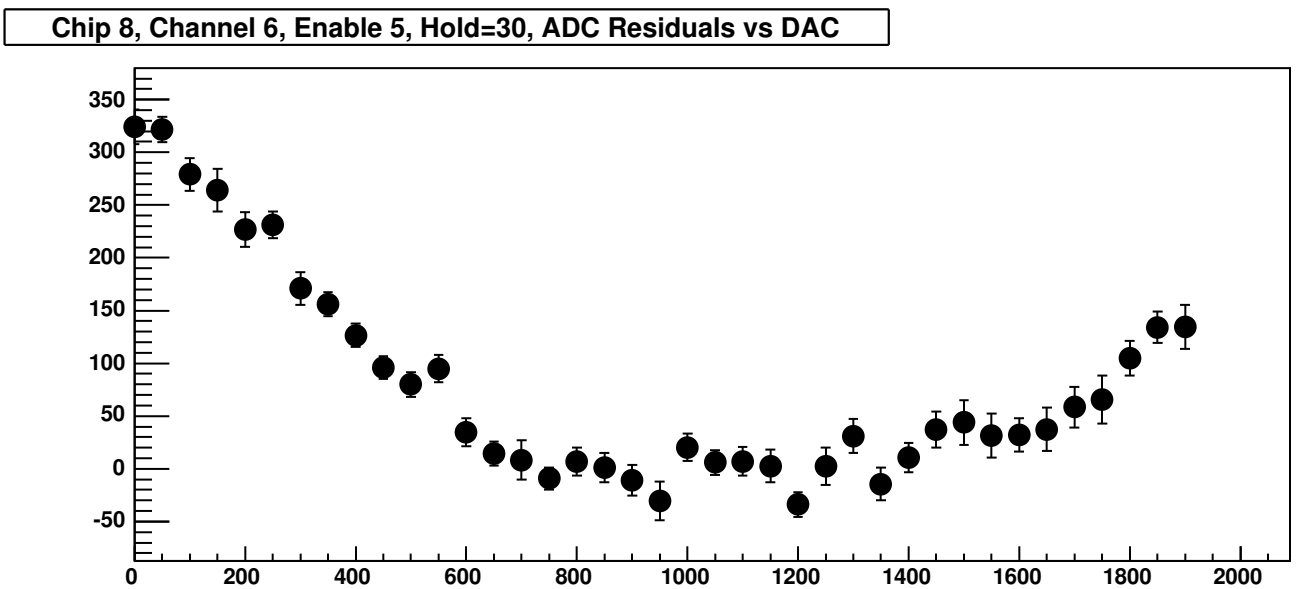


Chip 8, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC

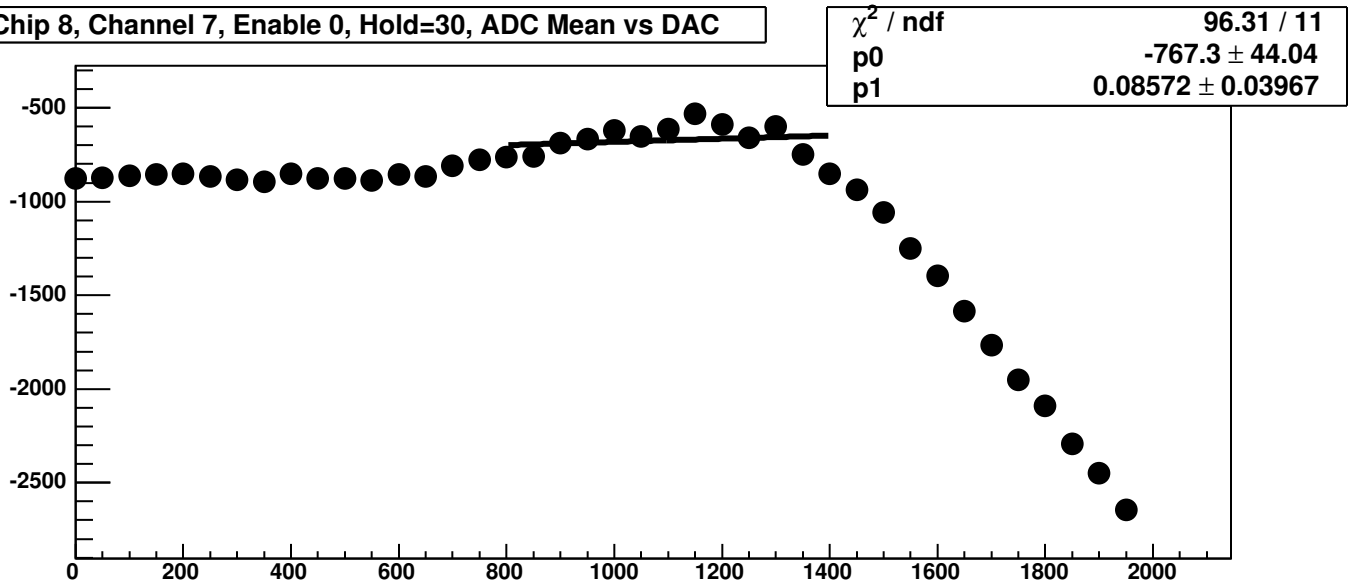



Chip 8, Channel 6, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 20.25 / 11
p0 -899 ± 23.61
p1 0.509 ± 0.02132
300
200
100
0
-100
-200
-300
-400
-500
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

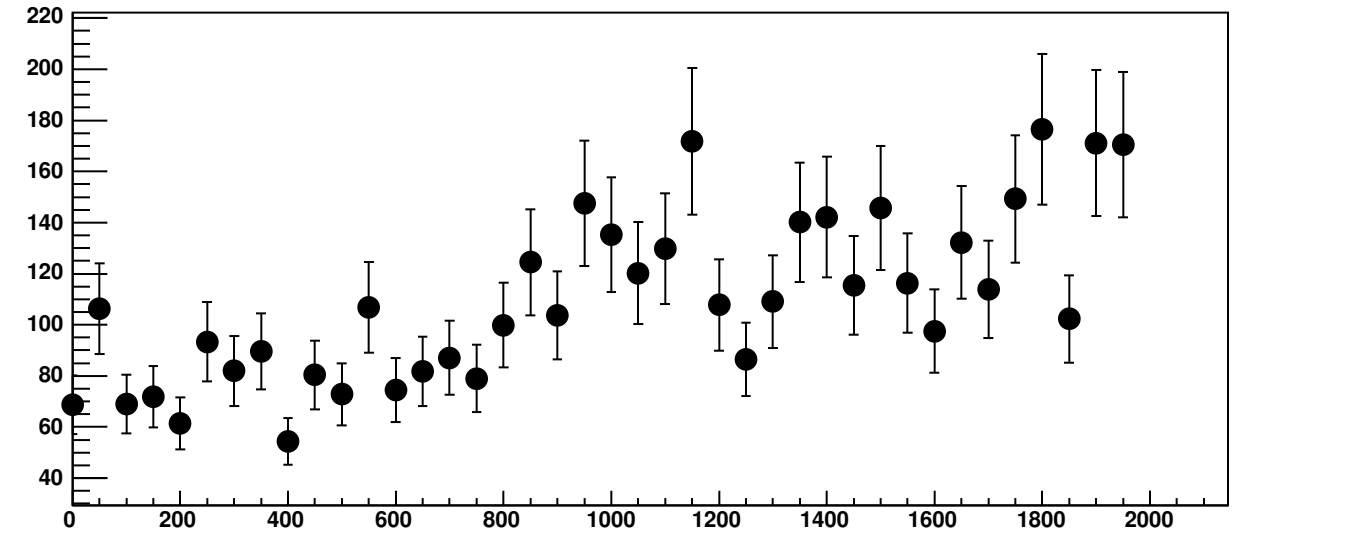

Chip 8, Channel 6, Enable 5, Hold=30, ADC Noise vs DAC
120
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 6, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

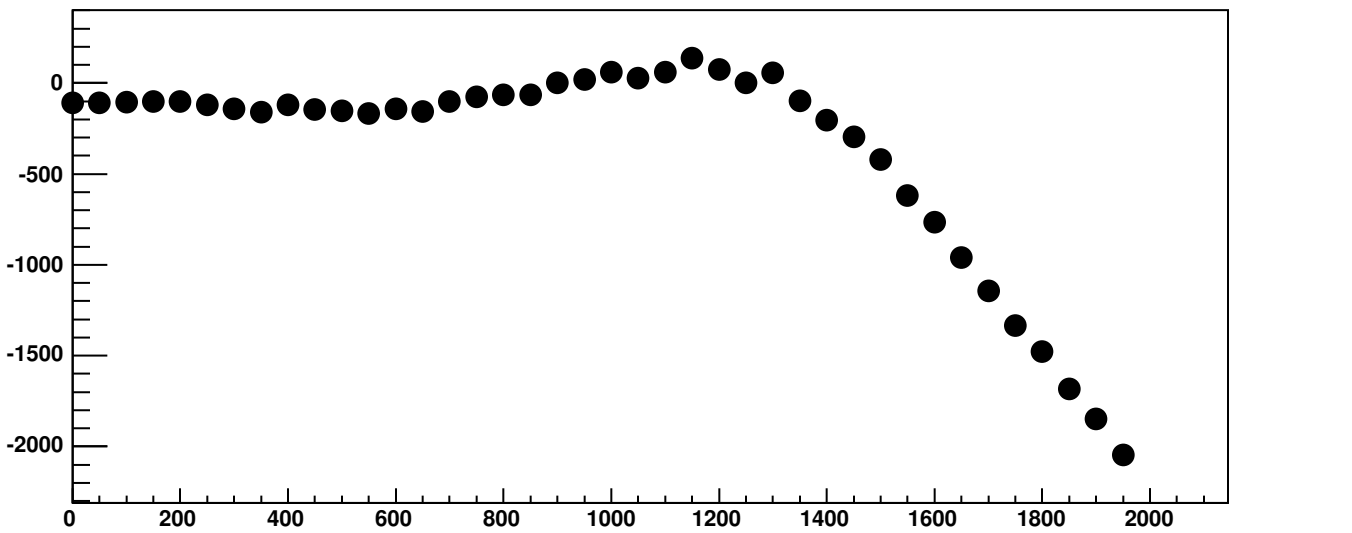
Chip 8, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC

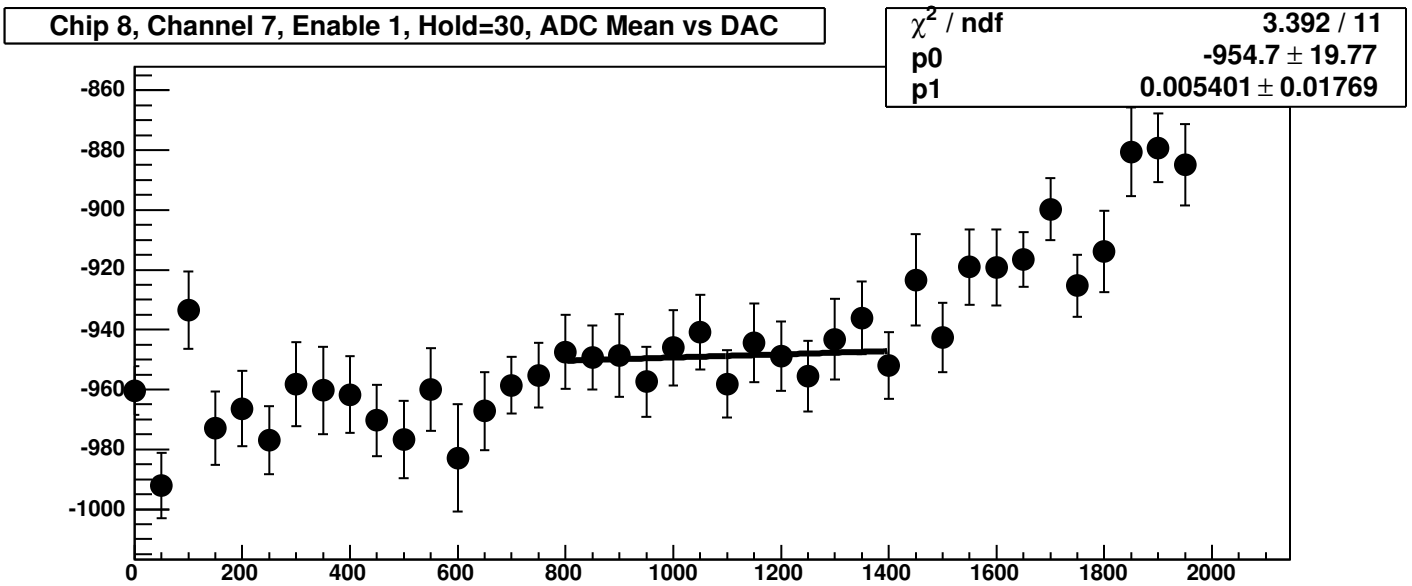


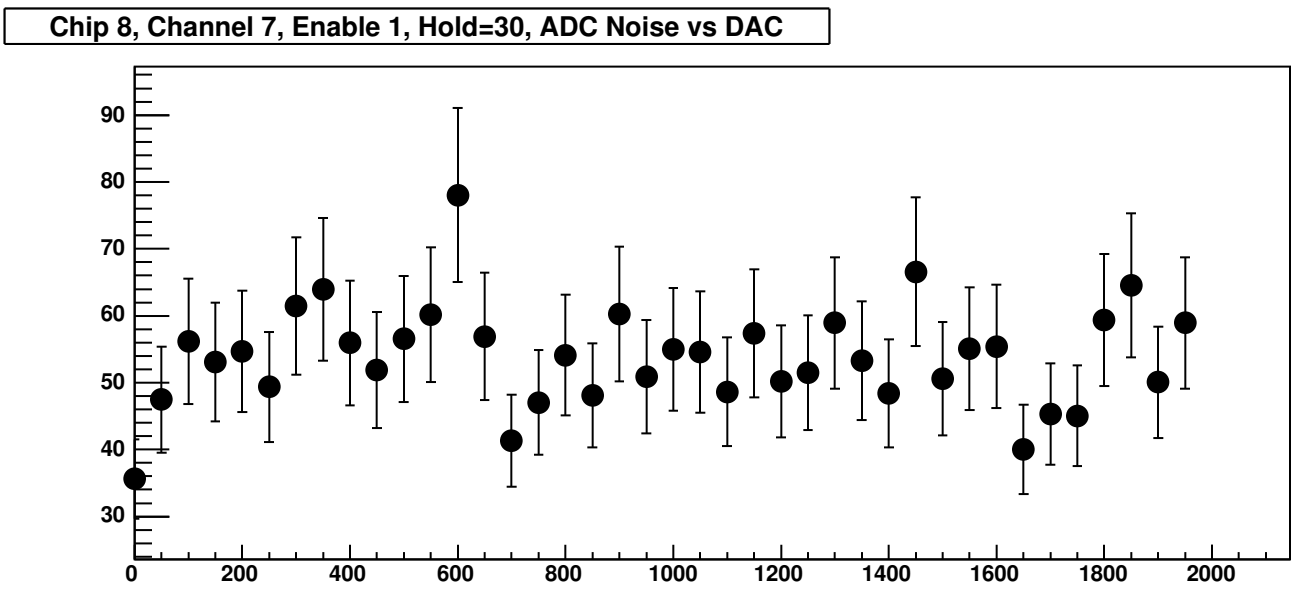
Chip 8, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC

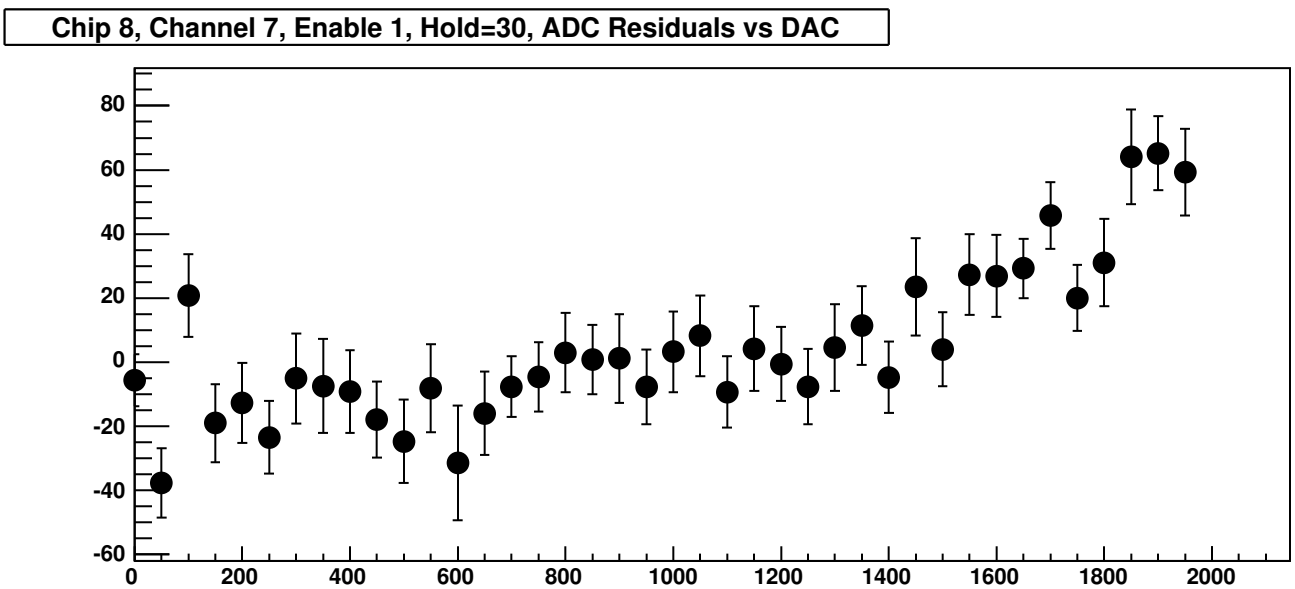


Chip 8, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC

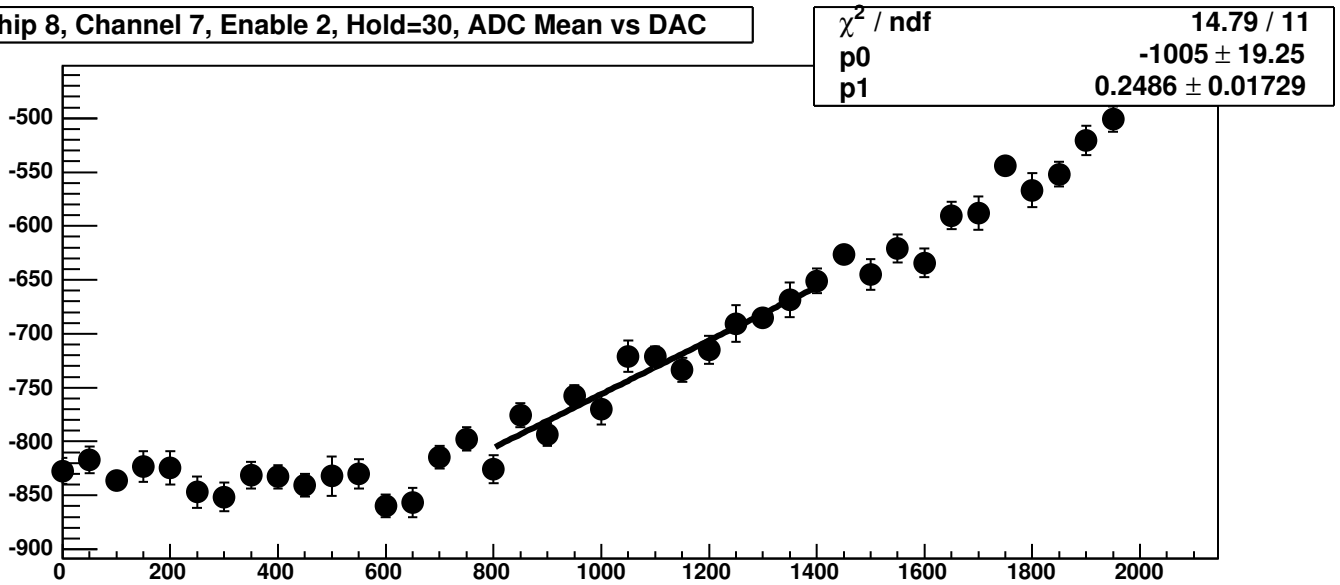



Chip 8, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3.392 / 11
p0 -954.7 ± 19.77
p1 0.005401 ± 0.01769
-860
-880
-900
-920
-940
-960
-980
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

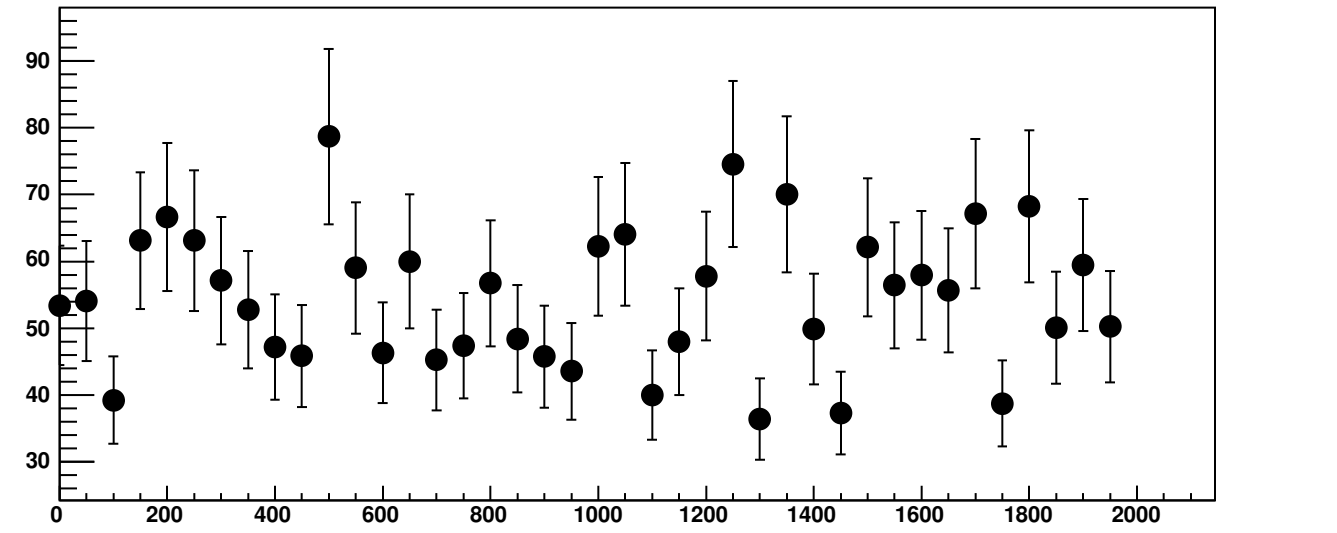

Chip 8, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

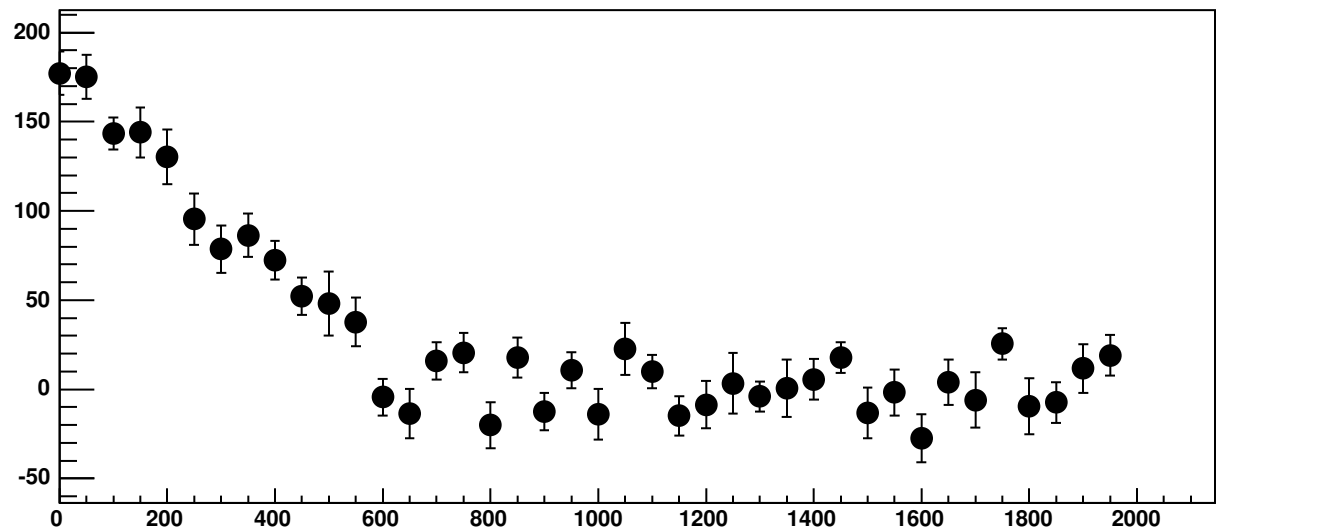
Chip 8, Channel 7, Enable 2, Hold=30, ADC Mean vs DAC

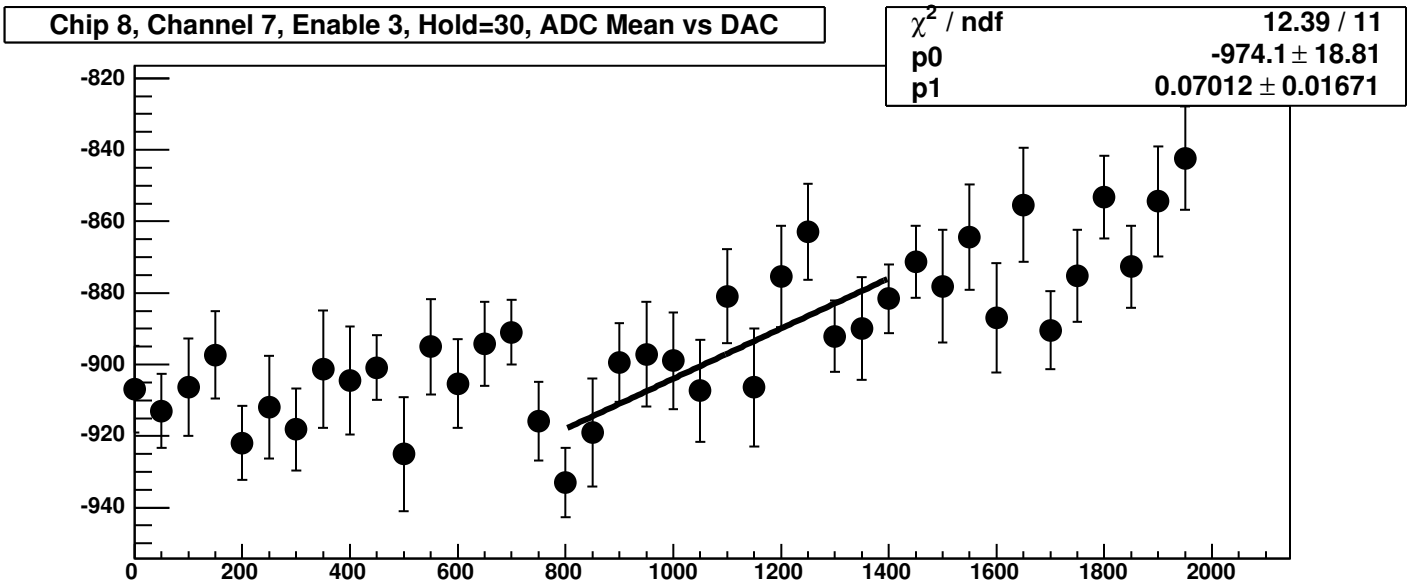


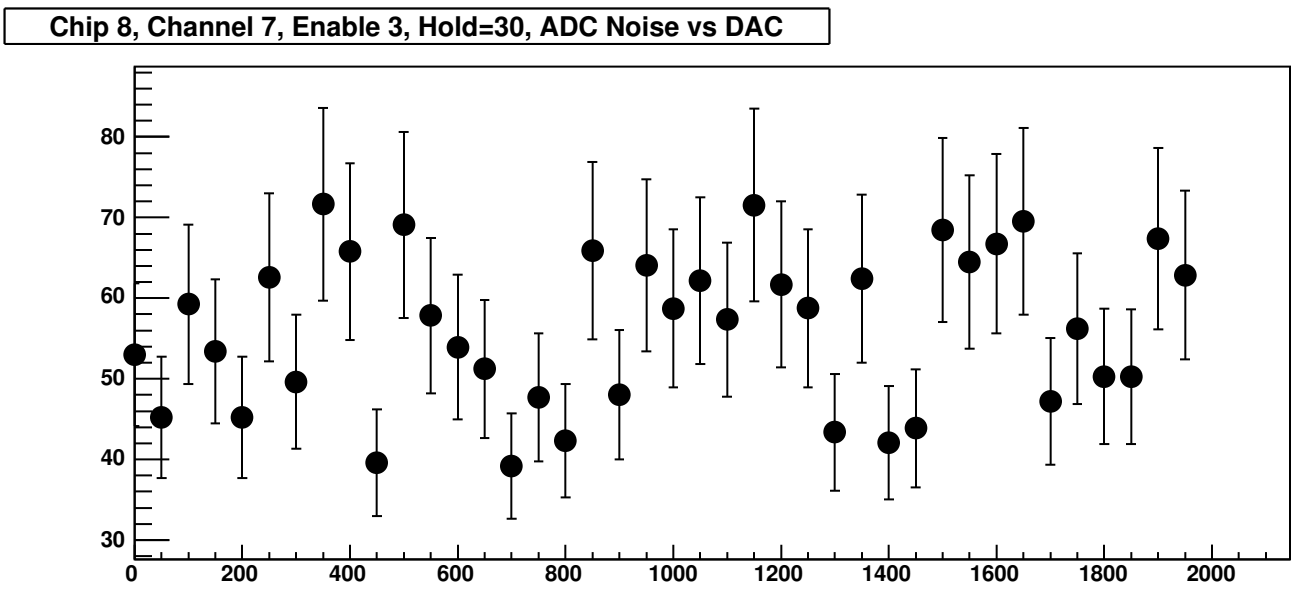
Chip 8, Channel 7, Enable 2, Hold=30, ADC Noise vs DAC

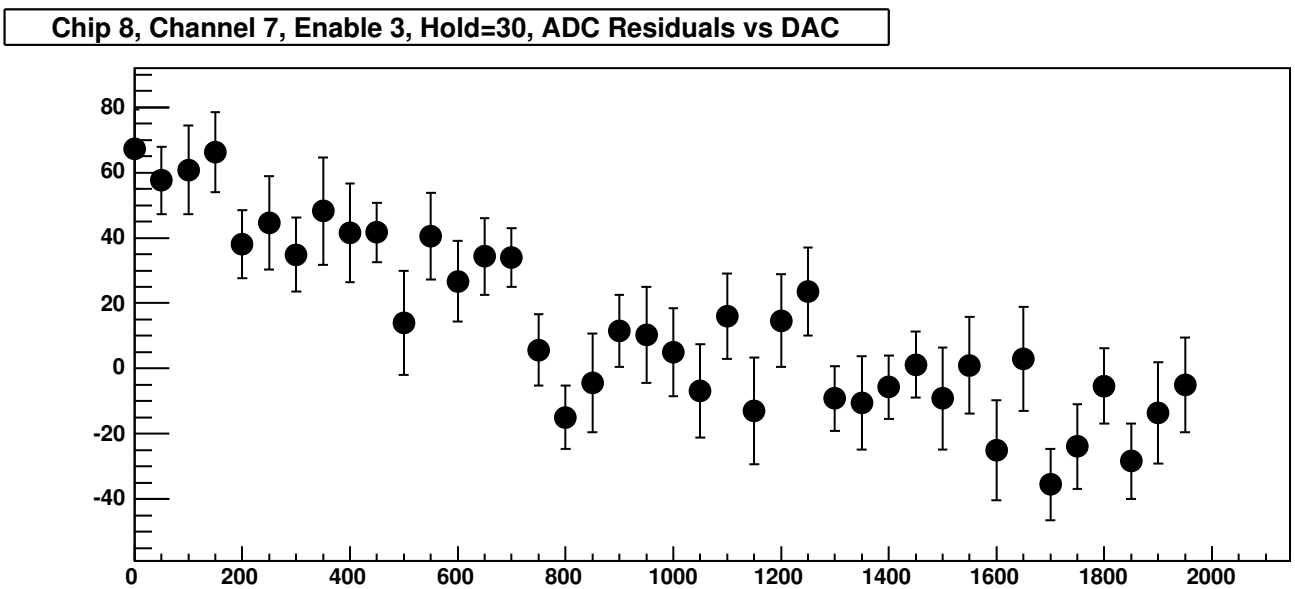


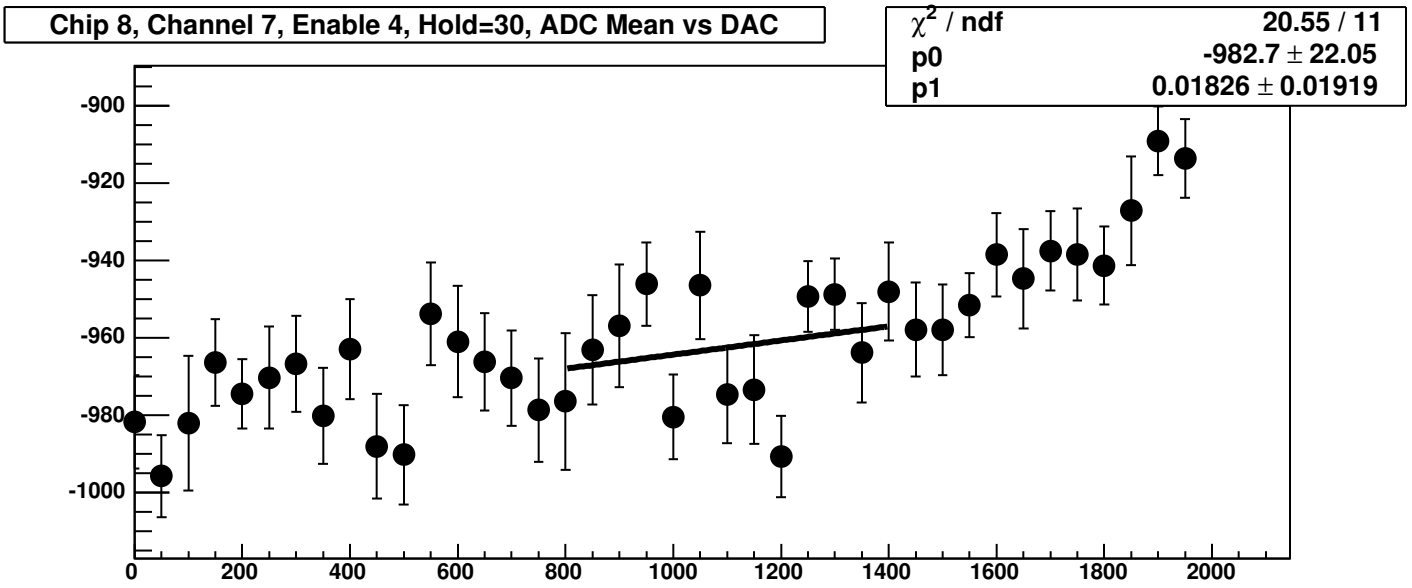
Chip 8, Channel 7, Enable 2, Hold=30, ADC Residuals vs DAC

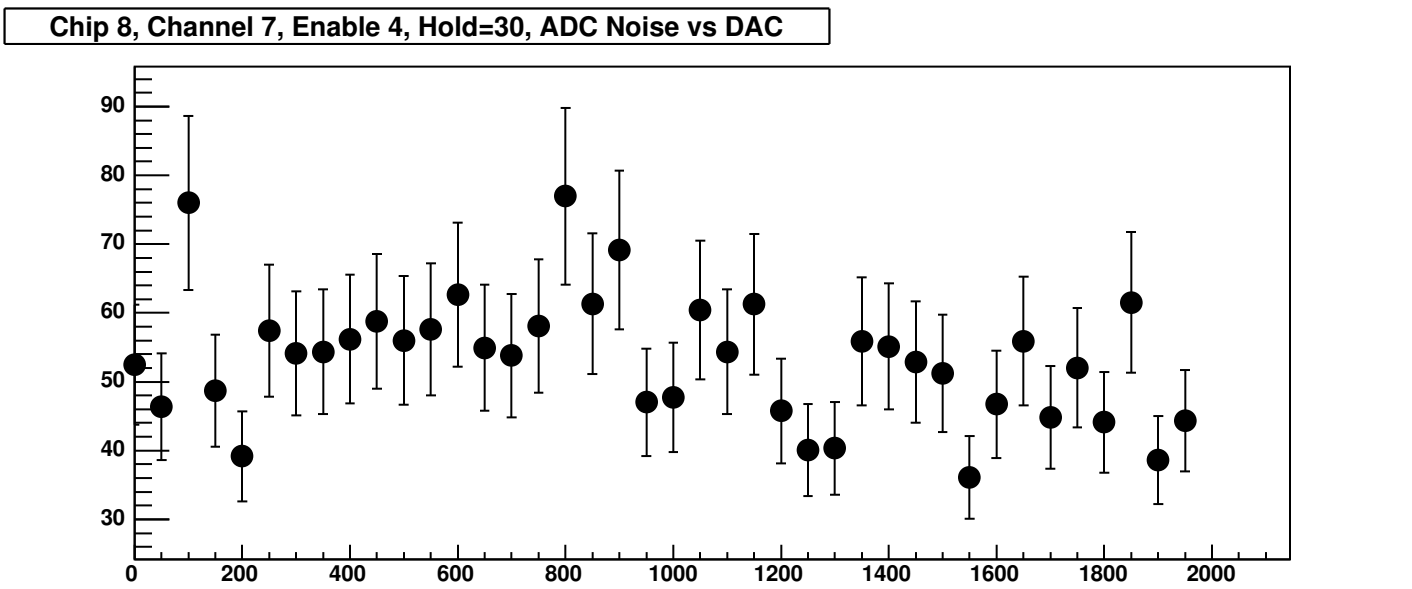


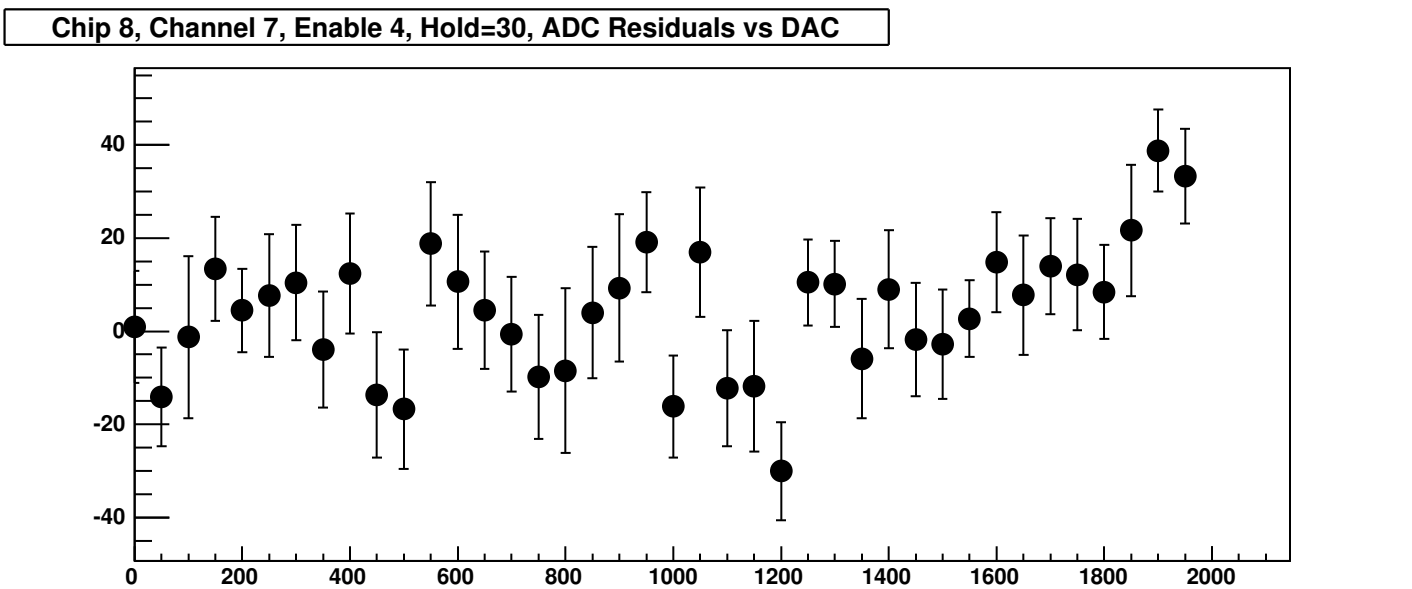

Chip 8, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 12.39 / 11
p0 -974.1 ± 18.81
p1 0.07012 ± 0.01671
-820
-840
-860
-880
-900
-920
-940
0
200
400
600
800
1000
1200
1400
1600
1800
2000

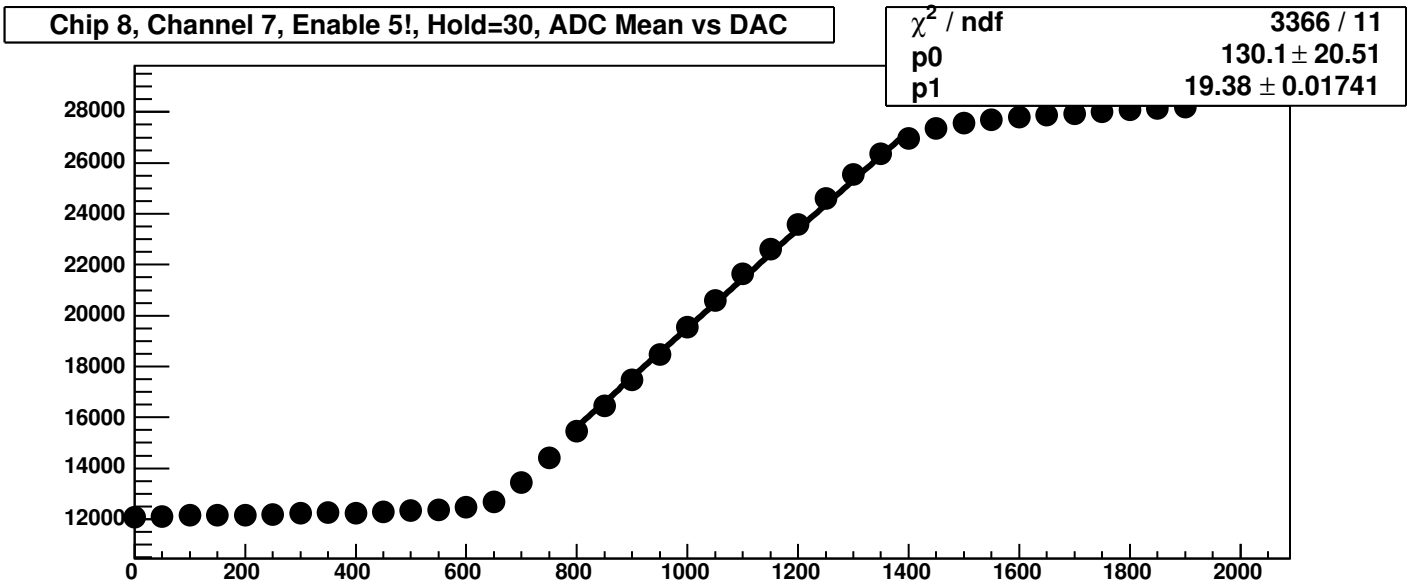

Chip 8, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

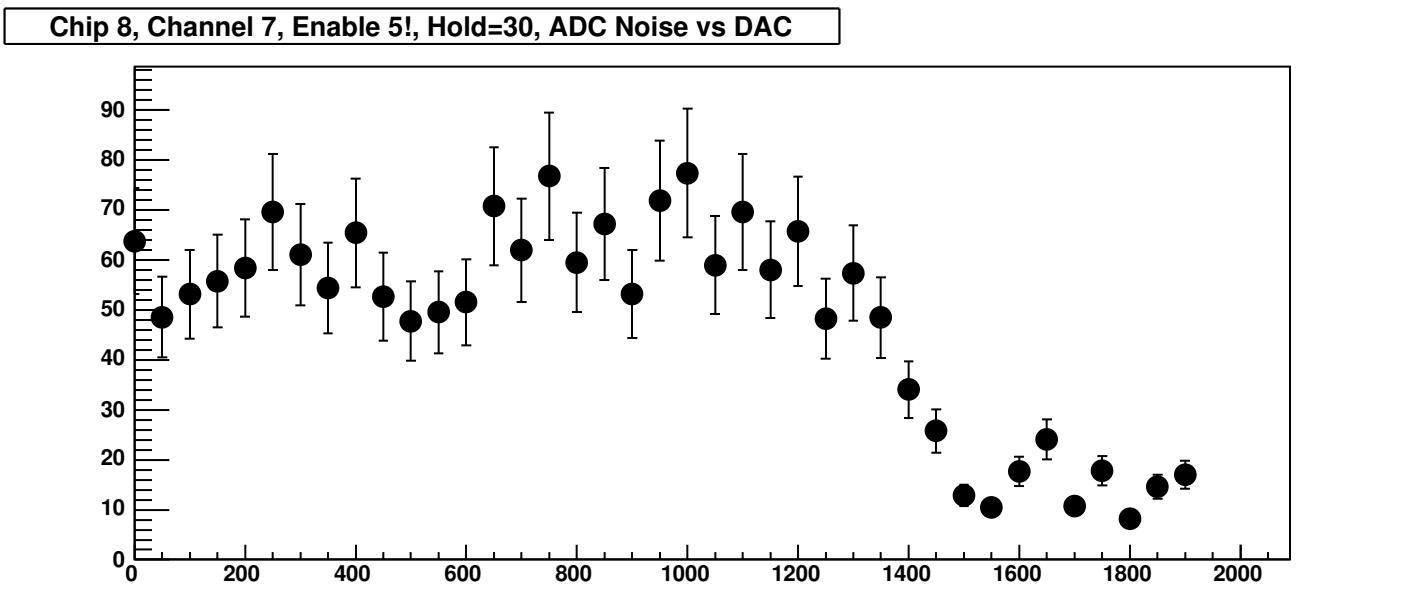

Chip 8, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

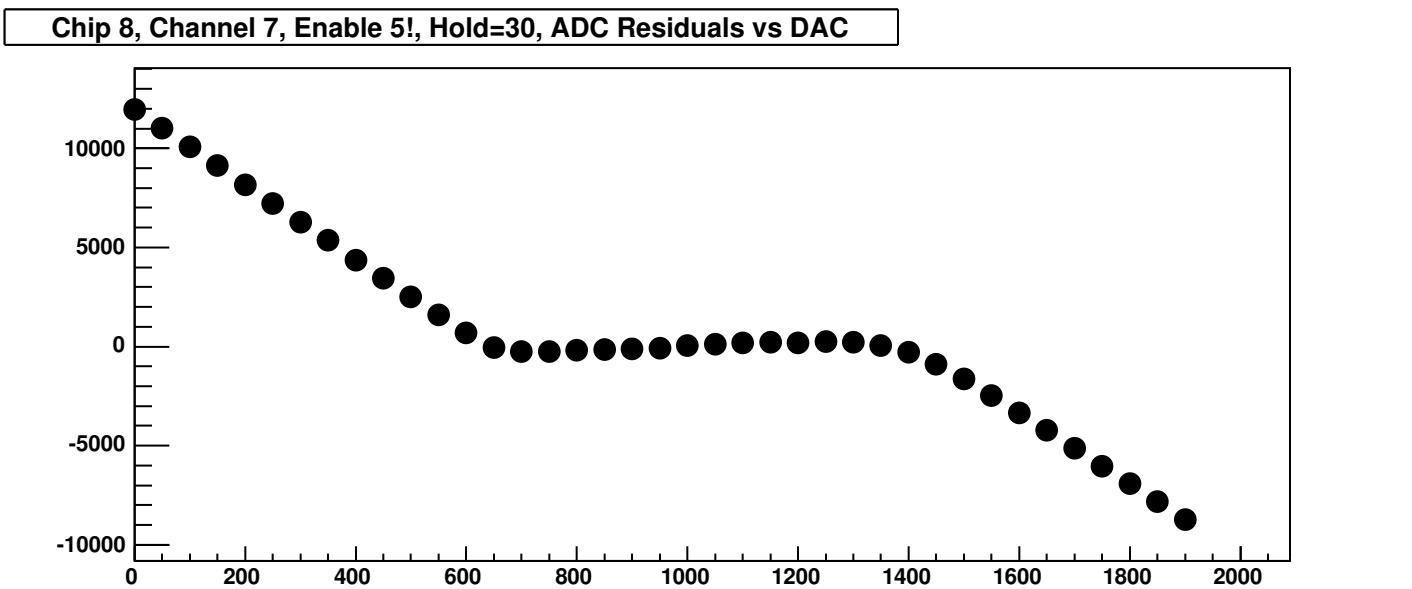

Chip 8, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 20.55 / 11
p0 -982.7 ± 22.05
p1 0.01826 ± 0.01919
-900
-920
-940
-960
-980
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

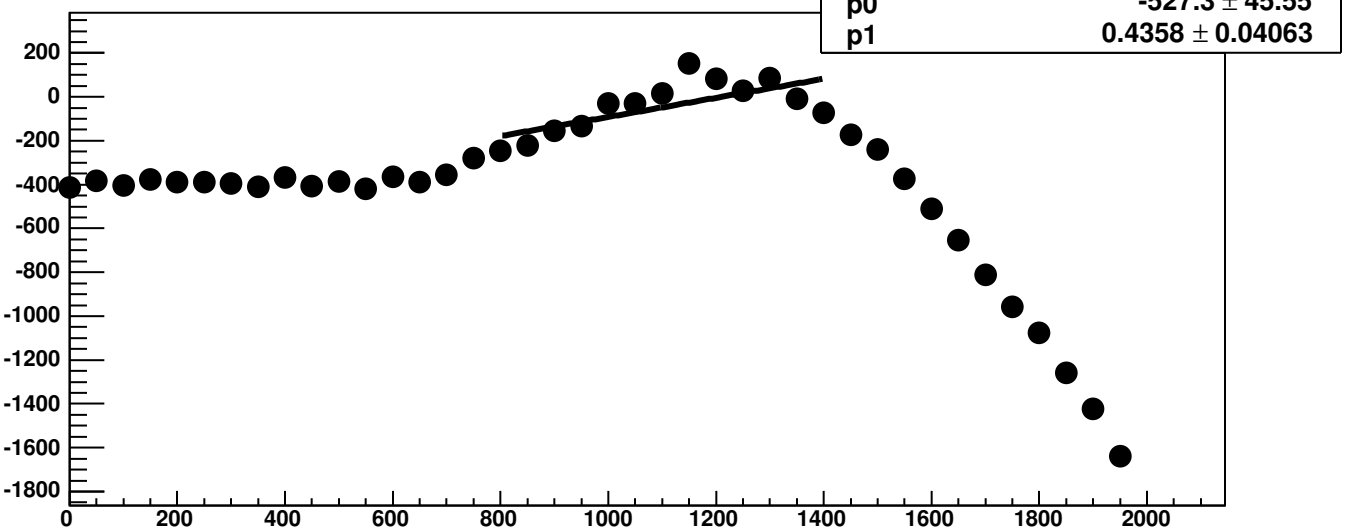

Chip 8, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 7, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 3366 / 11
p0 130.1 ± 20.51
p1 19.38 ± 0.01741
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

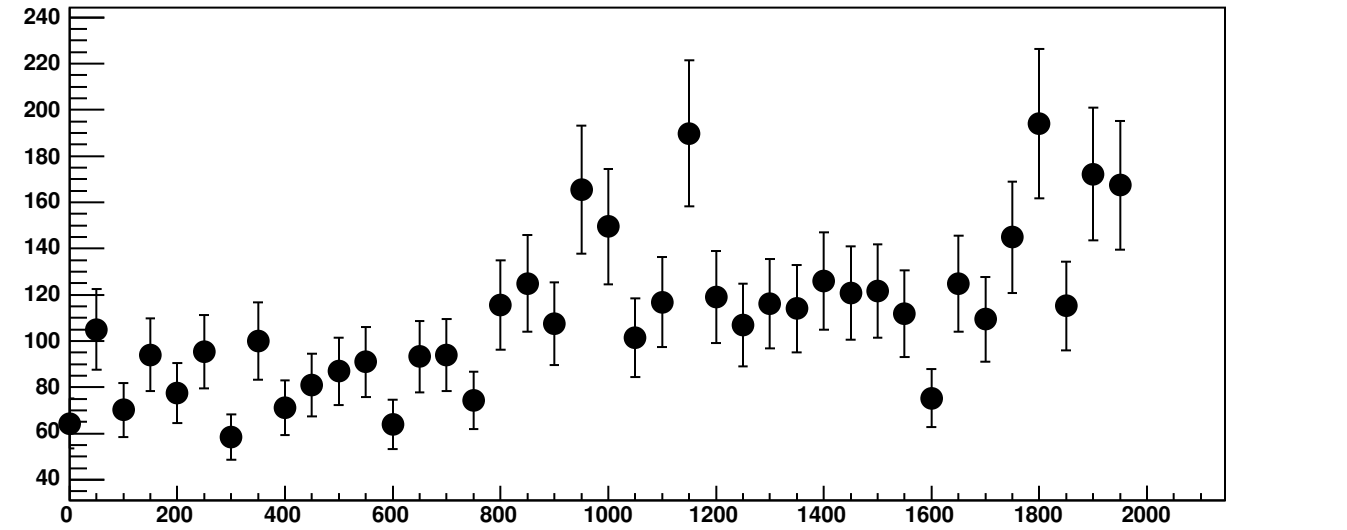

Chip 8, Channel 7, Enable 5!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 7, Enable 5!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

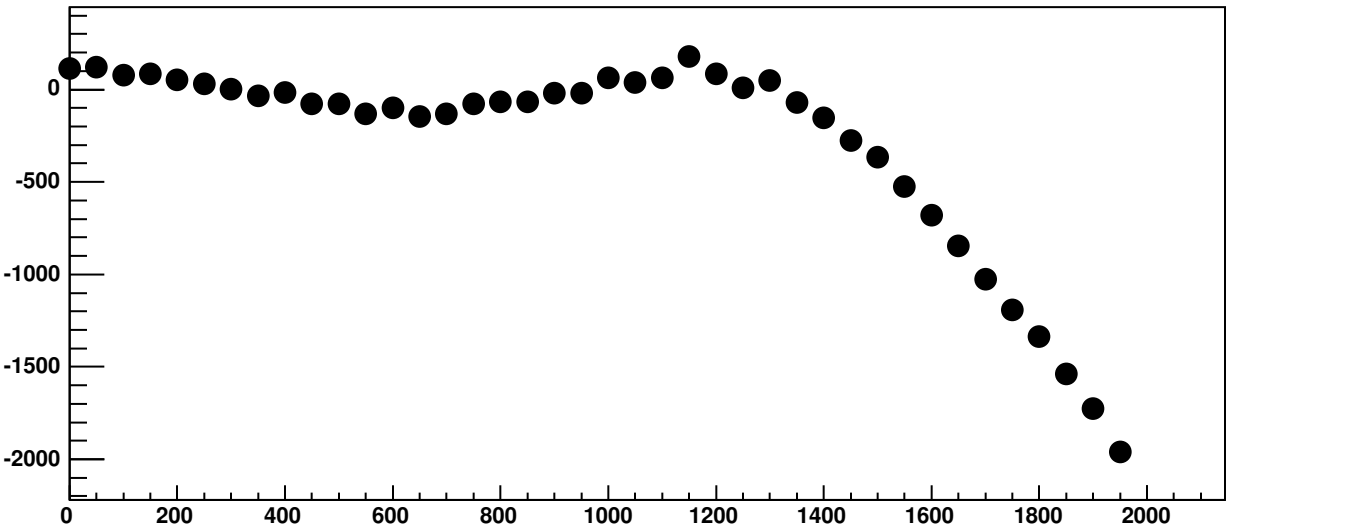
Chip 8, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC



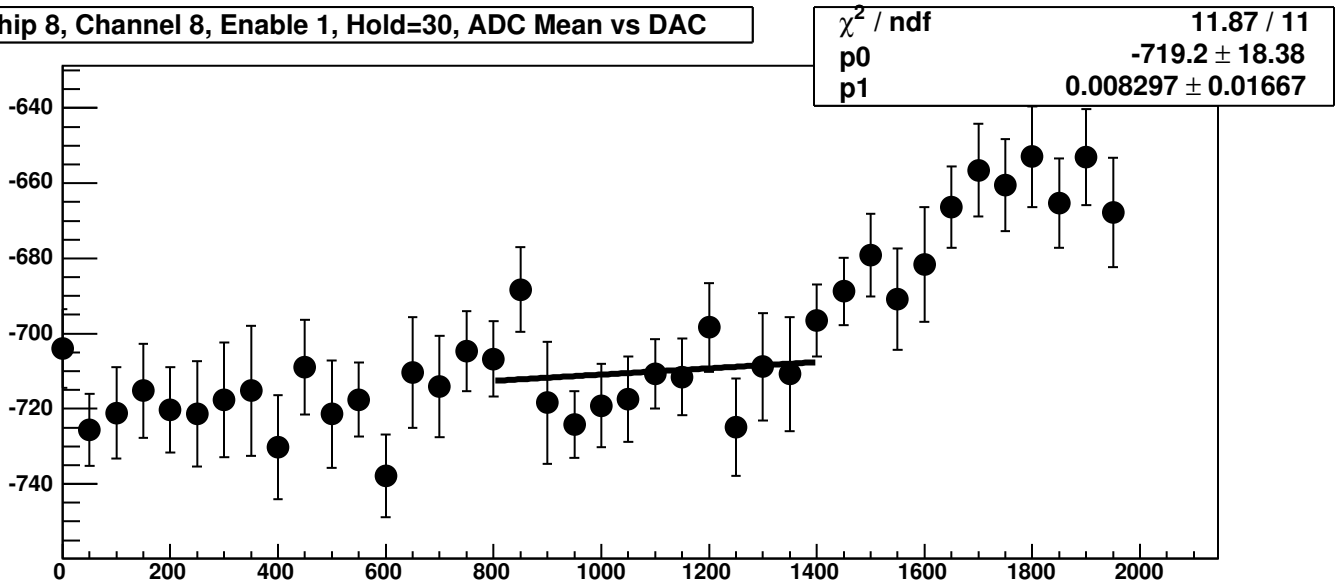
Chip 8, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC



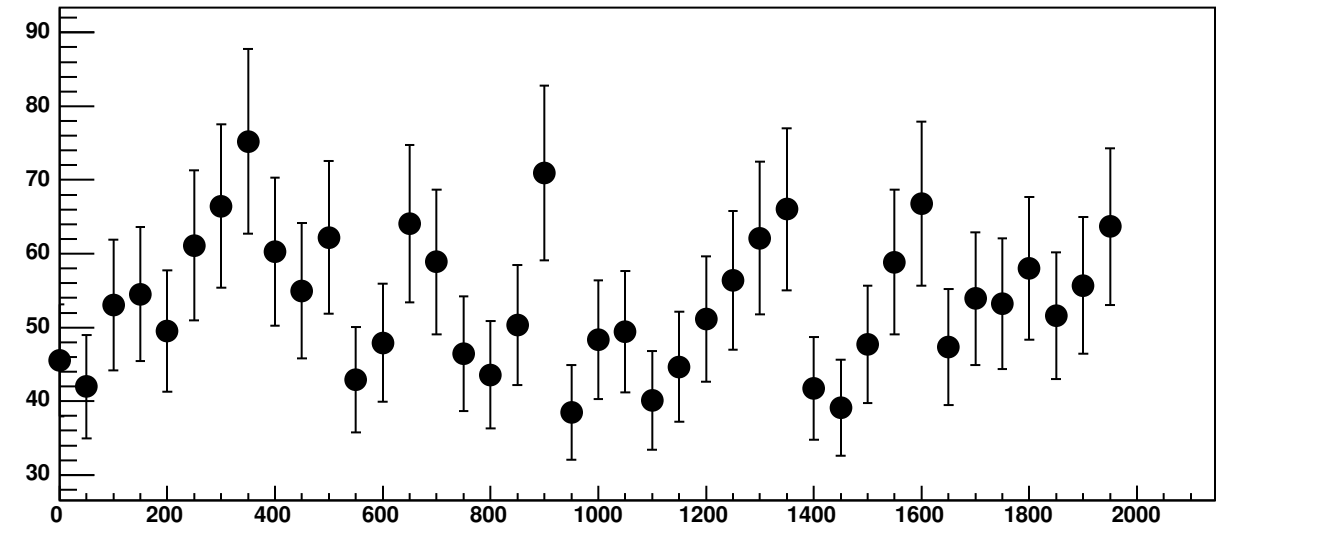
Chip 8, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC



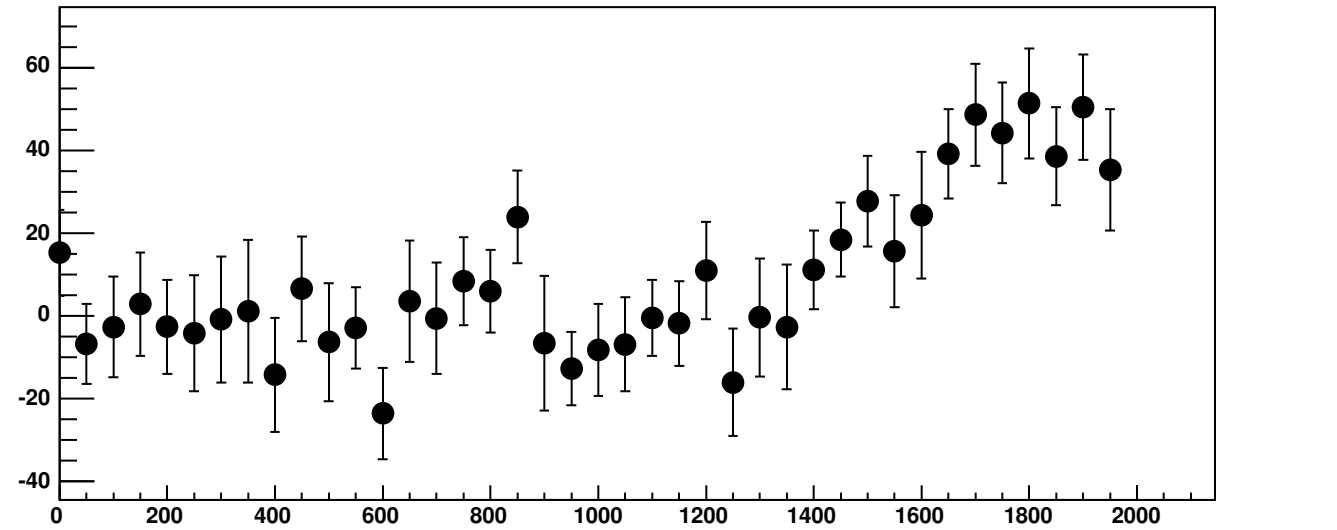
Chip 8, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC

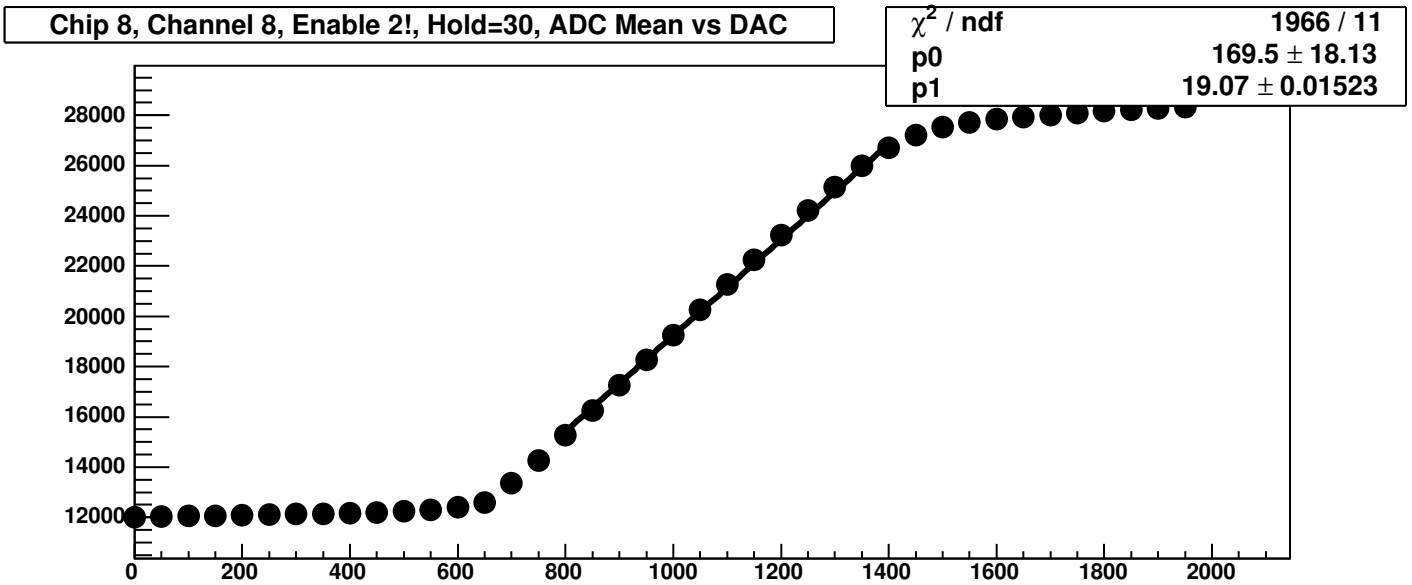


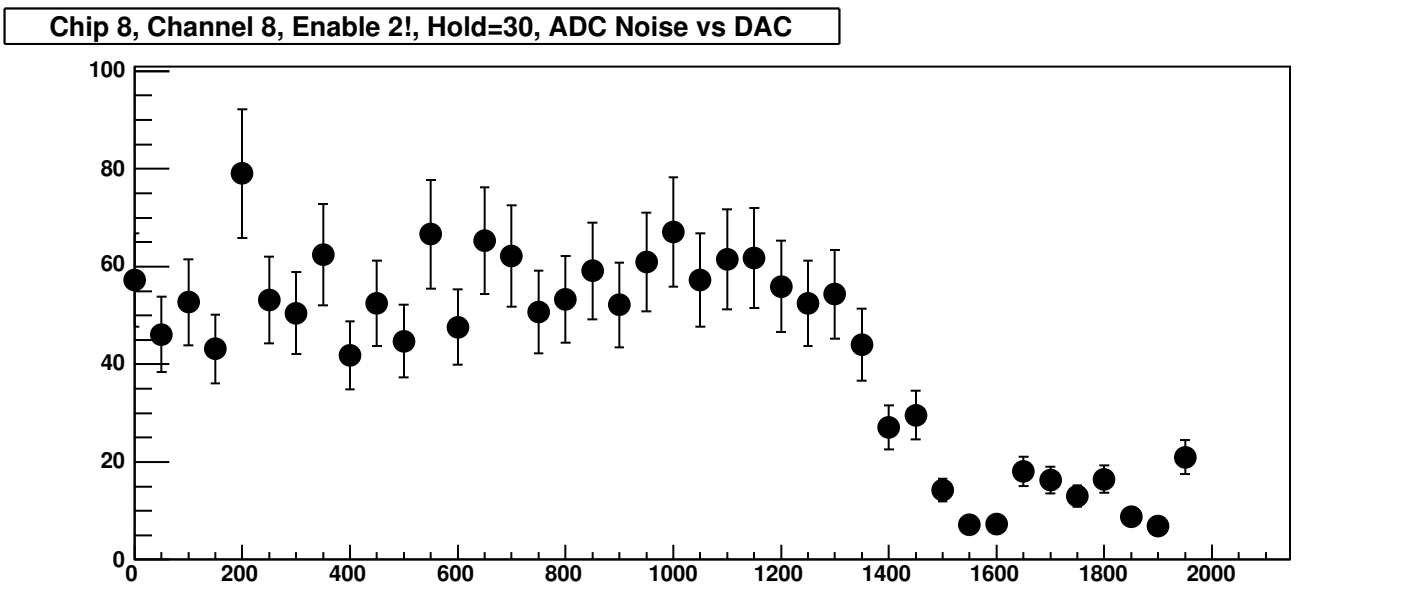
Chip 8, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC

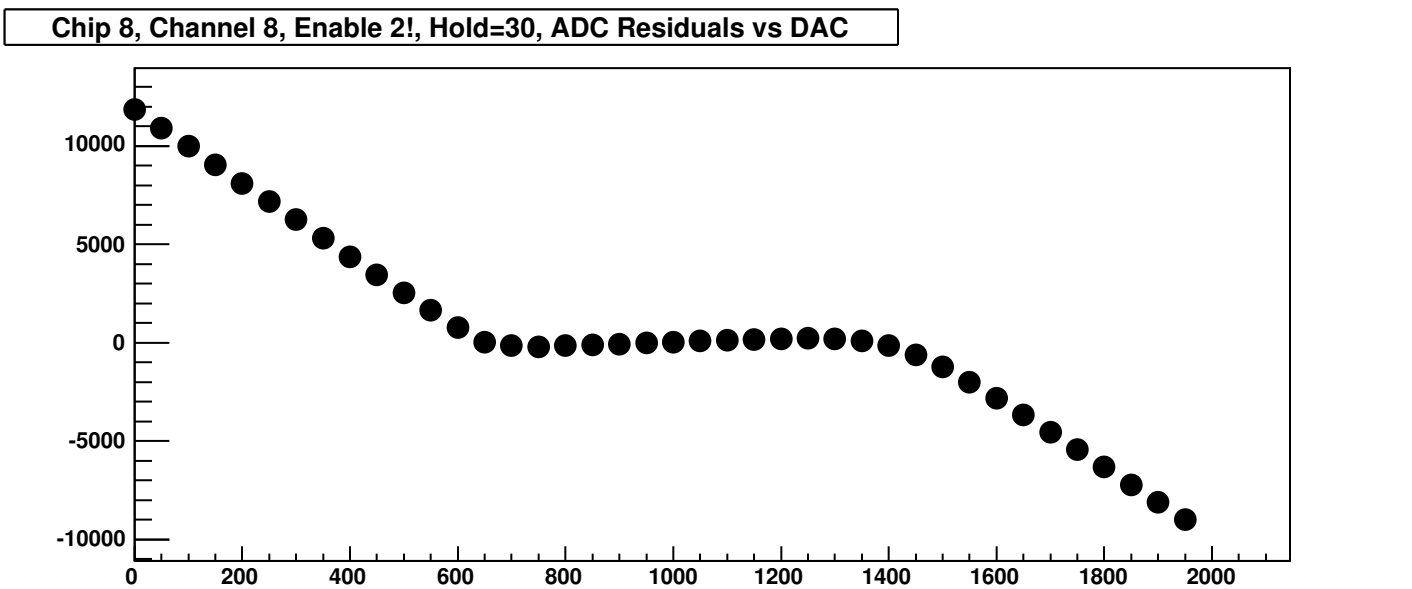


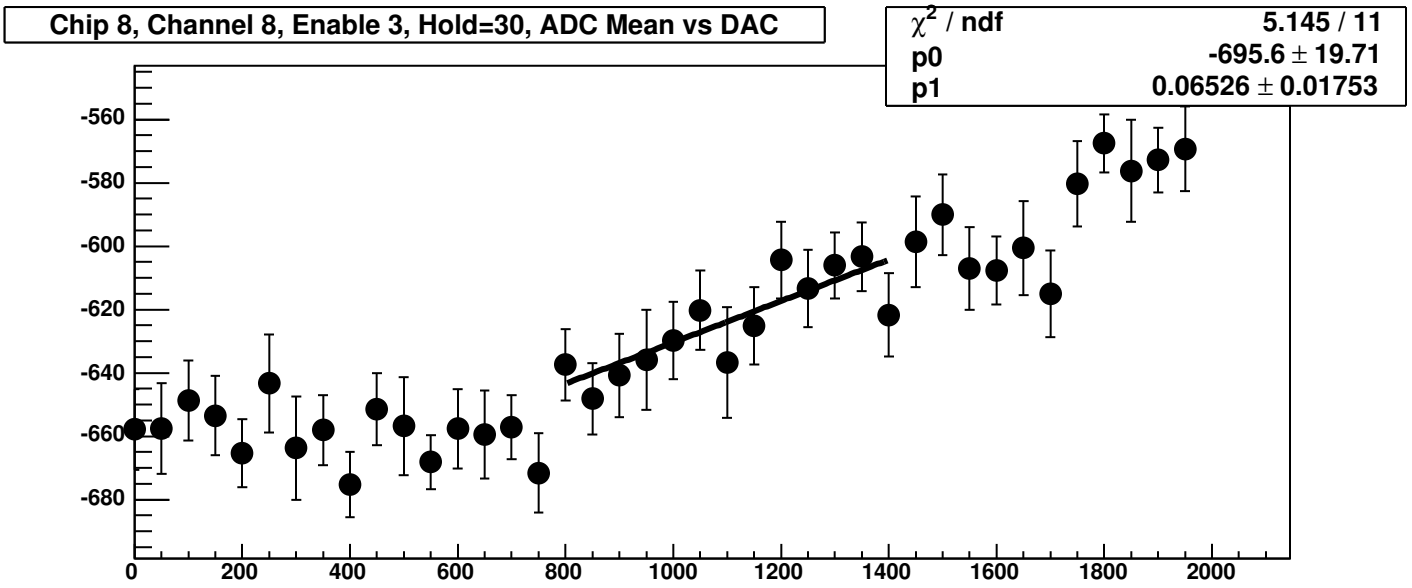
Chip 8, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC

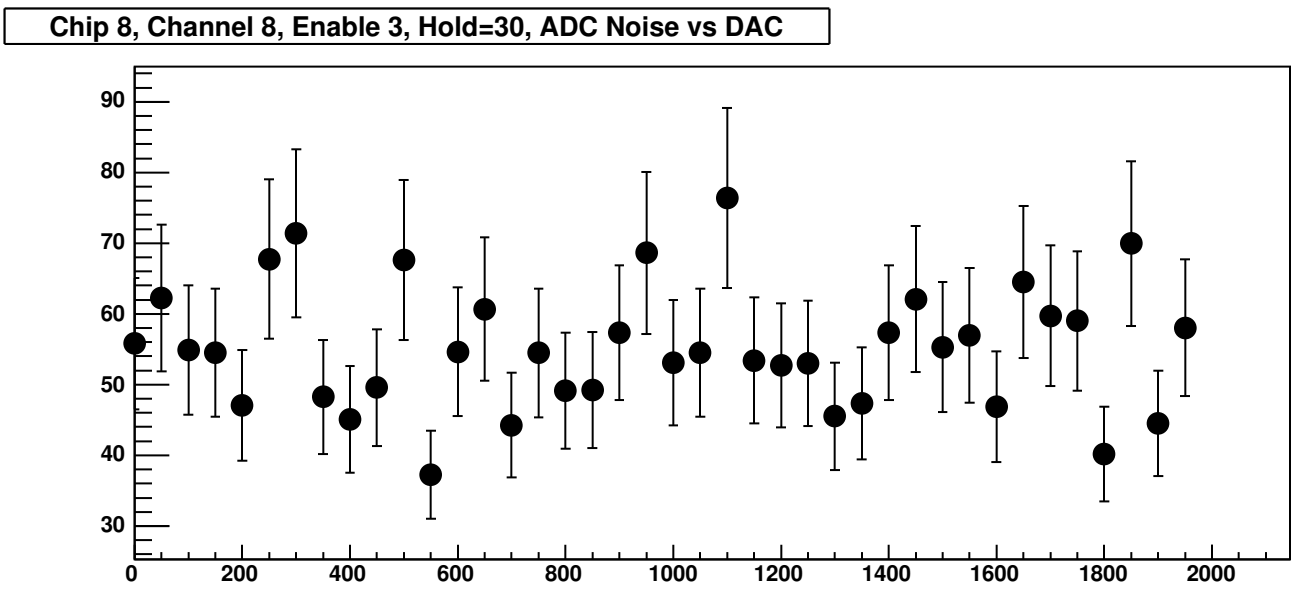


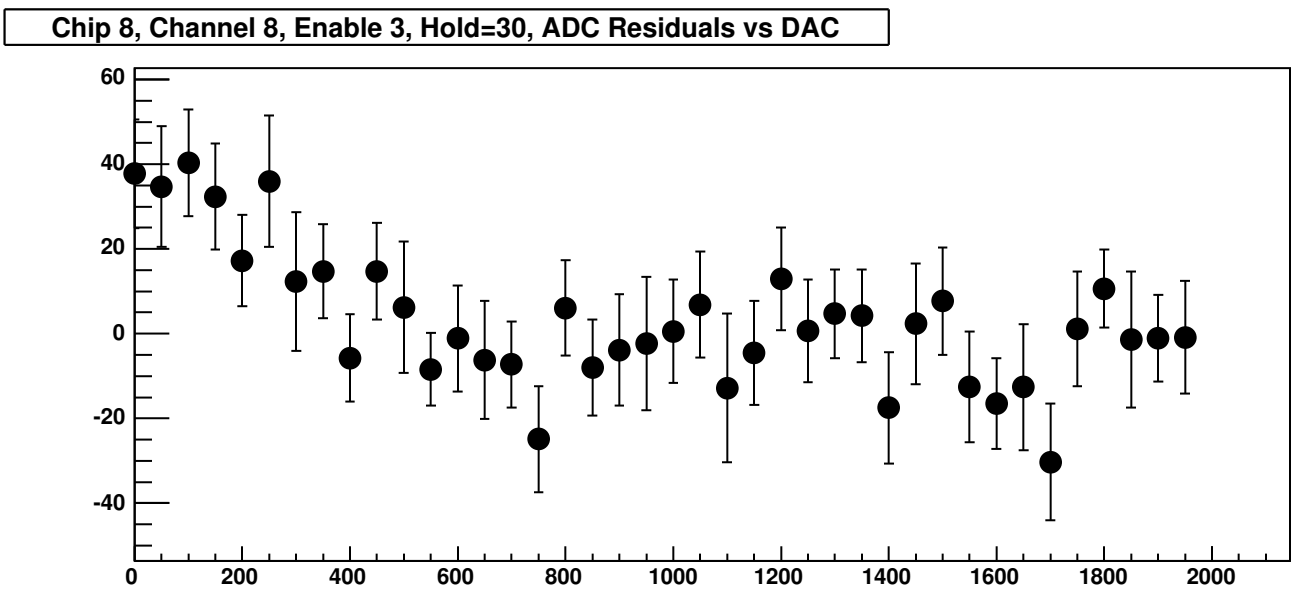

Chip 8, Channel 8, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 1966 / 11
p0 169.5 ± 18.13
p1 19.07 ± 0.01523
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

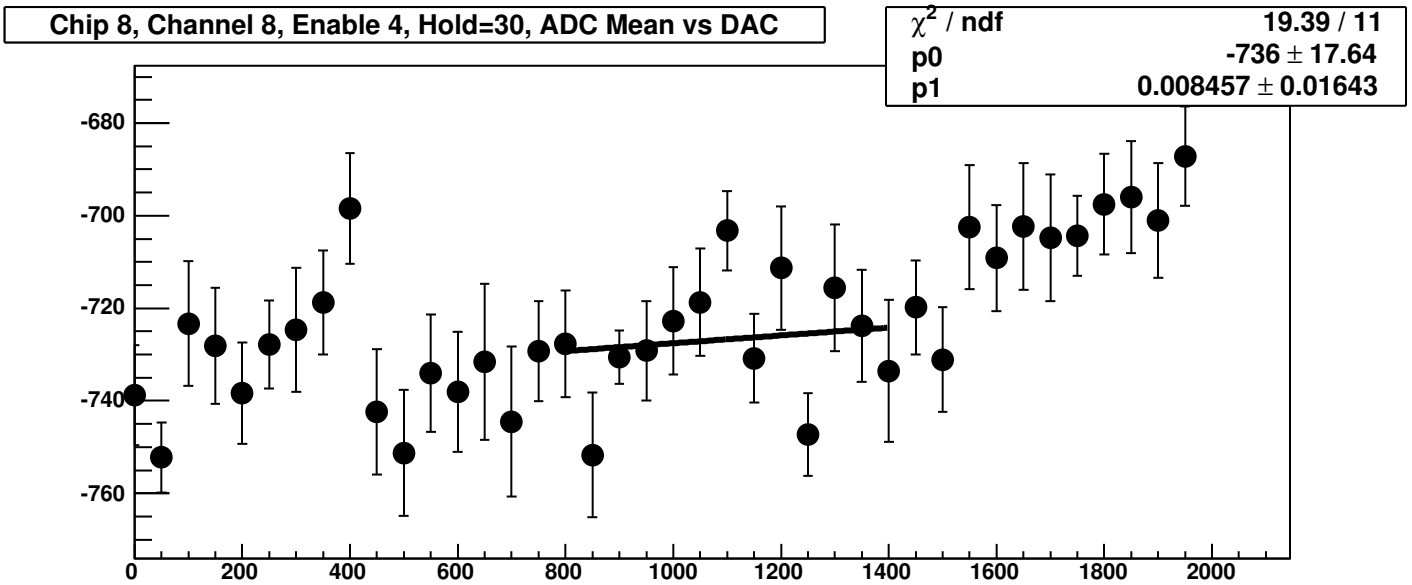

Chip 8, Channel 8, Enable 2!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

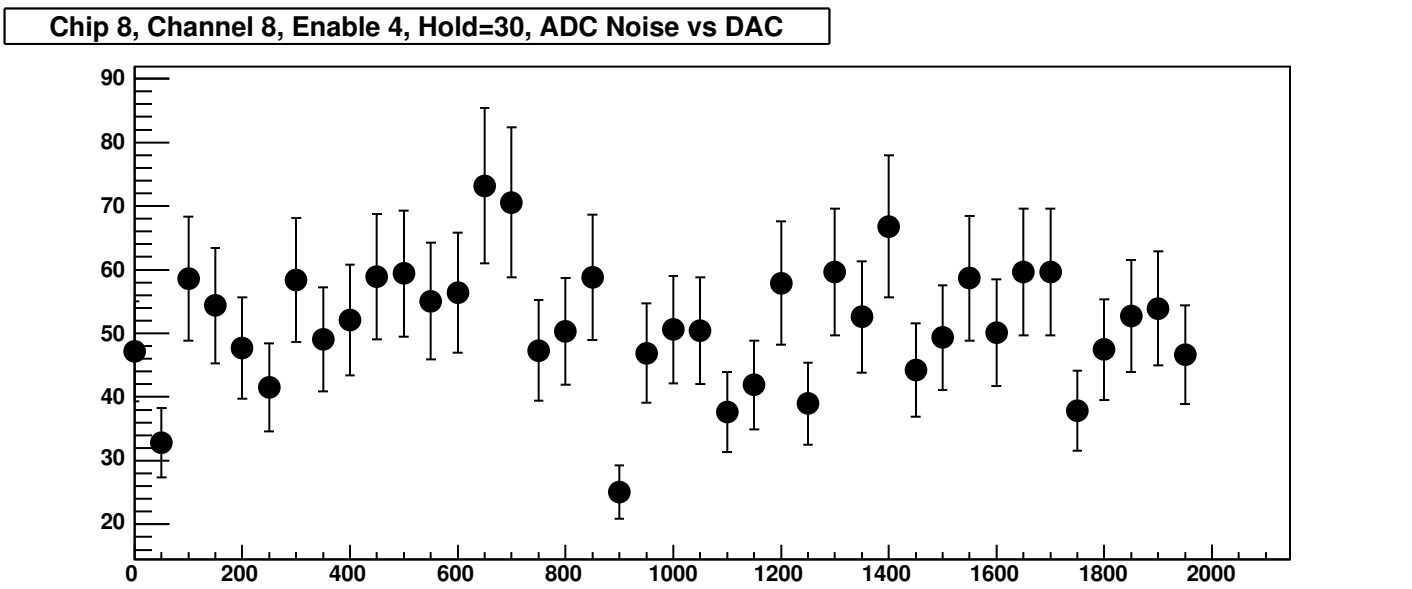

Chip 8, Channel 8, Enable 2!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

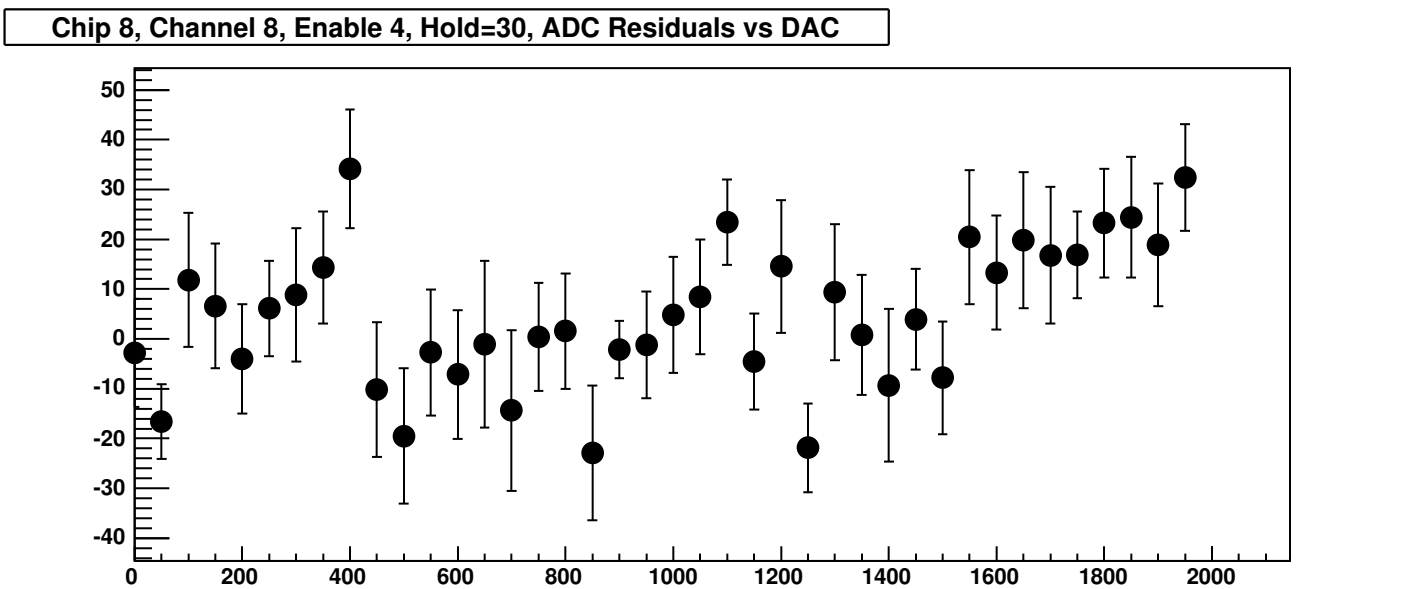

Chip 8, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5.145 / 11
p0 -695.6 ± 19.71
p1 0.06526 ± 0.01753
-560
-580
-600
-620
-640
-660
-680
0
200
400
600
800
1000
1200
1400
1600
1800
2000

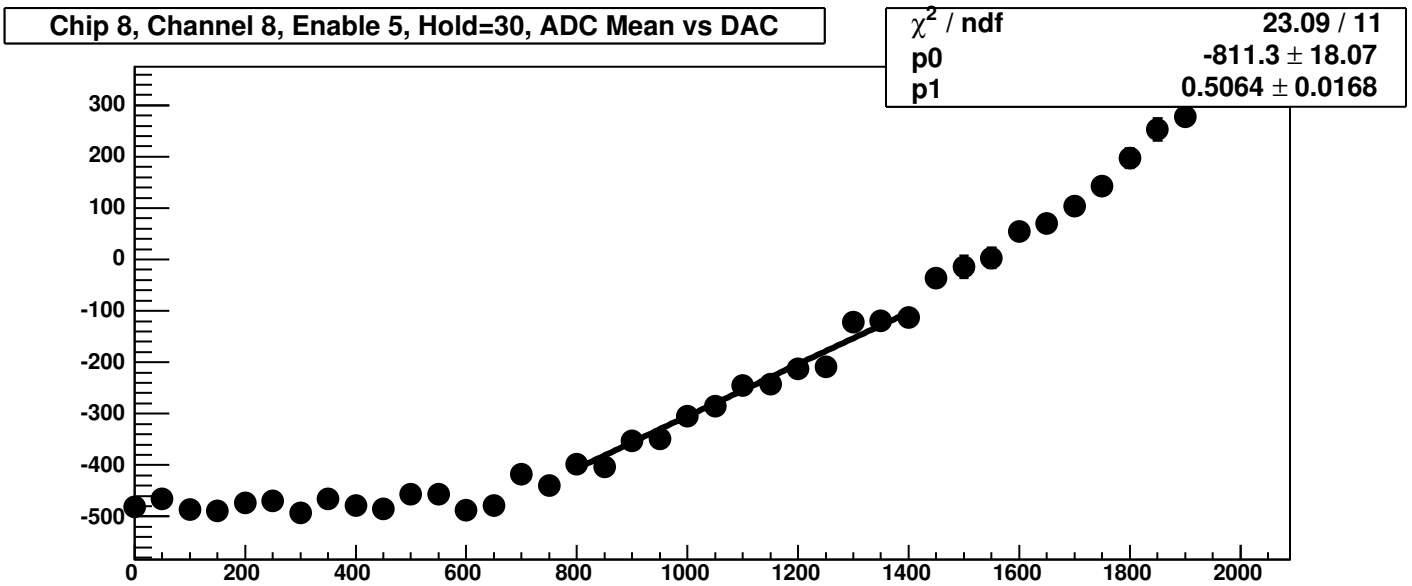

Chip 8, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

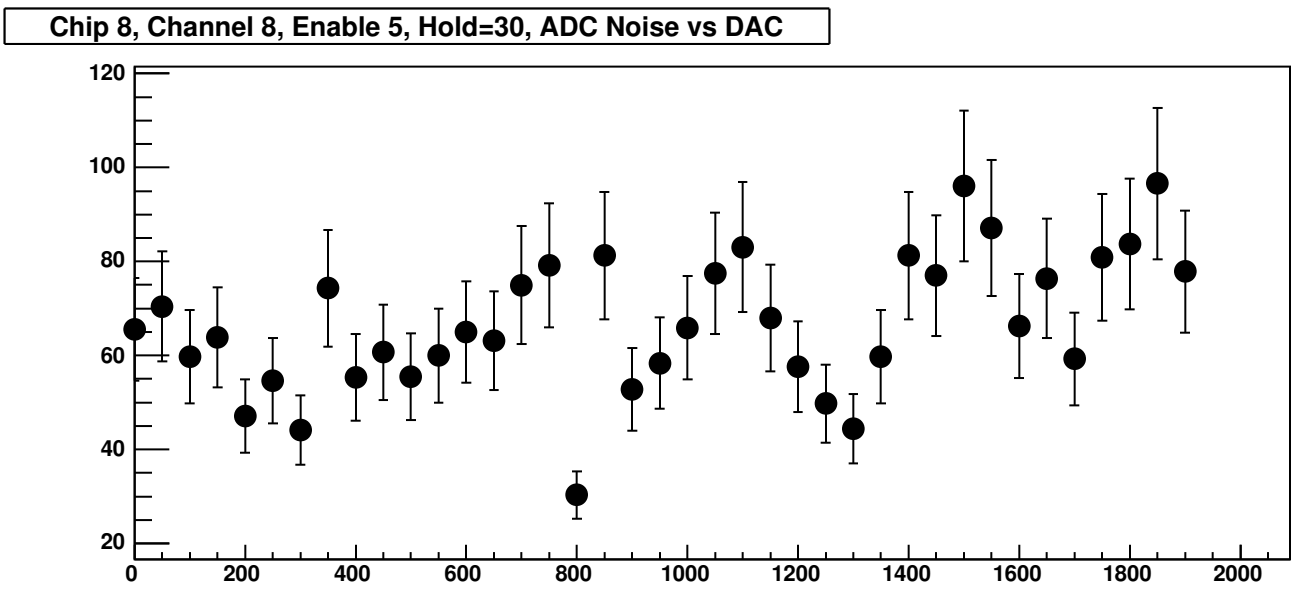

Chip 8, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

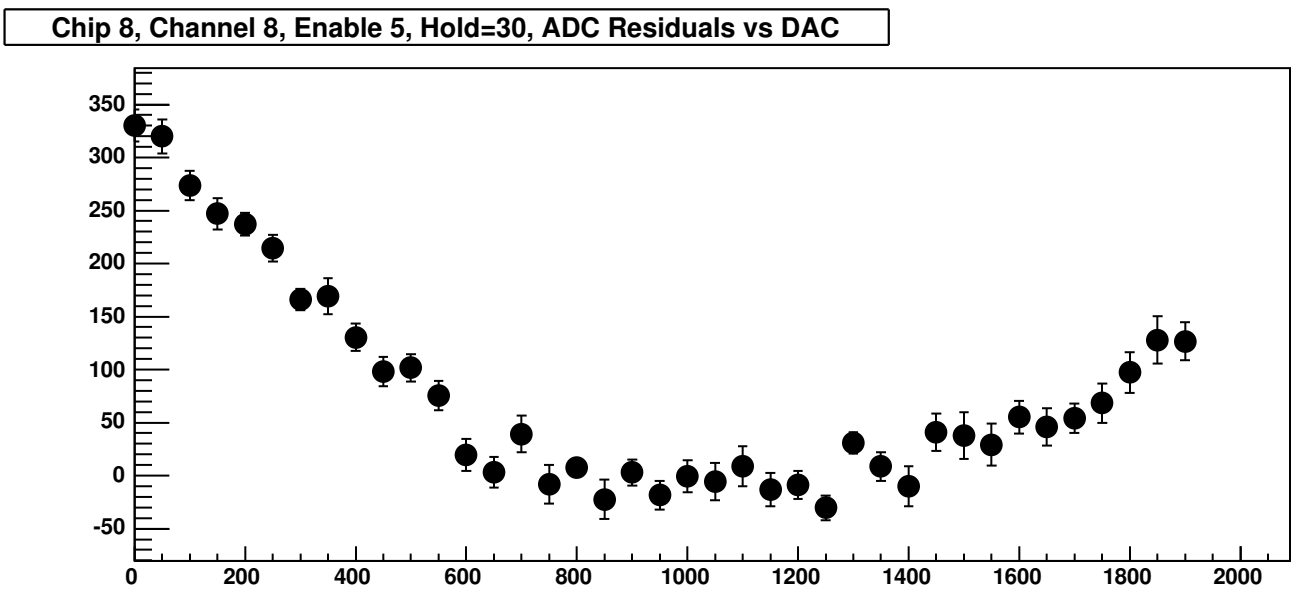

Chip 8, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 19.39 / 11
p0 -736 ± 17.64
p1 0.008457 ± 0.01643
-680
-700
-720
-740
-760
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

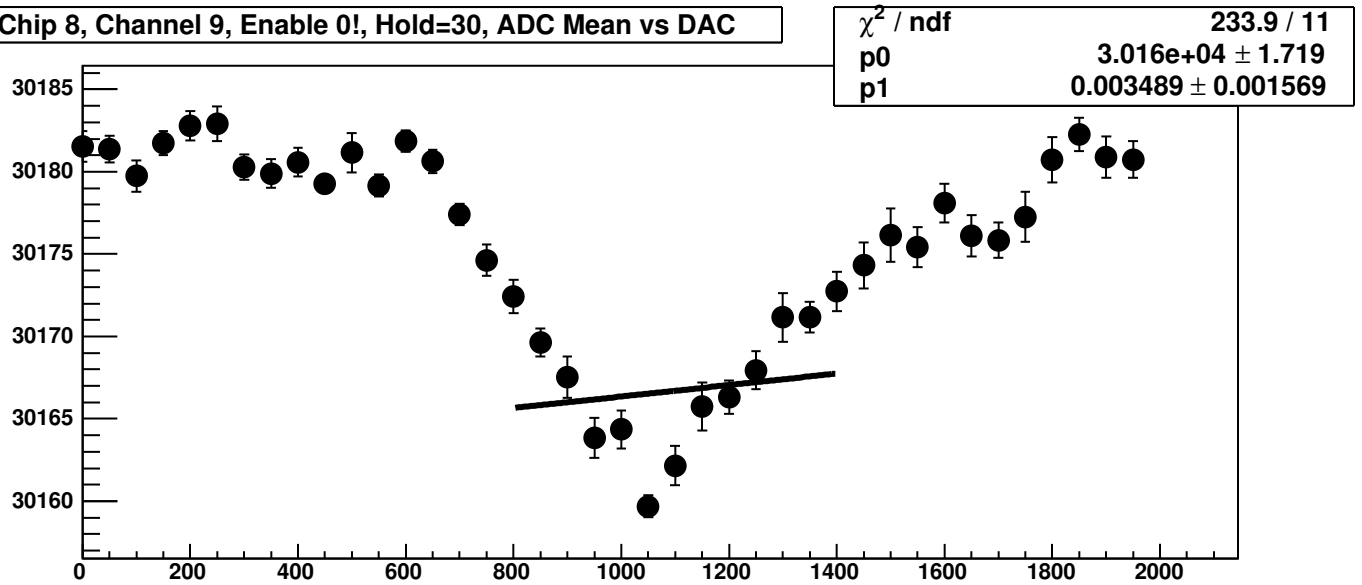

Chip 8, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC
50
40
30
20
10
0
-10
-20
-30
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 8, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 23.09 / 11
p0 -811.3 ± 18.07
p1 0.5064 ± 0.0168
300
200
100
0
-100
-200
-300
-400
-500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

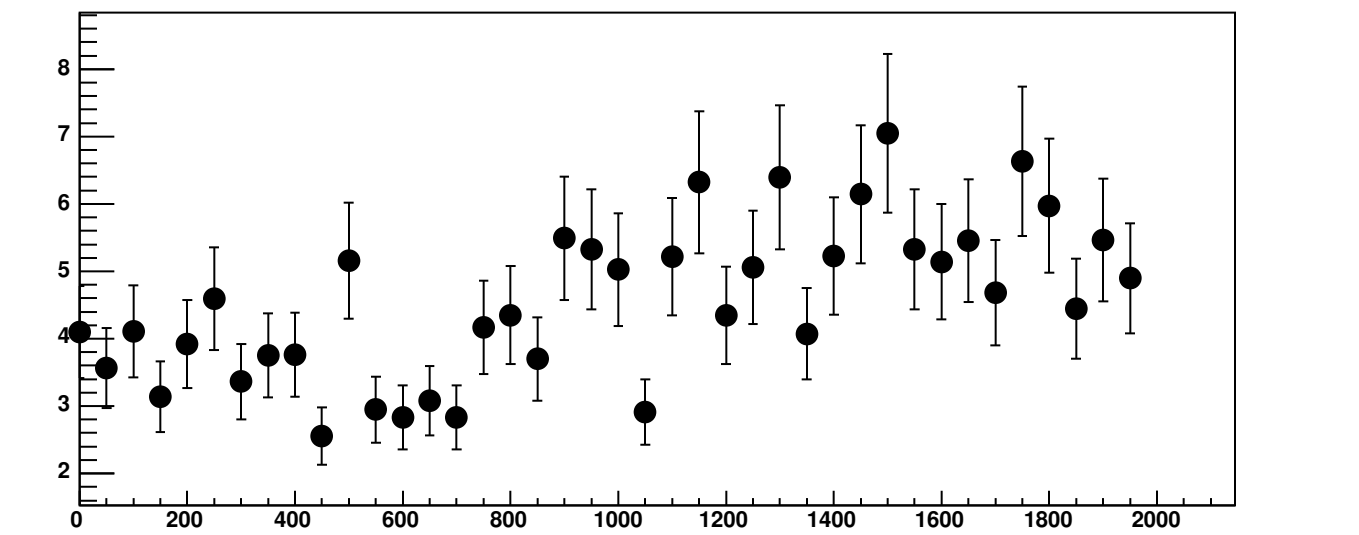

Chip 8, Channel 8, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 8, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

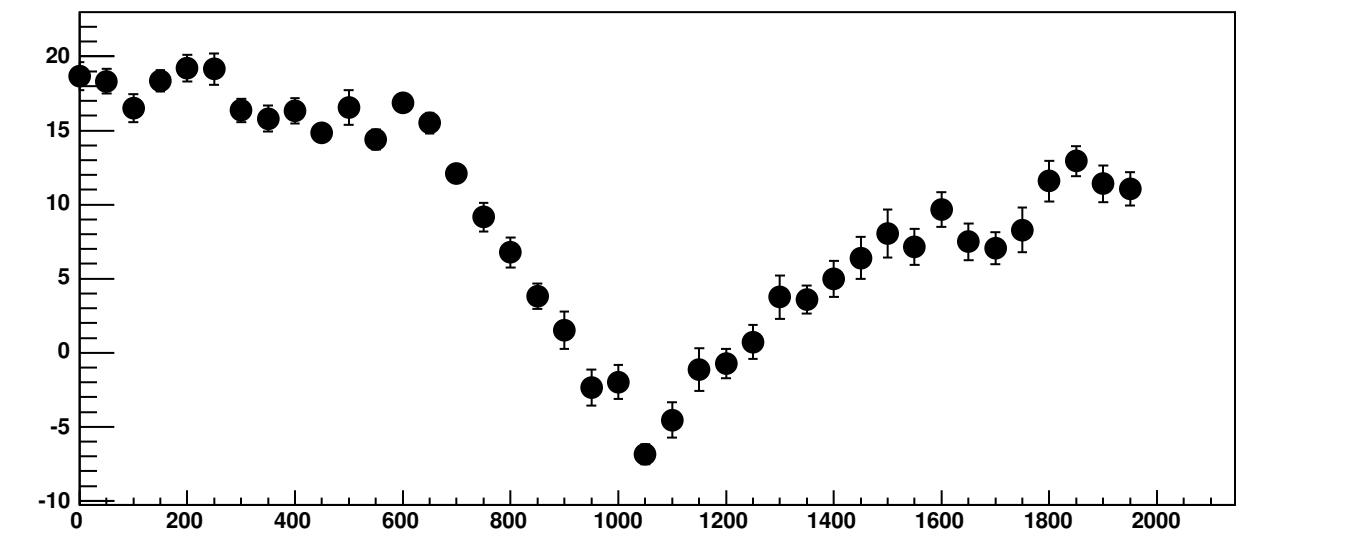
Chip 8, Channel 9, Enable 0!, Hold=30, ADC Mean vs DAC

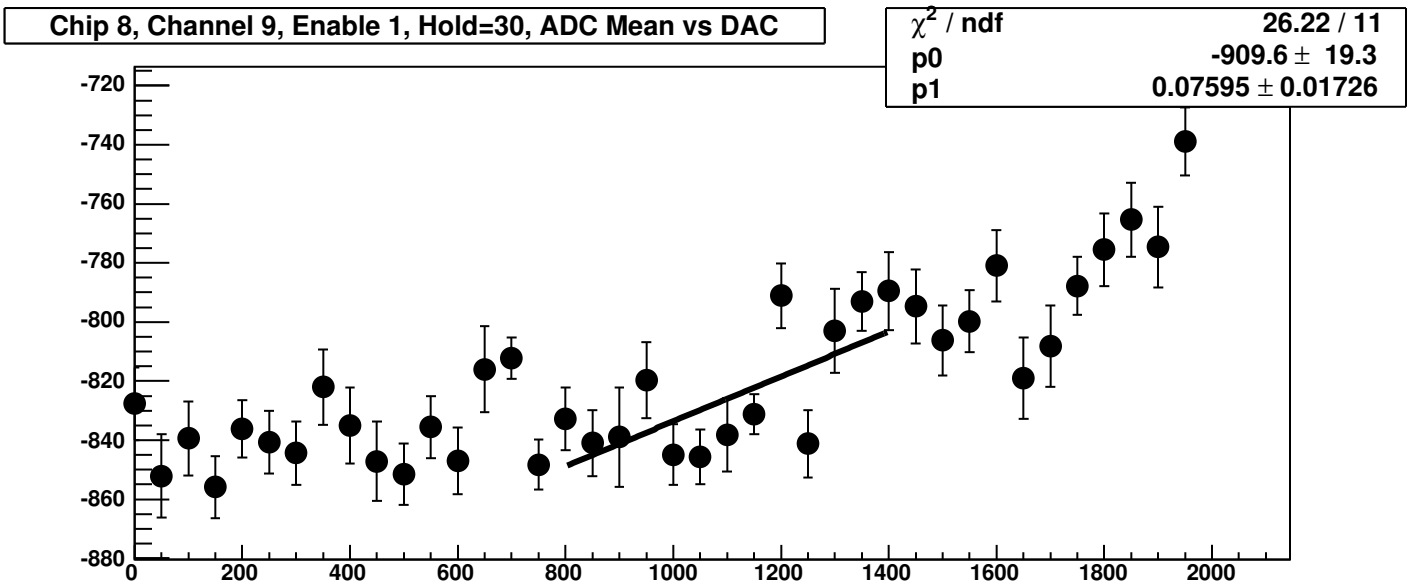


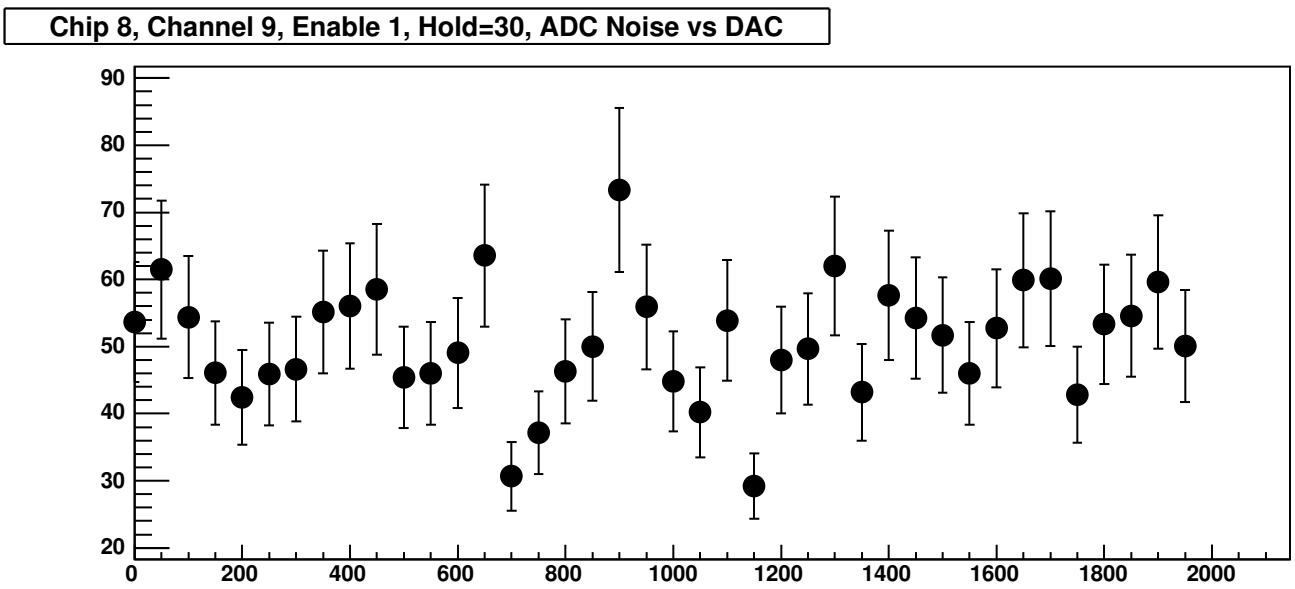
Chip 8, Channel 9, Enable 0!, Hold=30, ADC Noise vs DAC

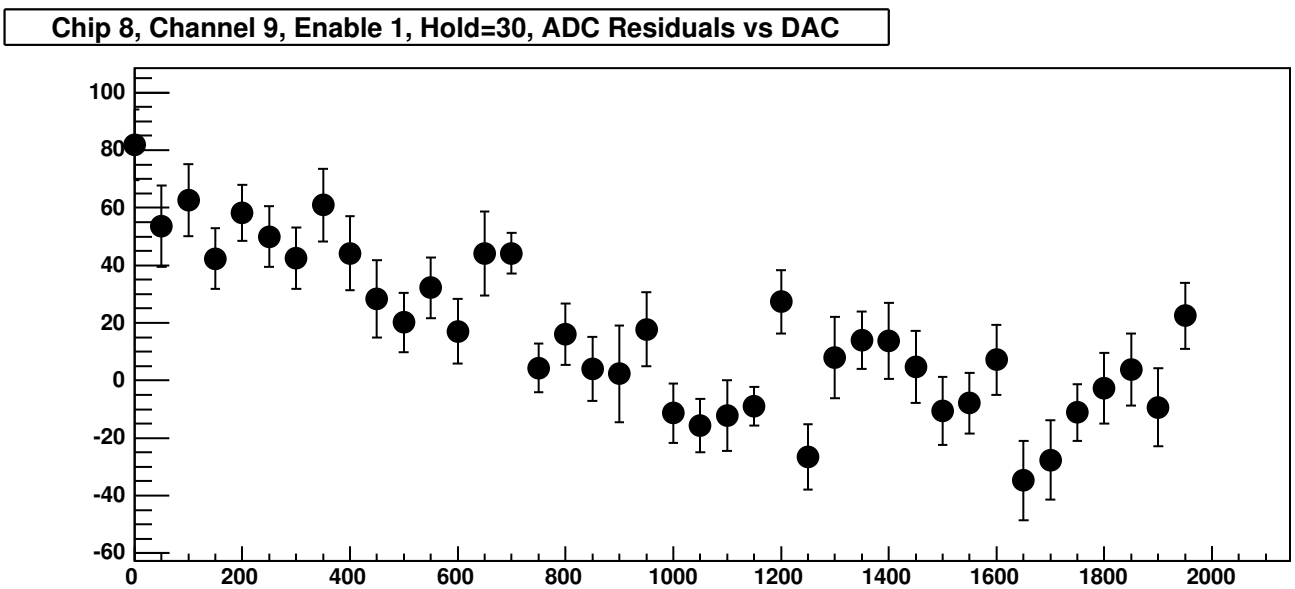


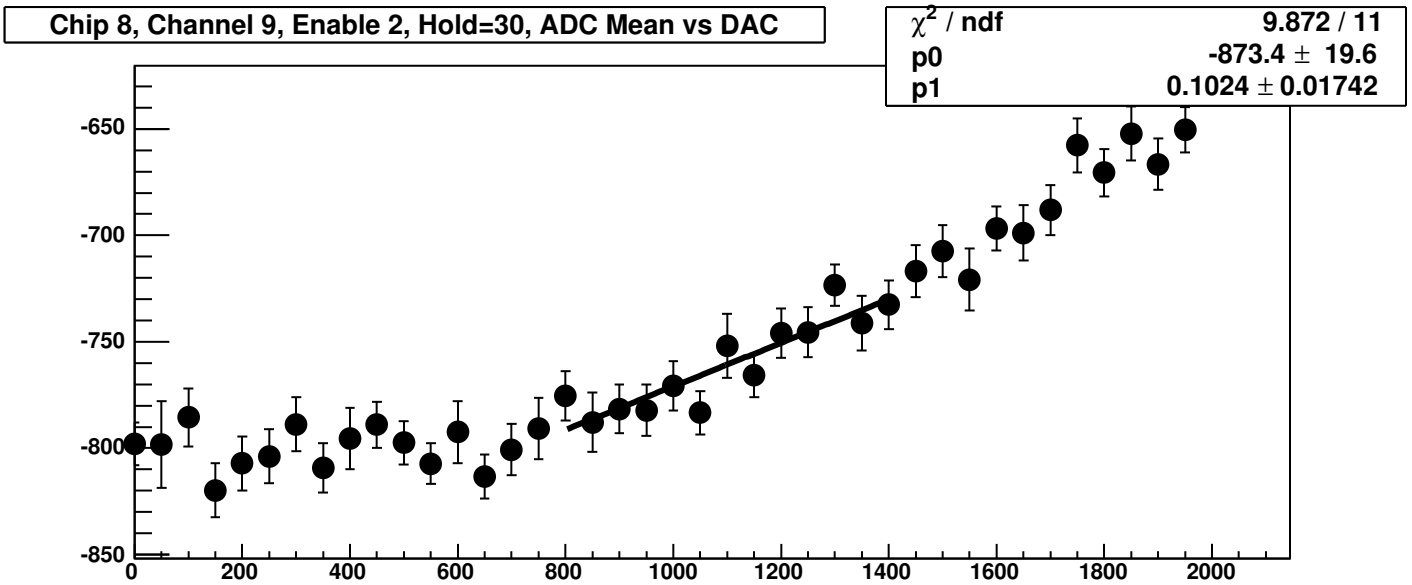
Chip 8, Channel 9, Enable 0!, Hold=30, ADC Residuals vs DAC

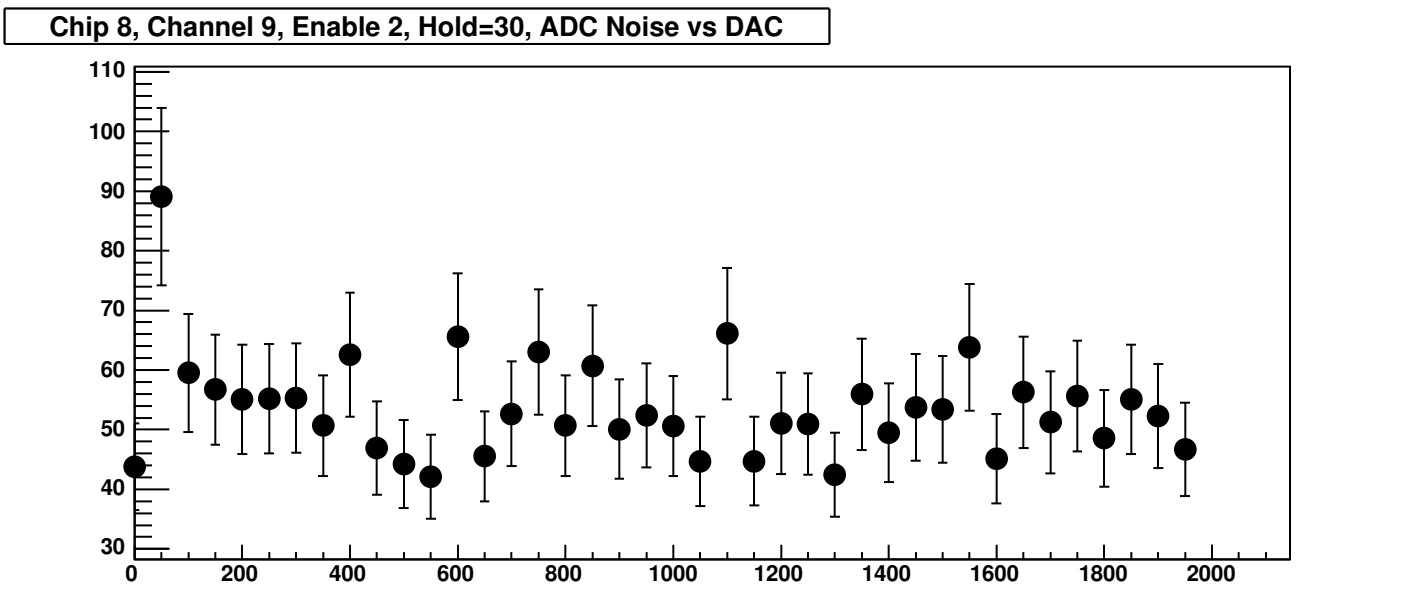


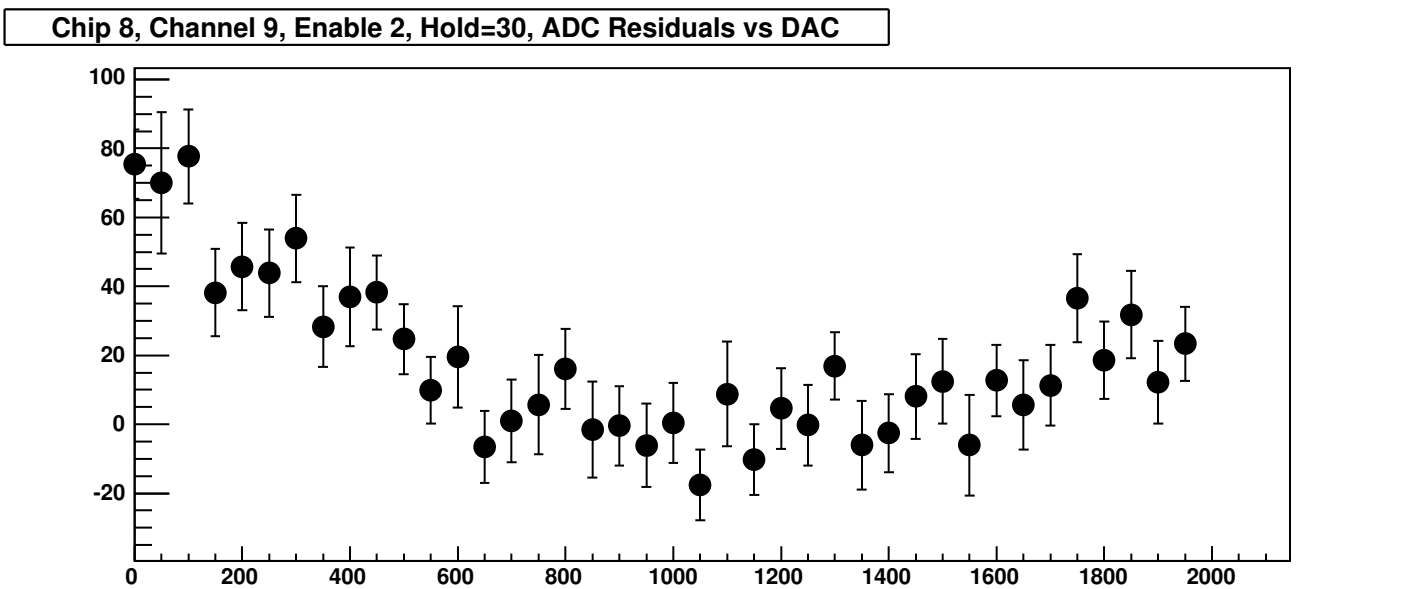

Chip 8, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 26.22 / 11
p0 -909.6 ± 19.3
p1 0.07595 ± 0.01726
-720
-740
-760
-780
-800
-820
-840
-860
-880
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

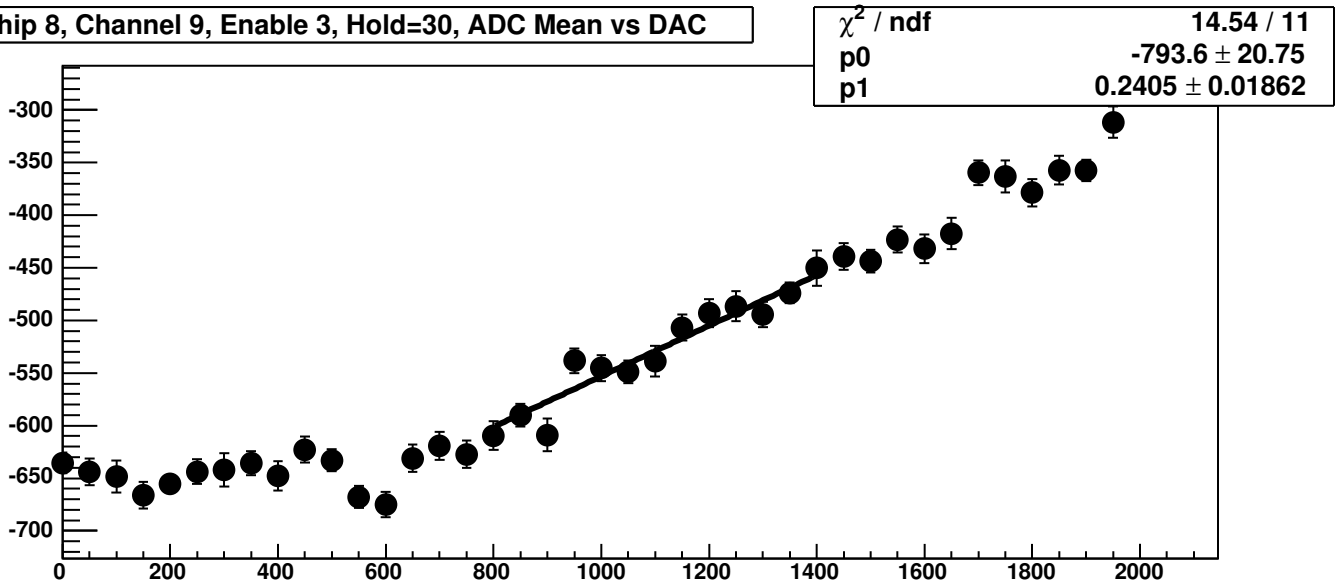

Chip 8, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 9.872 / 11
p0 -873.4 ± 19.6
p1 0.1024 ± 0.01742
-650
-700
-750
-800
-850
0
200
400
600
800
1000
1200
1400
1600
1800
2000

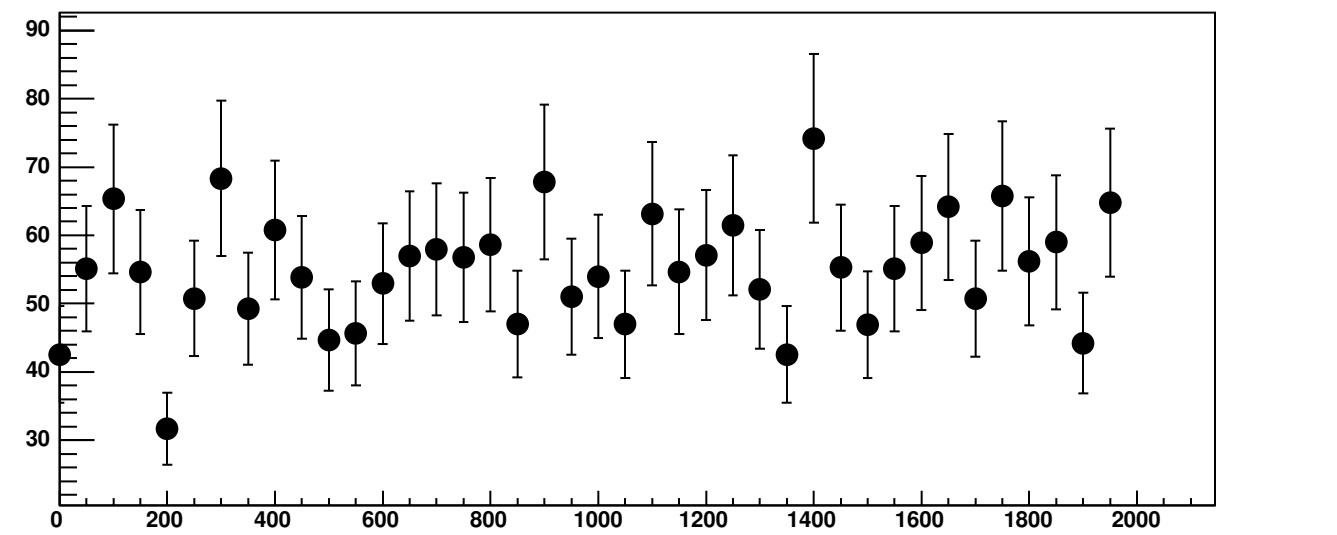

Chip 8, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

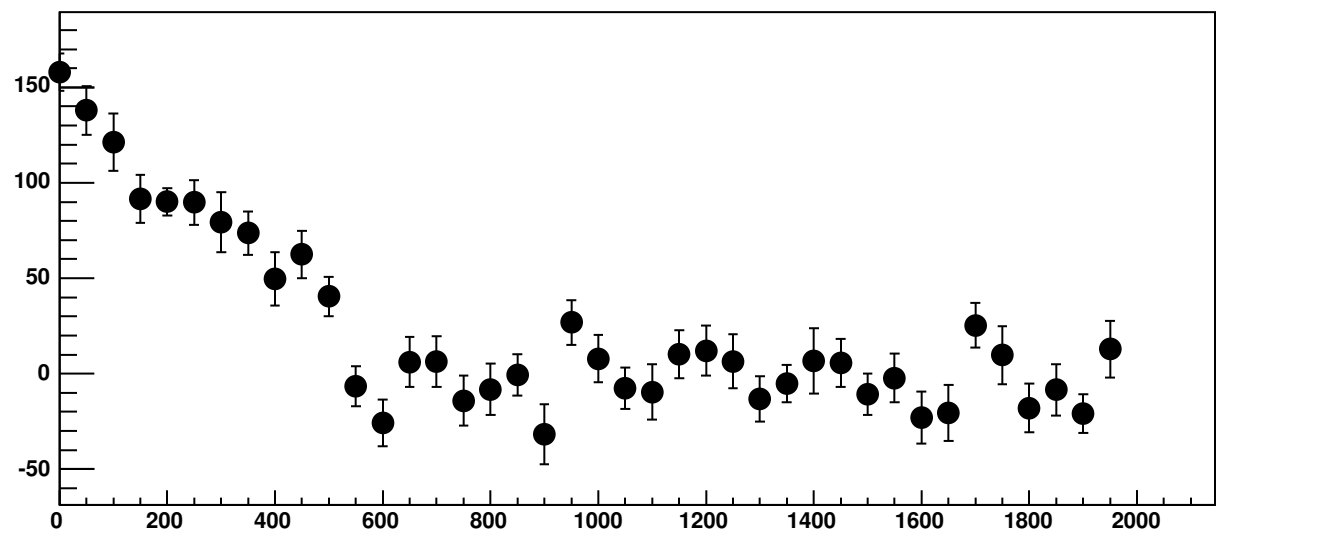
Chip 8, Channel 9, Enable 3, Hold=30, ADC Mean vs DAC

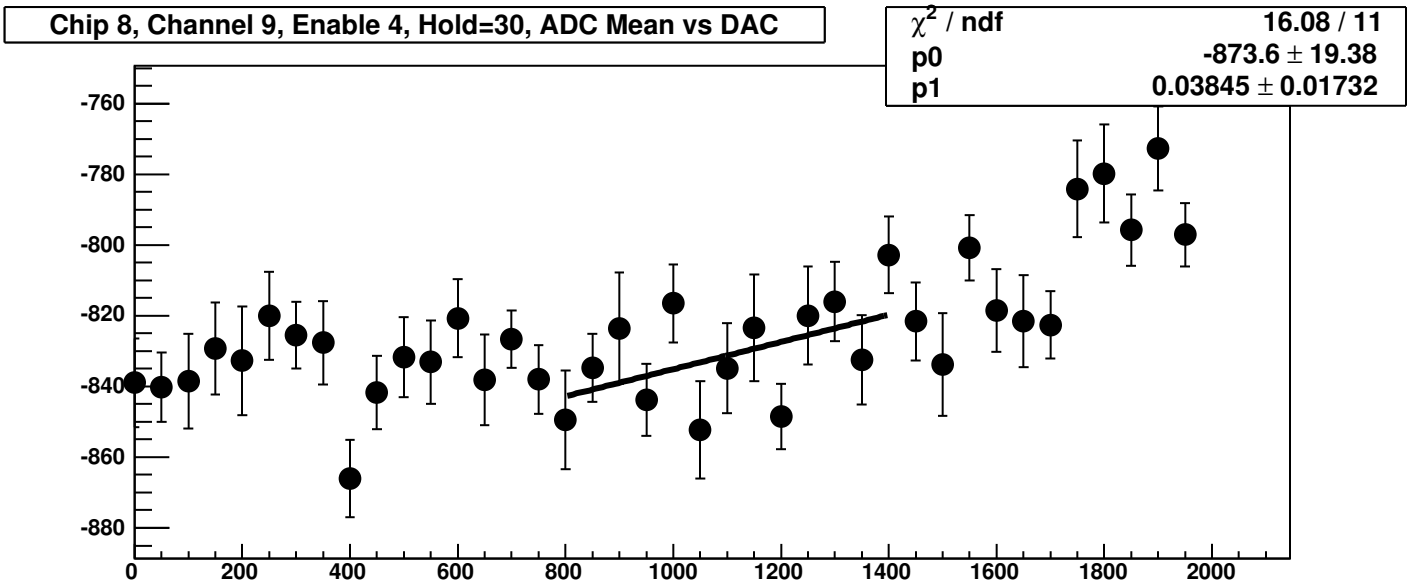


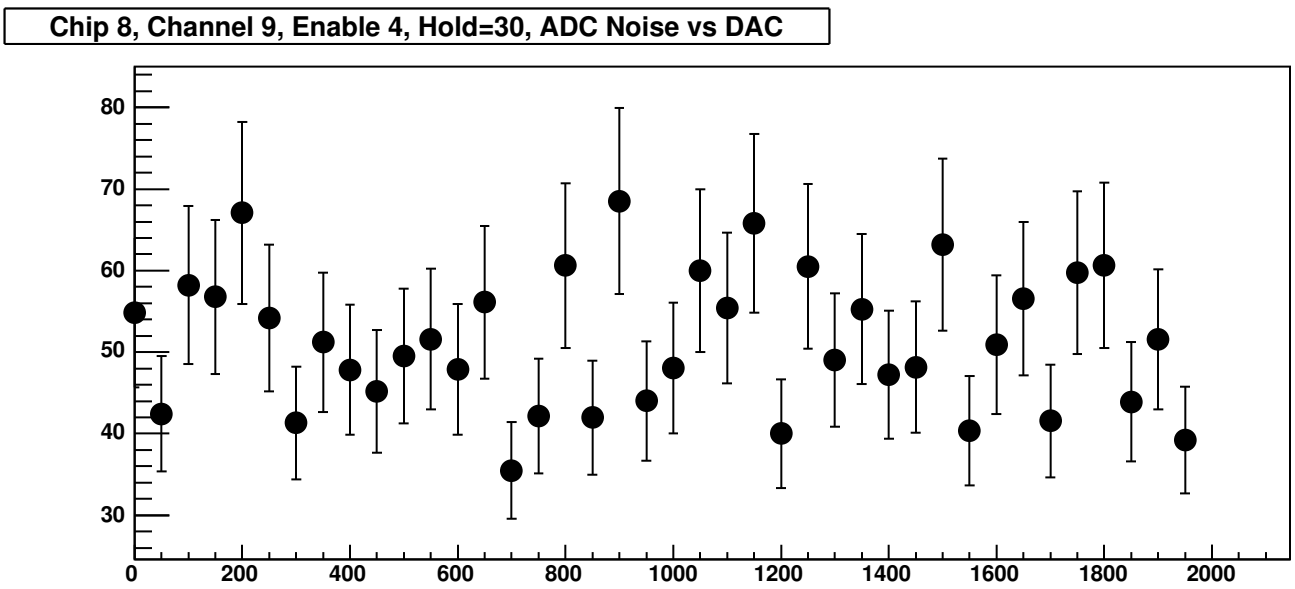
Chip 8, Channel 9, Enable 3, Hold=30, ADC Noise vs DAC

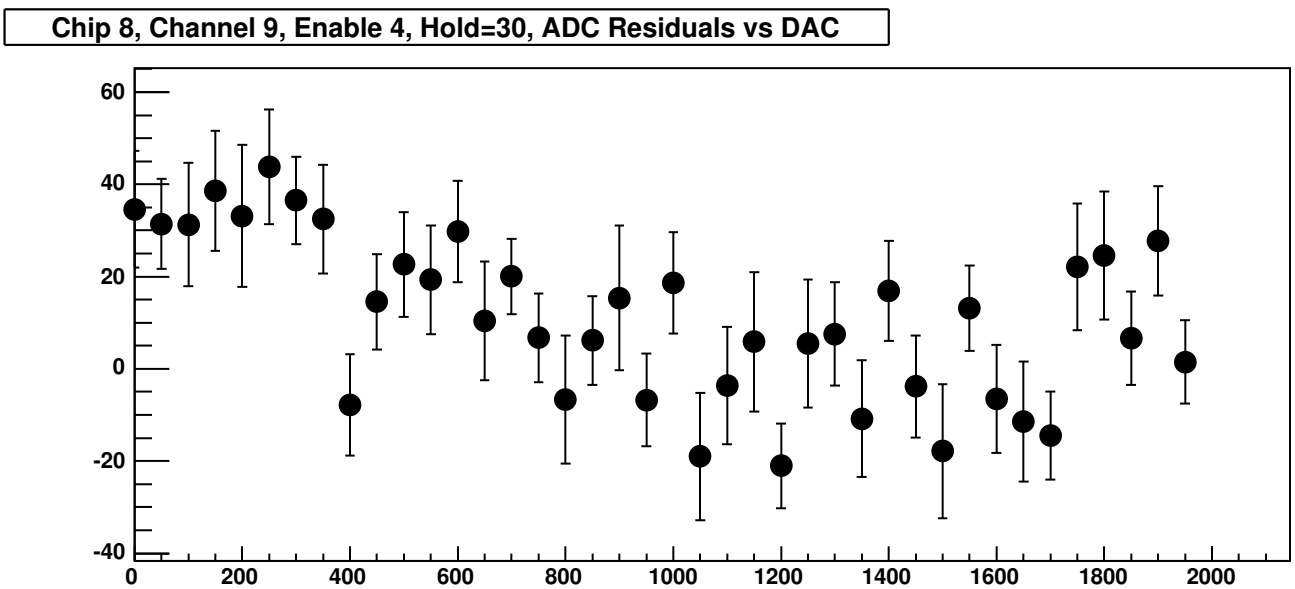


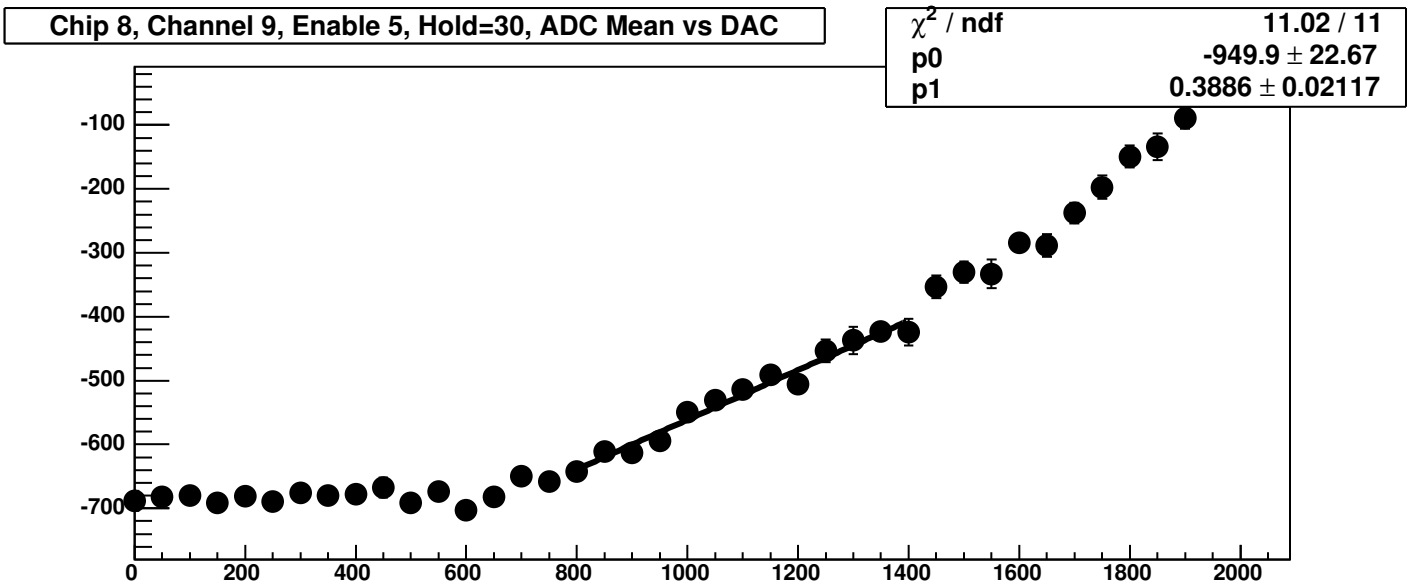
Chip 8, Channel 9, Enable 3, Hold=30, ADC Residuals vs DAC

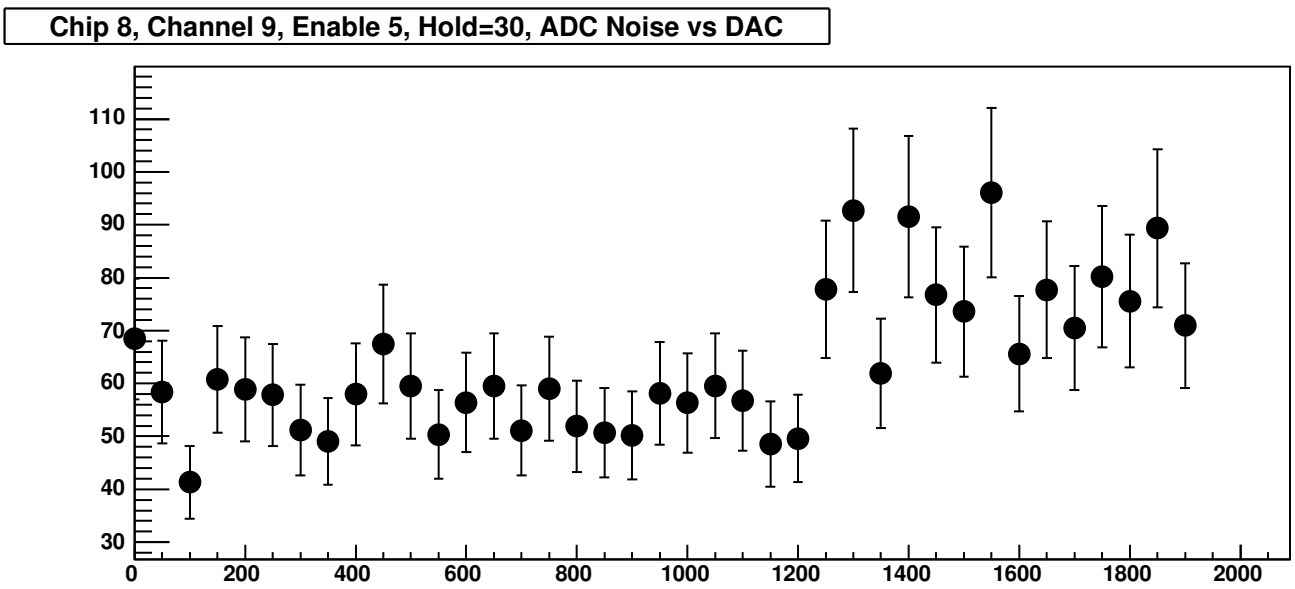


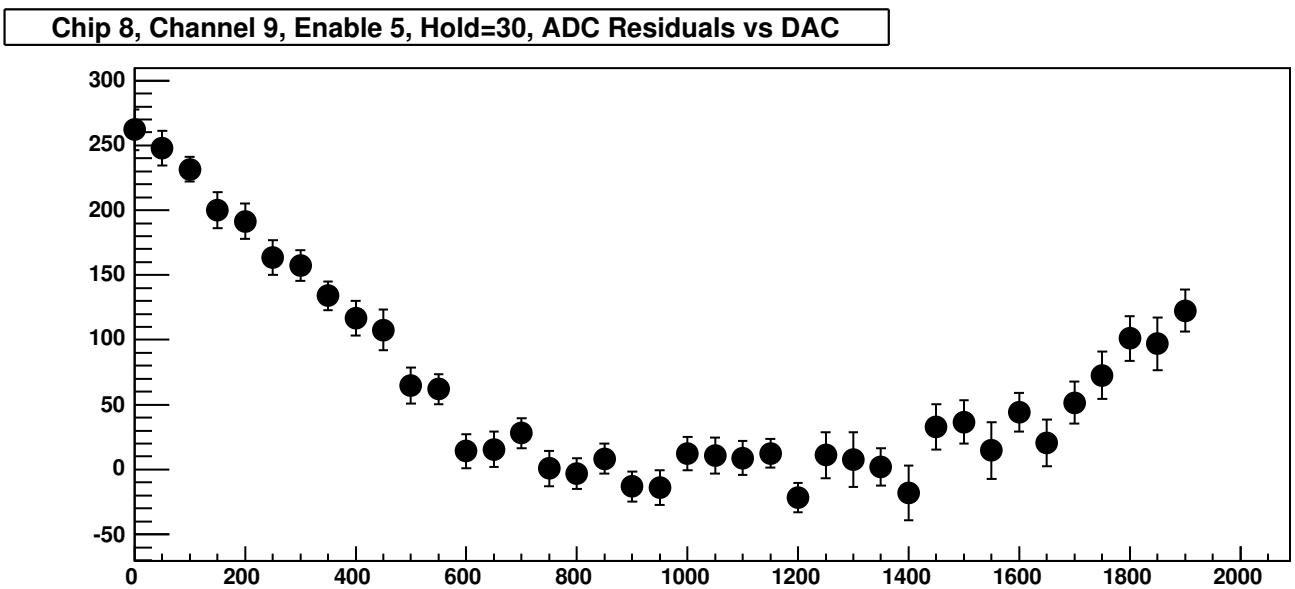

Chip 8, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 16.08 / 11
p0 -873.6 ± 19.38
p1 0.03845 ± 0.01732
-760
-780
-800
-820
-840
-860
-880
0
200
400
600
800
1000
1200
1400
1600
1800
2000

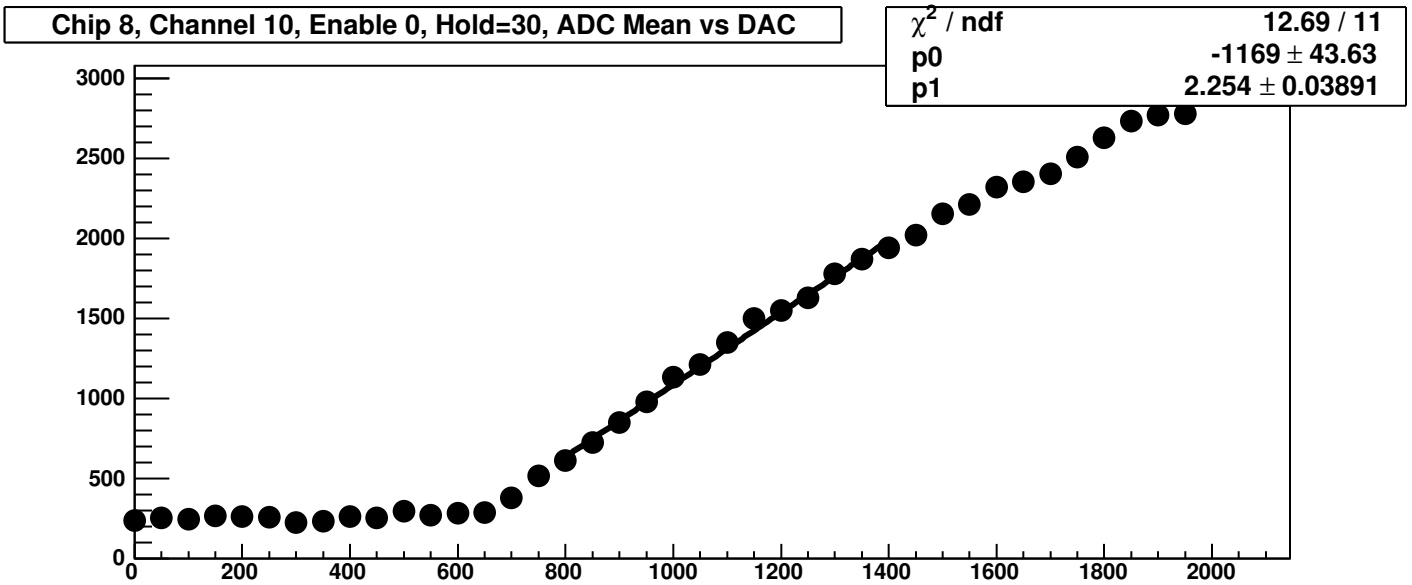

Chip 8, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

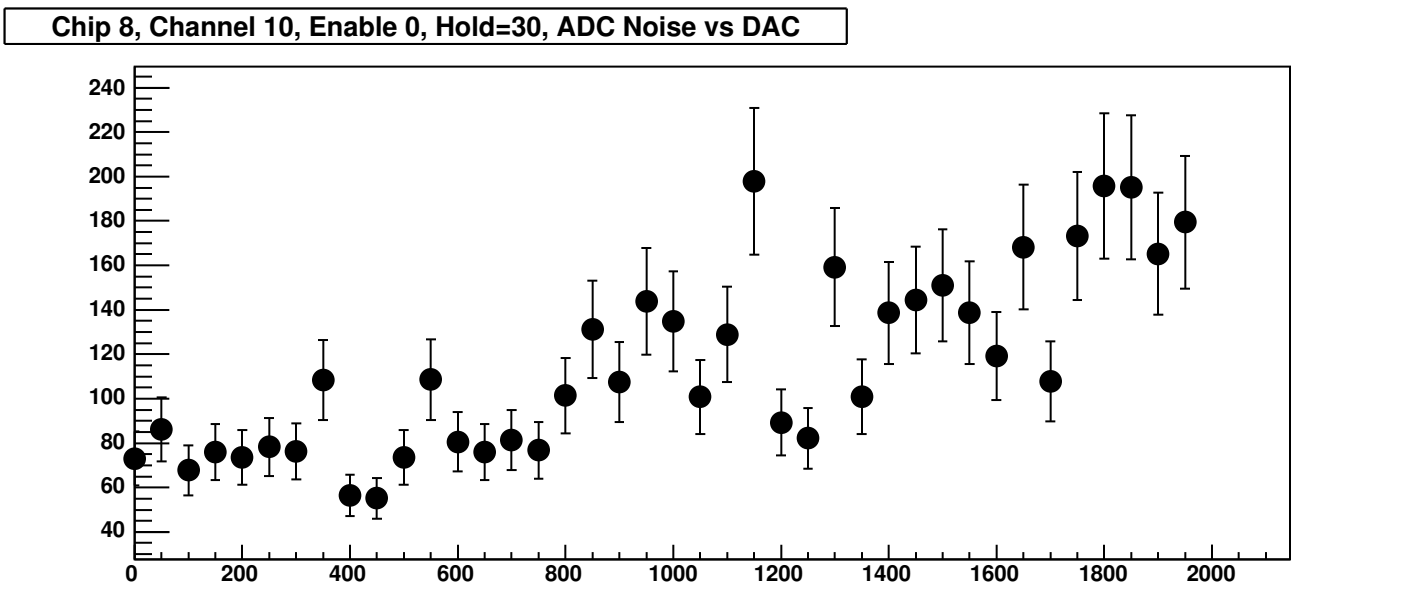

Chip 8, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

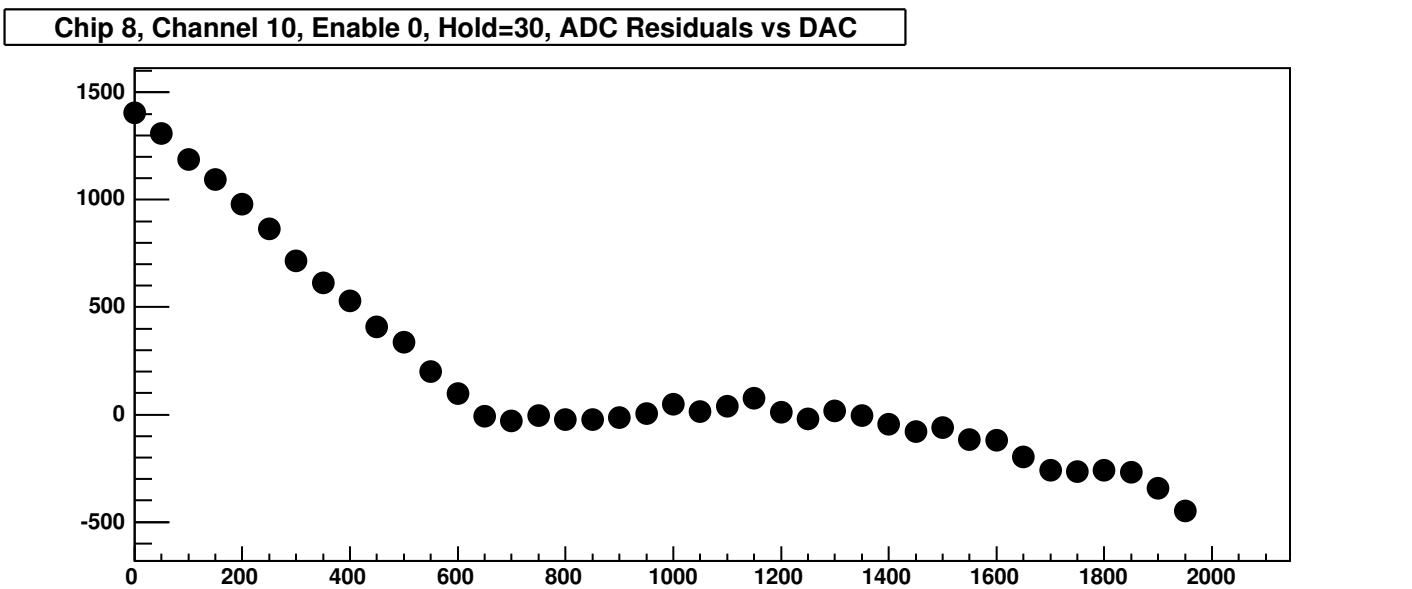

Chip 8, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 11.02 / 11
p0 -949.9 ± 22.67
p1 0.3886 ± 0.02117
-100
-200
-300
-400
-500
-600
-700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

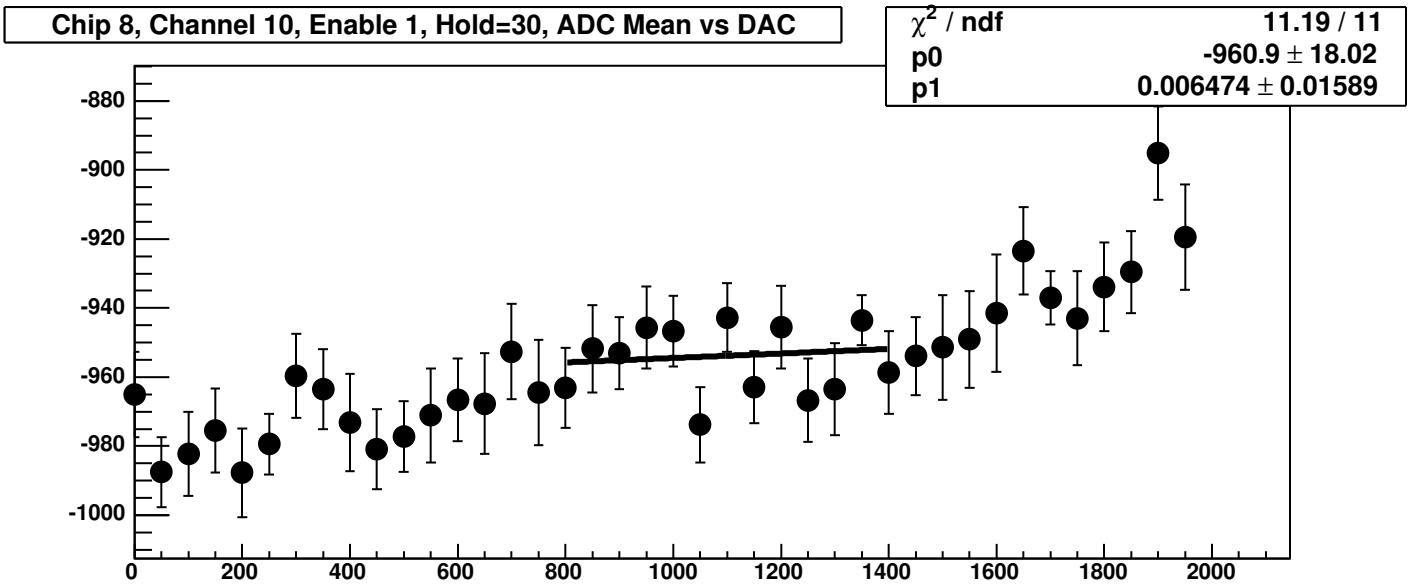

Chip 8, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

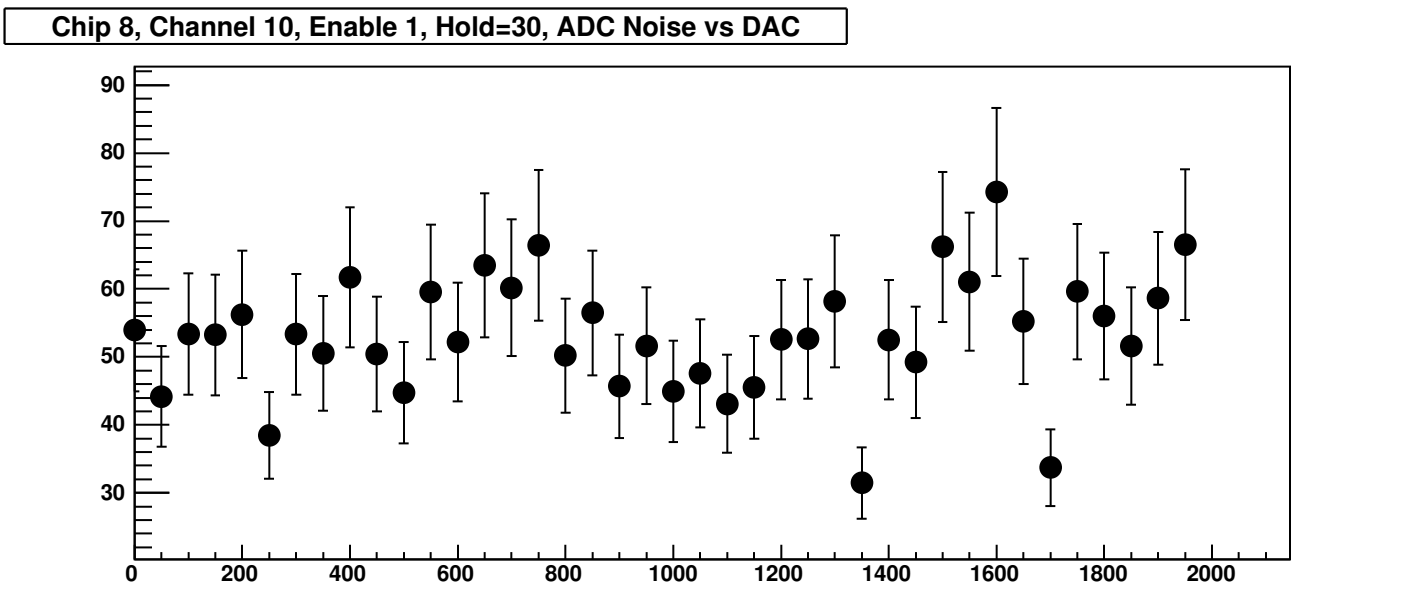

Chip 8, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

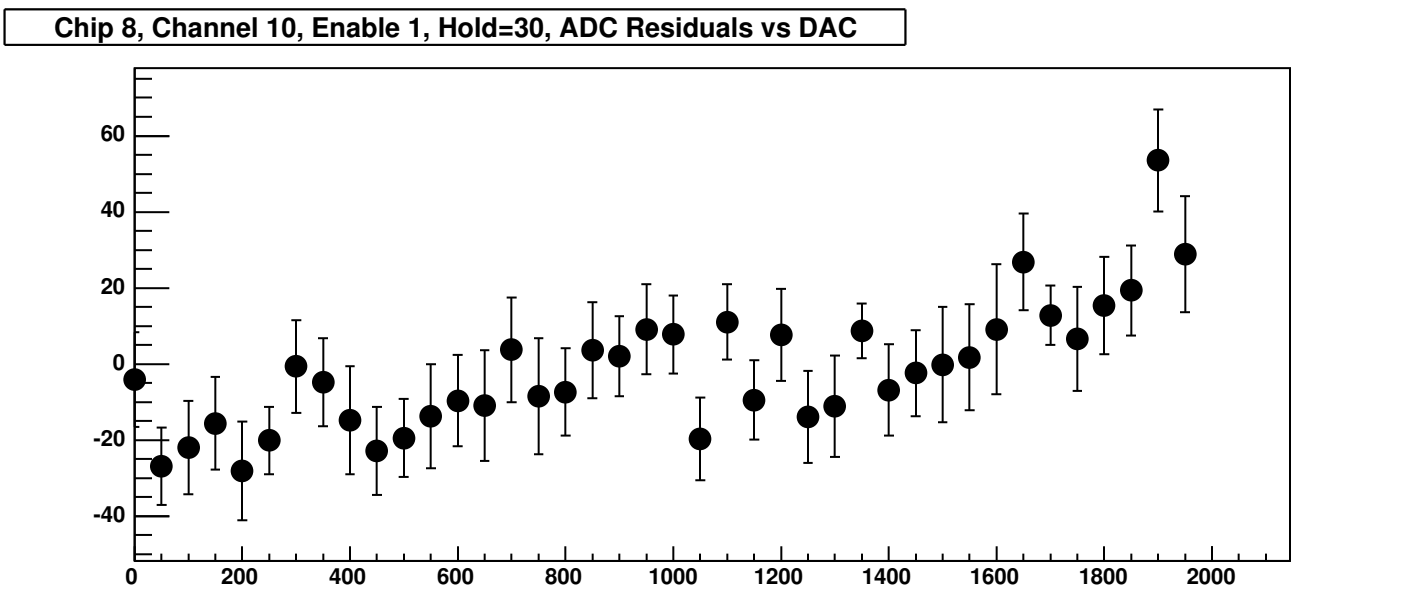

Chip 8, Channel 10, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 12.69 / 11
p0 -1169 ± 43.63
p1 2.254 ± 0.03891
3000
2500
2000
1500
1000
500
0
0 200 400 600 800 1000 1200 1400 1600 1800 2000

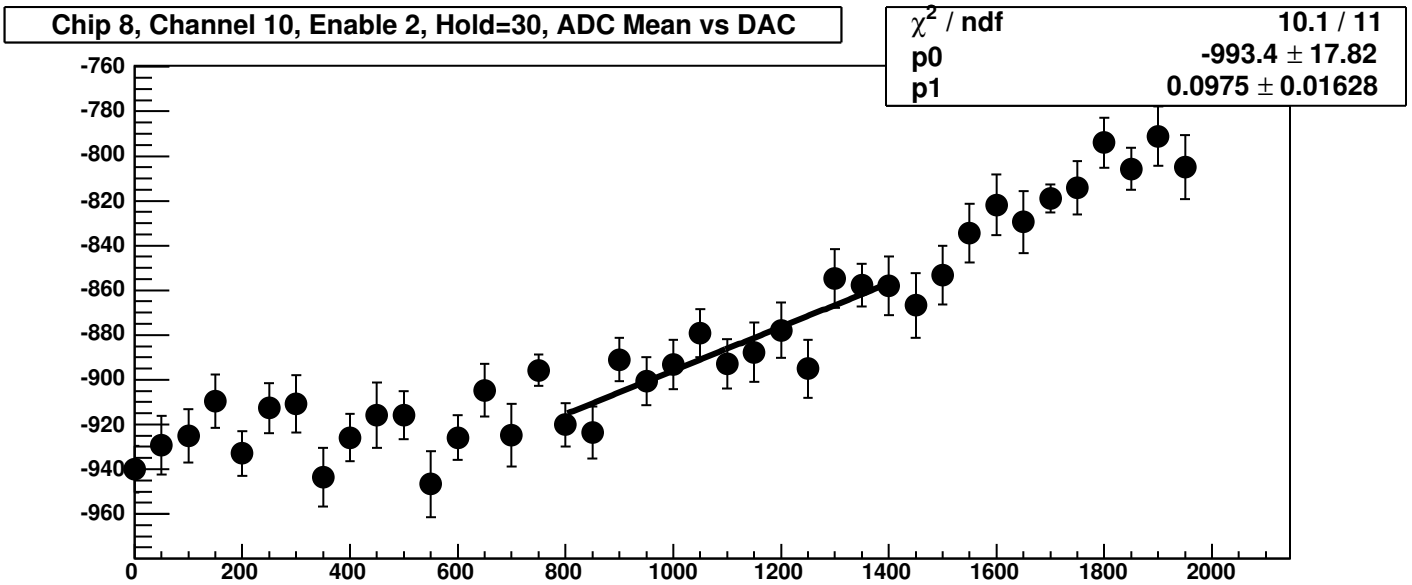

Chip 8, Channel 10, Enable 0, Hold=30, ADC Noise vs DAC
240
220
200
180
160
140
120
100
80
60
40
0 200 400 600 800 1000 1200 1400 1600 1800 2000

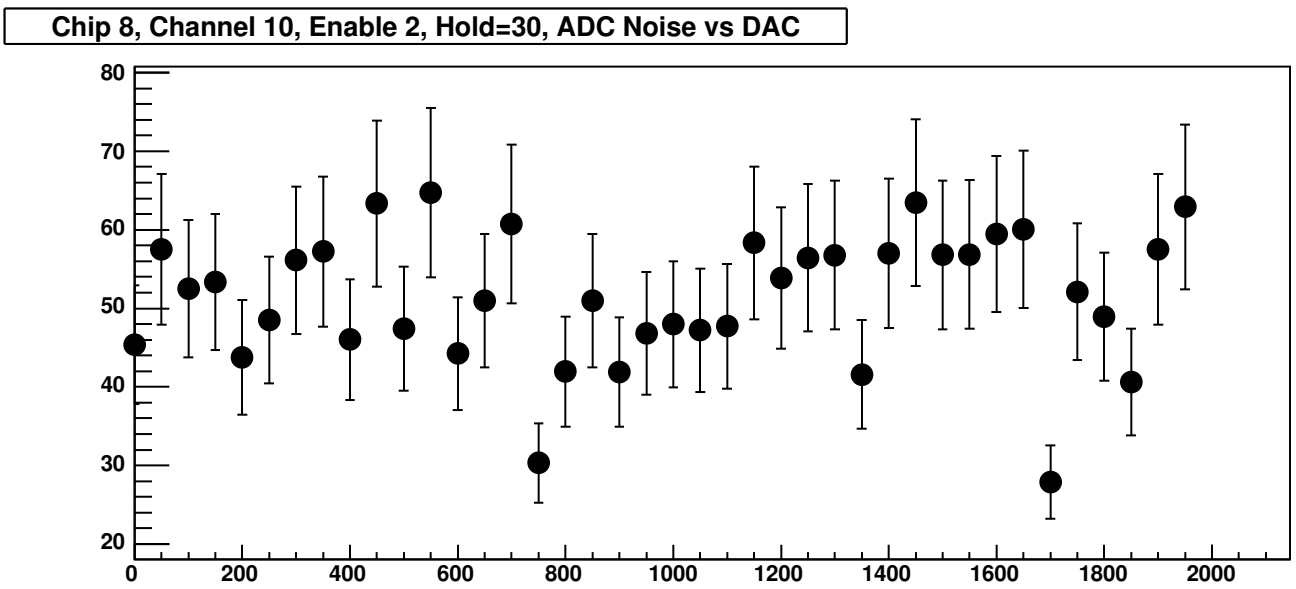

Chip 8, Channel 10, Enable 0, Hold=30, ADC Residuals vs DAC
1500
1000
500
0
-500
0 200 400 600 800 1000 1200 1400 1600 1800 2000

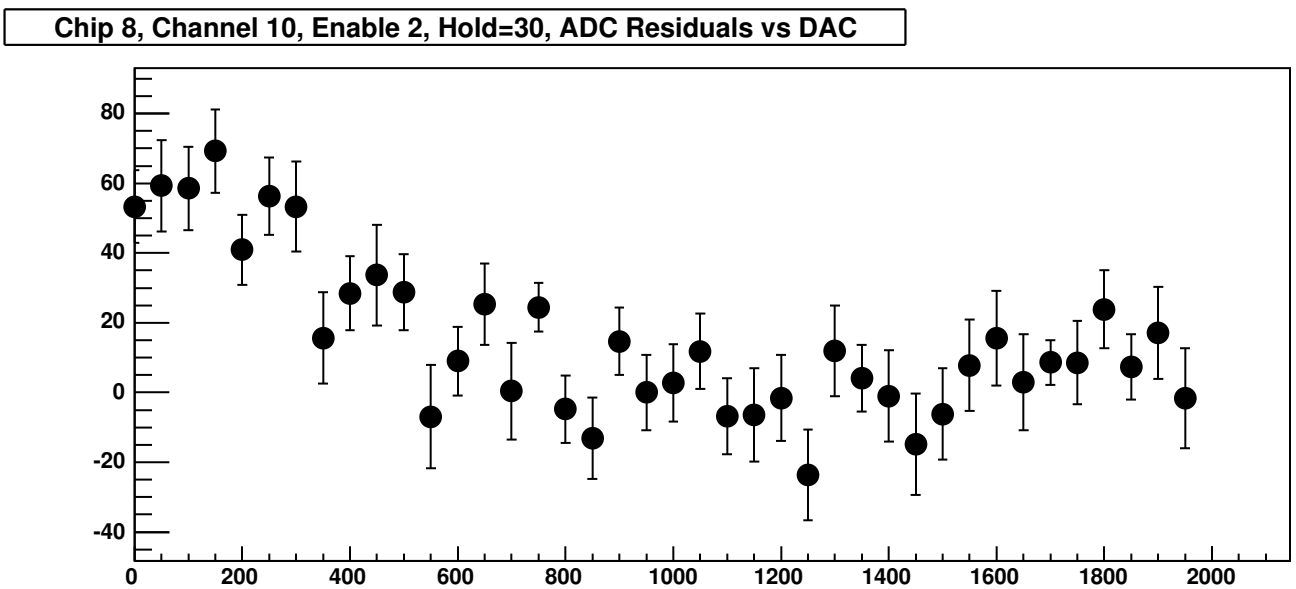

Chip 8, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 11.19 / 11
p0 -960.9 ± 18.02
p1 0.006474 ± 0.01589
-880
-900
-920
-940
-960
-980
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

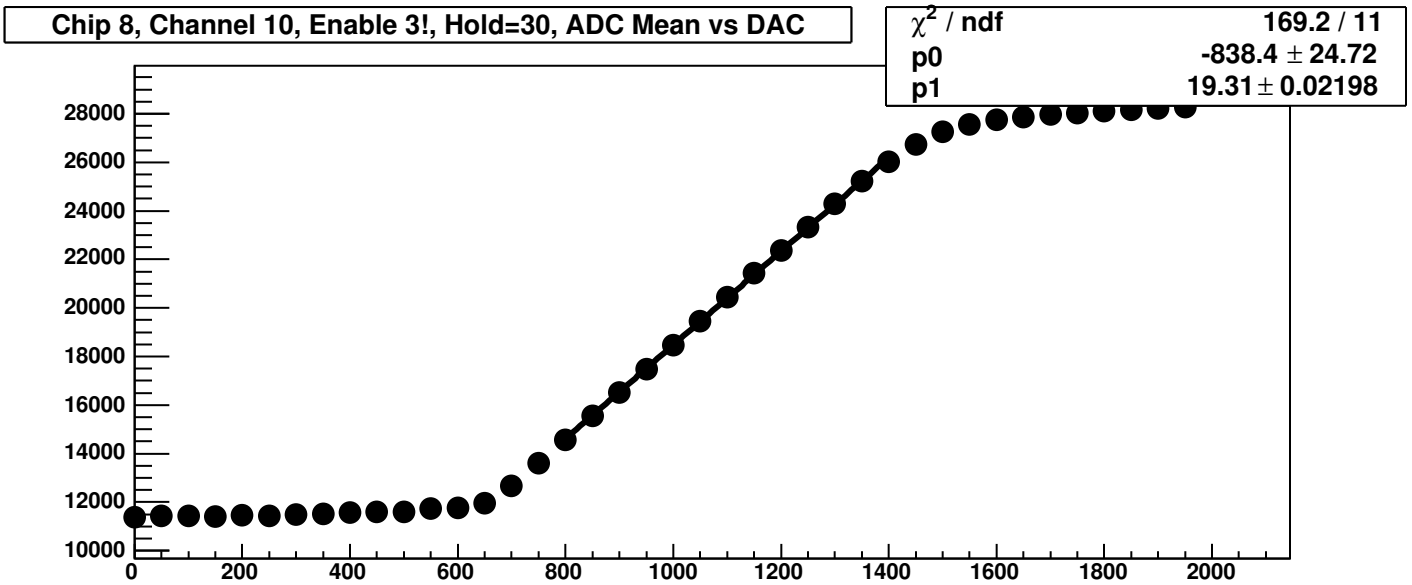

Chip 8, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

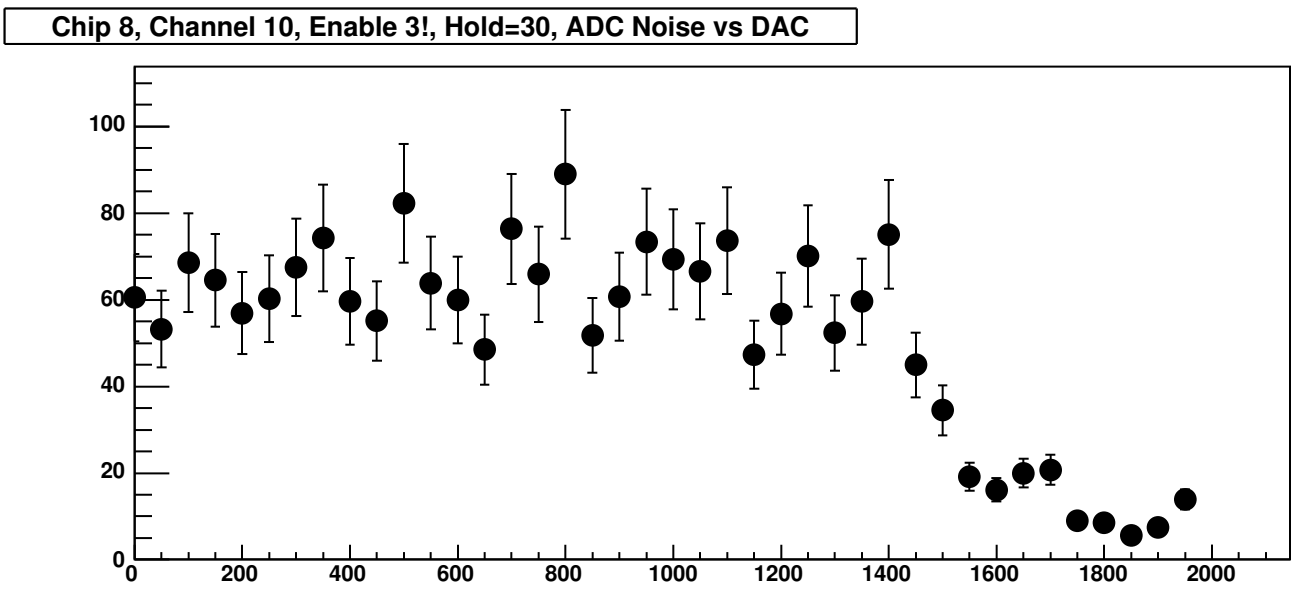

Chip 8, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

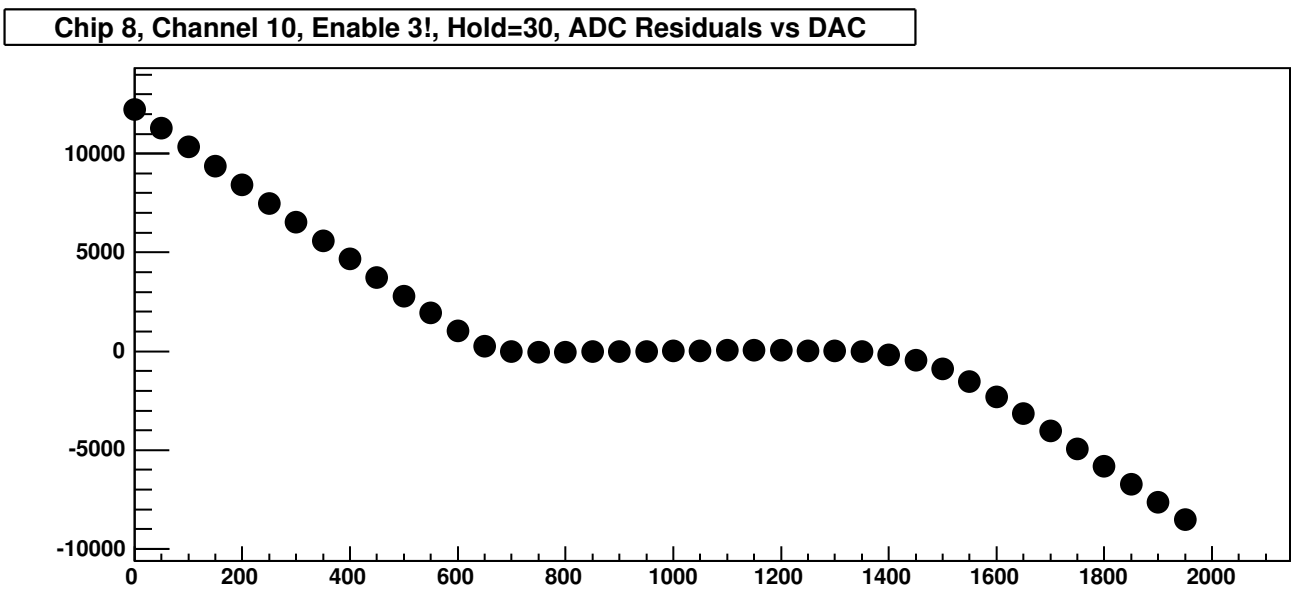

Chip 8, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 10.1 / 11
p0 -993.4 ± 17.82
p1 0.0975 ± 0.01628
-760
-780
-800
-820
-840
-860
-880
-900
-920
-940
-960
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

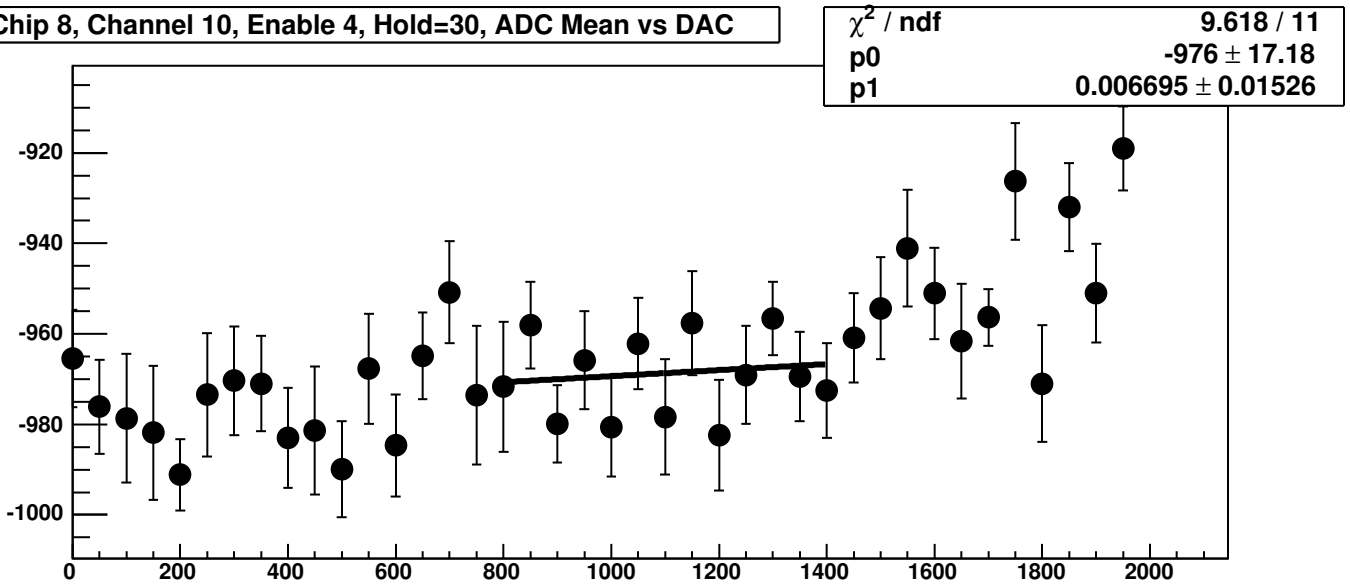

Chip 8, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 10, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 169.2 / 11
p0 -838.4 ± 24.72
p1 19.31 ± 0.02198
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

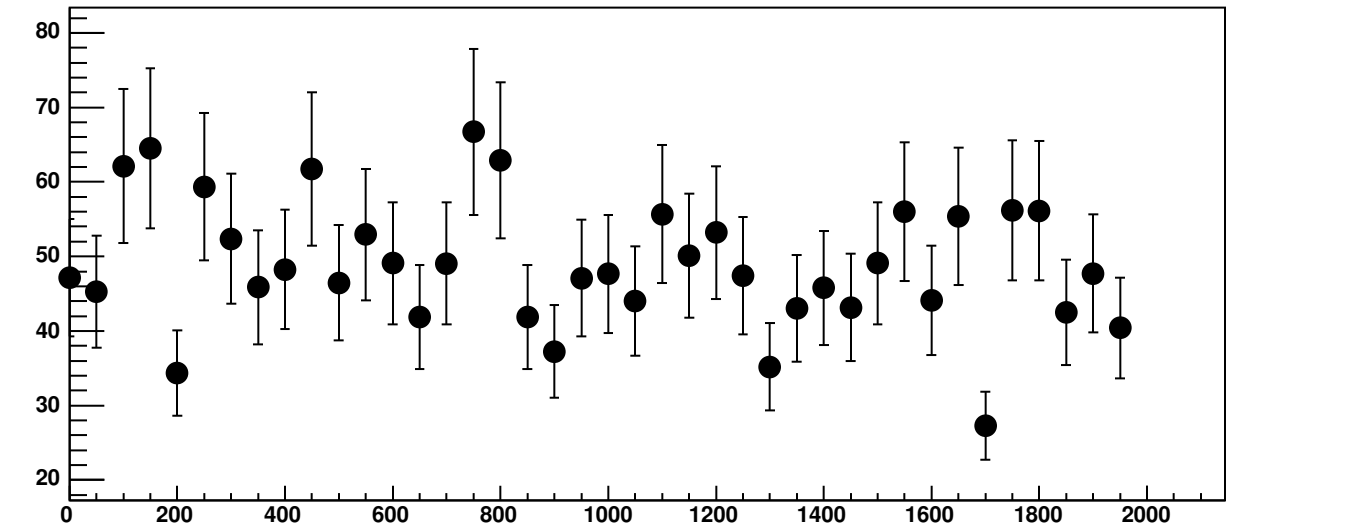

Chip 8, Channel 10, Enable 3!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 10, Enable 3!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

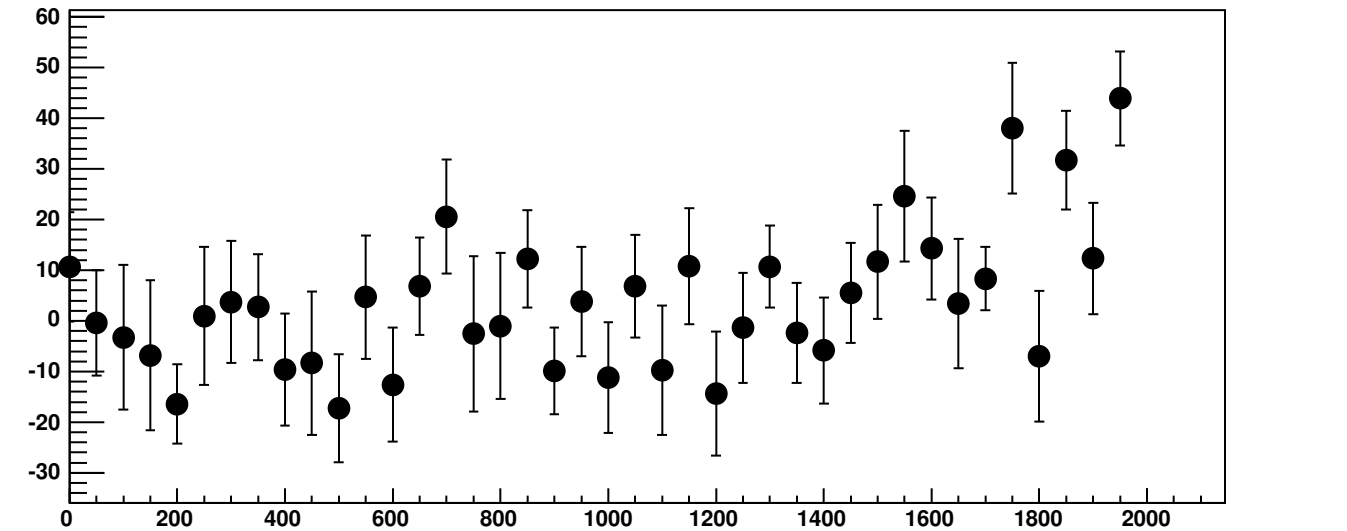
Chip 8, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC

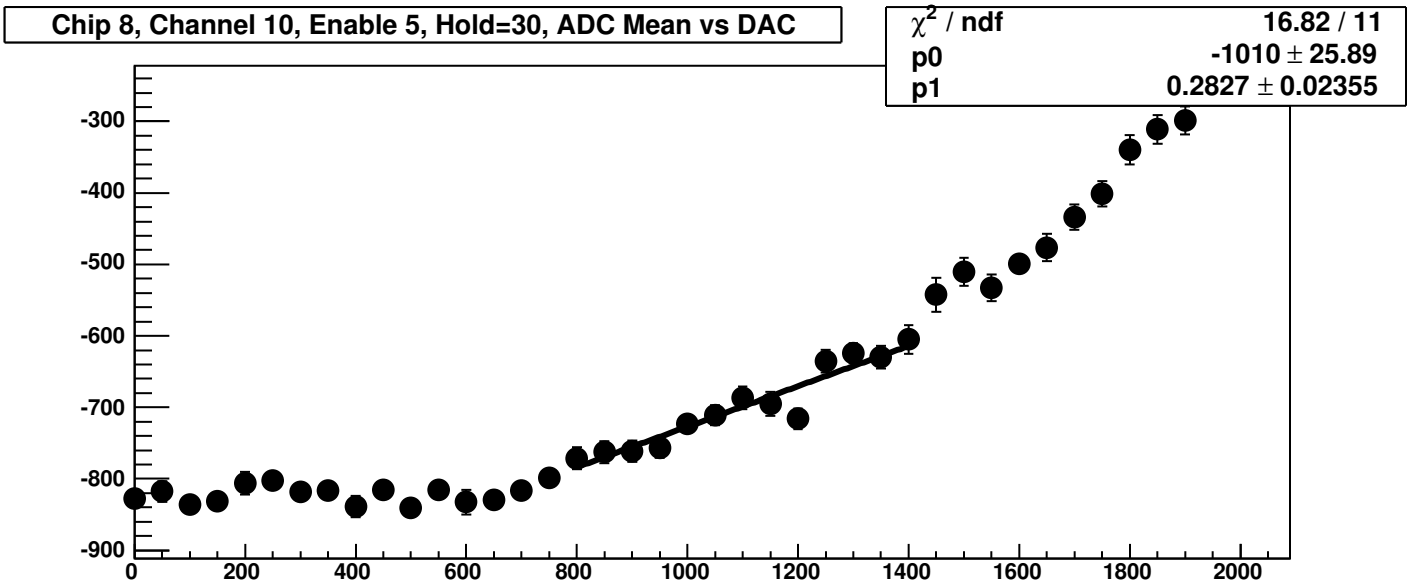


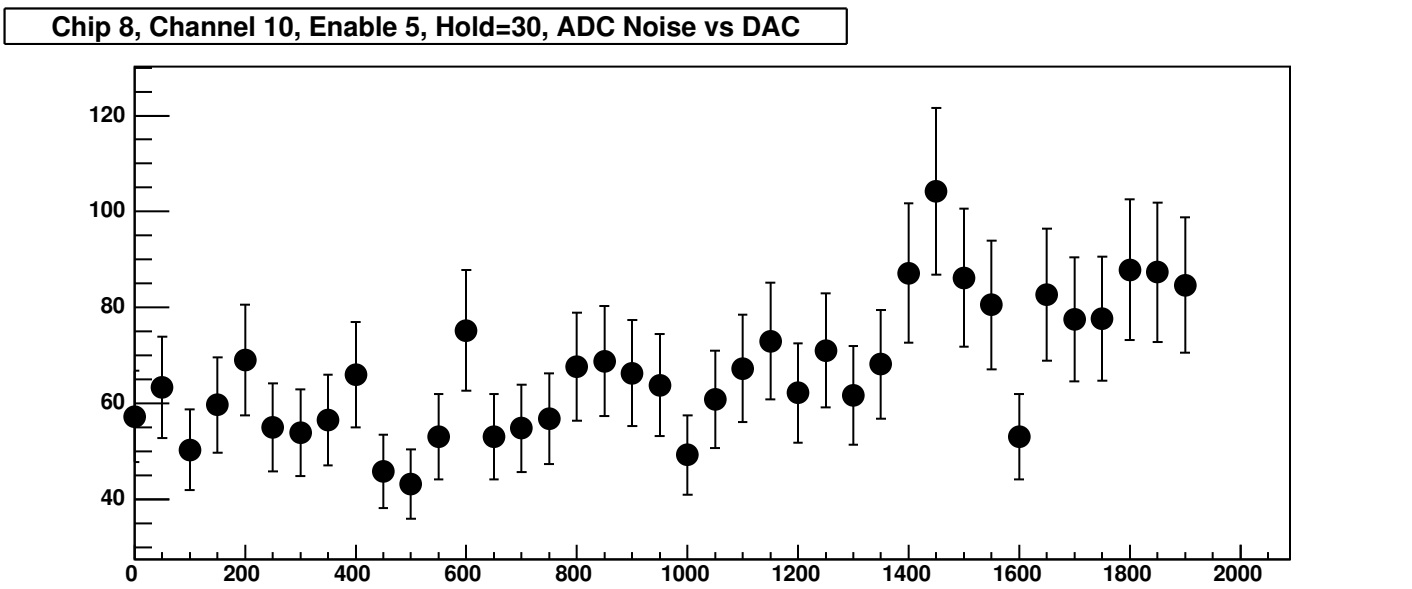
Chip 8, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC

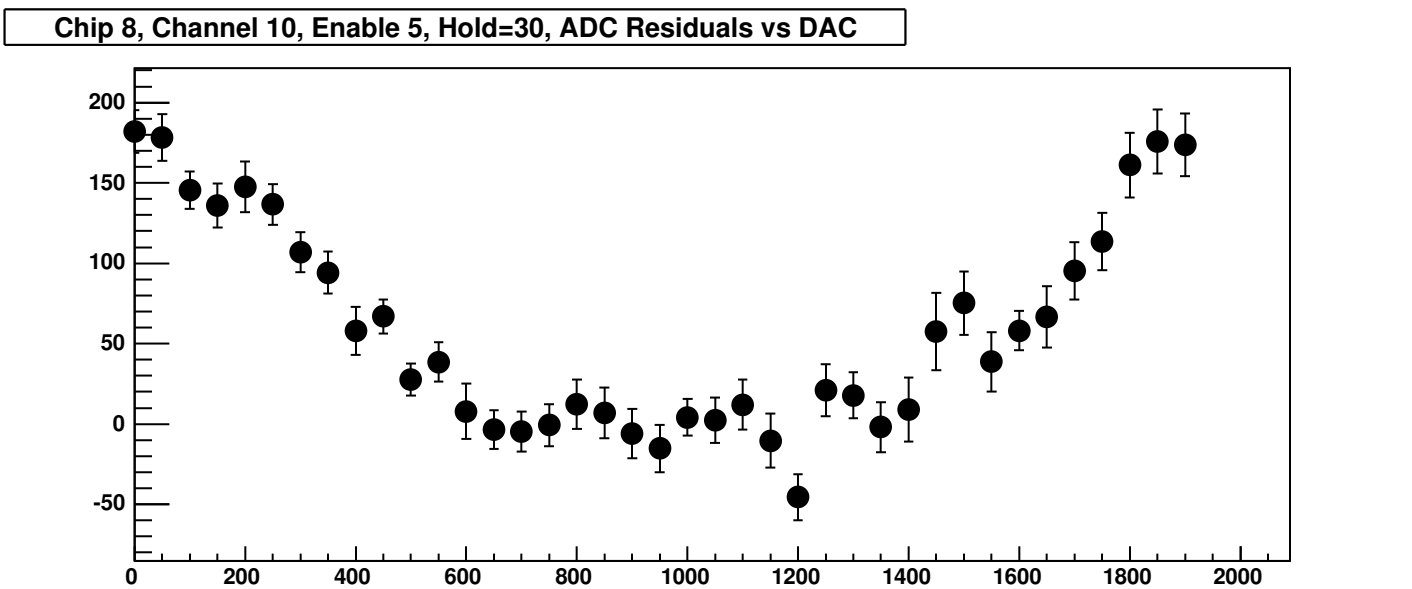


Chip 8, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC

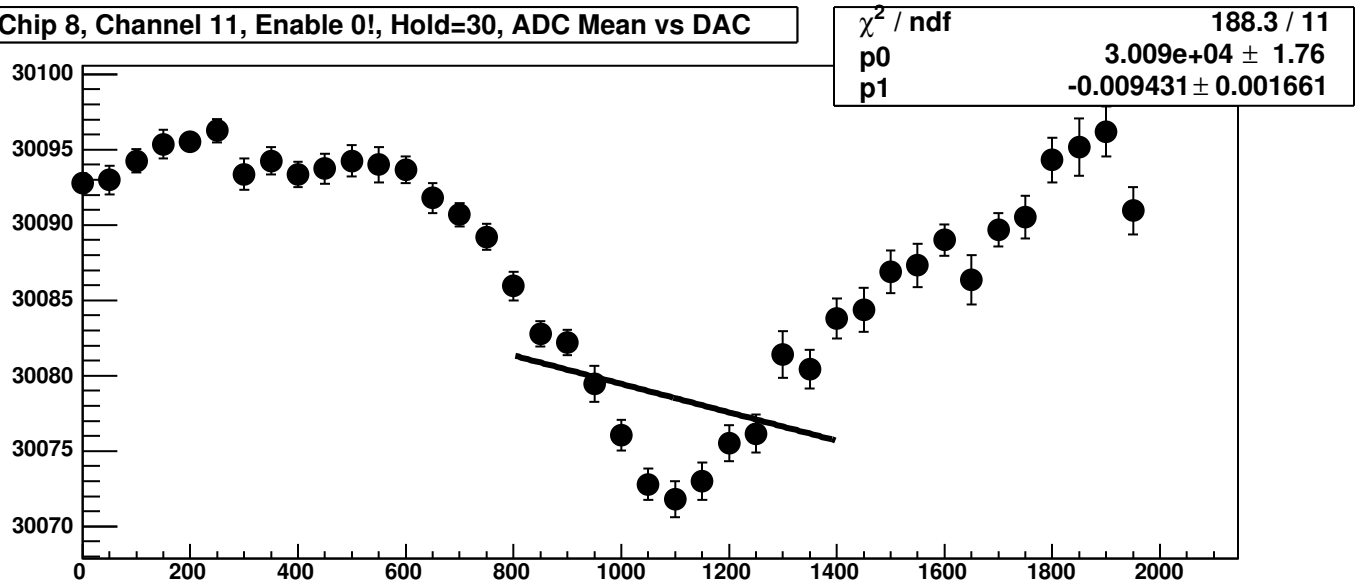



Chip 8, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 16.82 / 11
p0 -1010 ± 25.89
p1 0.2827 ± 0.02355
-300
-400
-500
-600
-700
-800
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

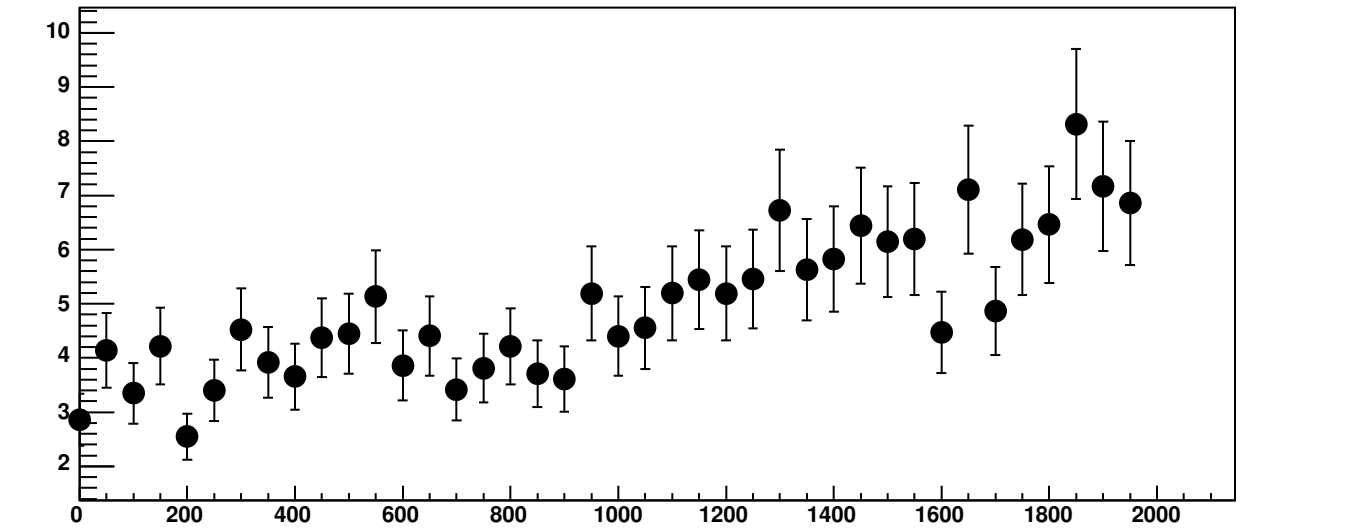

Chip 8, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

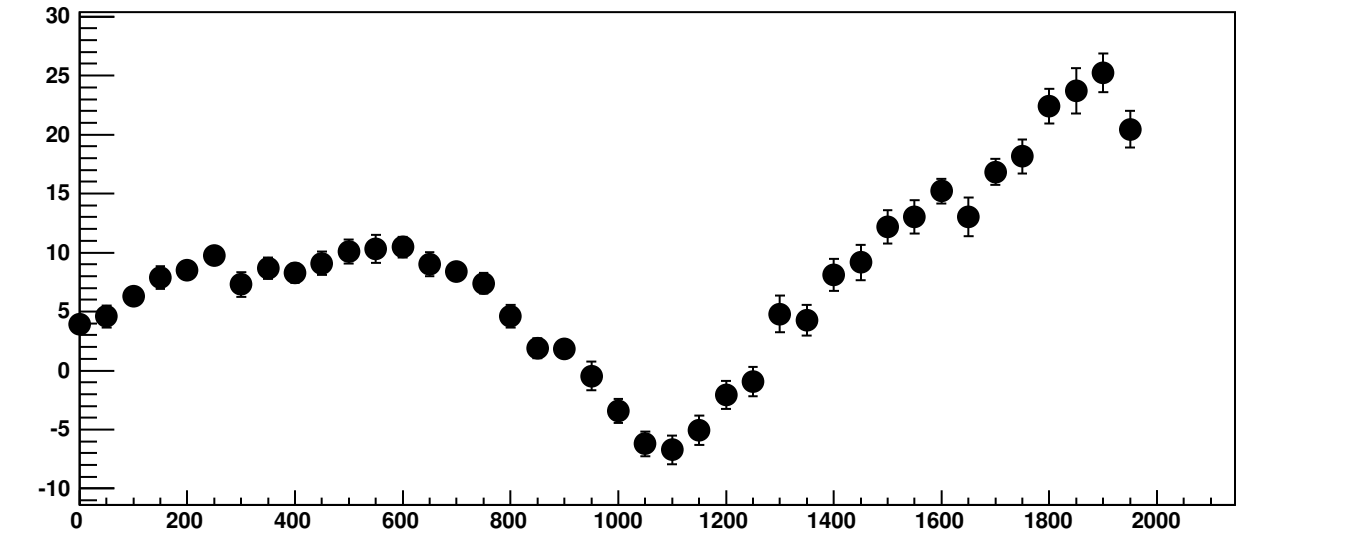
Chip 8, Channel 11, Enable 0!, Hold=30, ADC Mean vs DAC

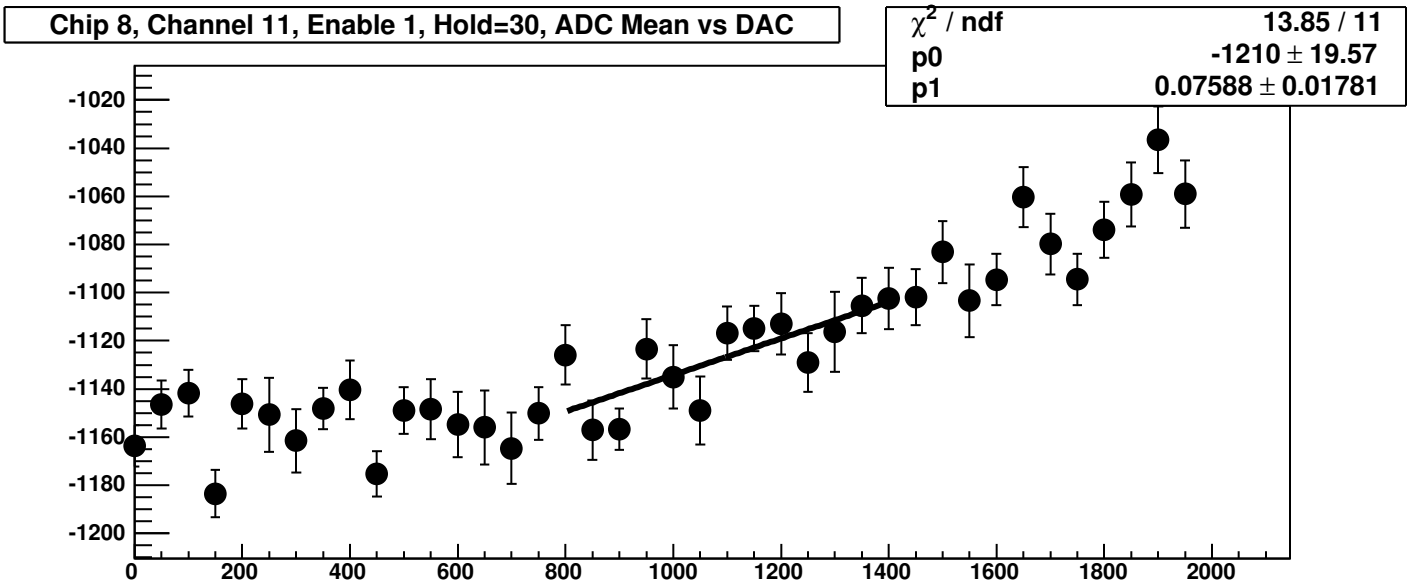


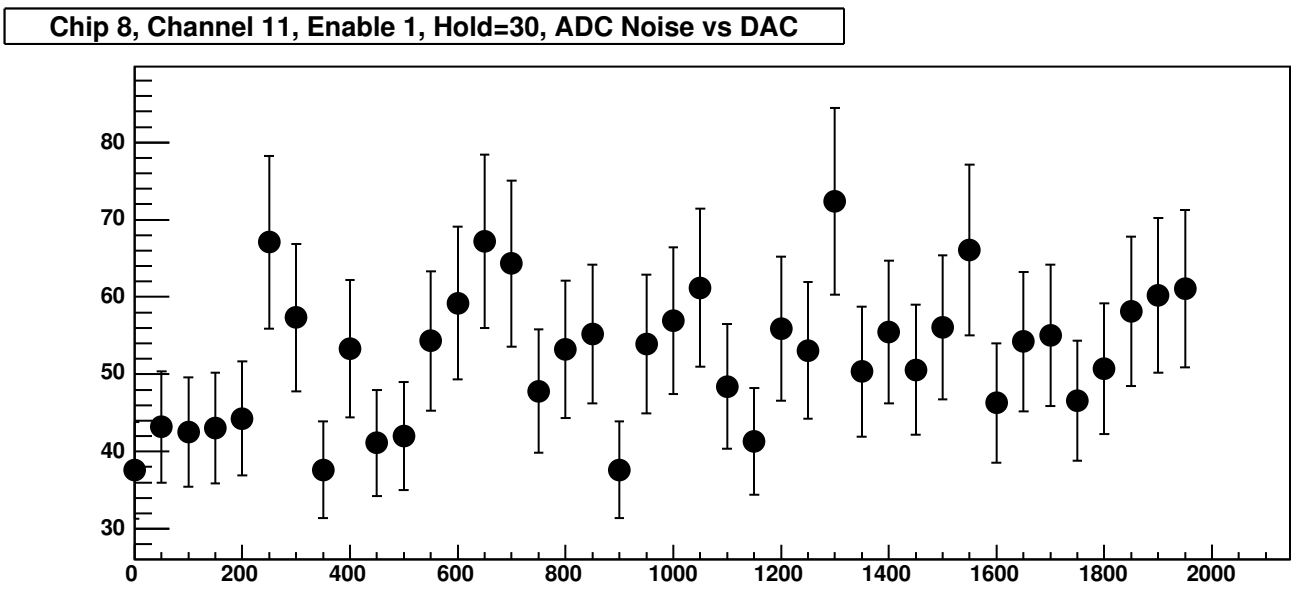
Chip 8, Channel 11, Enable 0!, Hold=30, ADC Noise vs DAC

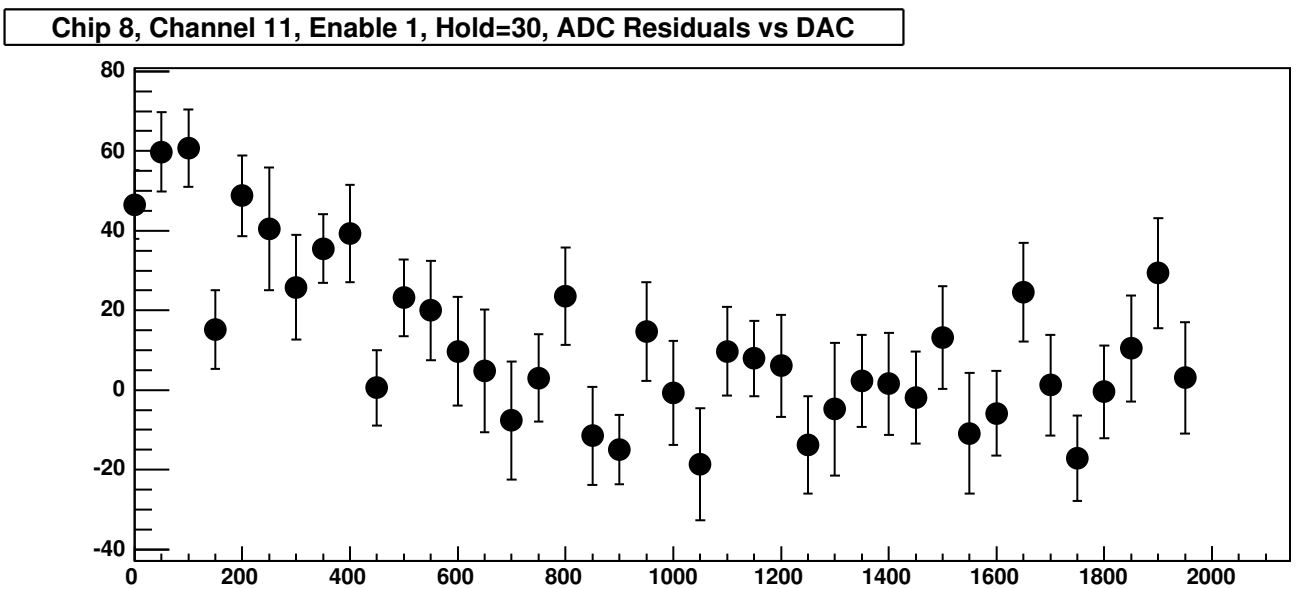


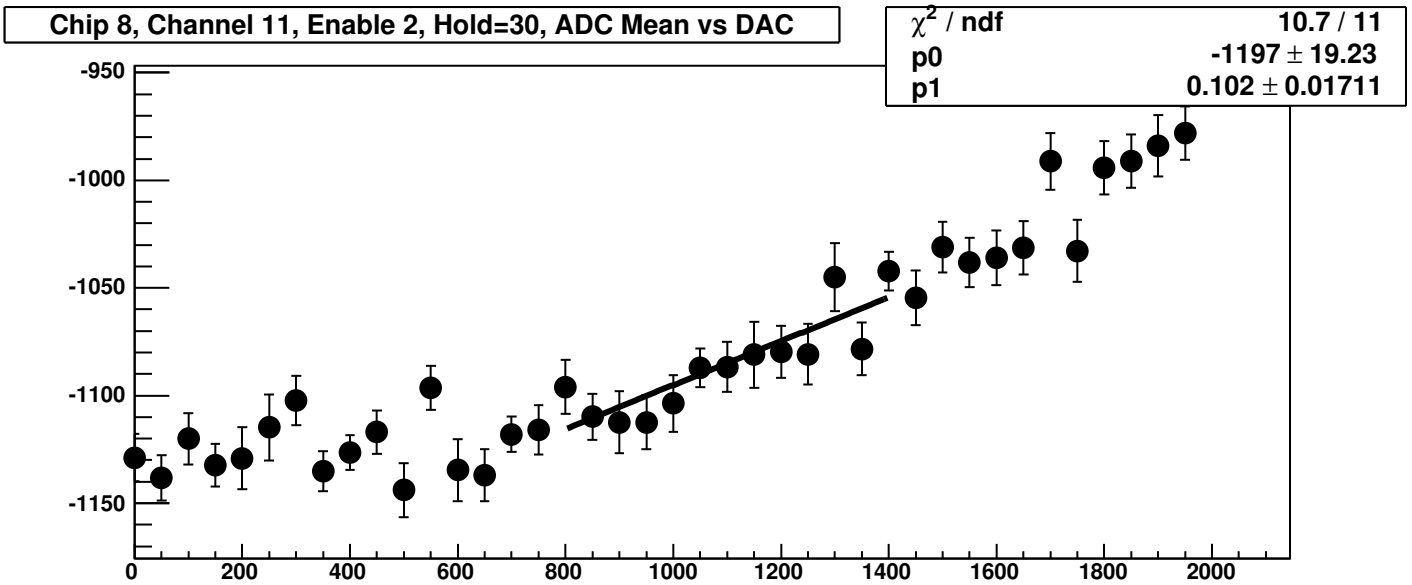
Chip 8, Channel 11, Enable 0!, Hold=30, ADC Residuals vs DAC

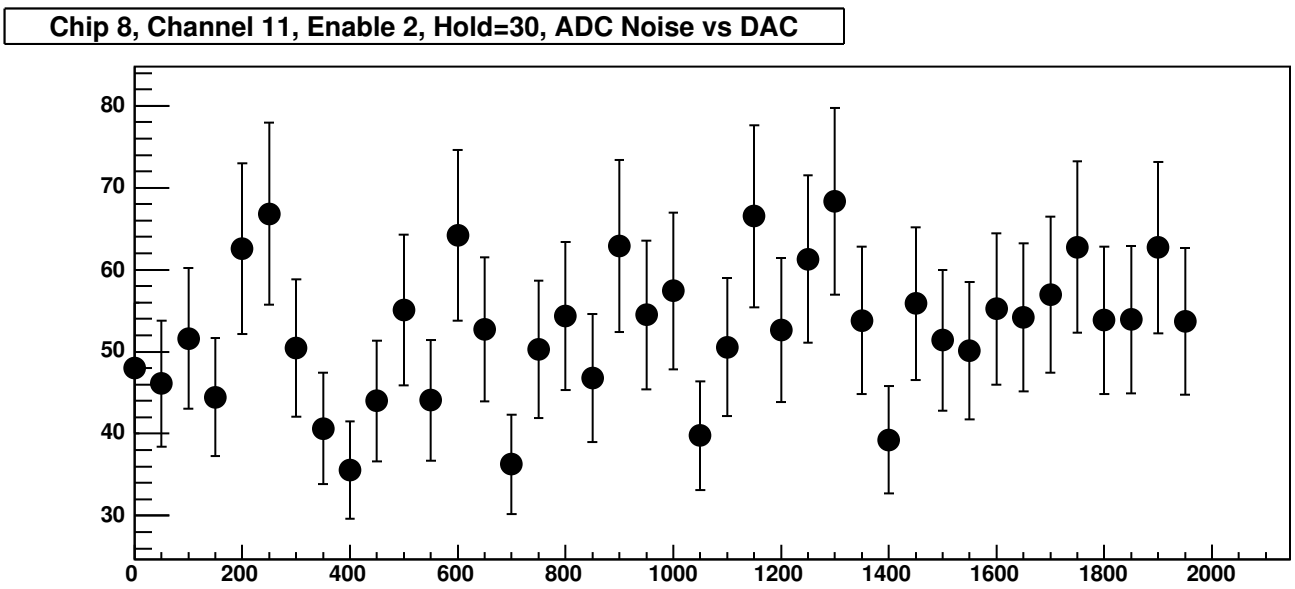


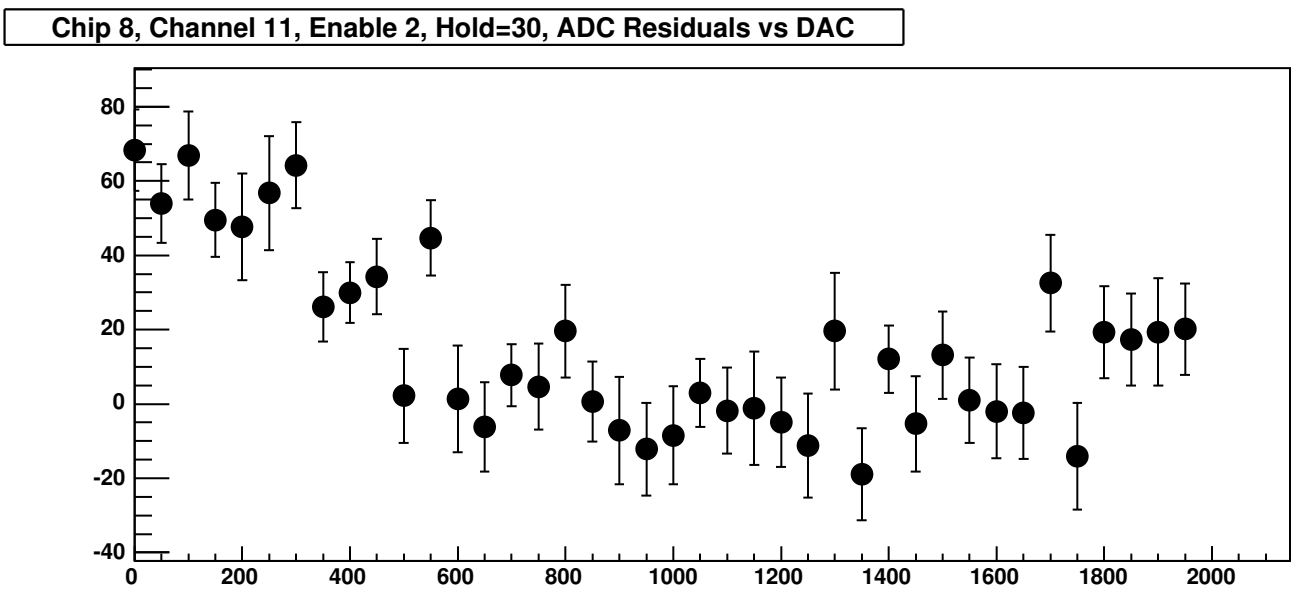

Chip 8, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 13.85 / 11
p0 -1210 ± 19.57
p1 0.07588 ± 0.01781
-1020
-1040
-1060
-1080
-1100
-1120
-1140
-1160
-1180
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

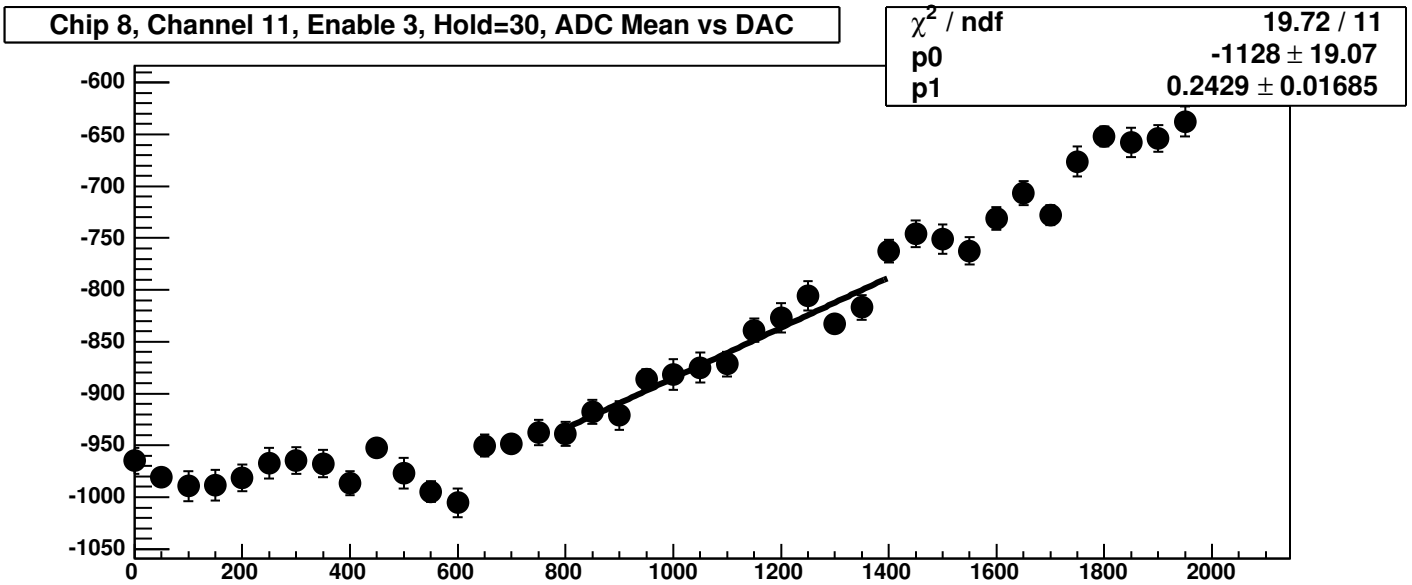

Chip 8, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

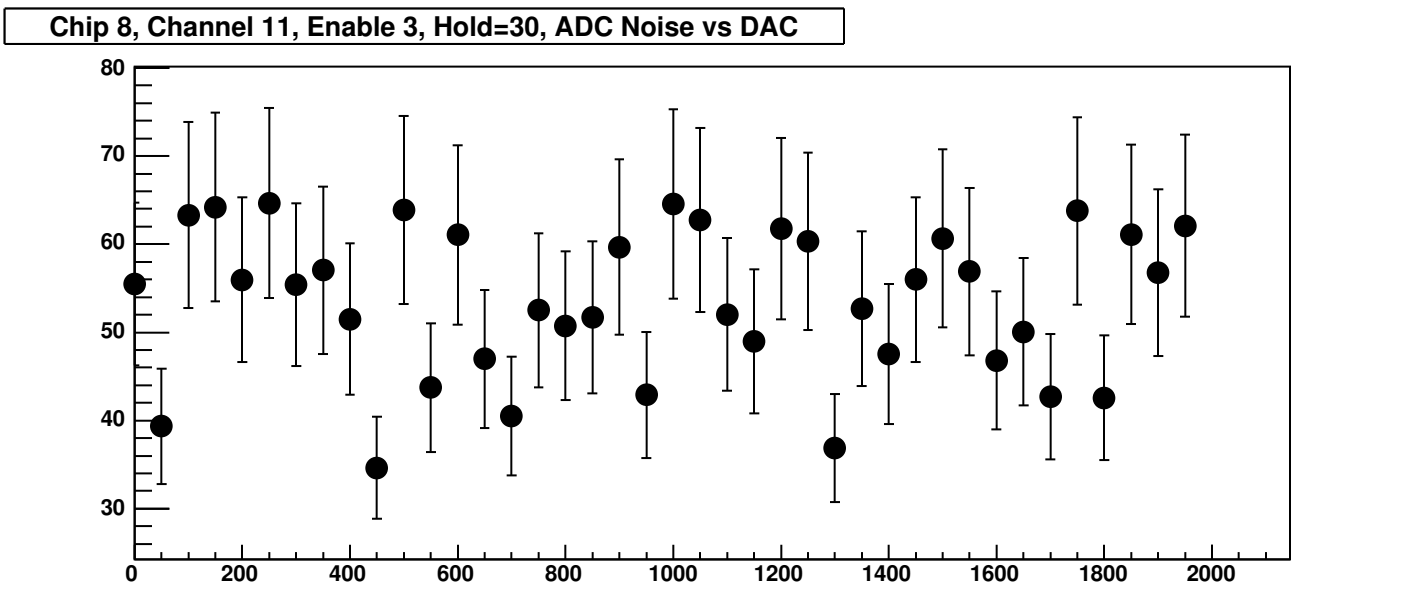

Chip 8, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

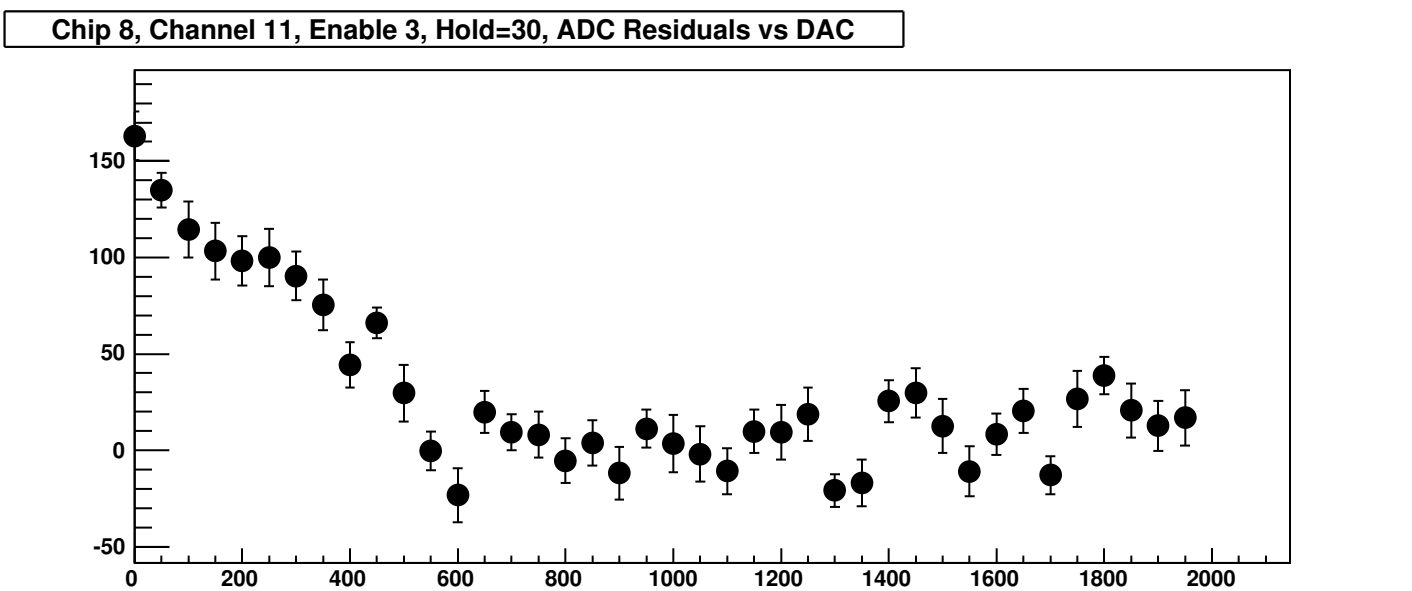

Chip 8, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 10.7 / 11
p0 -1197 ± 19.23
p1 0.102 ± 0.01711
-950
-1000
-1050
-1100
-1150
0
200
400
600
800
1000
1200
1400
1600
1800
2000

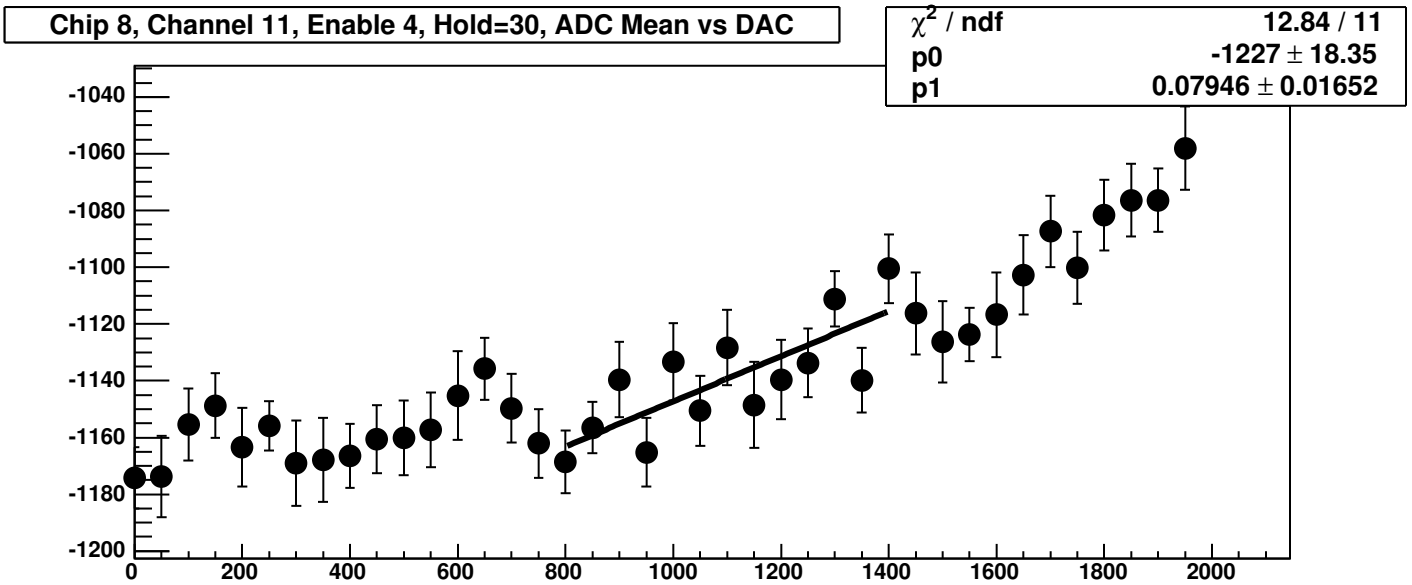

Chip 8, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

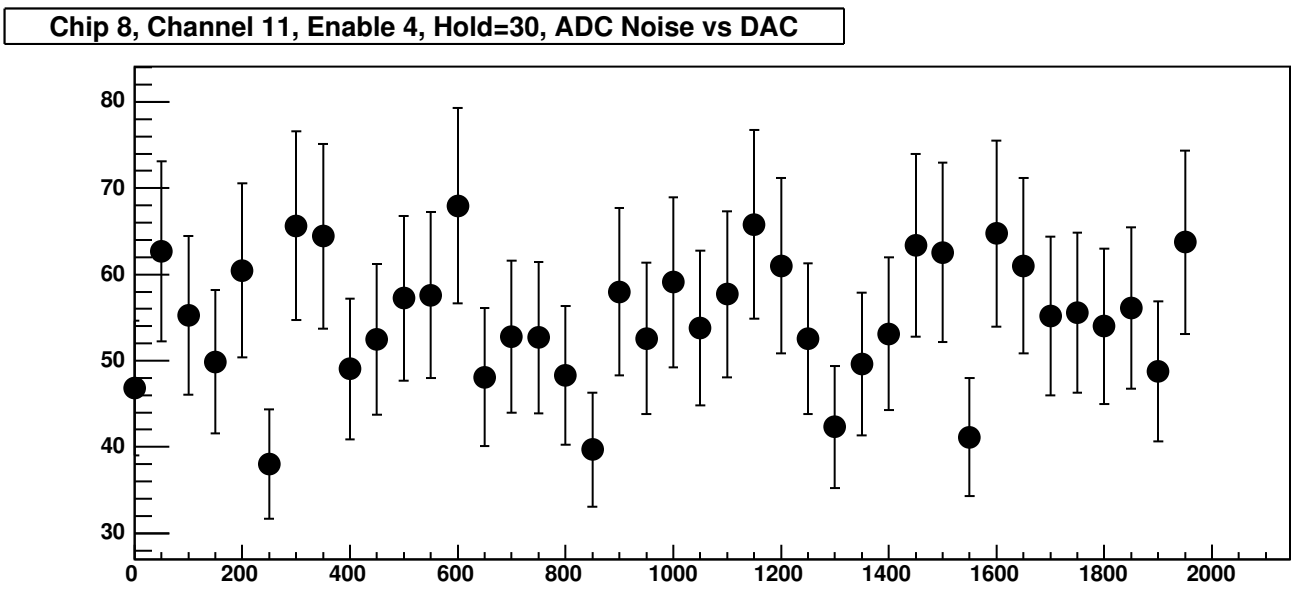

Chip 8, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

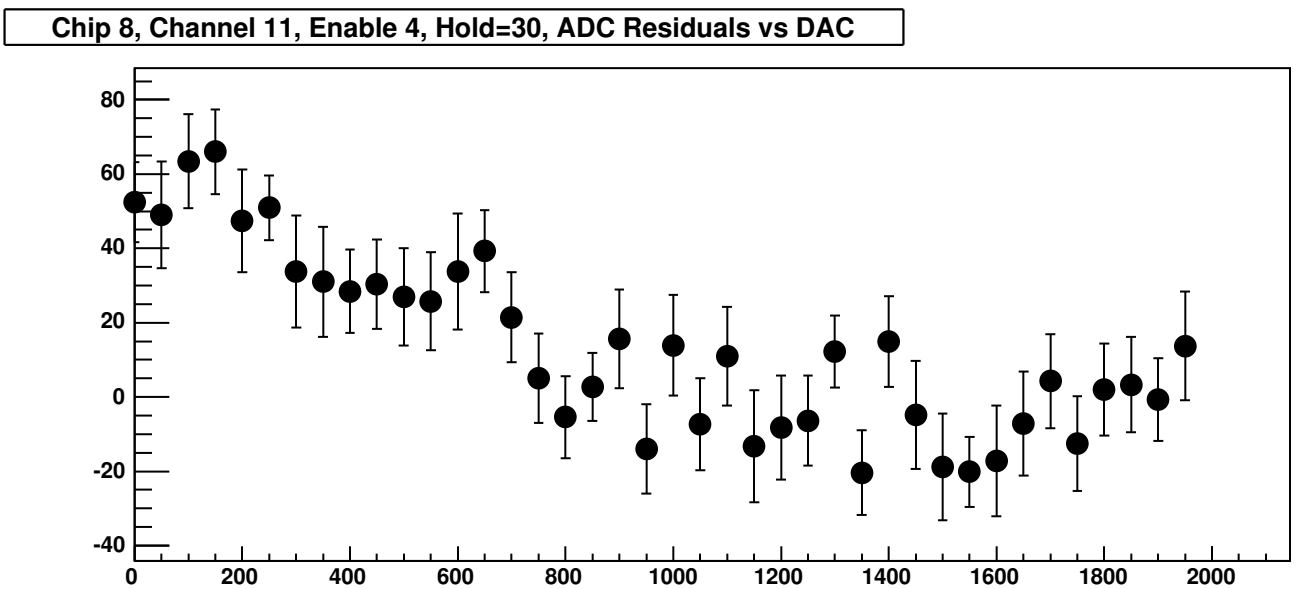

Chip 8, Channel 11, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 19.72 / 11
p0 -1128 ± 19.07
p1 0.2429 ± 0.01685
-600
-650
-700
-750
-800
-850
-900
-950
-1000
-1050
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 11, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

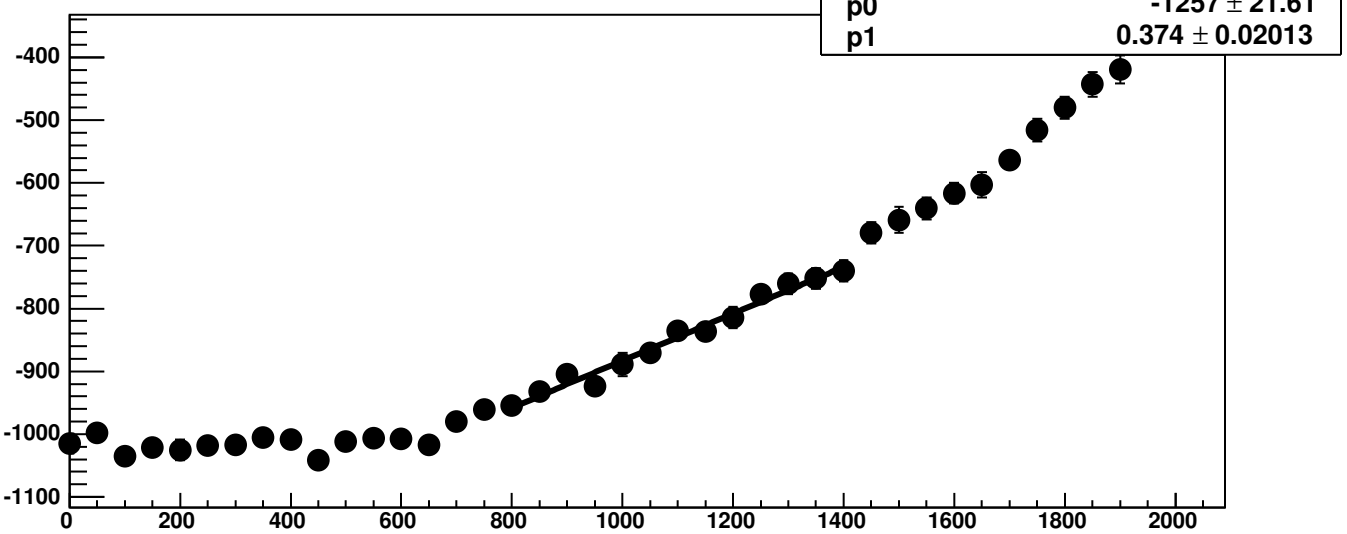

Chip 8, Channel 11, Enable 3, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 12.84 / 11
p0 -1227 ± 18.35
p1 0.07946 ± 0.01652
-1040
-1060
-1080
-1100
-1120
-1140
-1160
-1180
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

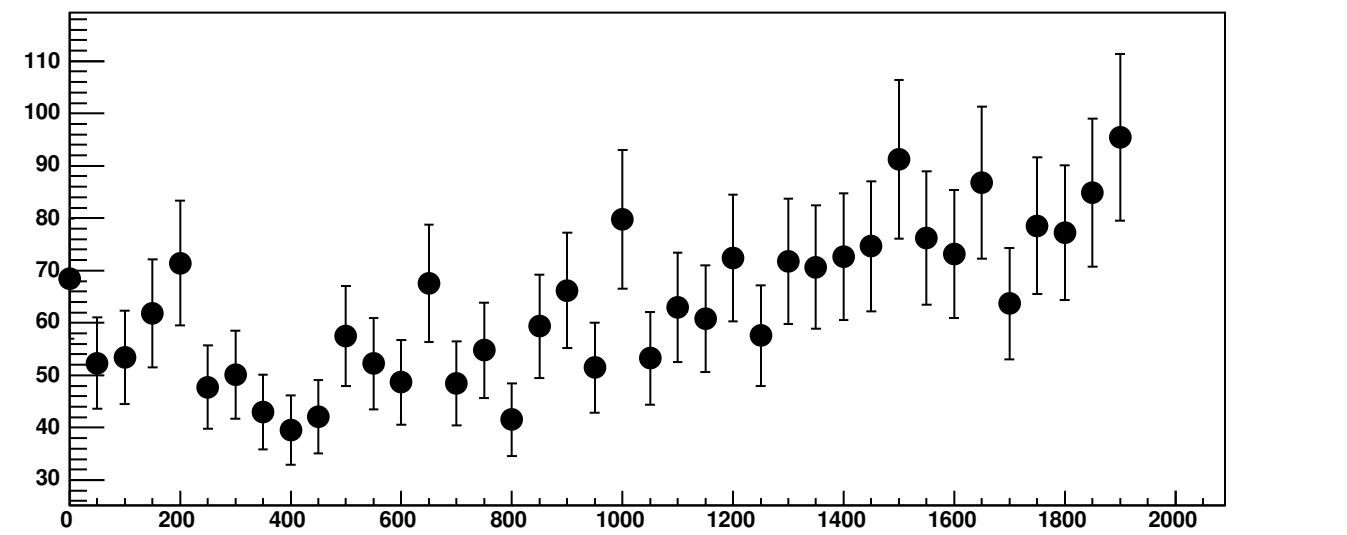

Chip 8, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

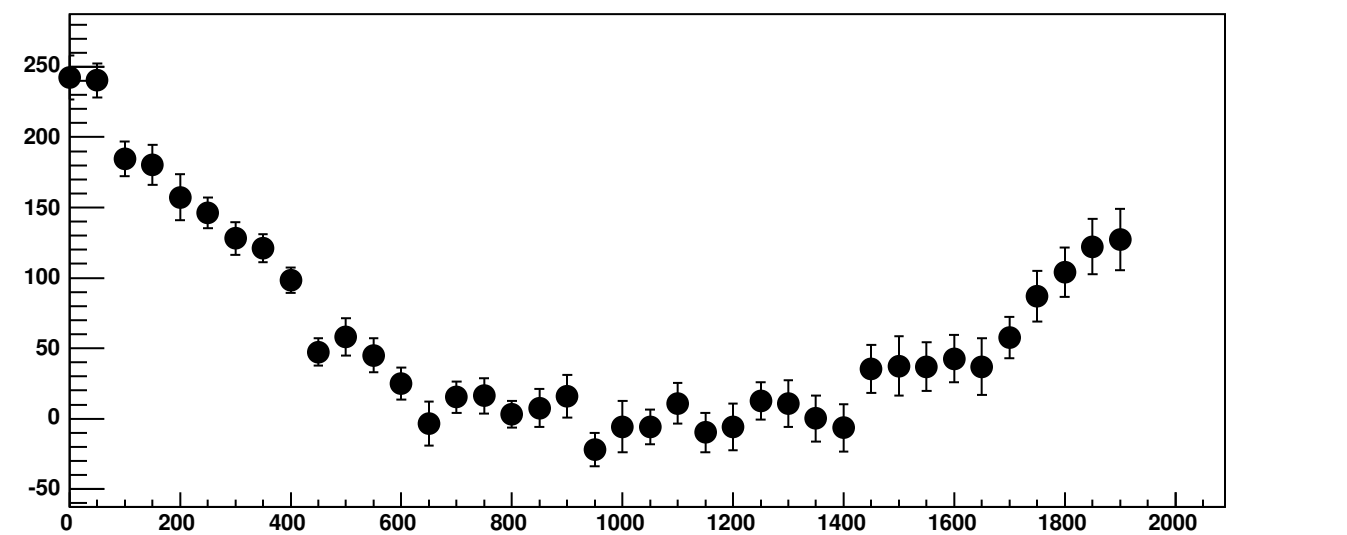
Chip 8, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC

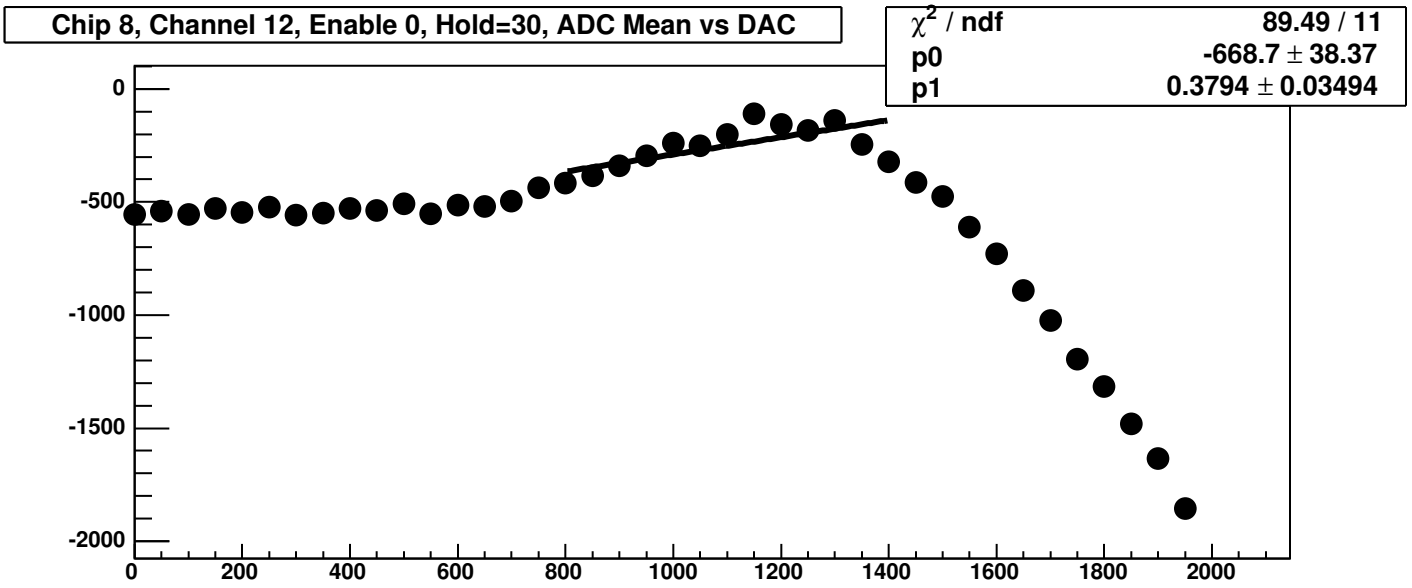


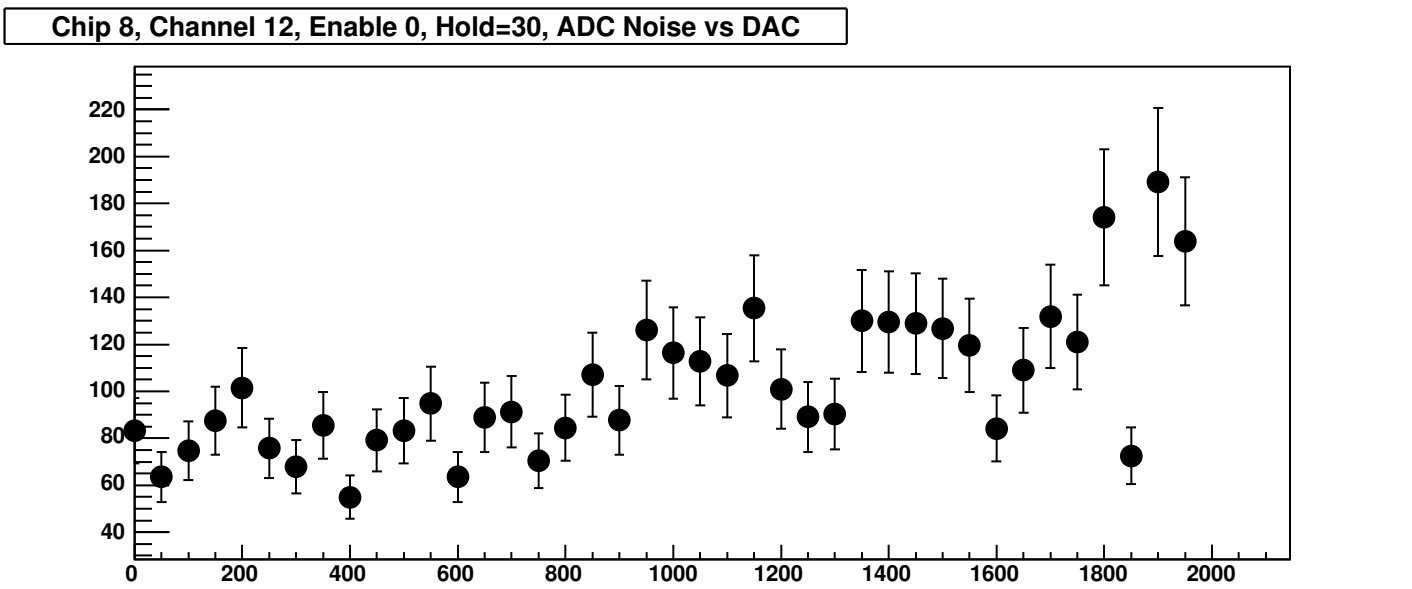
Chip 8, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC

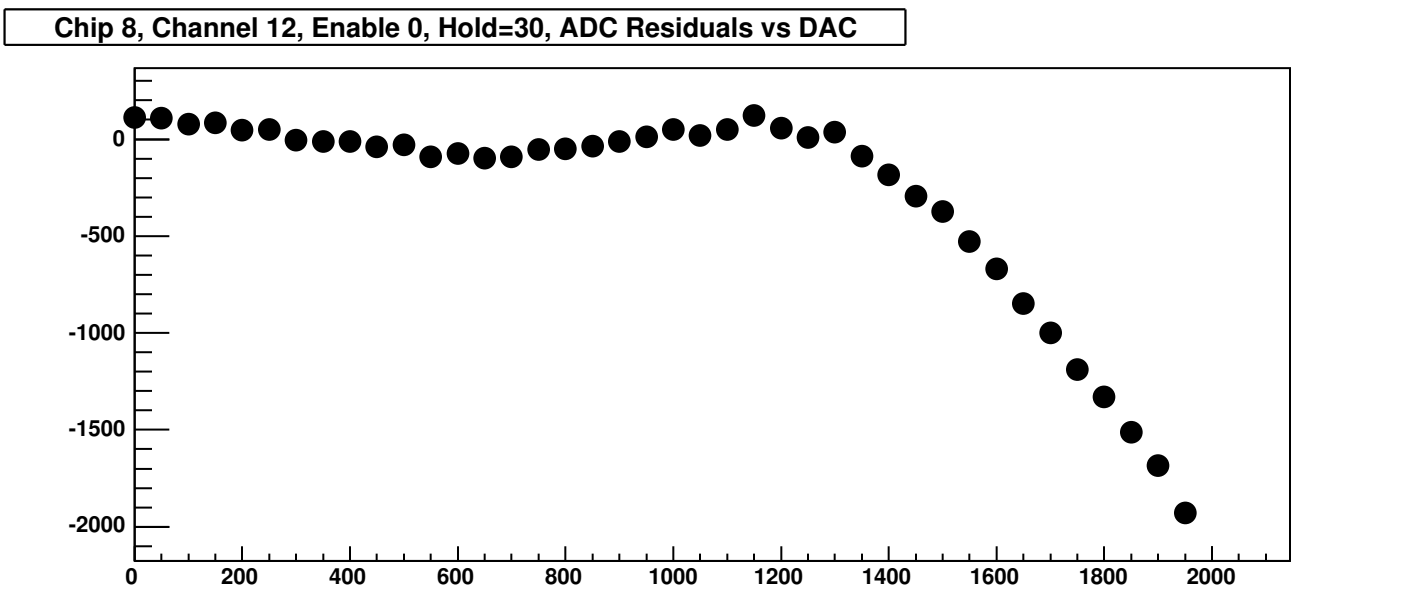


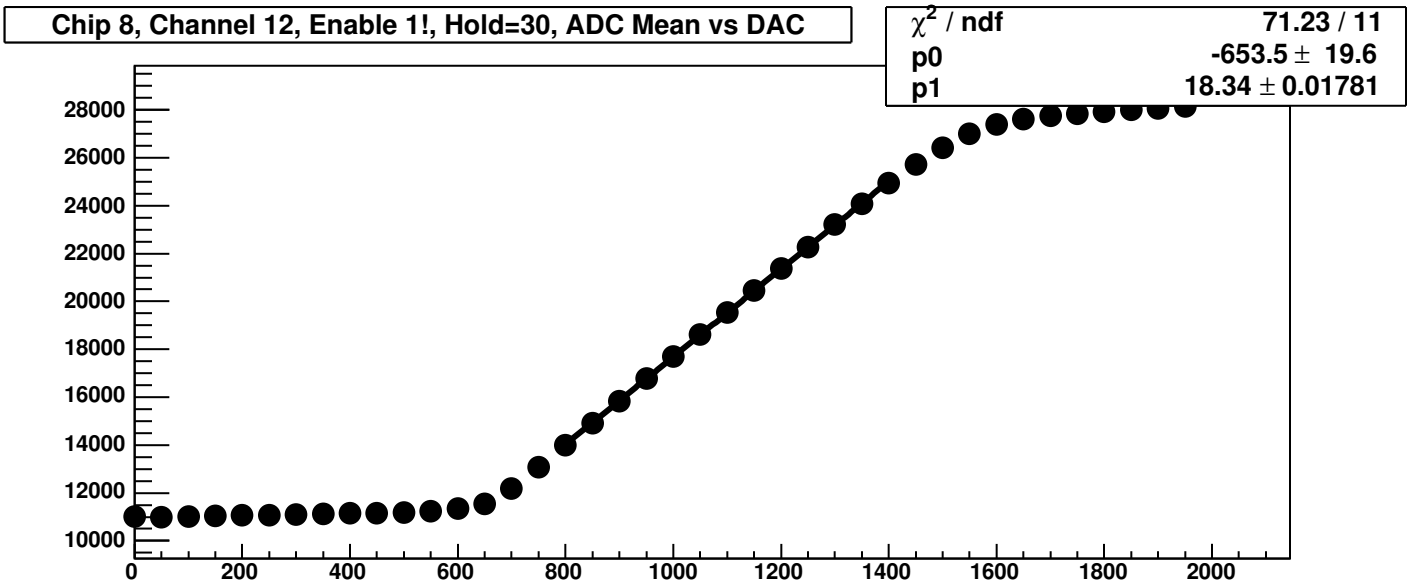
Chip 8, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC

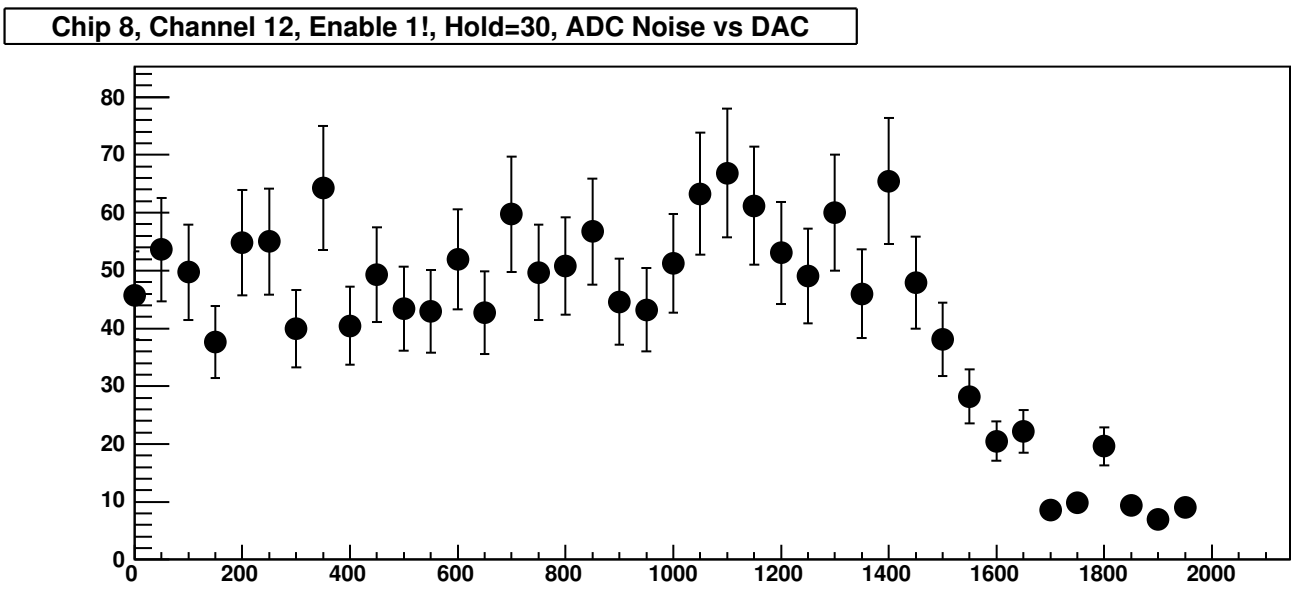


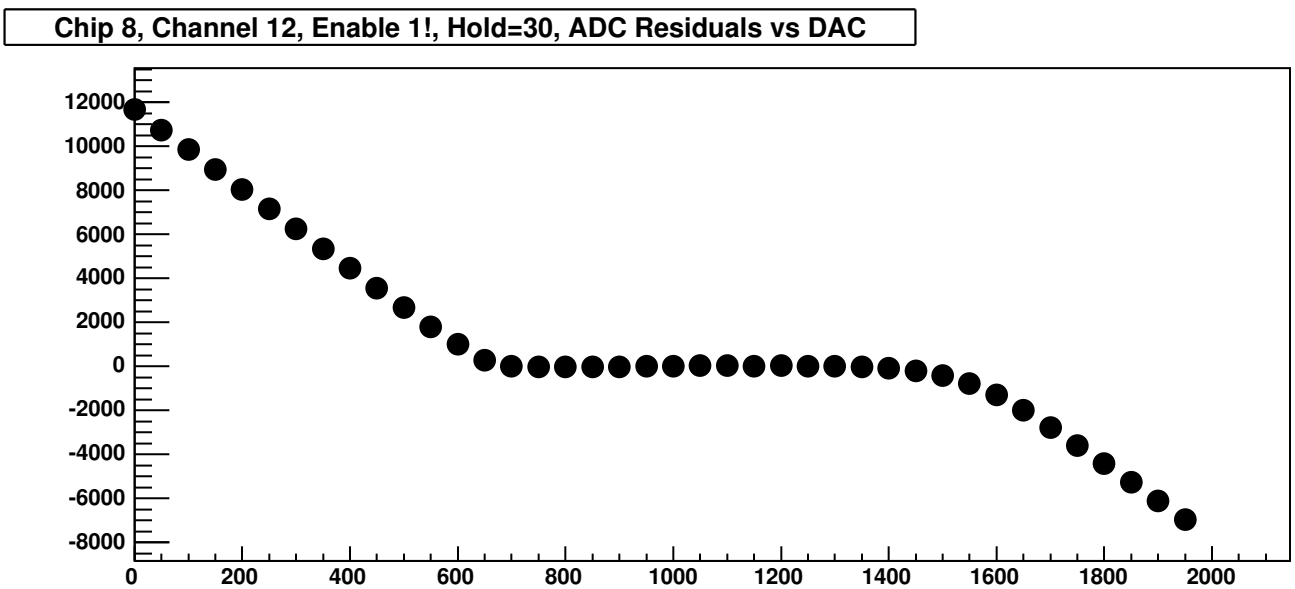

Chip 8, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 89.49 / 11
p0 -668.7 ± 38.37
p1 0.3794 ± 0.03494
0
-500
-1000
-1500
-2000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

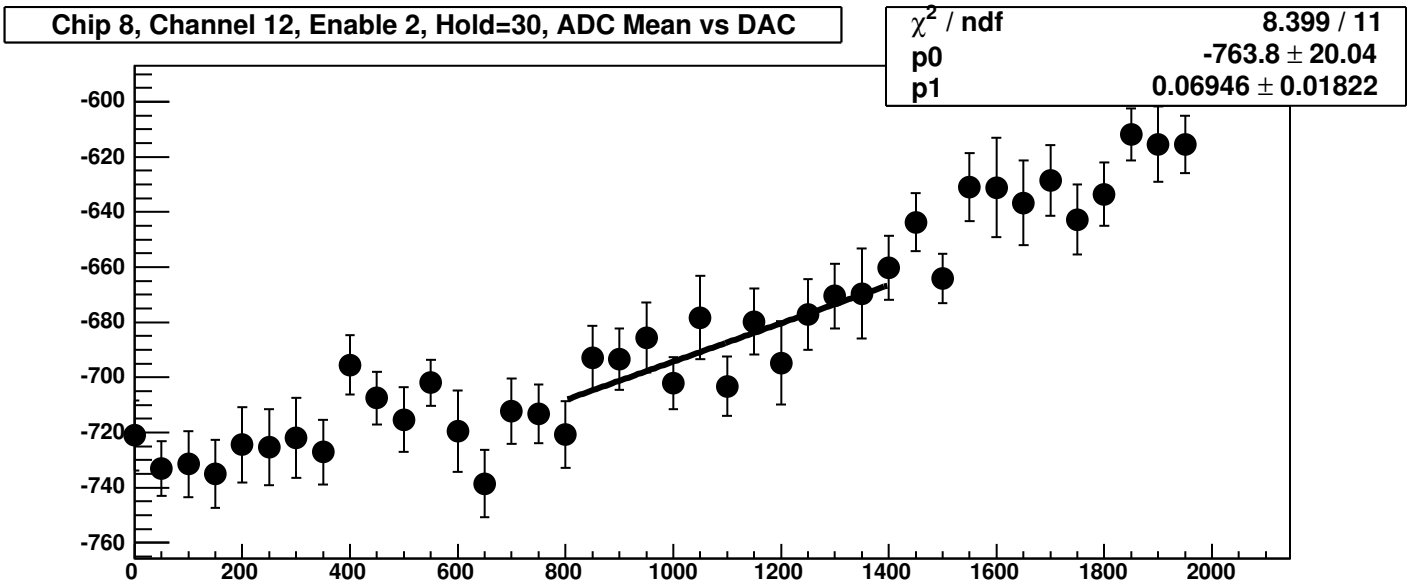

Chip 8, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

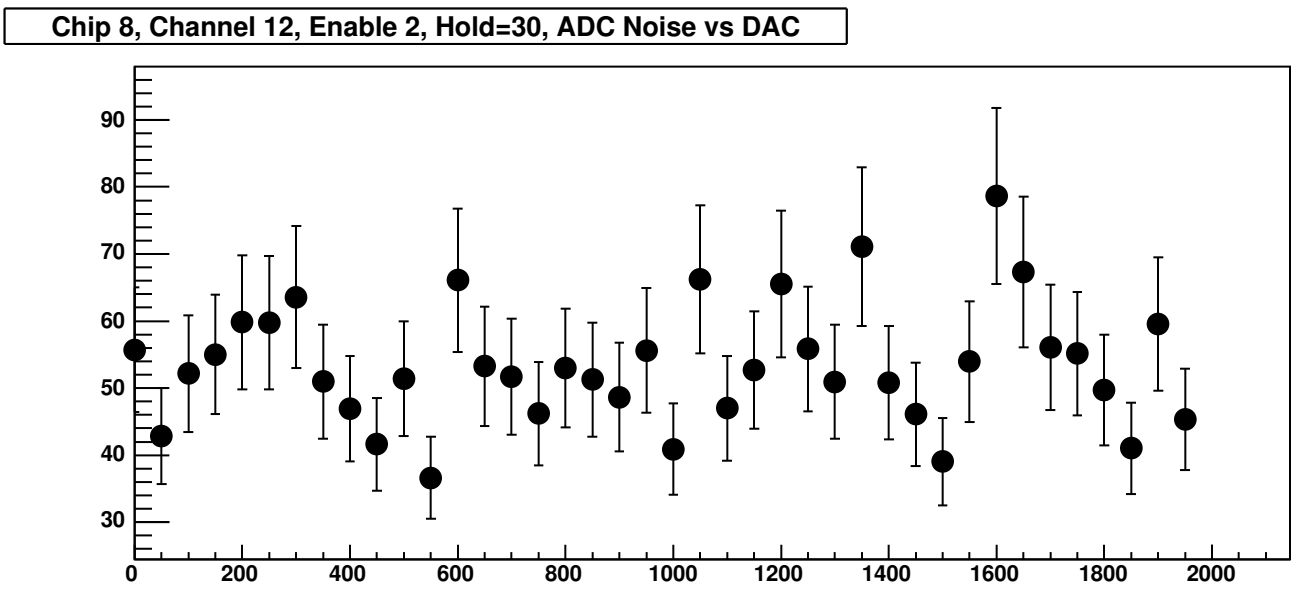

Chip 8, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

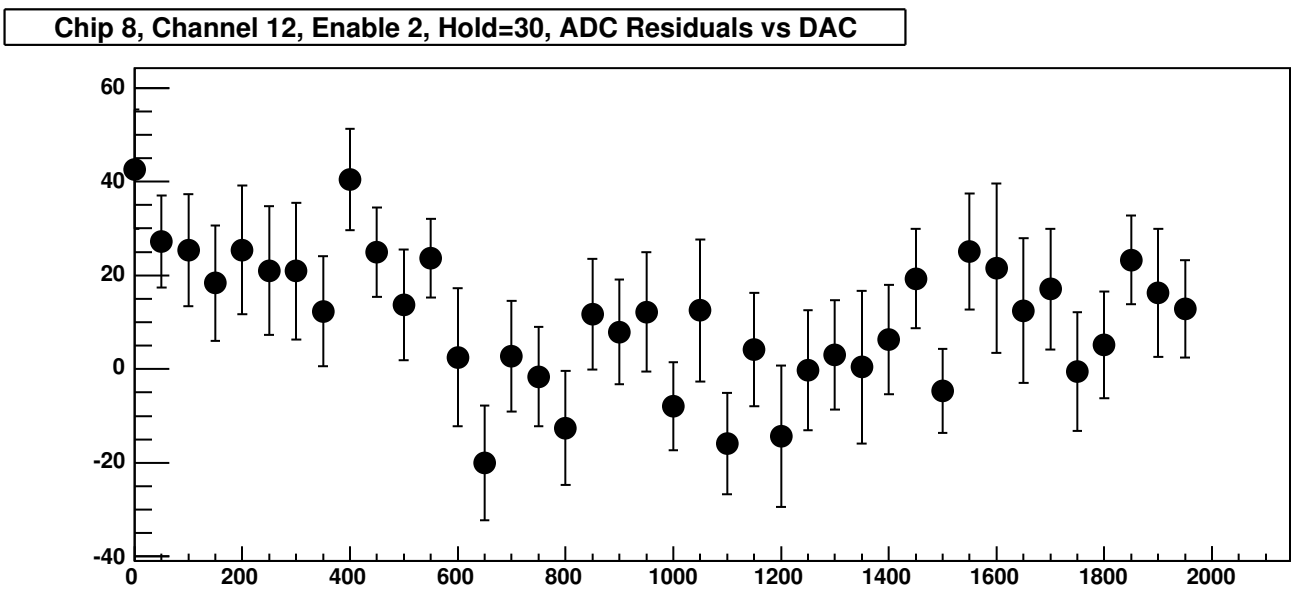

Chip 8, Channel 12, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 71.23 / 11
p0 -653.5 ± 19.6
p1 18.34 ± 0.01781
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 12, Enable 1!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

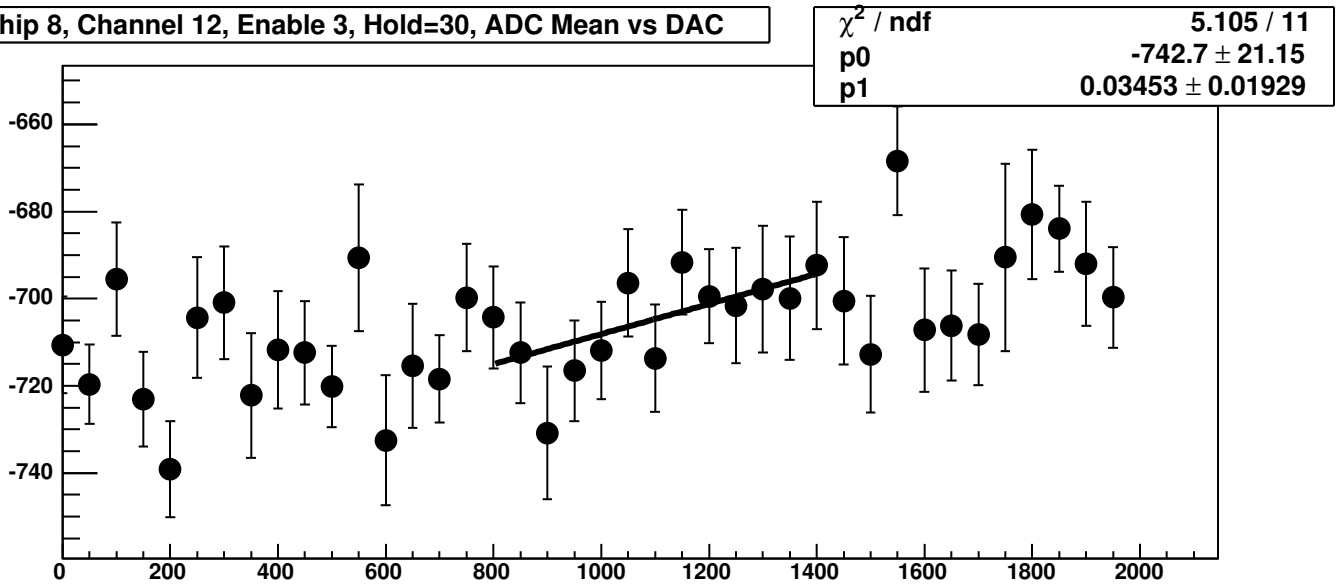

Chip 8, Channel 12, Enable 1!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 8.399 / 11
p0 -763.8 ± 20.04
p1 0.06946 ± 0.01822
-600
-620
-640
-660
-680
-700
-720
-740
-760
0
200
400
600
800
1000
1200
1400
1600
1800
2000

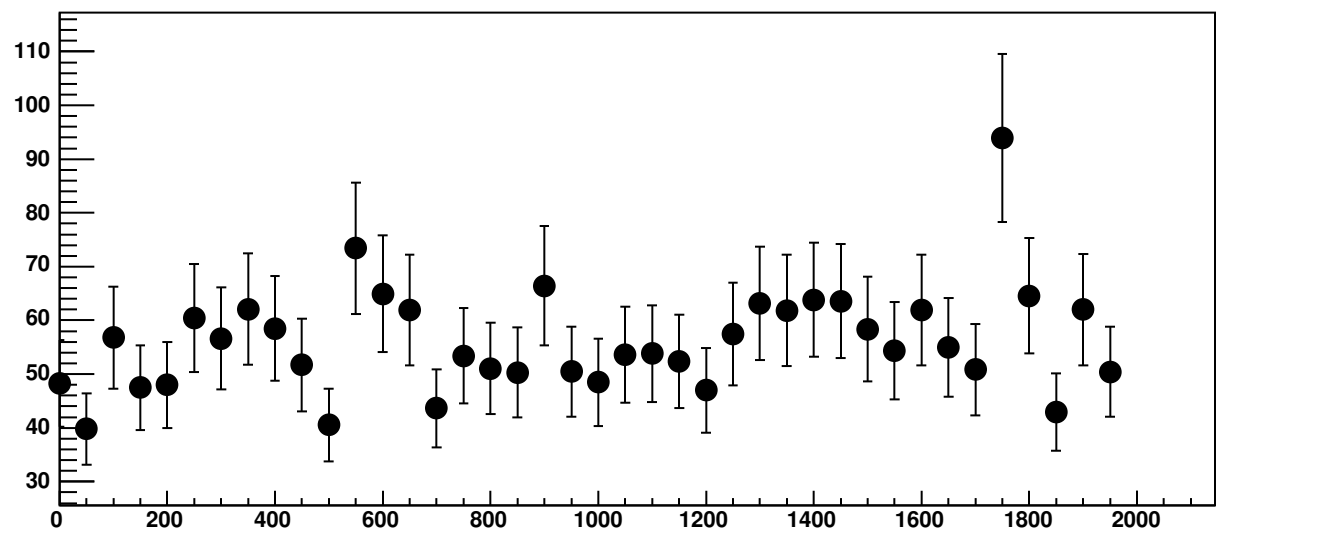

Chip 8, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

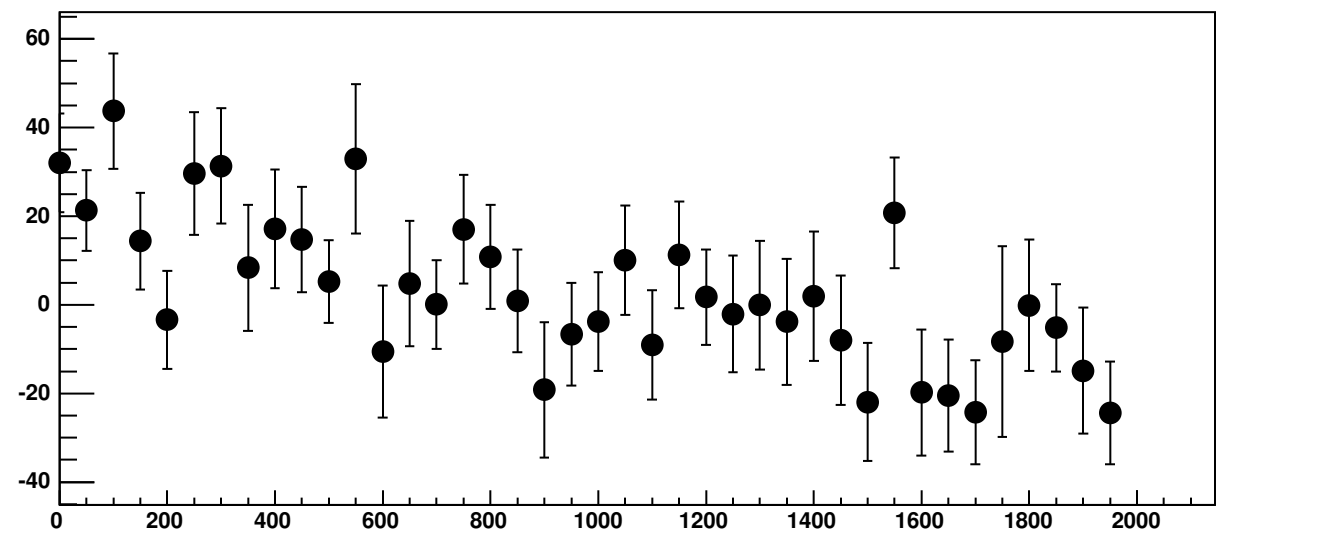
Chip 8, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC

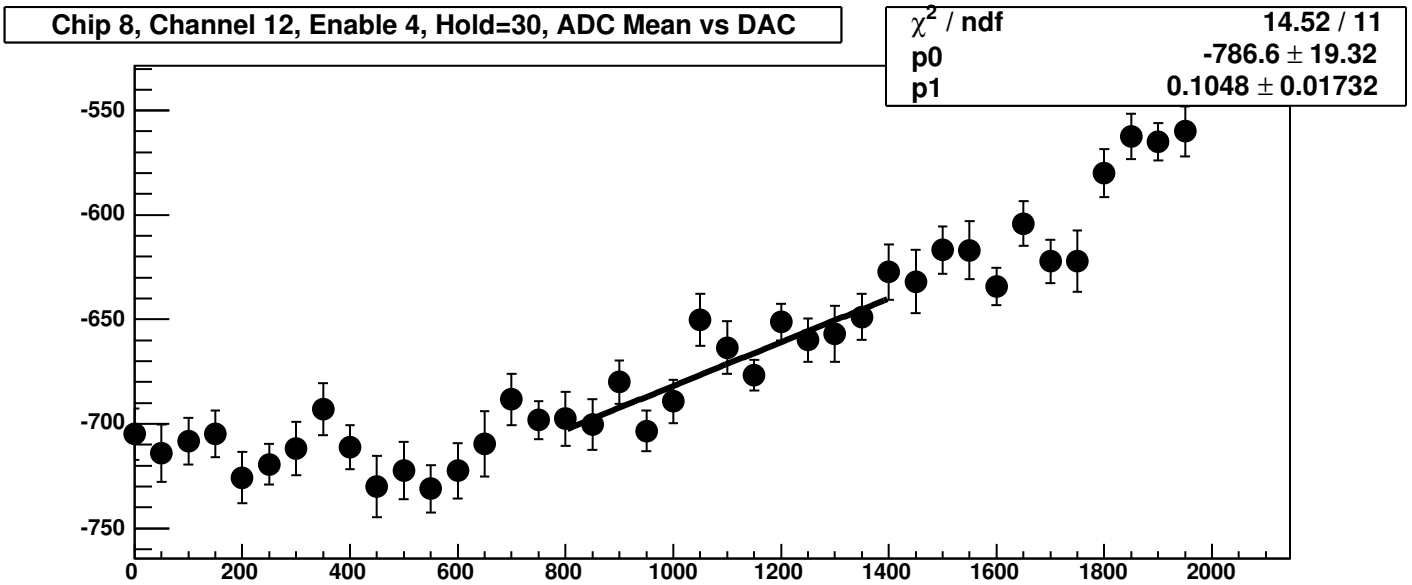


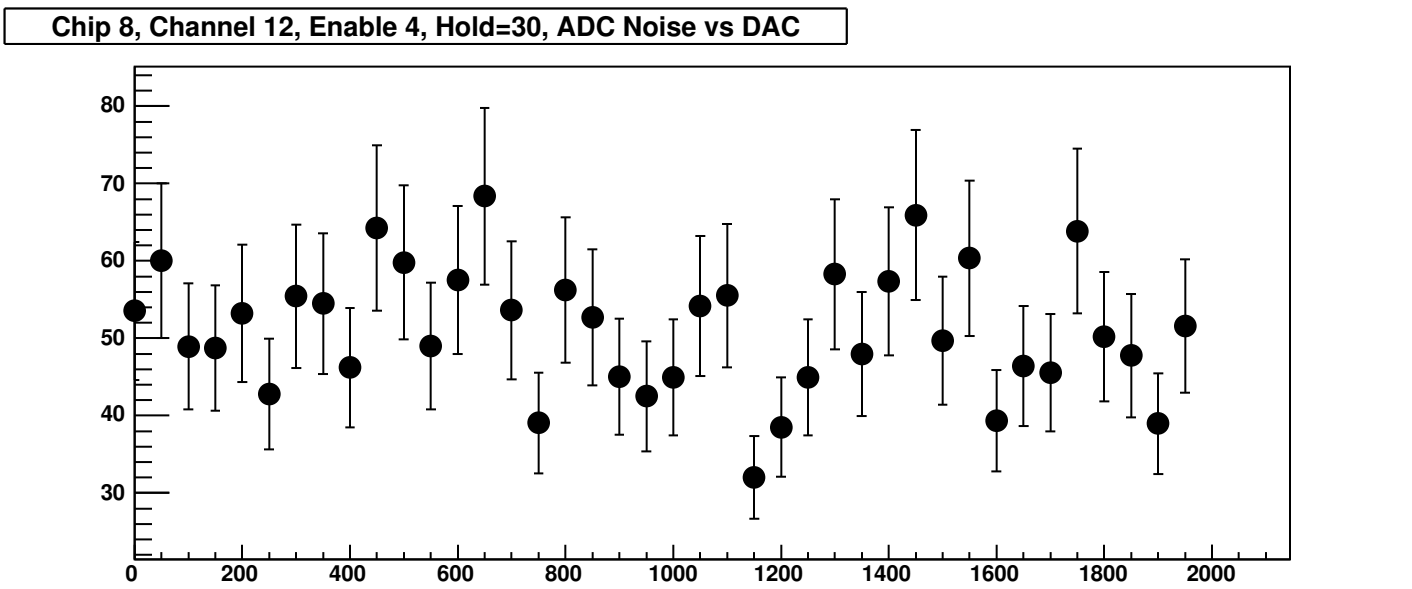
Chip 8, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC

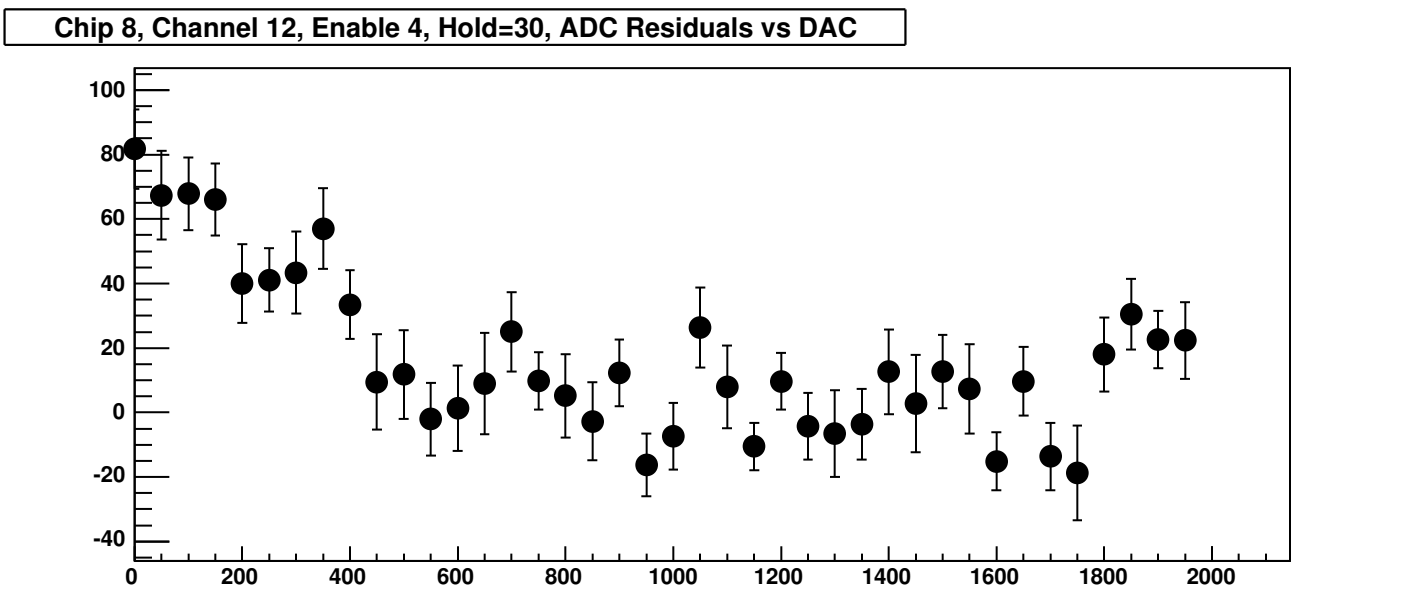


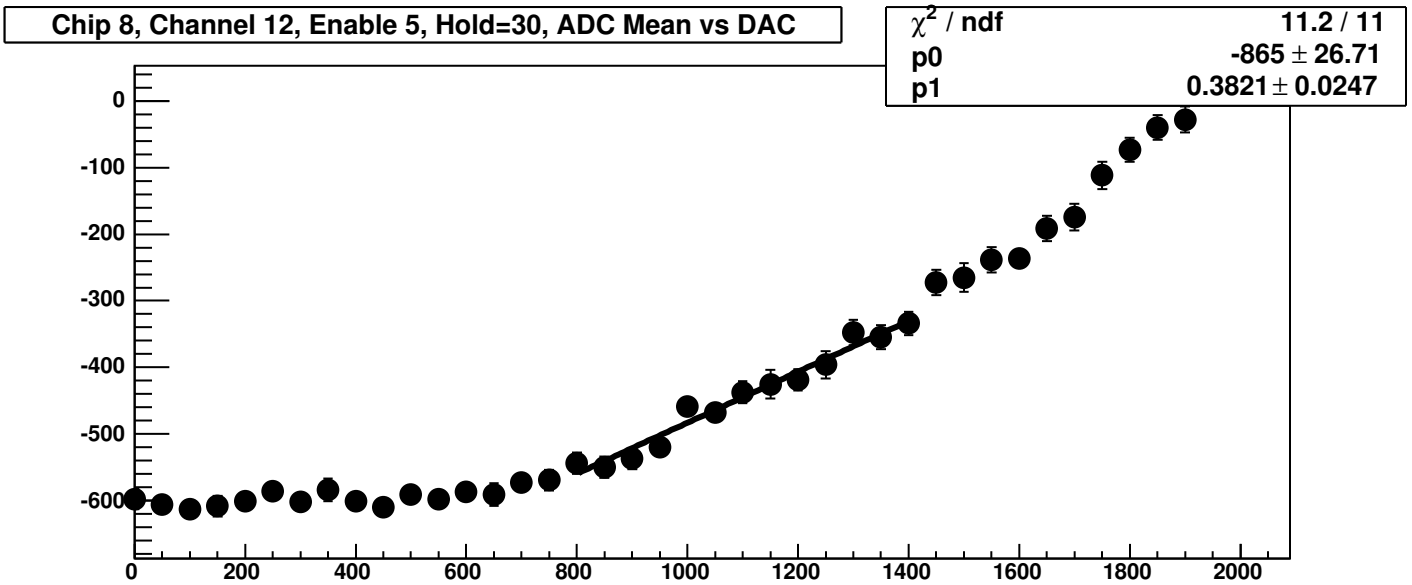
Chip 8, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC

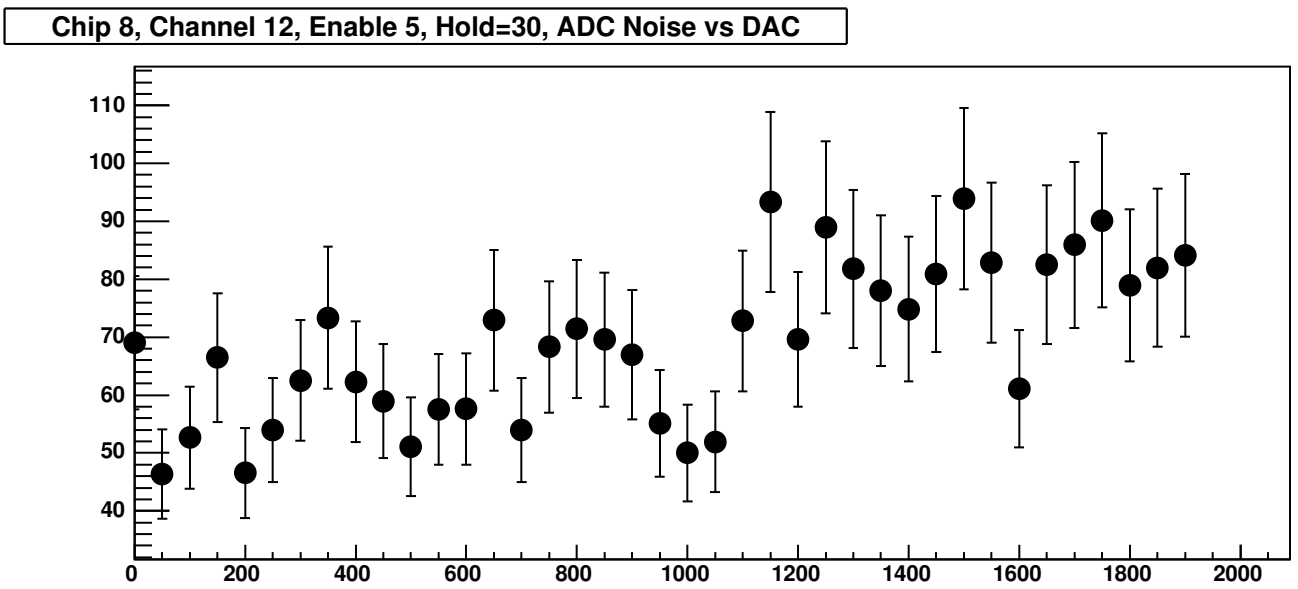


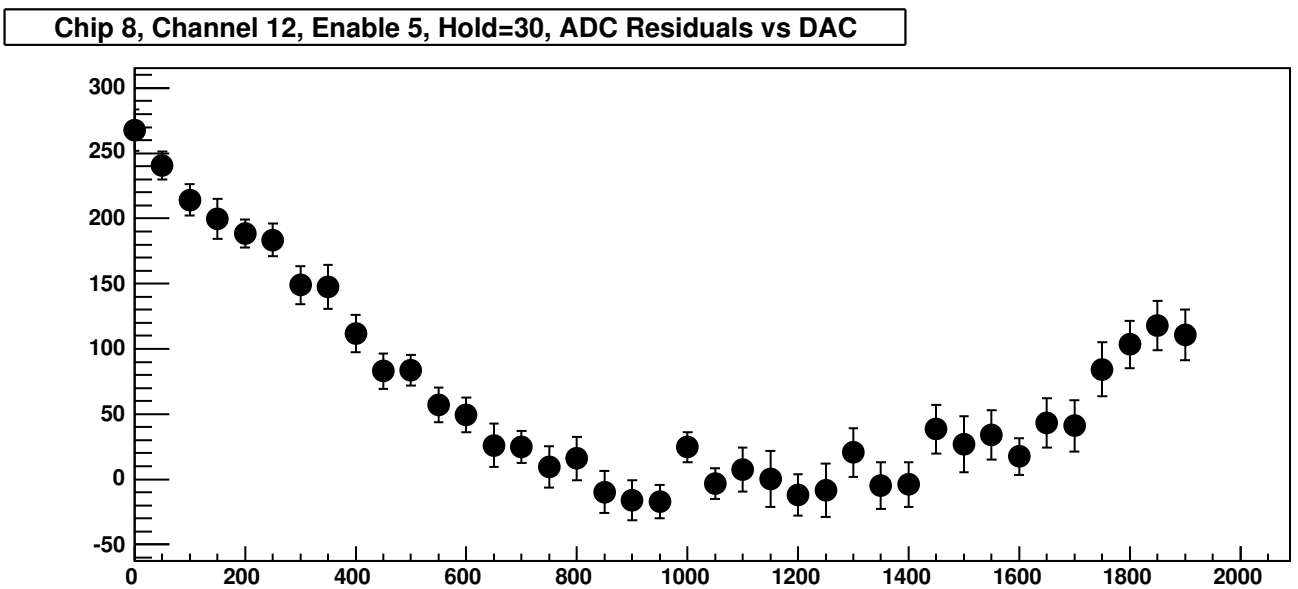

Chip 8, Channel 12, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 14.52 / 11
p0 -786.6 ± 19.32
p1 0.1048 ± 0.01732
-550
-600
-650
-700
-750
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 12, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

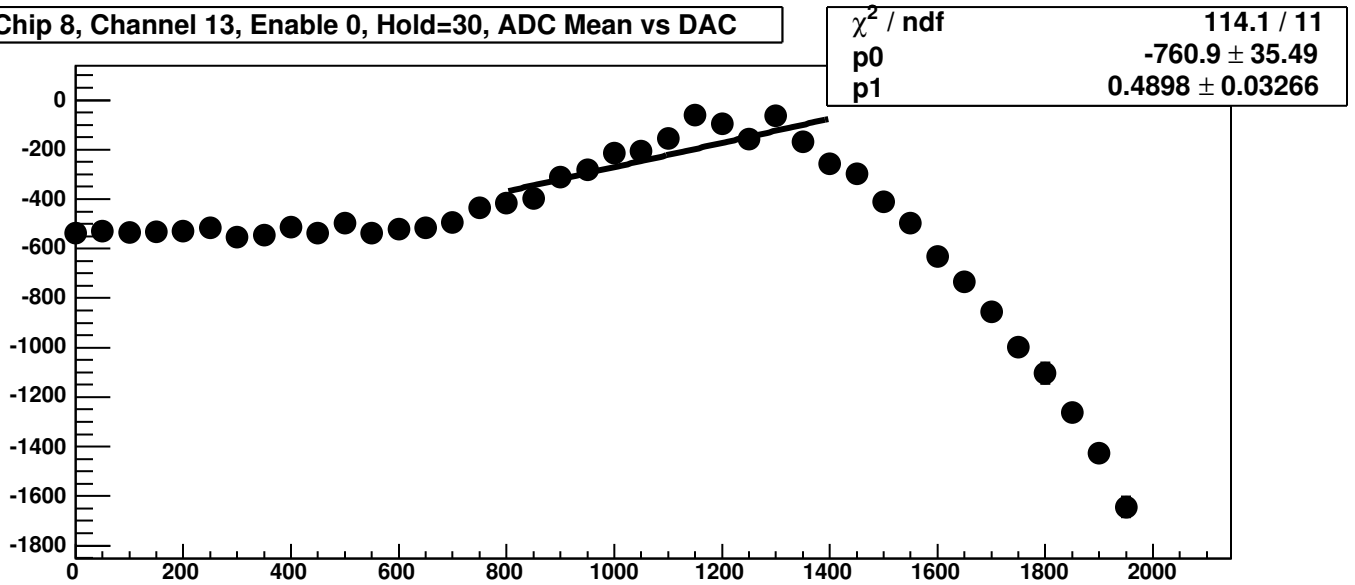

Chip 8, Channel 12, Enable 4, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 11.2 / 11
p0 -865 ± 26.71
p1 0.3821 ± 0.0247
0
-100
-200
-300
-400
-500
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

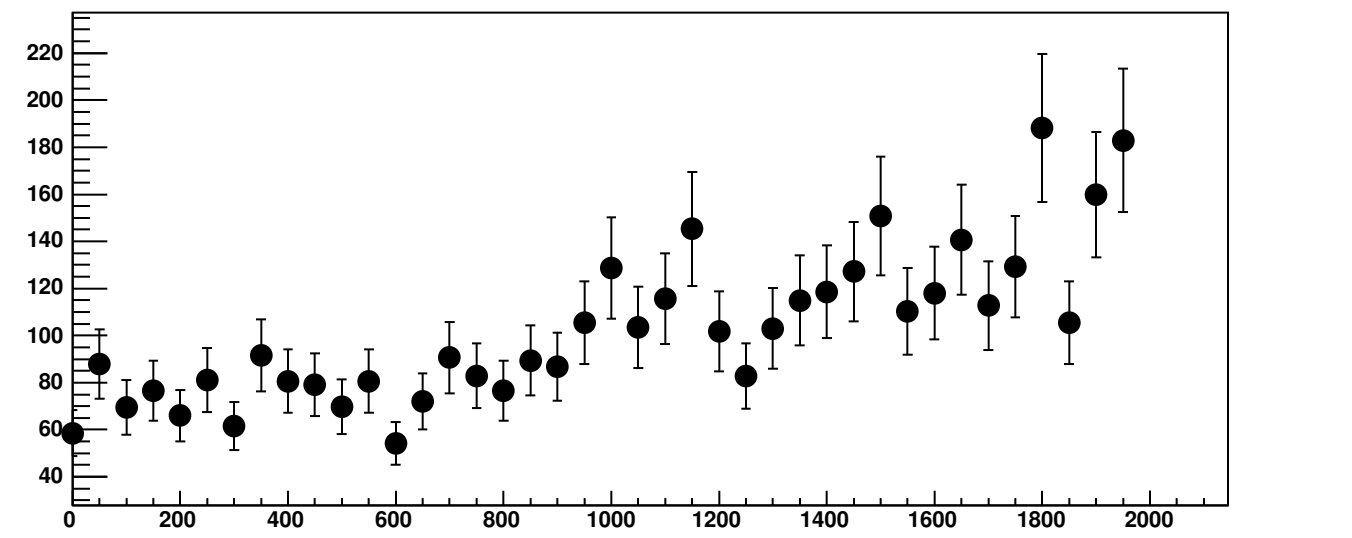

Chip 8, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

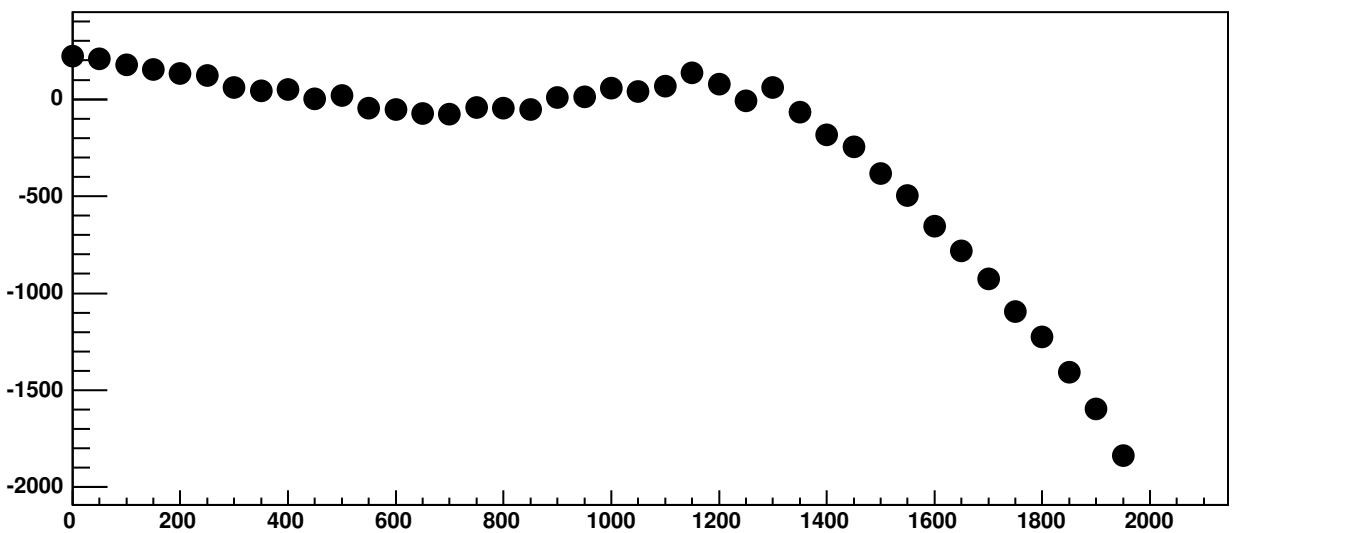
Chip 8, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC

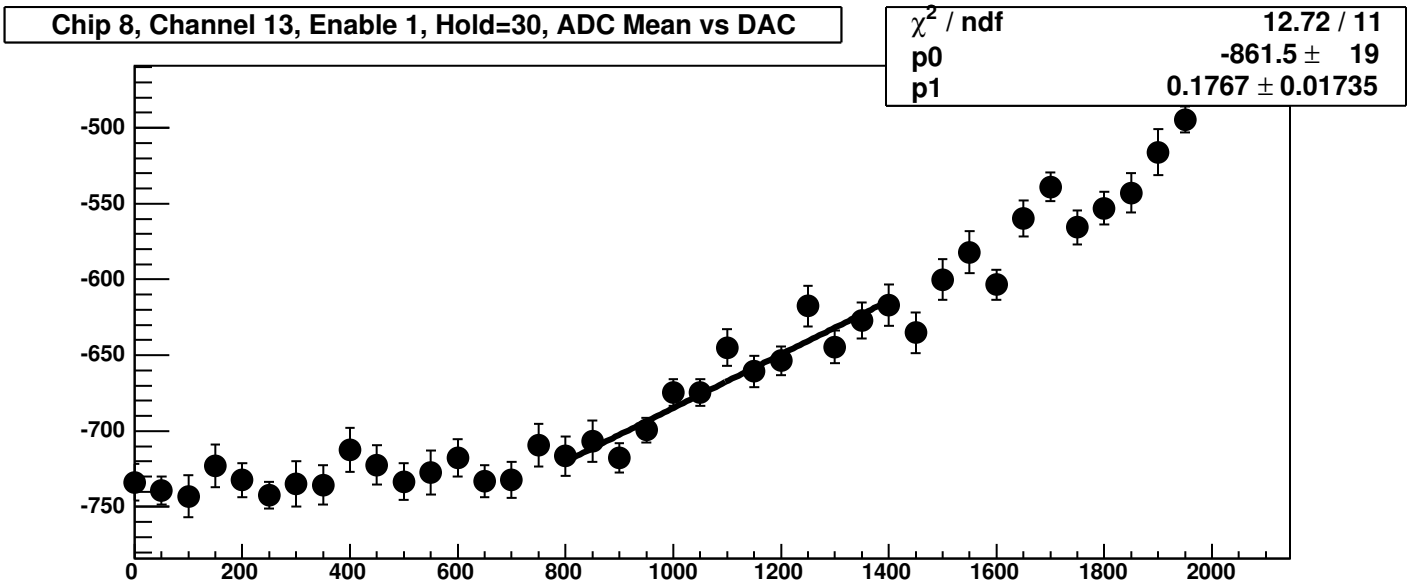


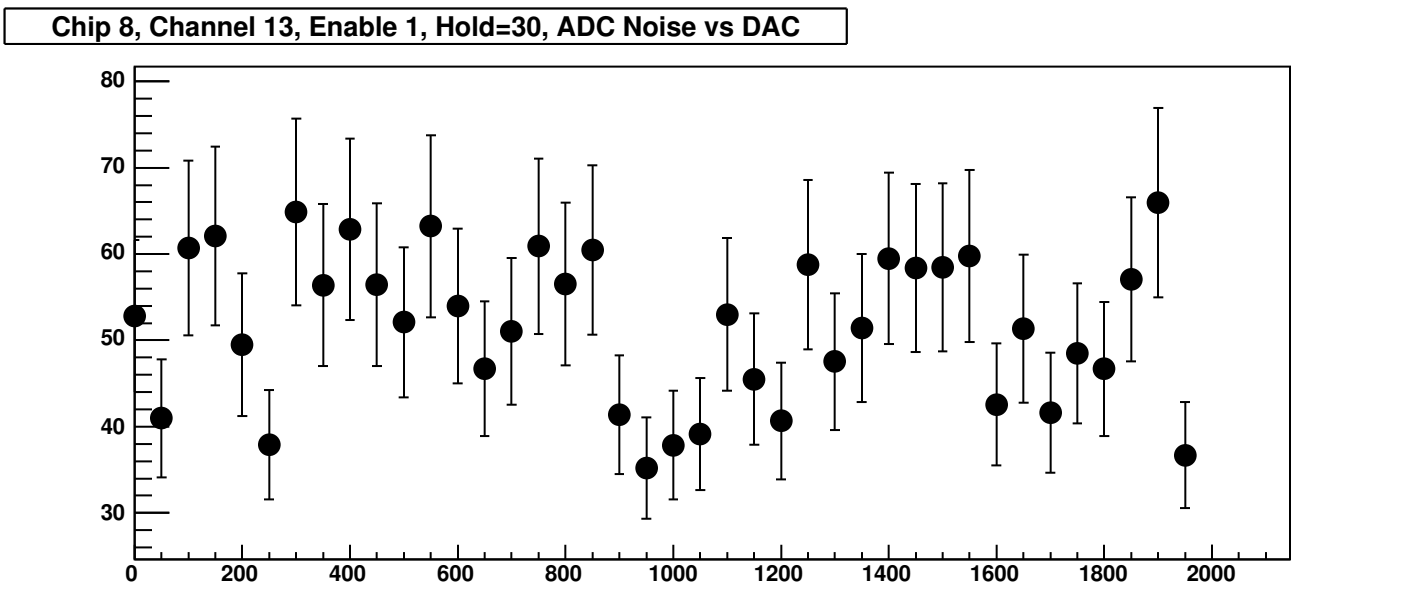
Chip 8, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC

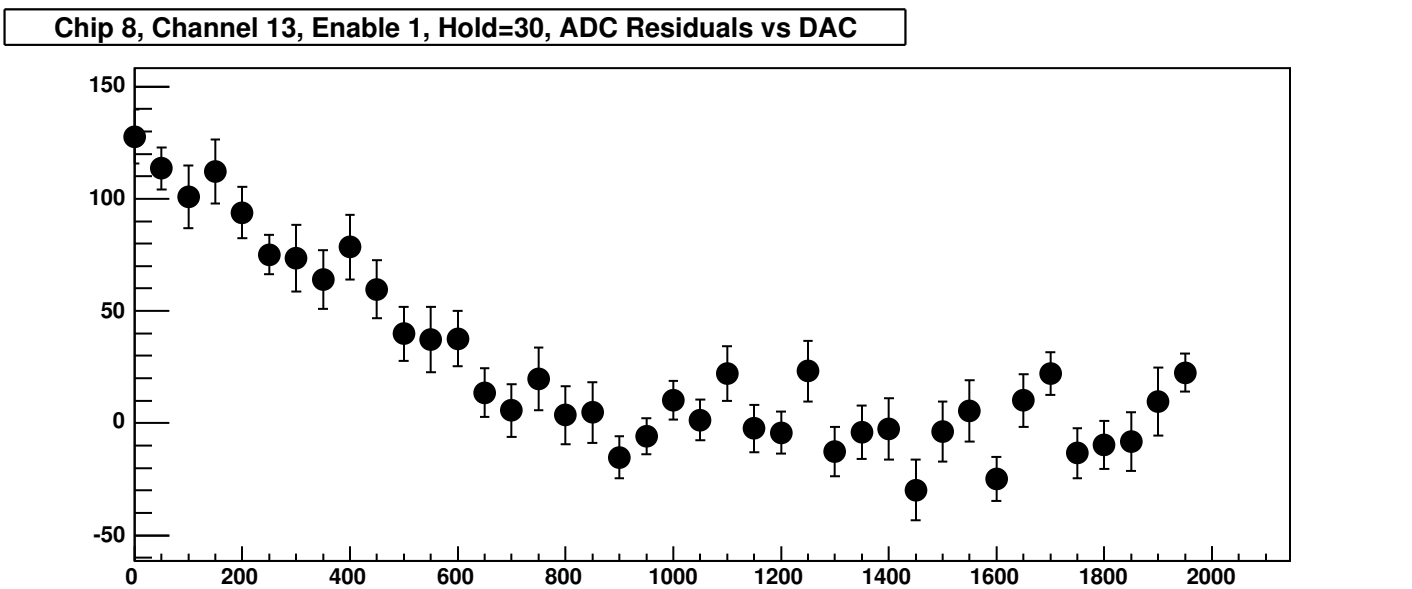


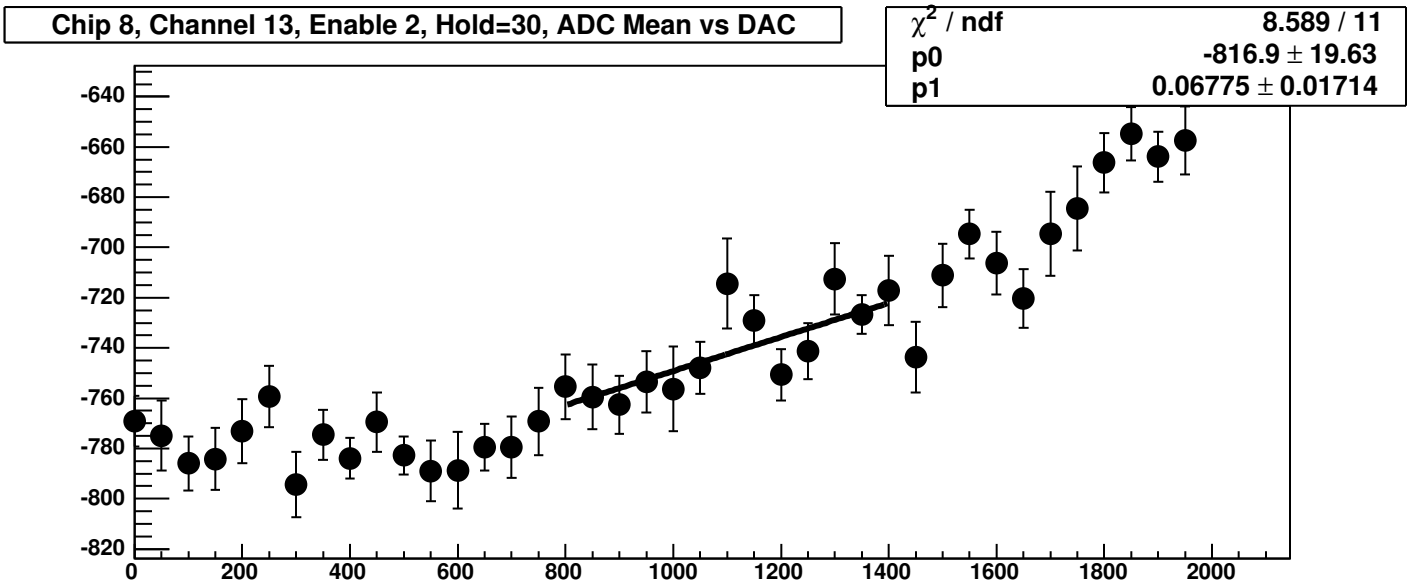
Chip 8, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC

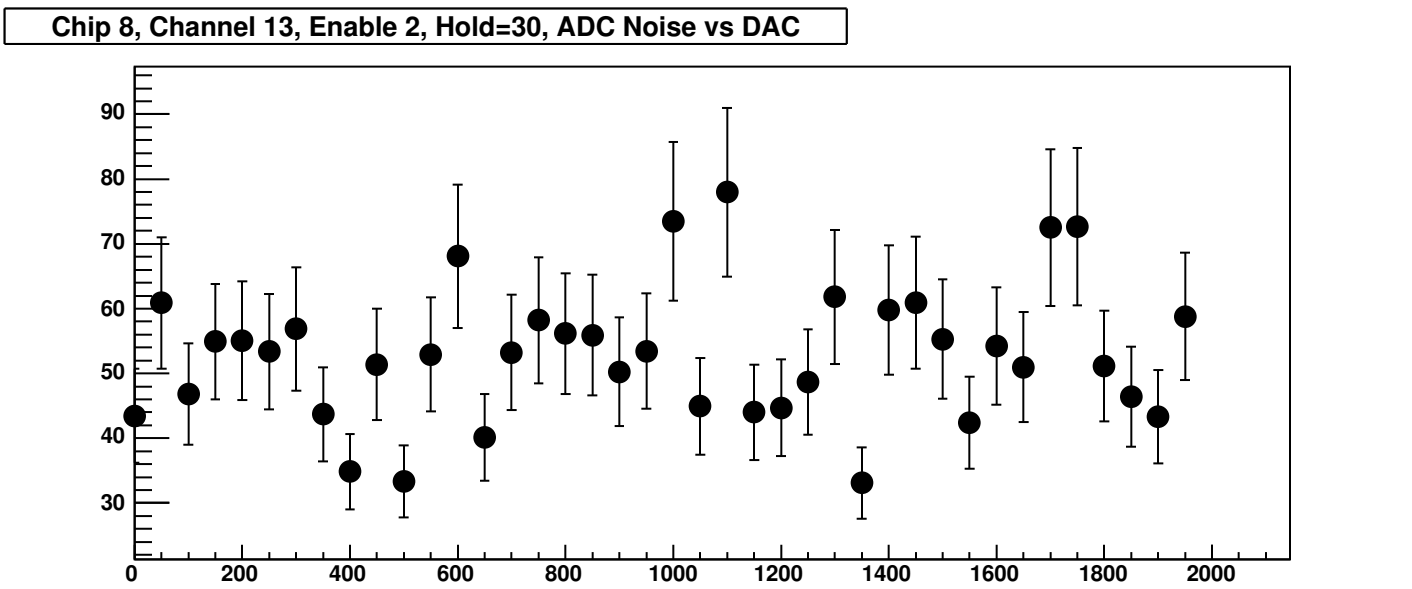


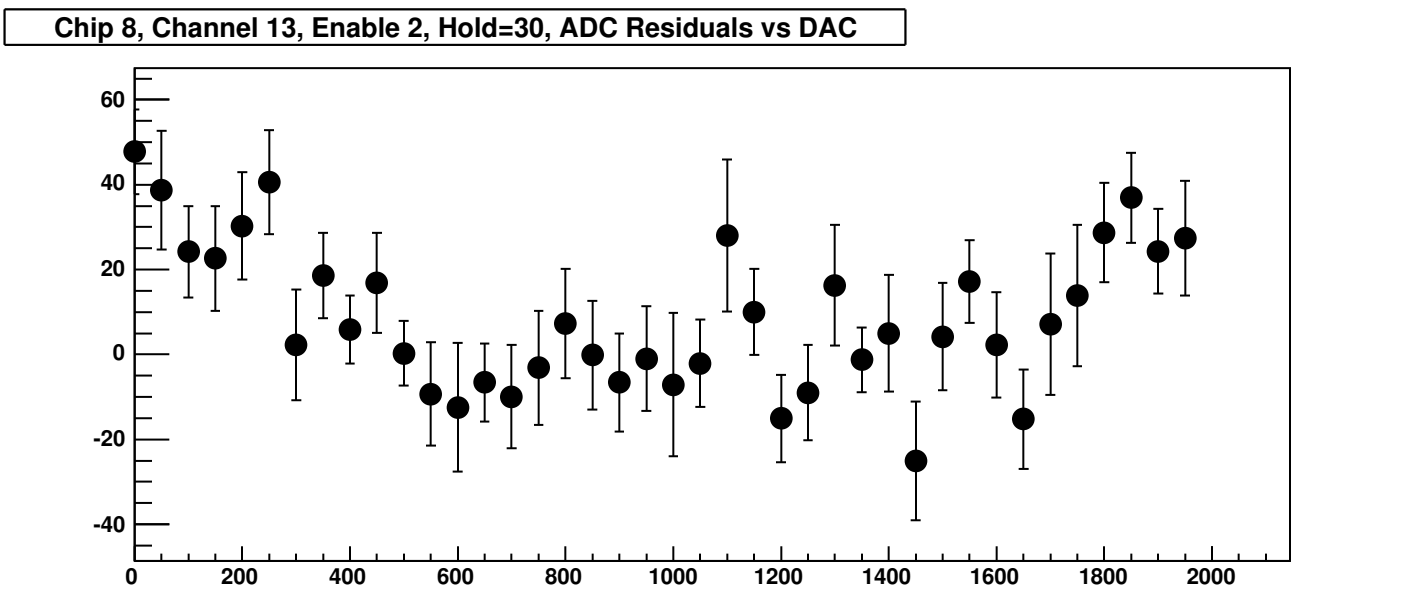

Chip 8, Channel 13, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 12.72 / 11
p0 -861.5 ± 19
p1 0.1767 ± 0.01735
-500
-550
-600
-650
-700
-750
0
200
400
600
800
1000
1200
1400
1600
1800
2000

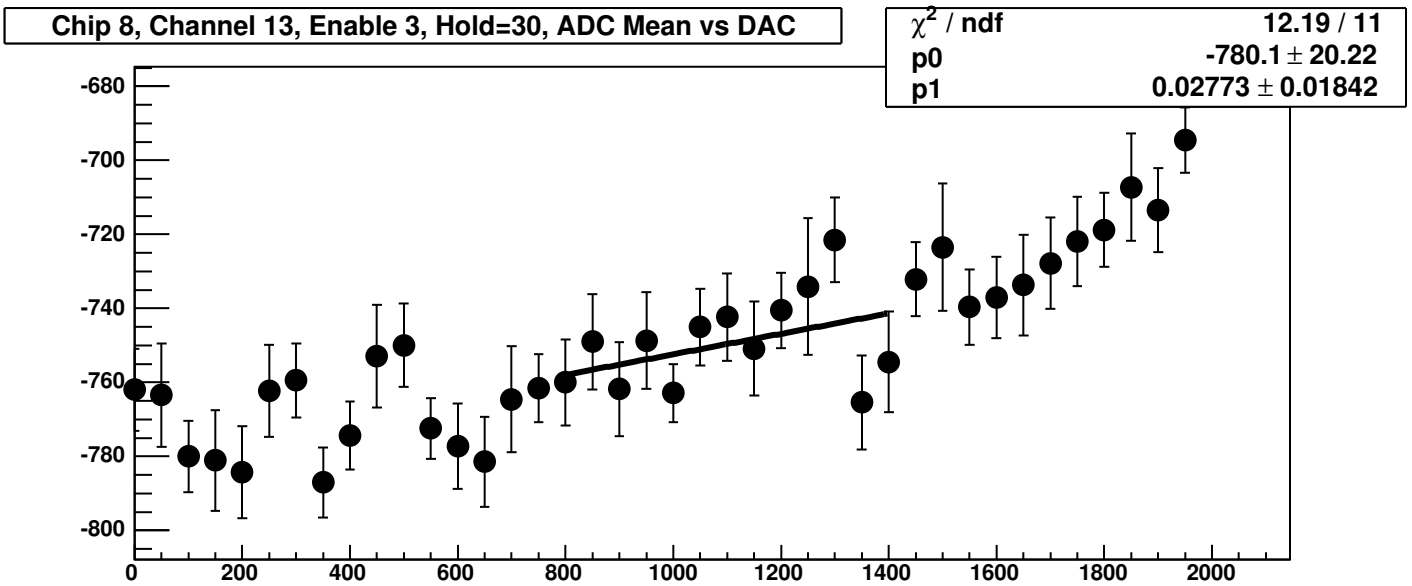

Chip 8, Channel 13, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

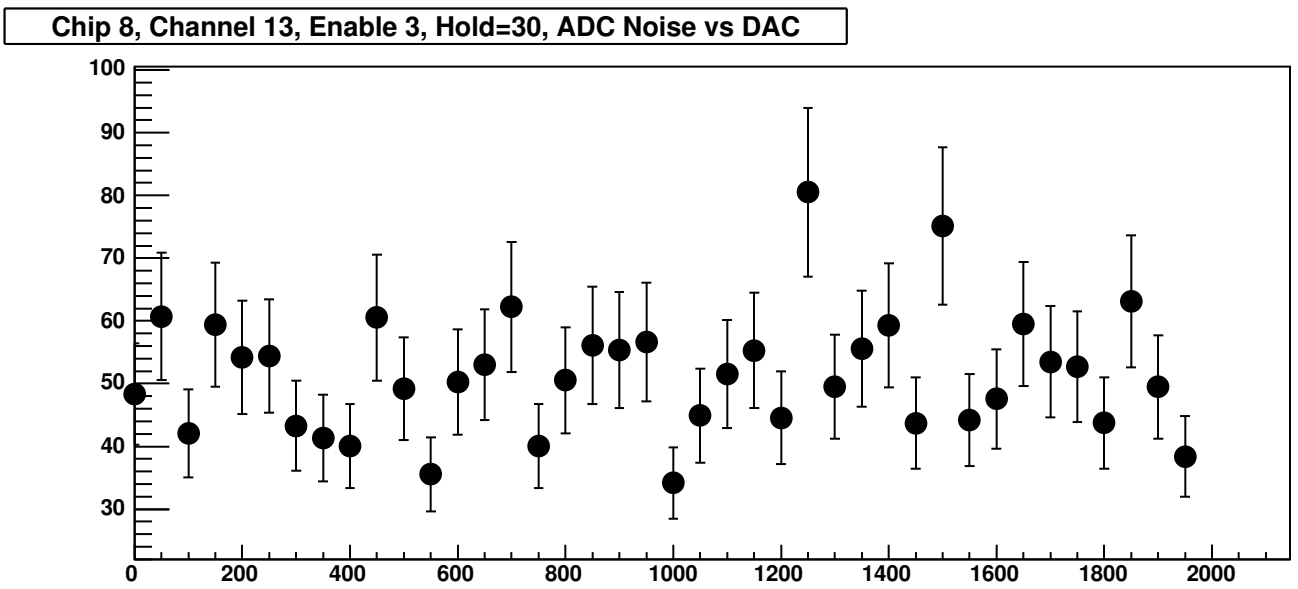

Chip 8, Channel 13, Enable 1, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

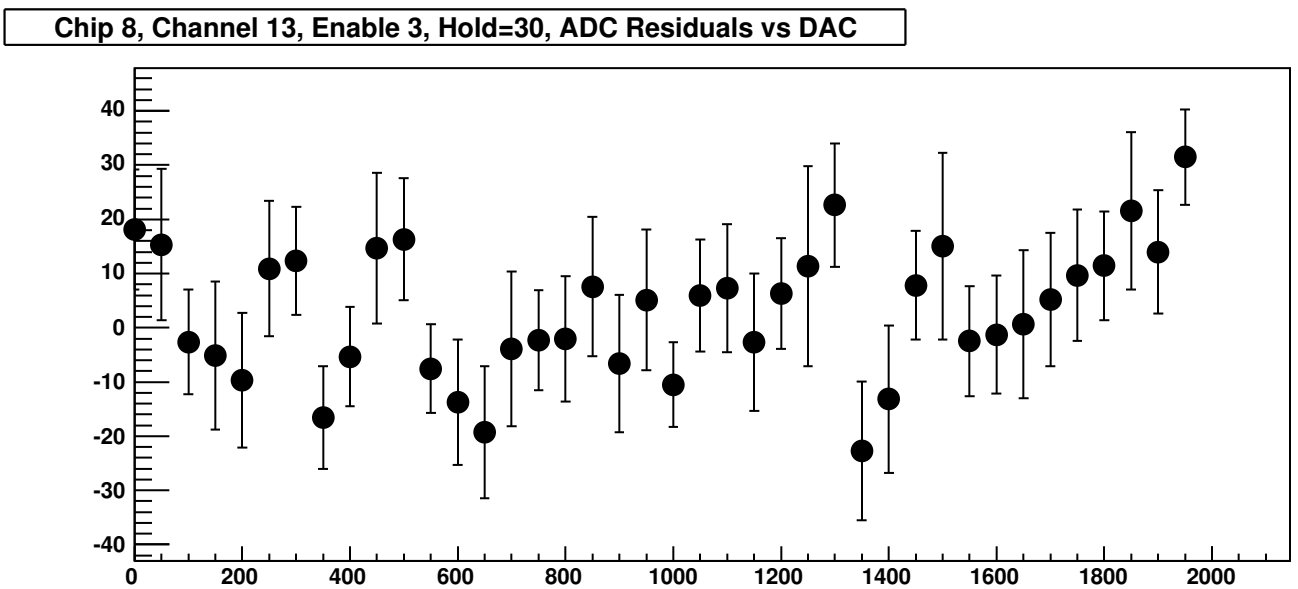

Chip 8, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 8.589 / 11
p0 -816.9 ± 19.63
p1 0.06775 ± 0.01714
-640
-660
-680
-700
-720
-740
-760
-780
-800
-820
0
200
400
600
800
1000
1200
1400
1600
1800
2000

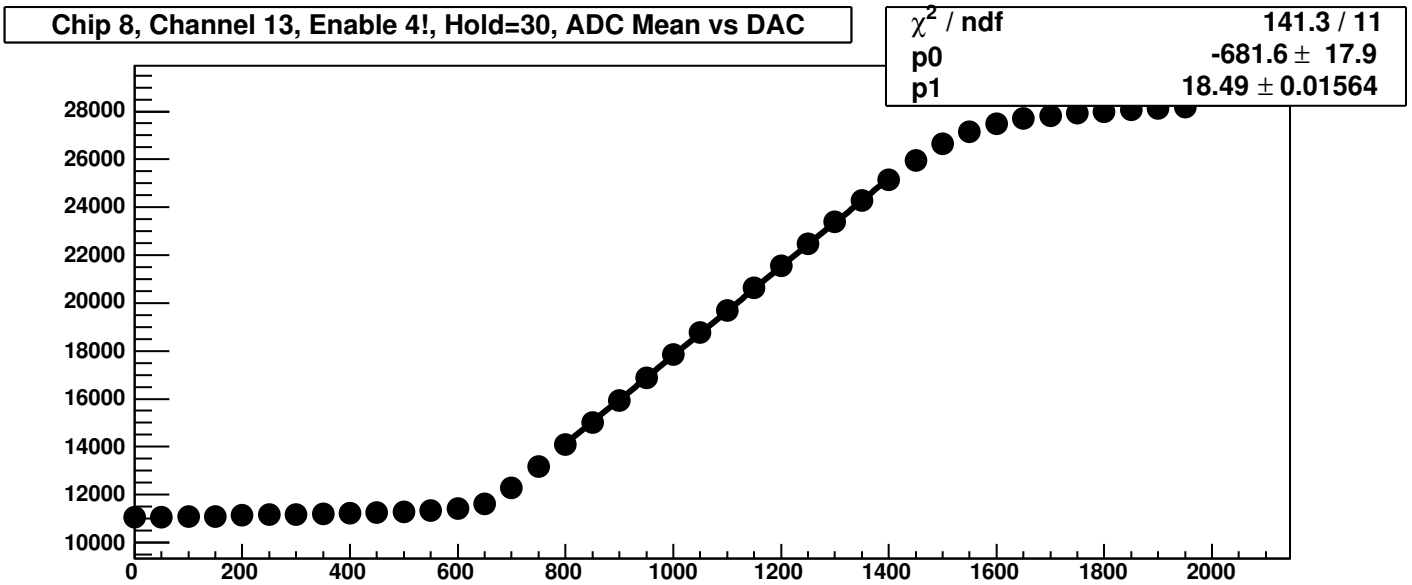

Chip 8, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

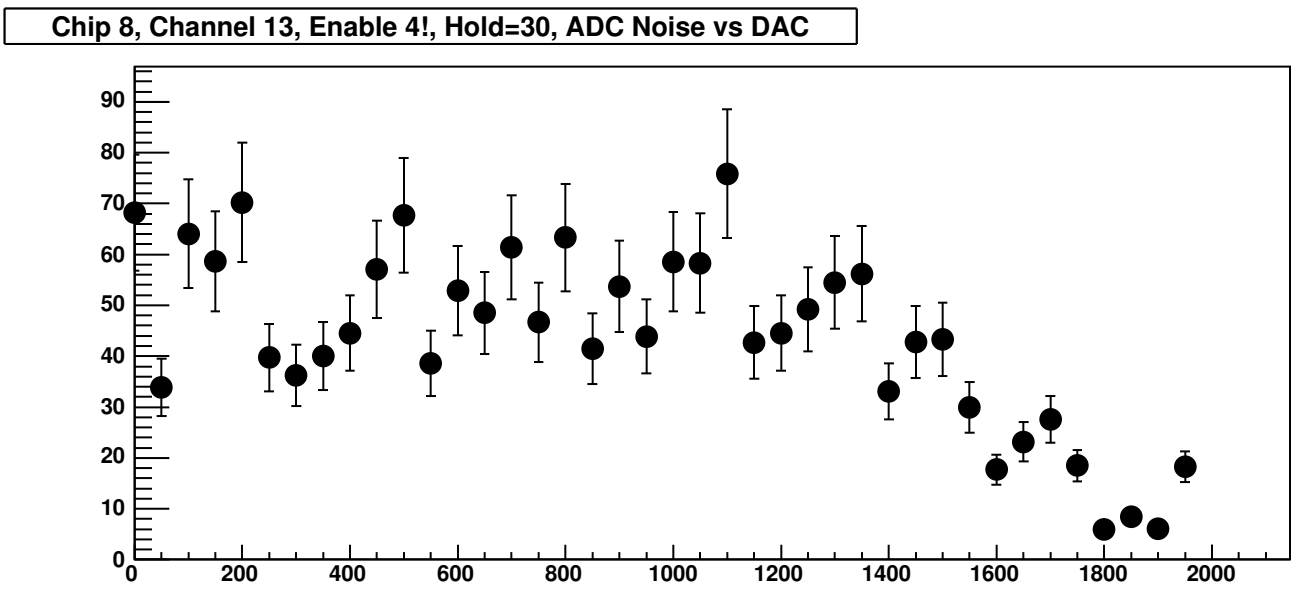

Chip 8, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

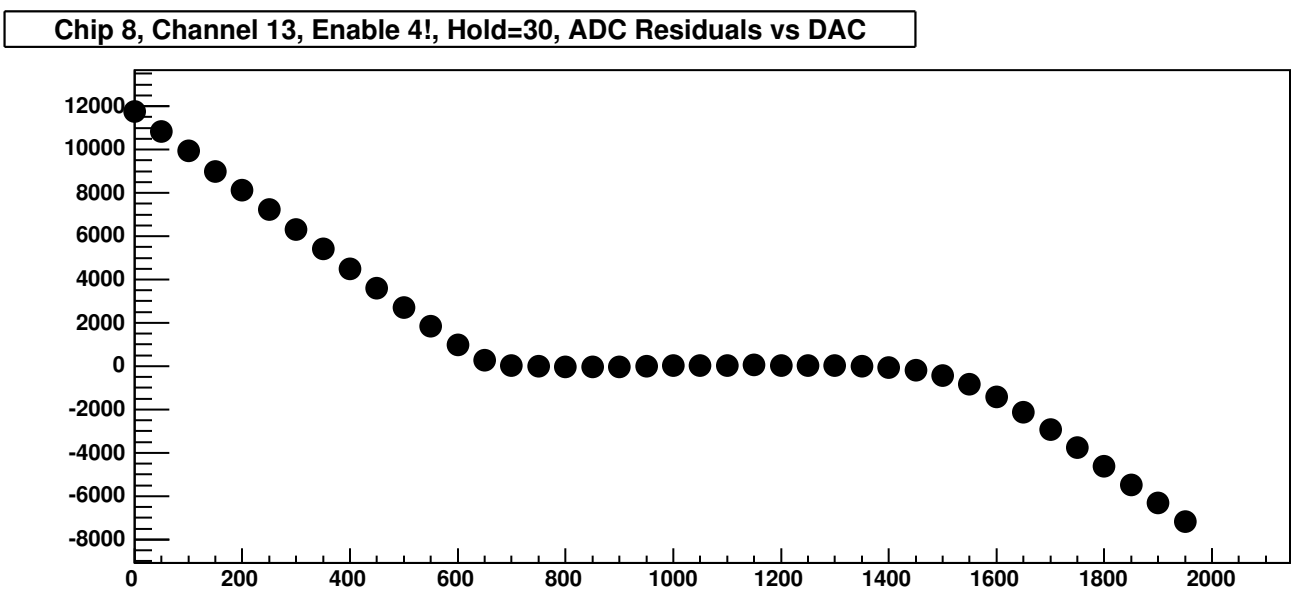

Chip 8, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 12.19 / 11
p0 -780.1 ± 20.22
p1 0.02773 ± 0.01842
-680
-700
-720
-740
-760
-780
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

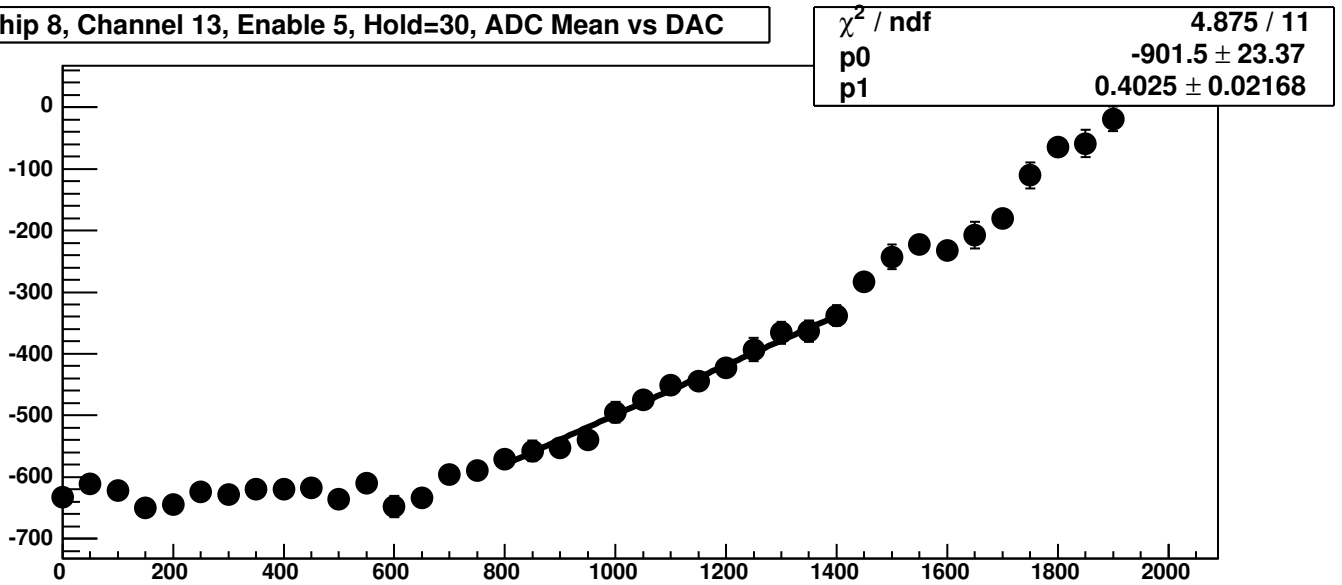

Chip 8, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
40
30
20
10
0
-10
-20
-30
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 13, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 141.3 / 11
p0 -681.6 ± 17.9
p1 18.49 ± 0.01564
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

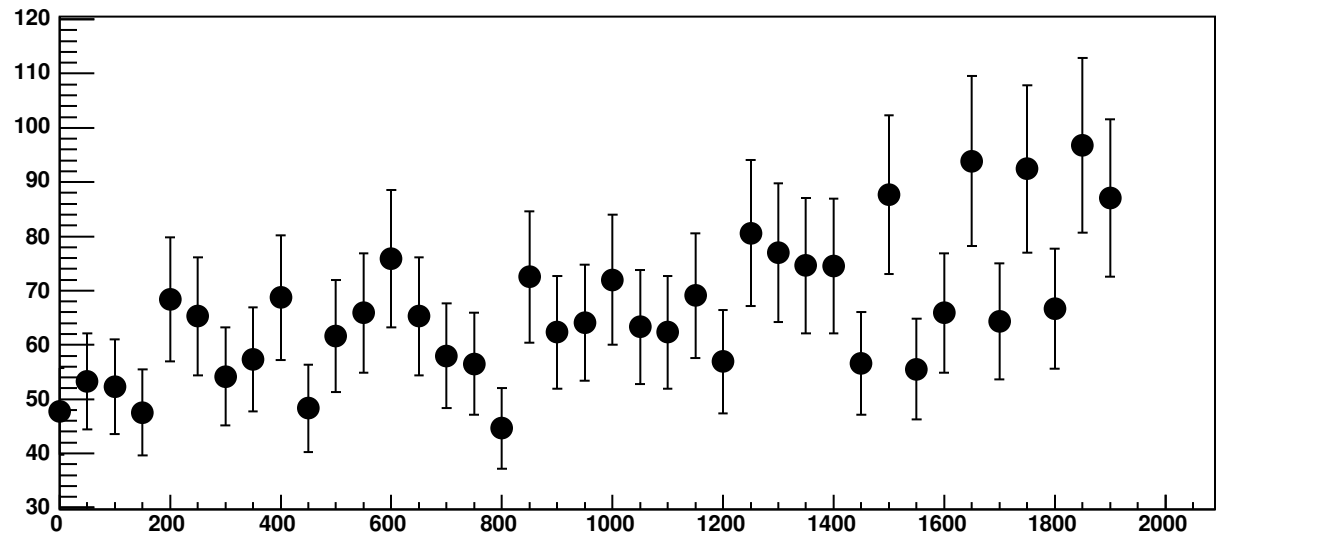

Chip 8, Channel 13, Enable 4!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 13, Enable 4!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

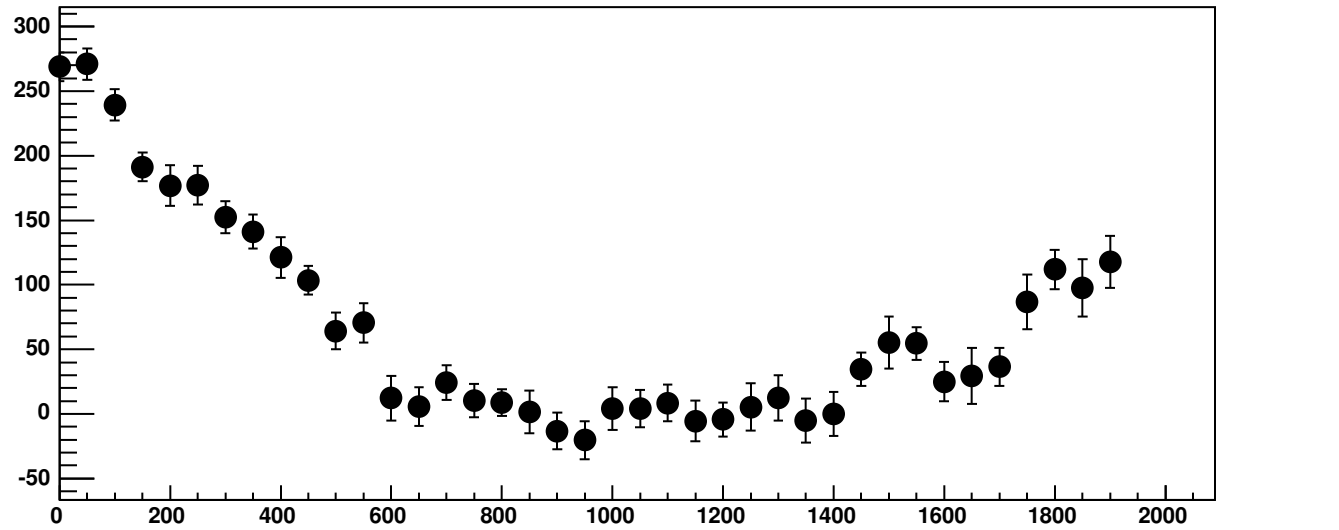
Chip 8, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC



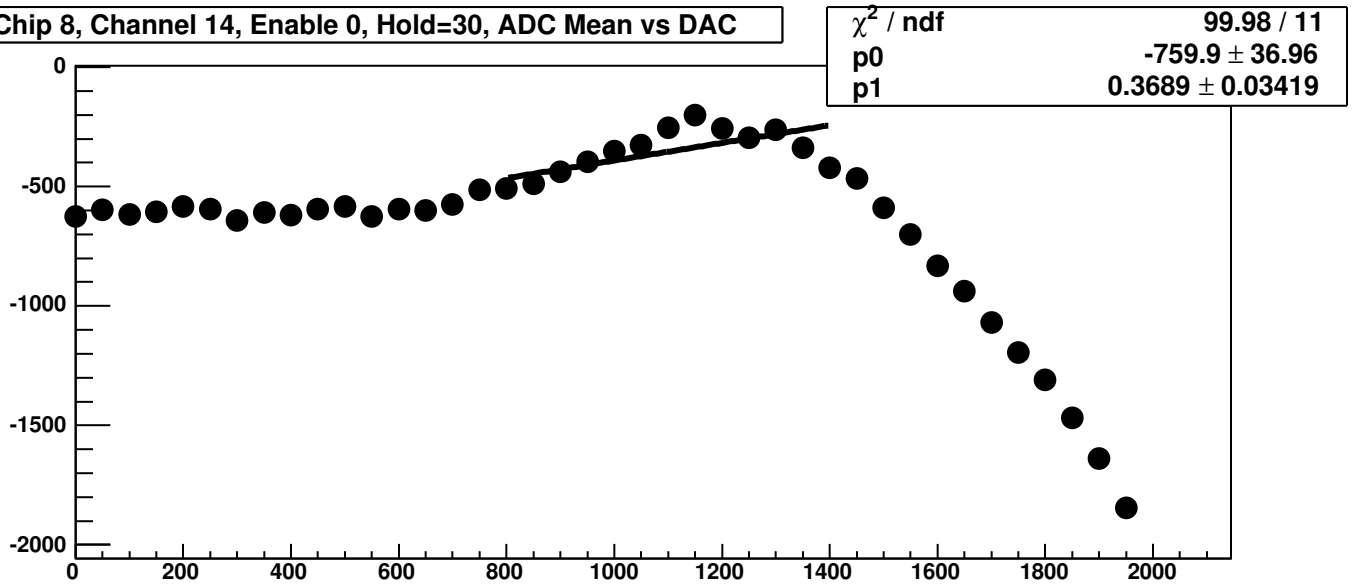
Chip 8, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC



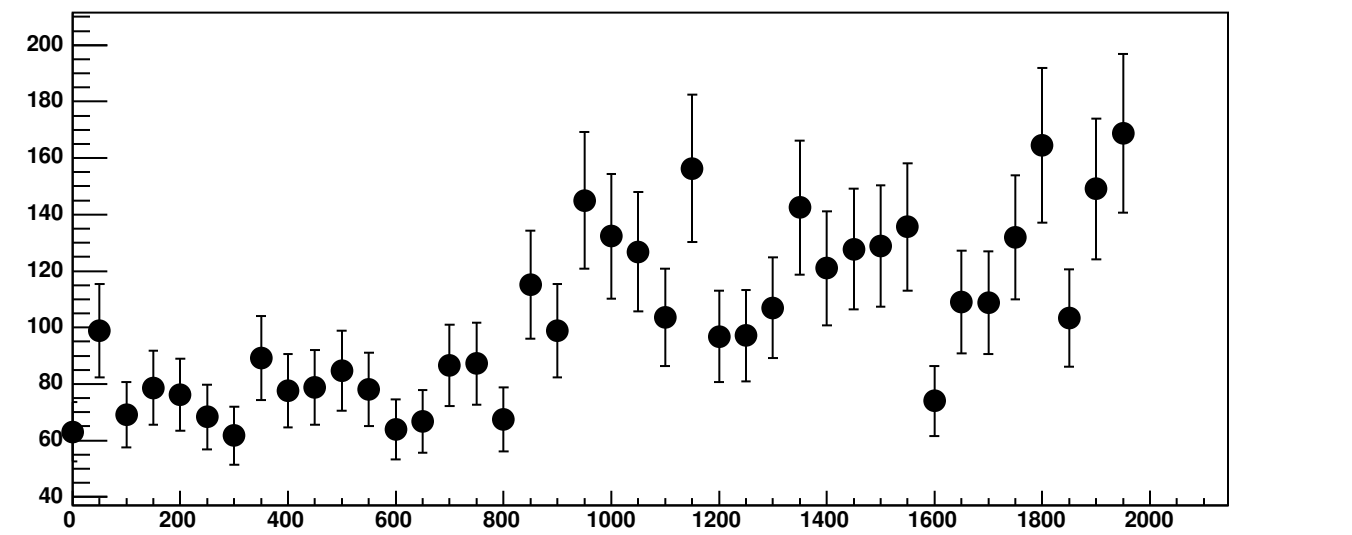
Chip 8, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC



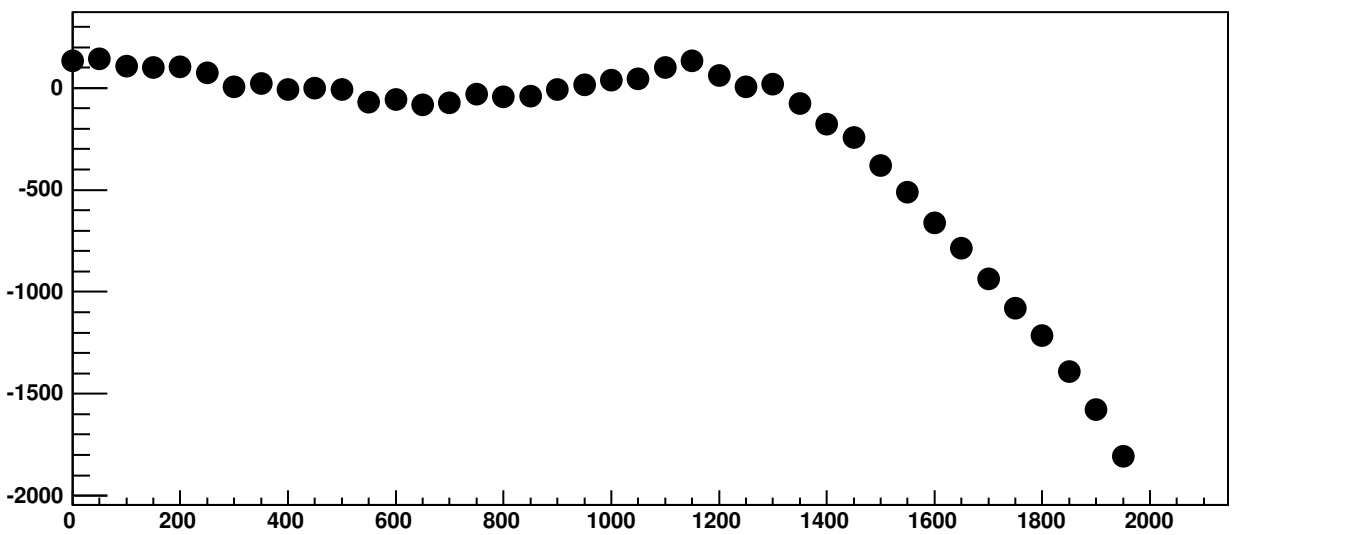
Chip 8, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC



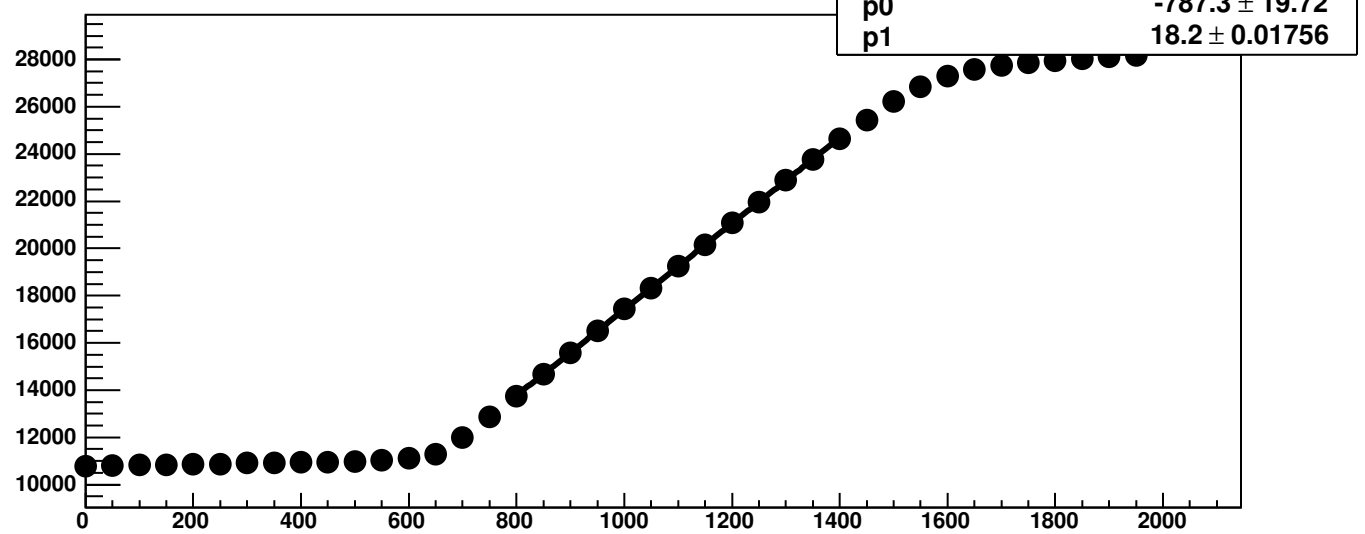
Chip 8, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC



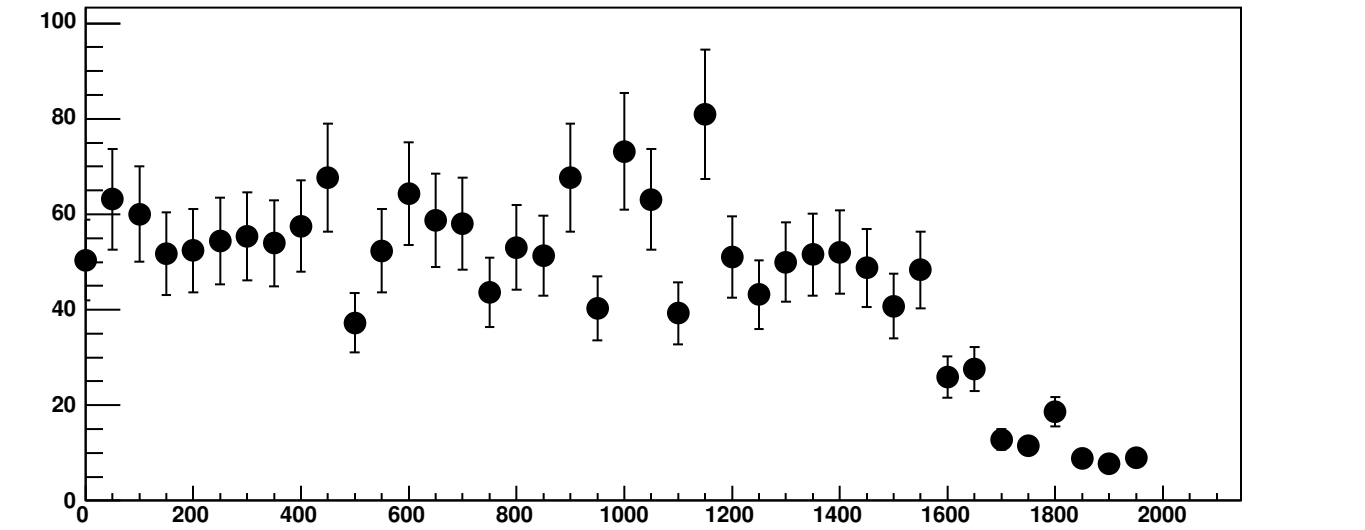
Chip 8, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC



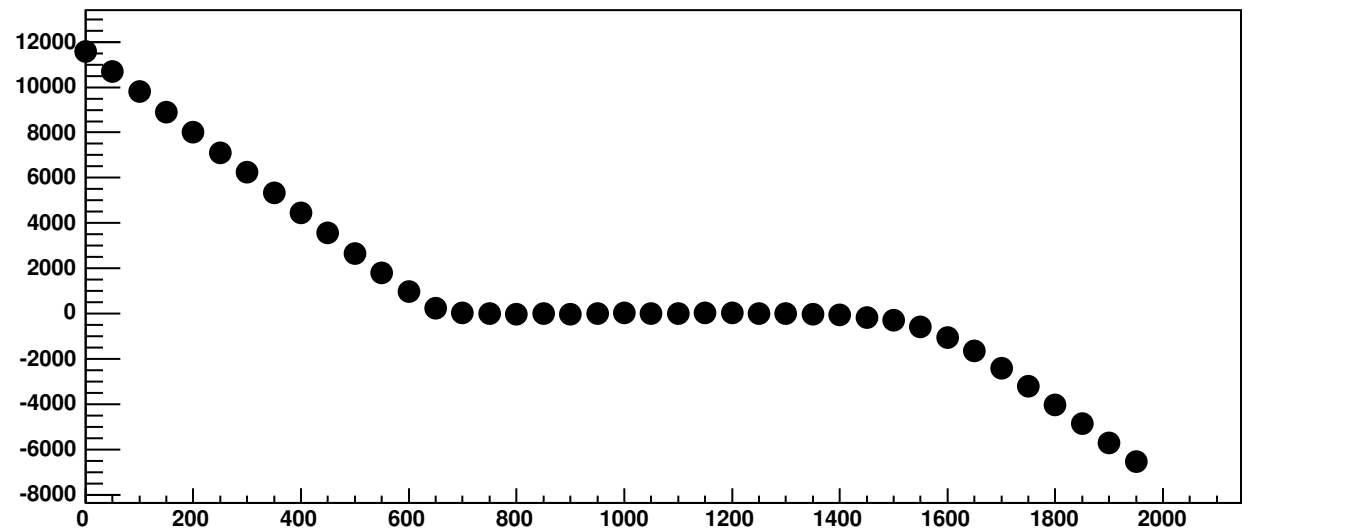
Chip 8, Channel 14, Enable 1!, Hold=30, ADC Mean vs DAC

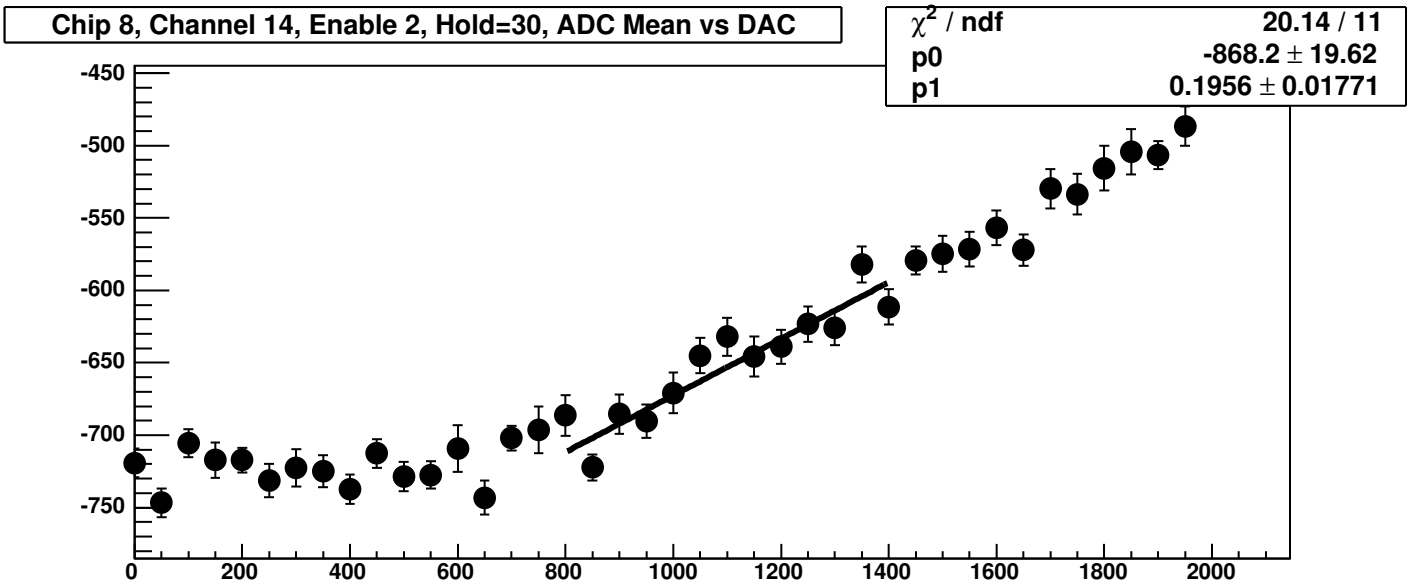


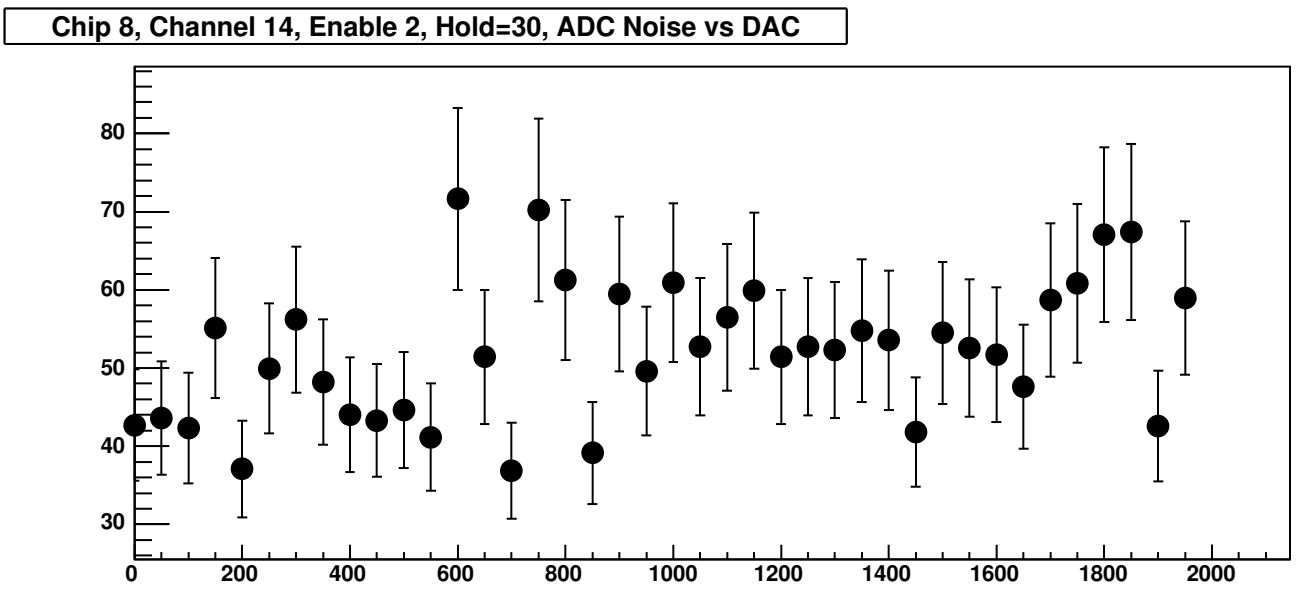
Chip 8, Channel 14, Enable 1!, Hold=30, ADC Noise vs DAC

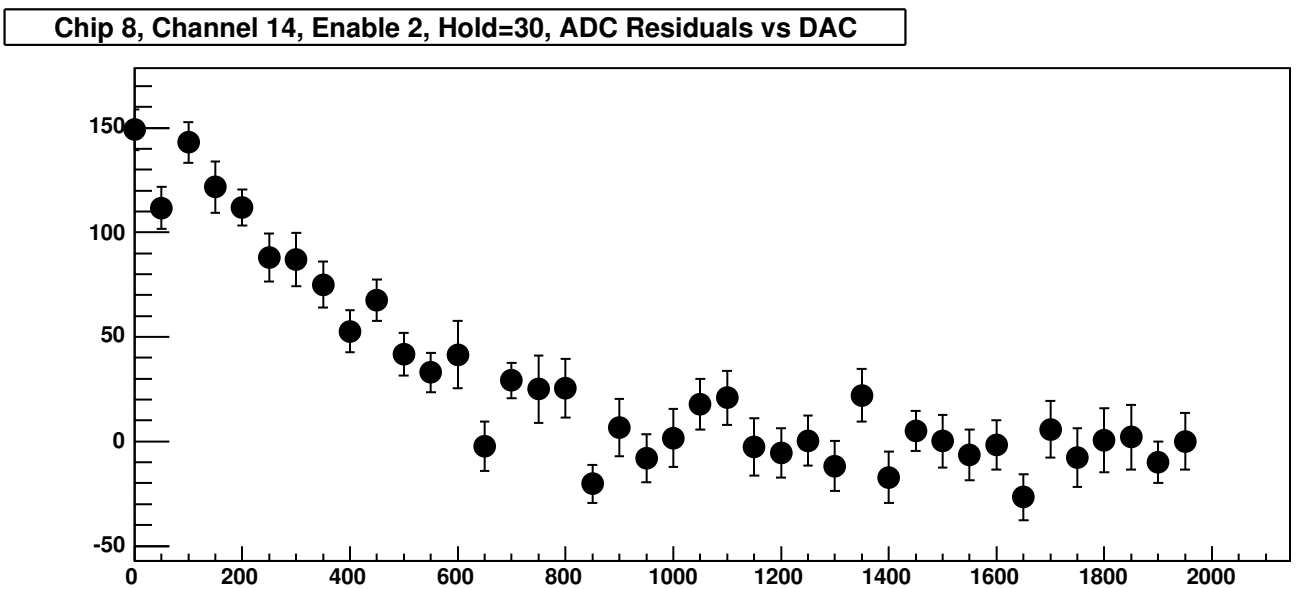


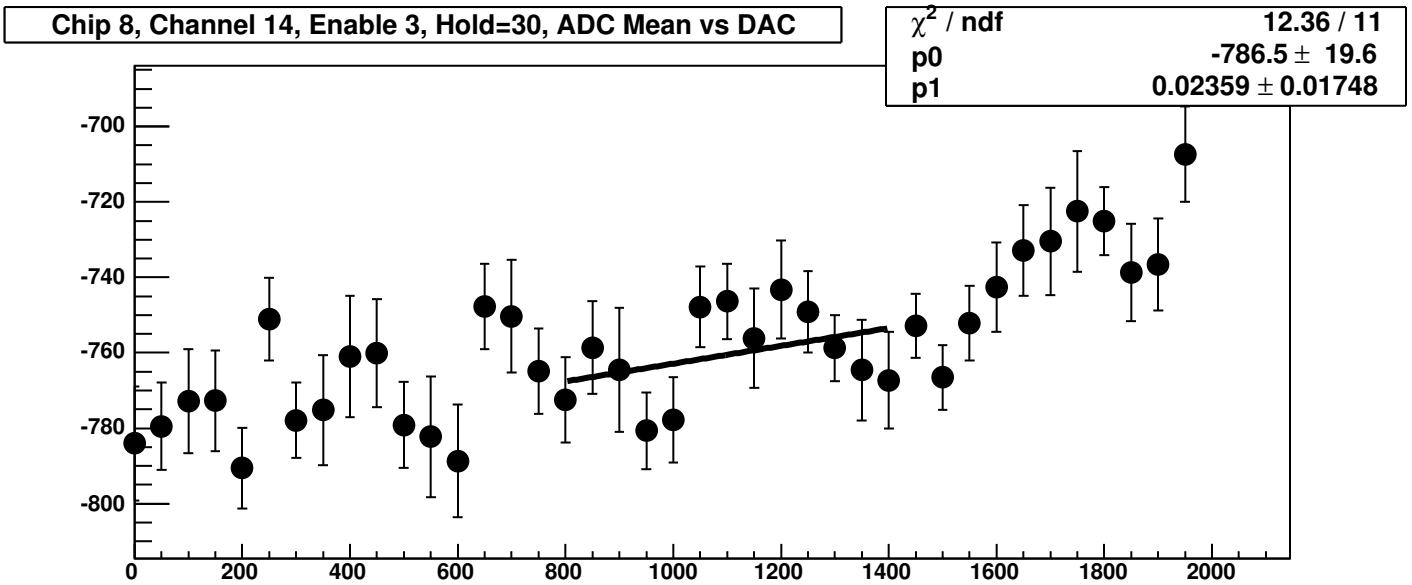
Chip 8, Channel 14, Enable 1!, Hold=30, ADC Residuals vs DAC

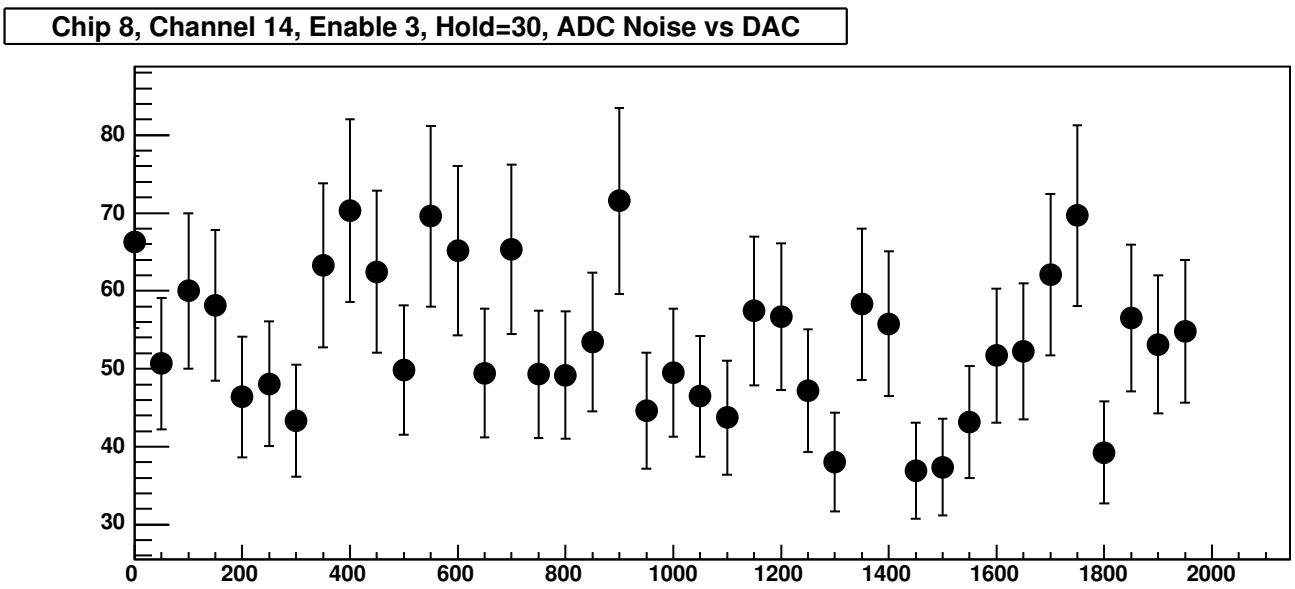


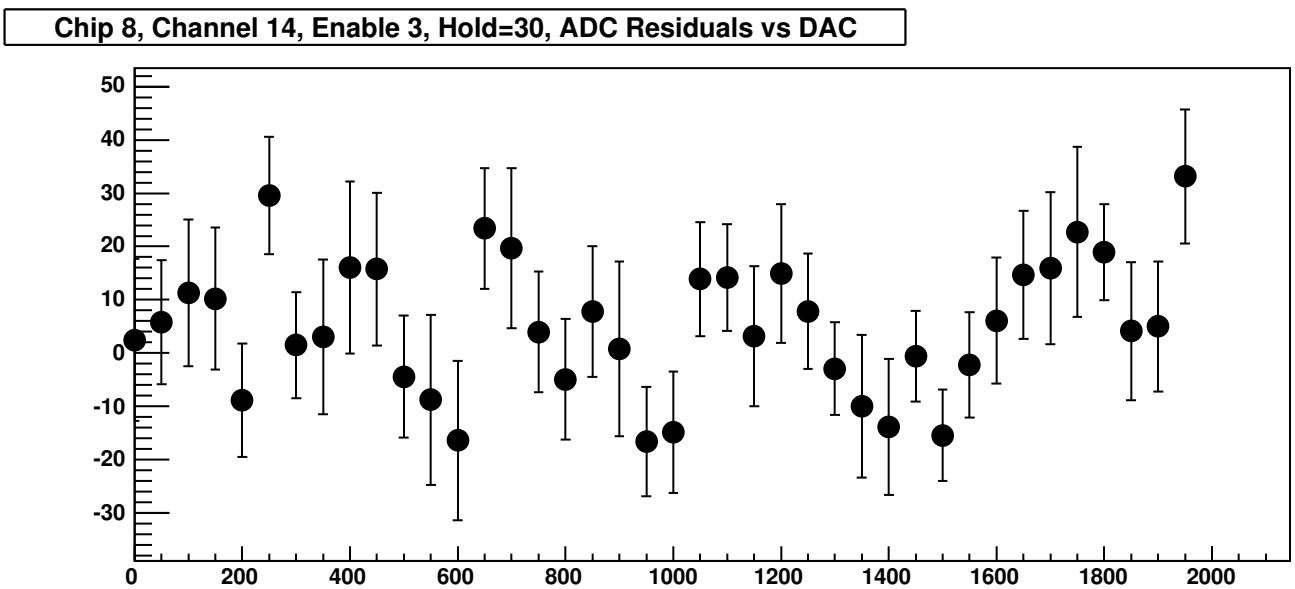

Chip 8, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 20.14 / 11
p0 -868.2 ± 19.62
p1 0.1956 ± 0.01771
-450
-500
-550
-600
-650
-700
-750
0
200
400
600
800
1000
1200
1400
1600
1800
2000

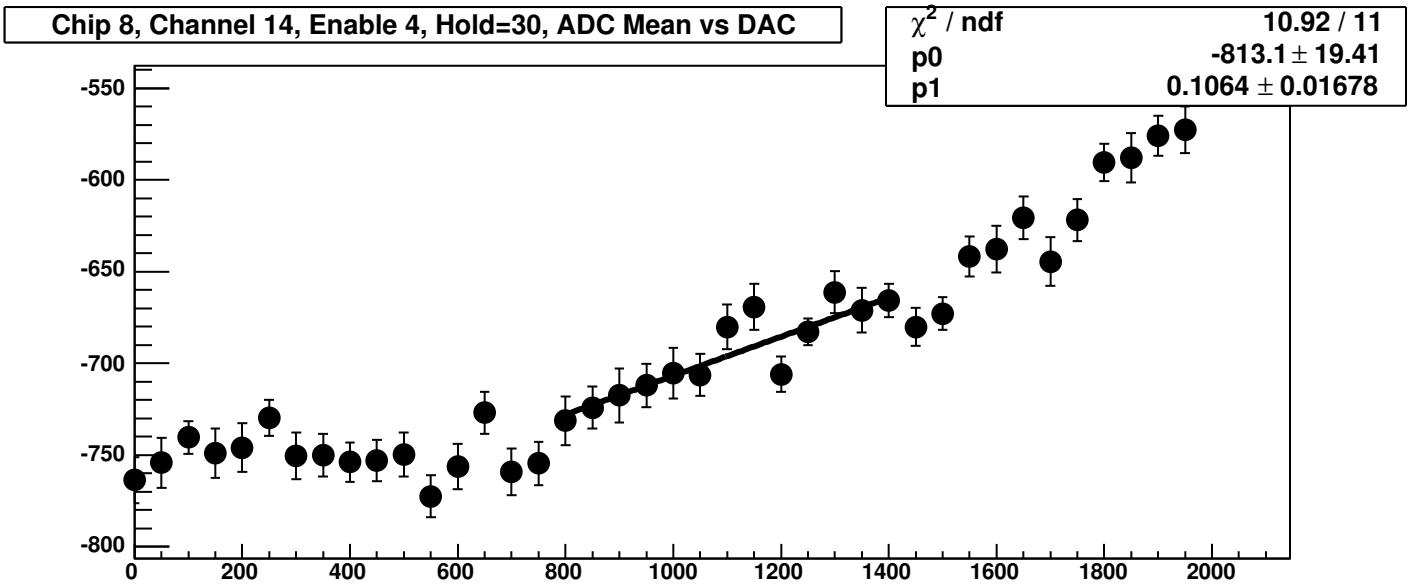

Chip 8, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

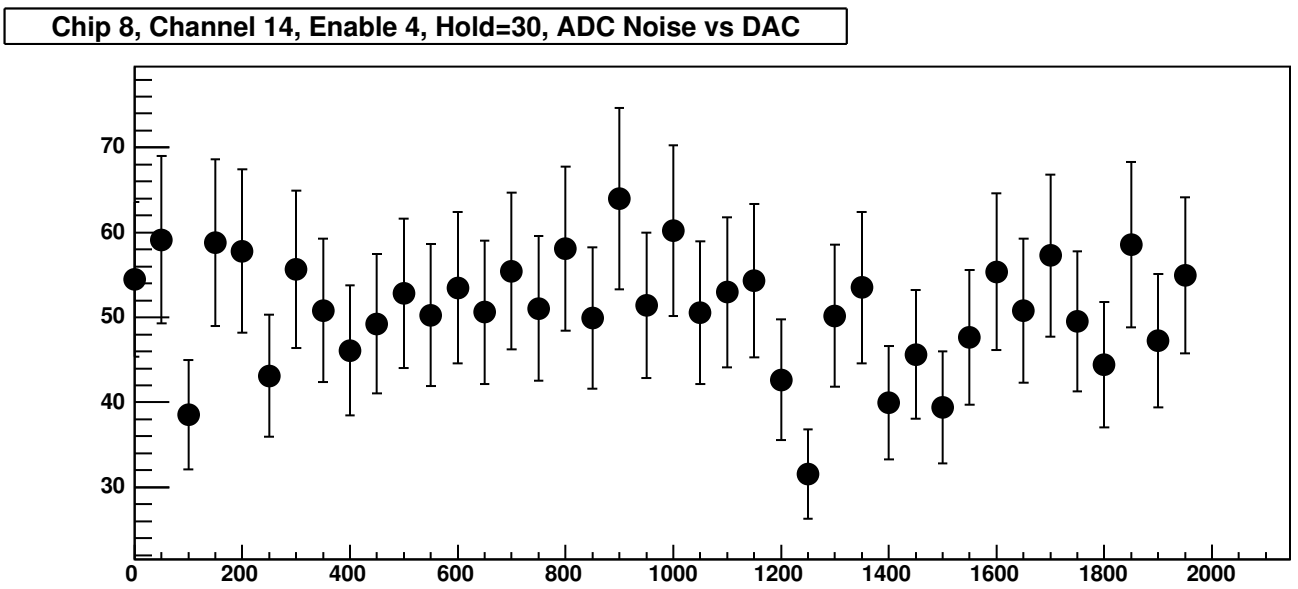

Chip 8, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

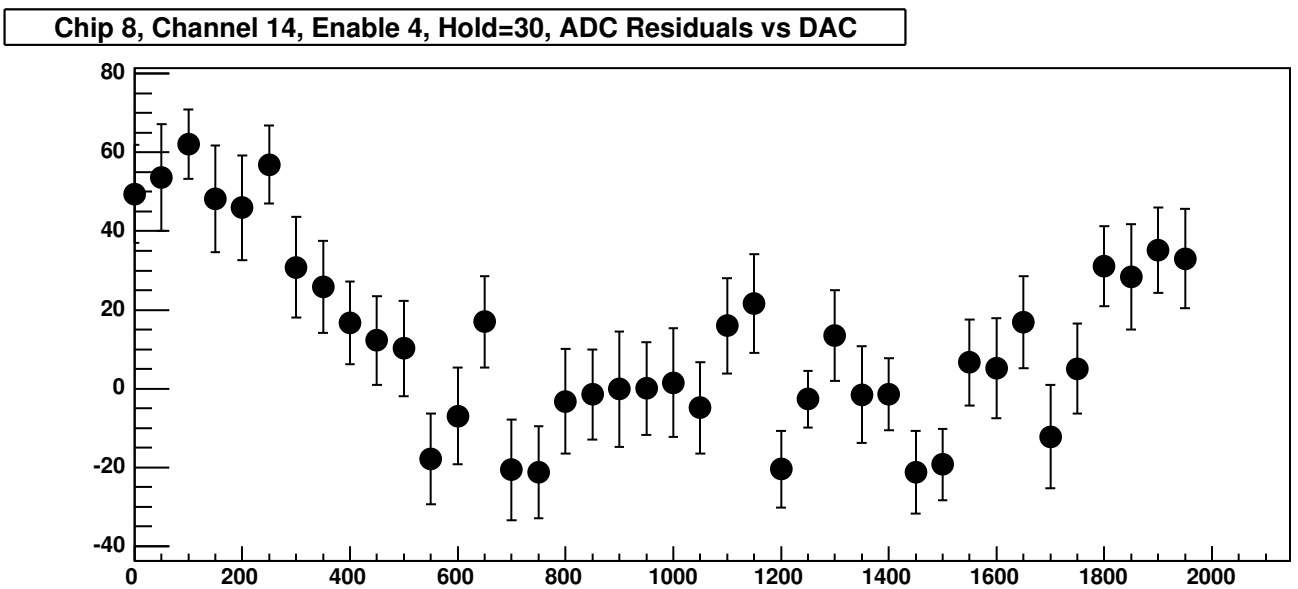

Chip 8, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 12.36 / 11
p0 -786.5 ± 19.6
p1 0.02359 ± 0.01748
-700
-720
-740
-760
-780
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

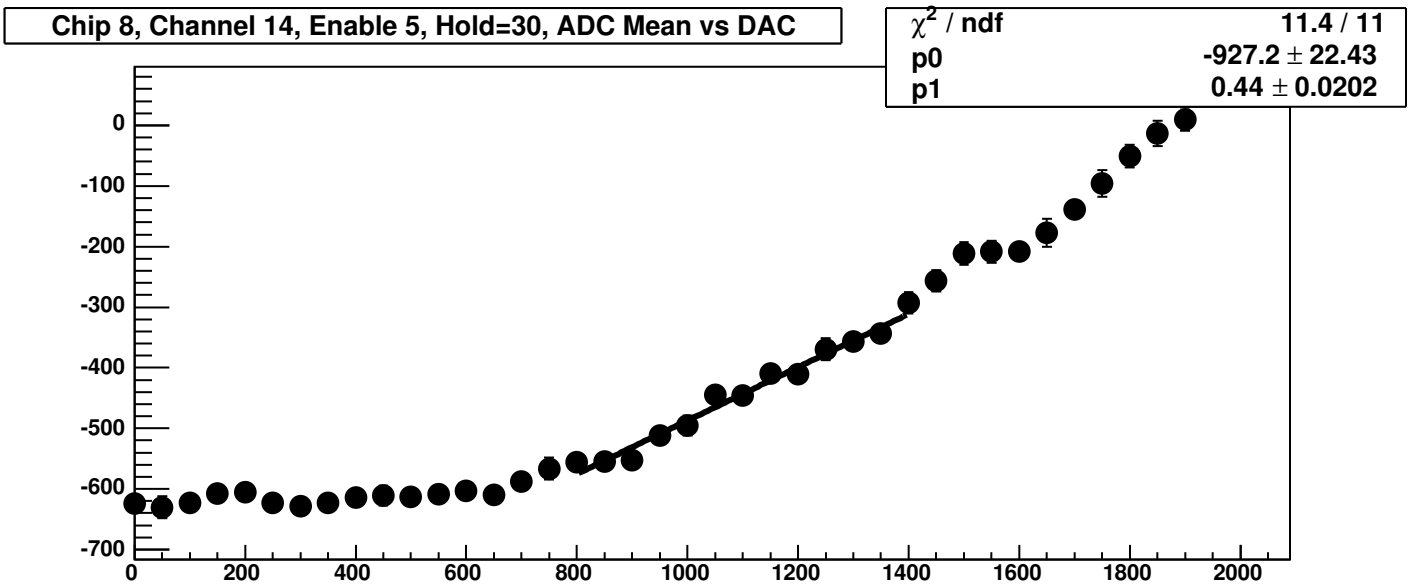

Chip 8, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

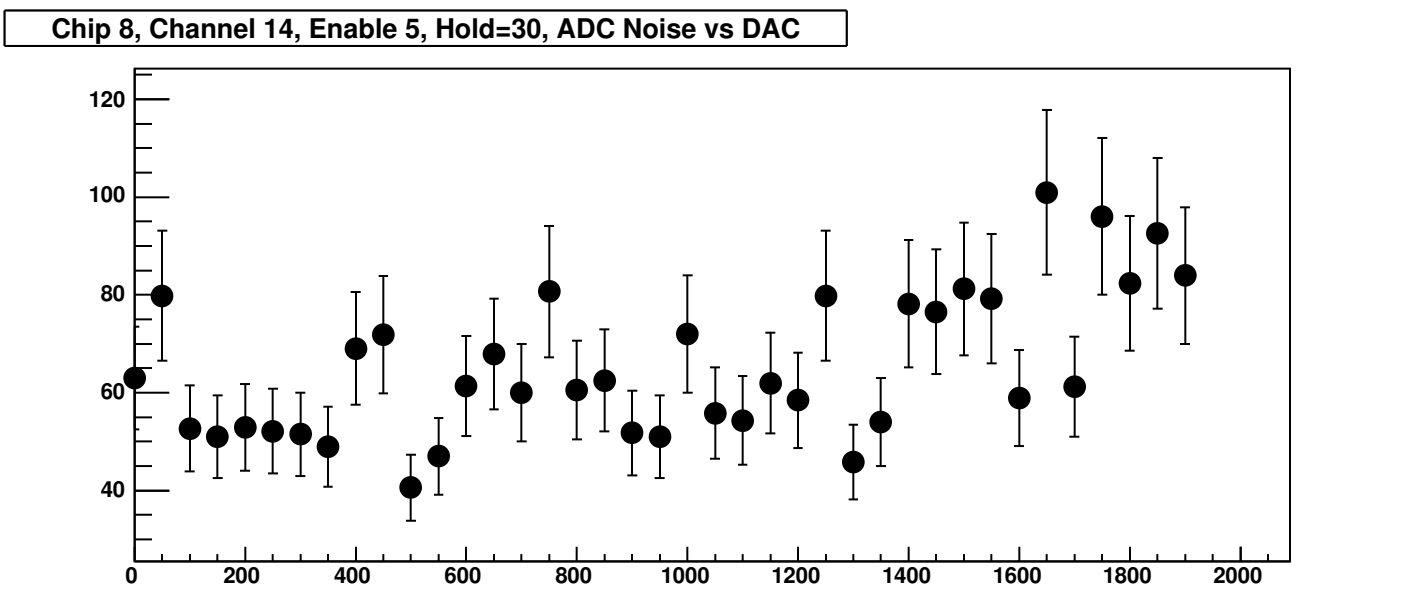

Chip 8, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
50
40
30
20
10
0
-10
-20
-30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

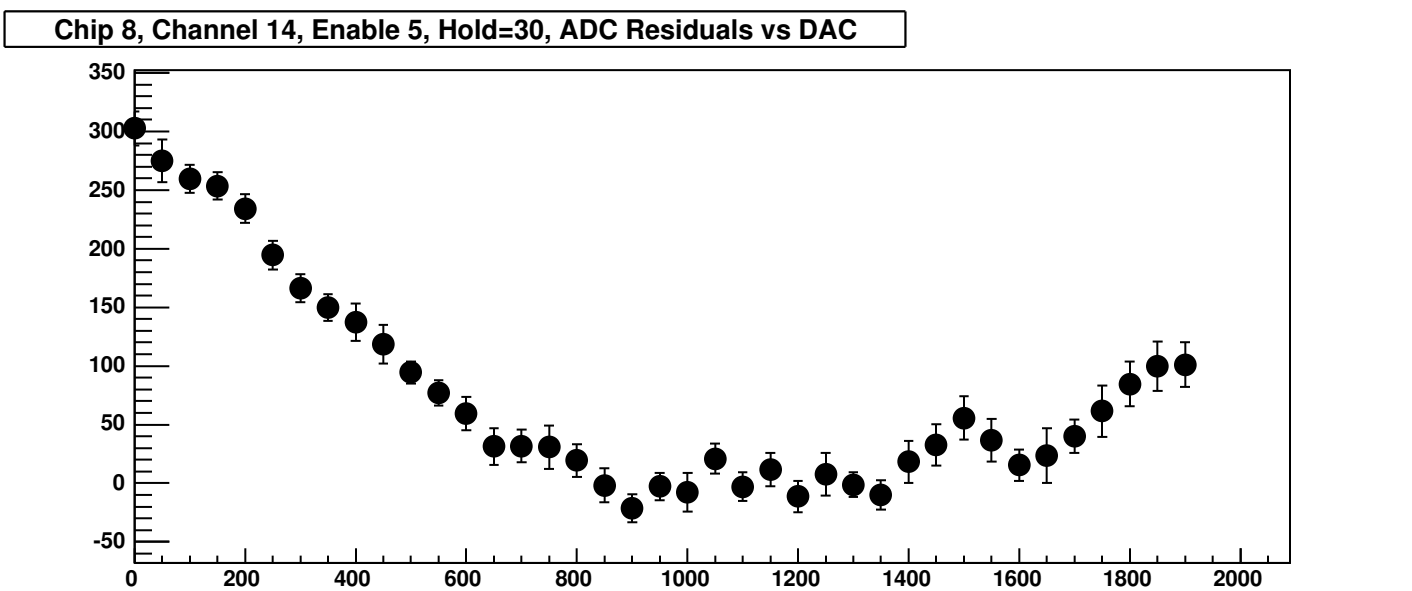

Chip 8, Channel 14, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 10.92 / 11
p0 -813.1 ± 19.41
p1 0.1064 ± 0.01678
-550
-600
-650
-700
-750
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

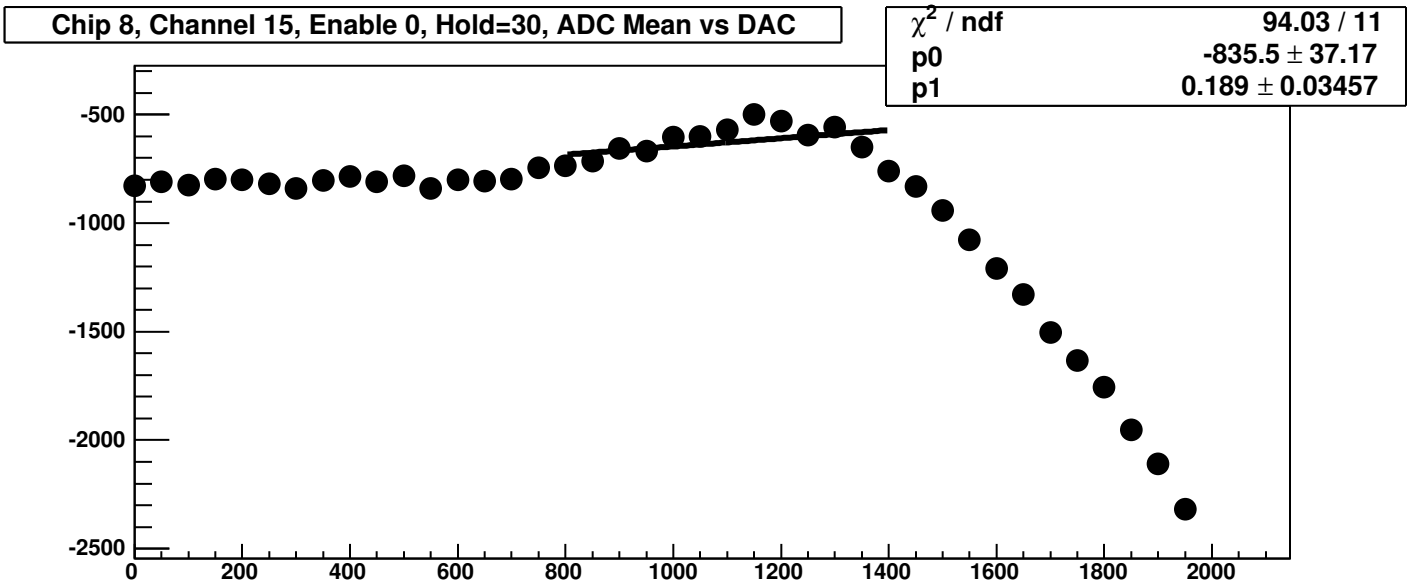

Chip 8, Channel 14, Enable 4, Hold=30, ADC Noise vs DAC
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

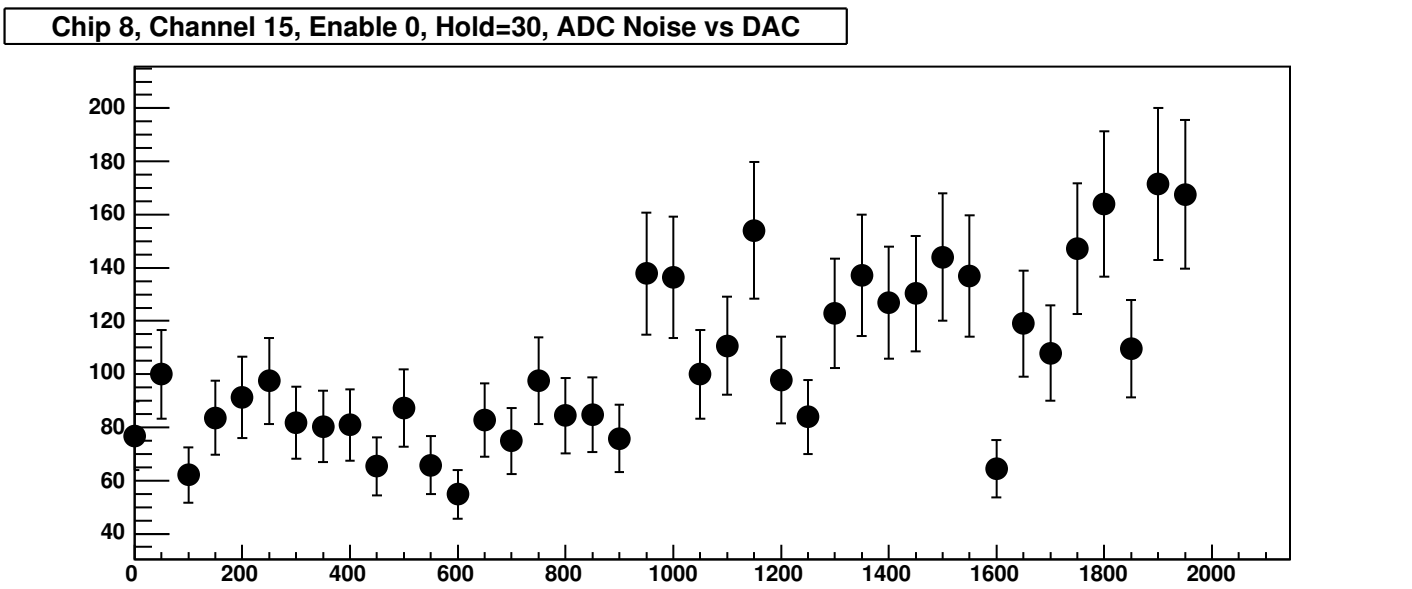

Chip 8, Channel 14, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

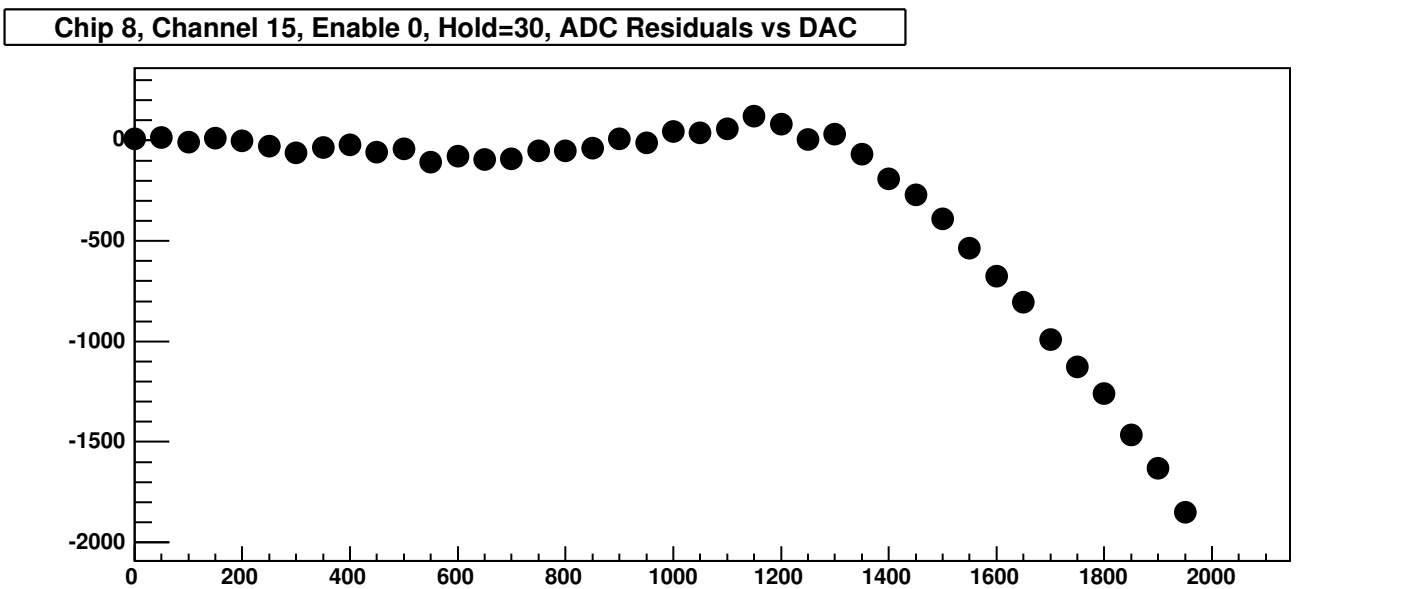

Chip 8, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 11.4 / 11
p0 -927.2 ± 22.43
p1 0.44 ± 0.0202
0
-100
-200
-300
-400
-500
-600
-700
0 200 400 600 800 1000 1200 1400 1600 1800 2000

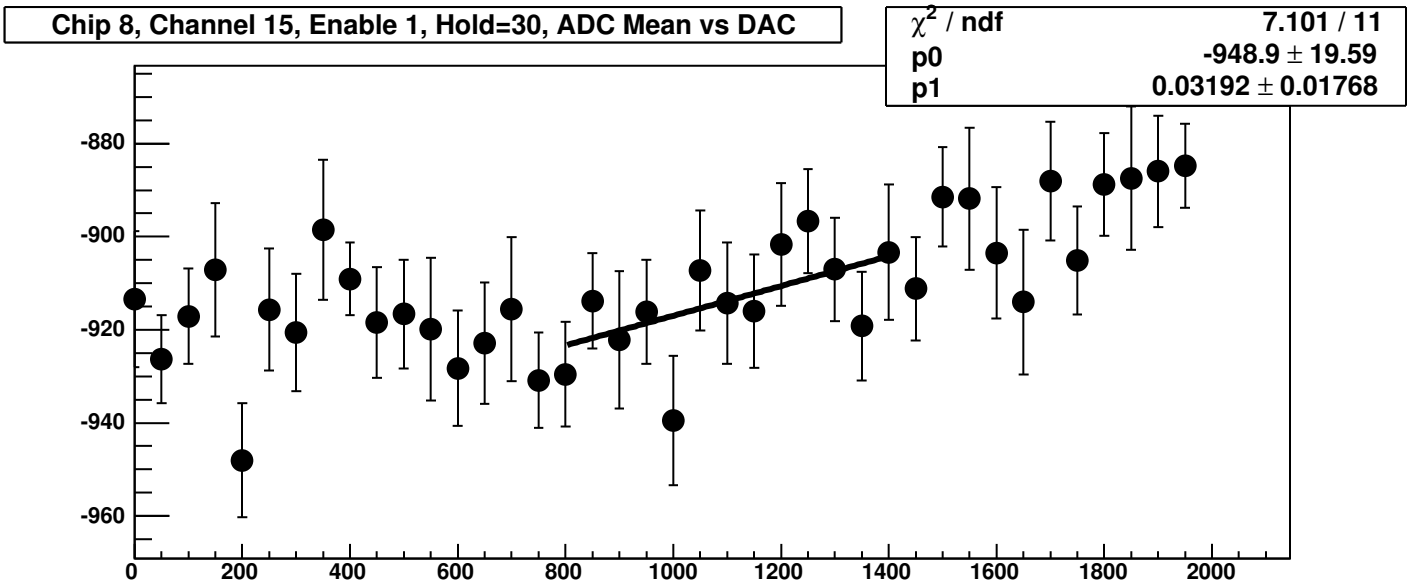

Chip 8, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0 200 400 600 800 1000 1200 1400 1600 1800 2000

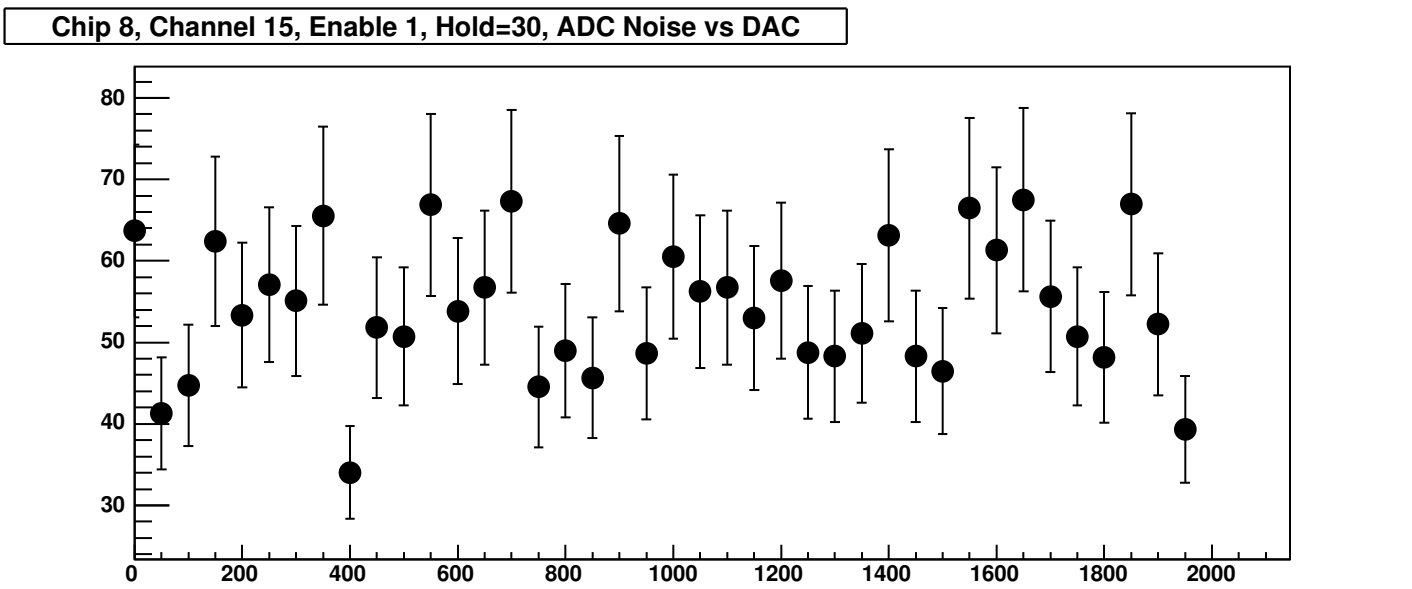

Chip 8, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0 200 400 600 800 1000 1200 1400 1600 1800 2000

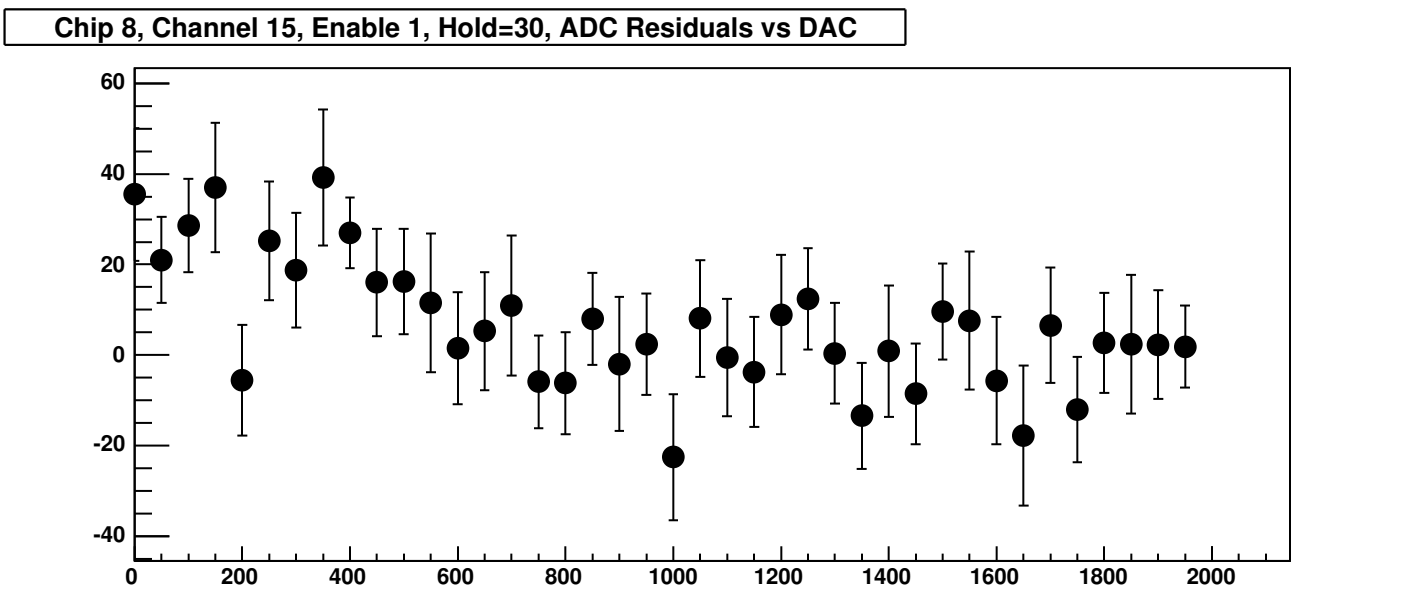

Chip 8, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 94.03 / 11
p0 -835.5 ± 37.17
p1 0.189 ± 0.03457
-500
-1000
-1500
-2000
-2500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

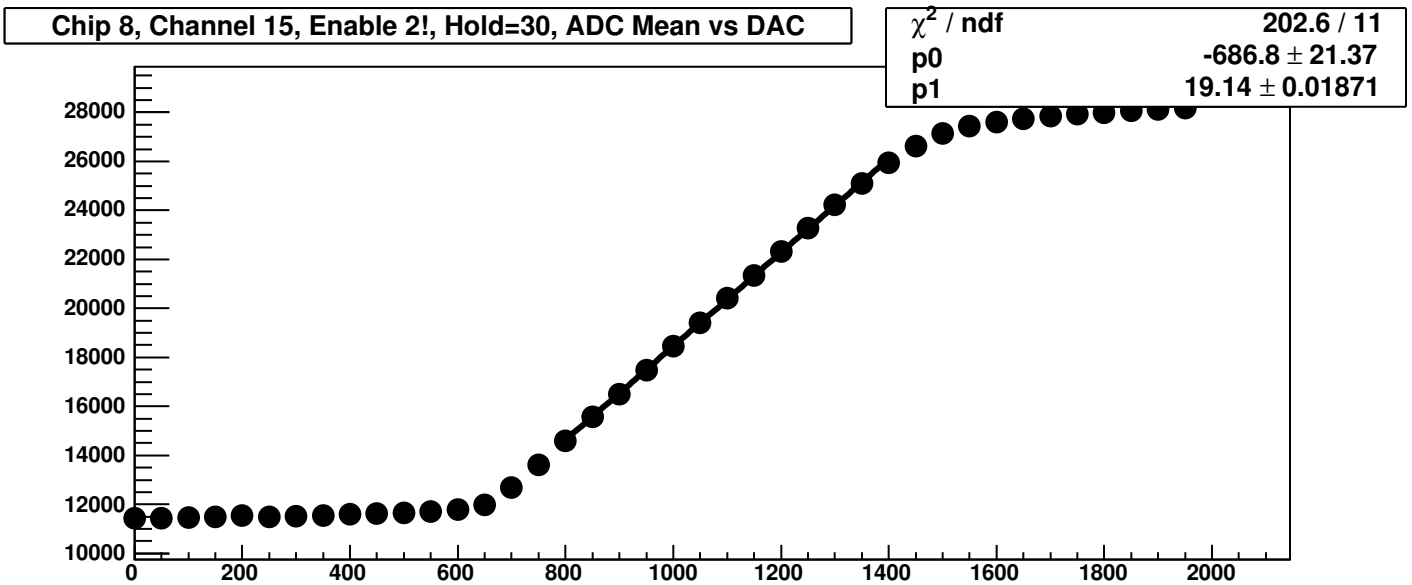

Chip 8, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

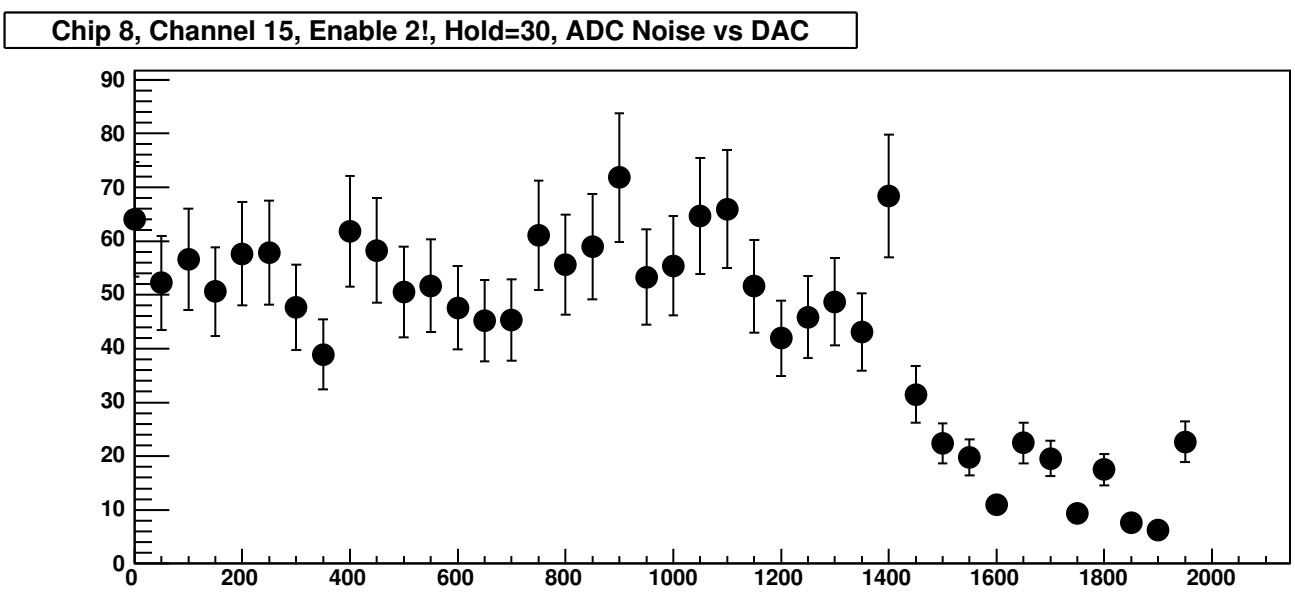

Chip 8, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

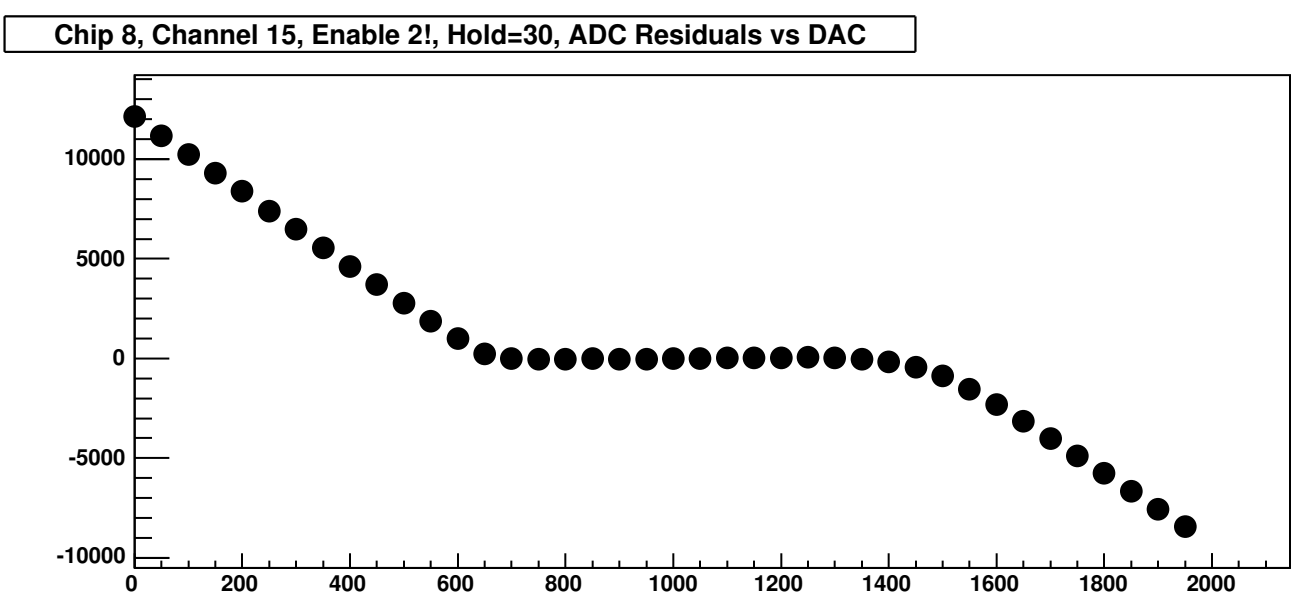

Chip 8, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7.101 / 11
p0 -948.9 ± 19.59
p1 0.03192 ± 0.01768
-880
-900
-920
-940
-960
0
200
400
600
800
1000
1200
1400
1600
1800
2000

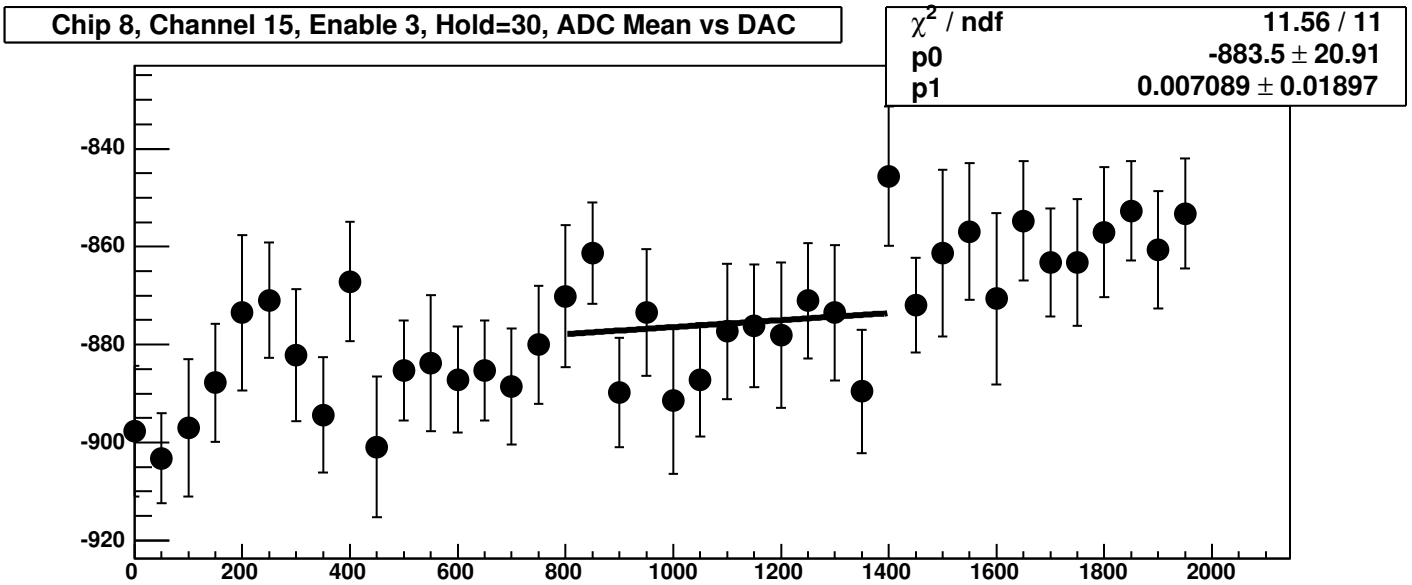

Chip 8, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

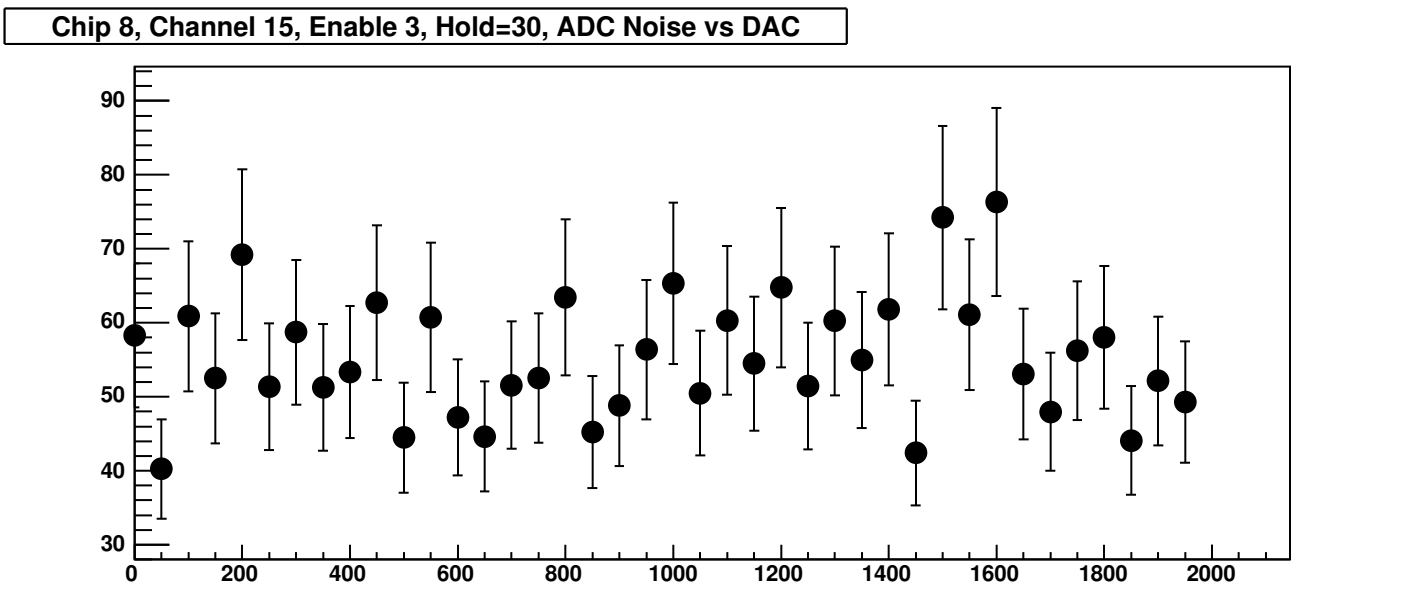

Chip 8, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

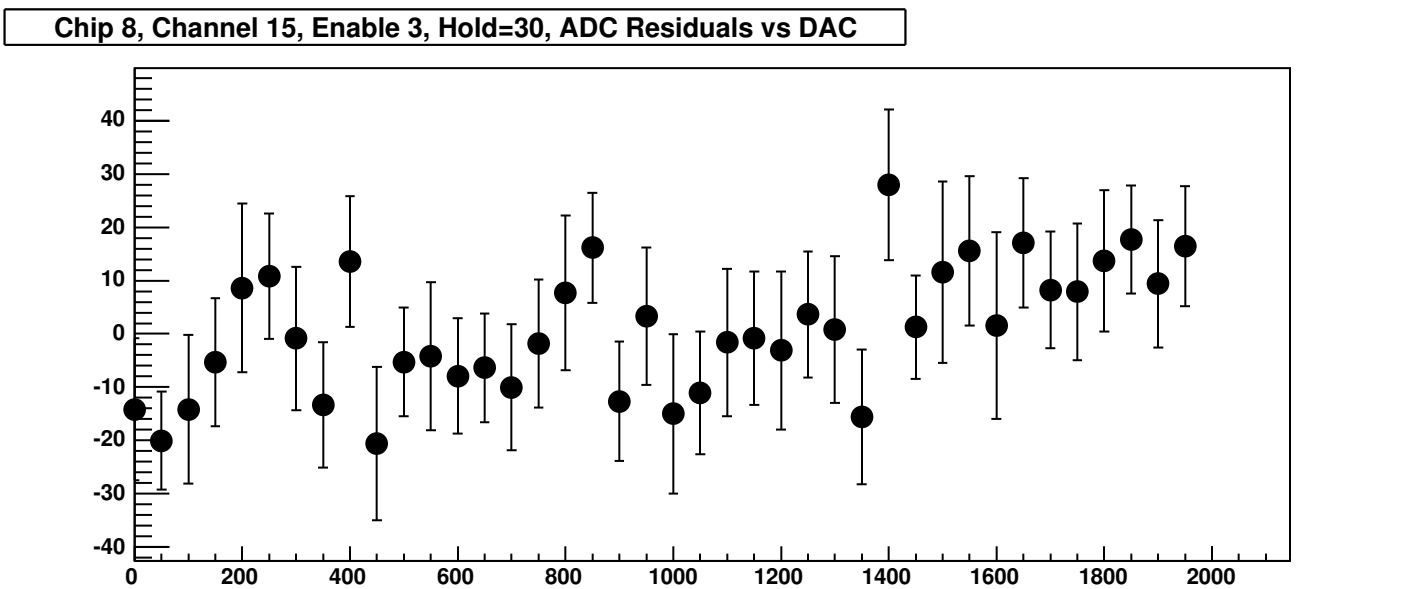

Chip 8, Channel 15, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 202.6 / 11
p0 -686.8 ± 21.37
p1 19.14 ± 0.01871
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 15, Enable 2!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

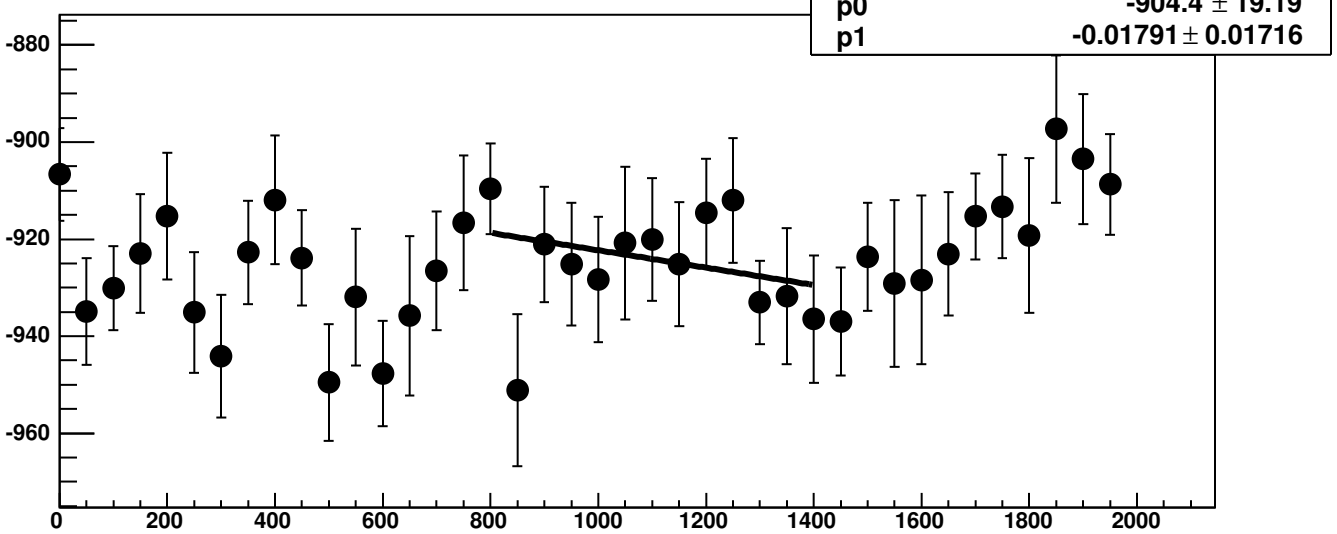

Chip 8, Channel 15, Enable 2!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 11.56 / 11
p0 -883.5 ± 20.91
p1 0.007089 ± 0.01897
-840
-860
-880
-900
-920
0
200
400
600
800
1000
1200
1400
1600
1800
2000

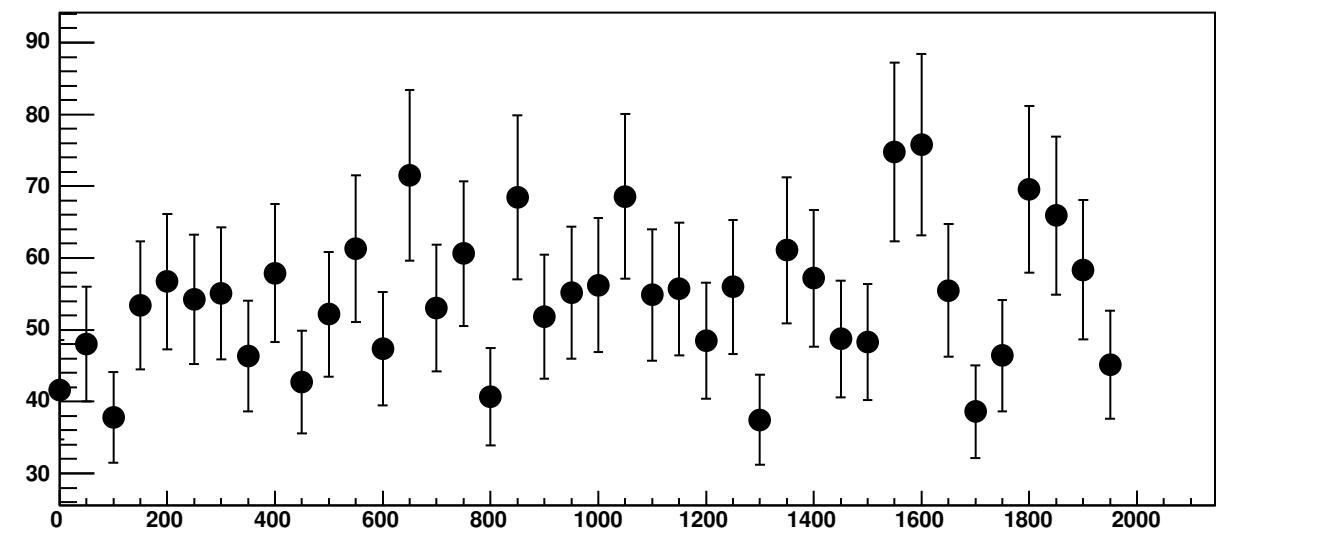

Chip 8, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
40
30
20
10
0
-10
-20
-30
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

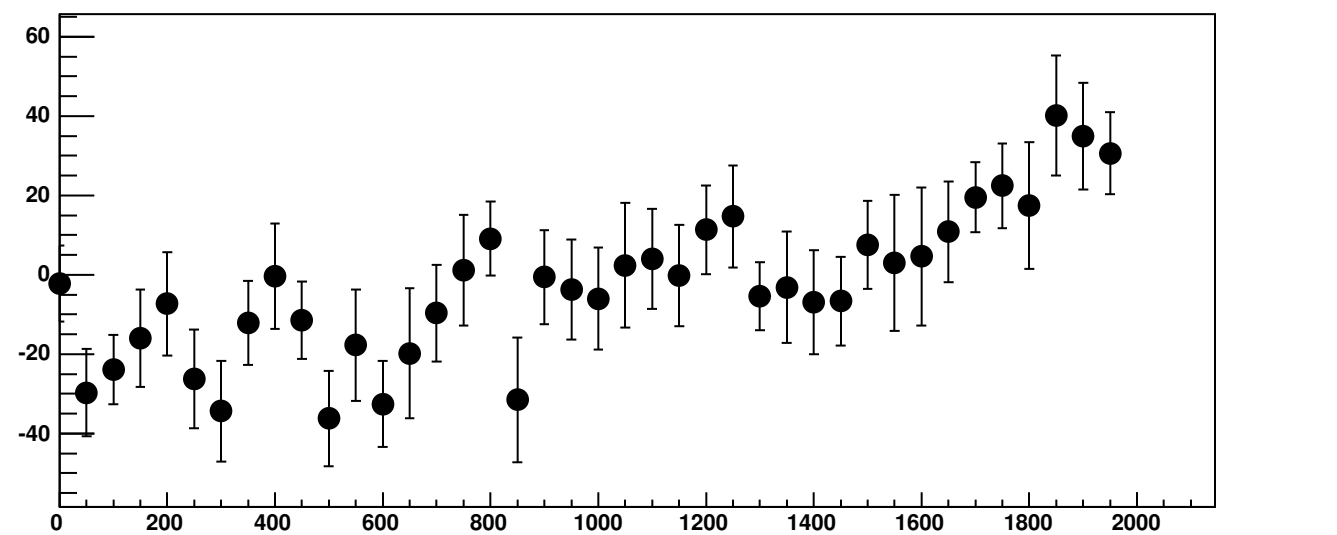
Chip 8, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC

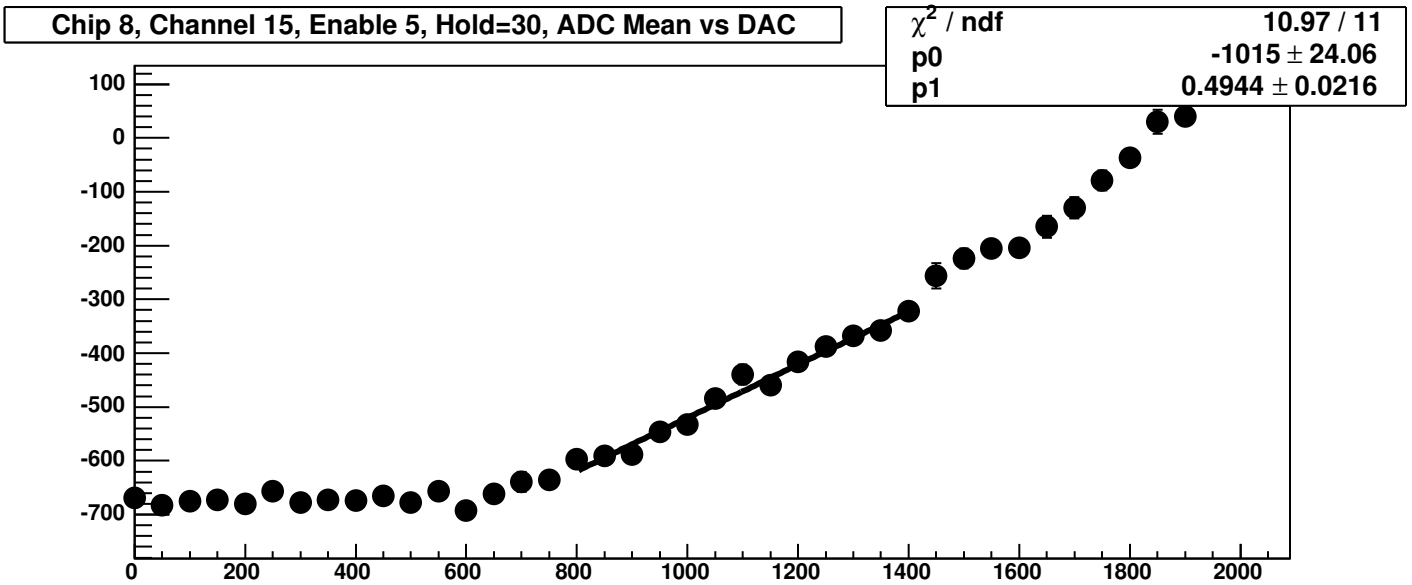


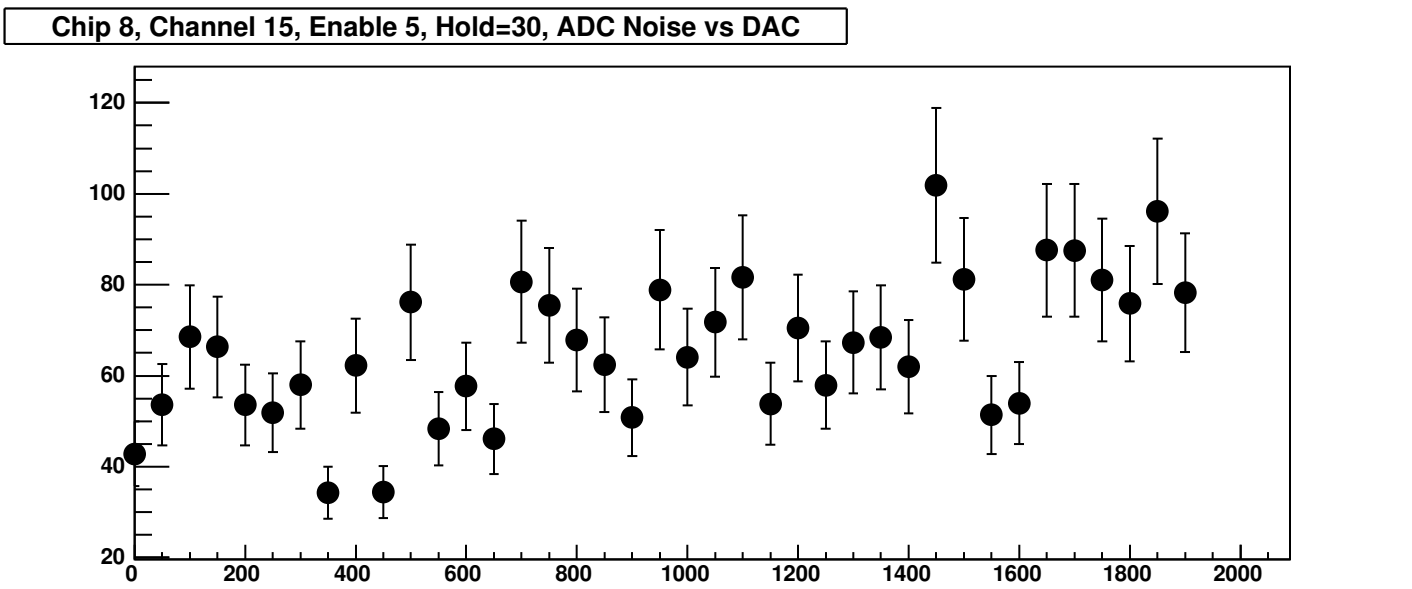
Chip 8, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC

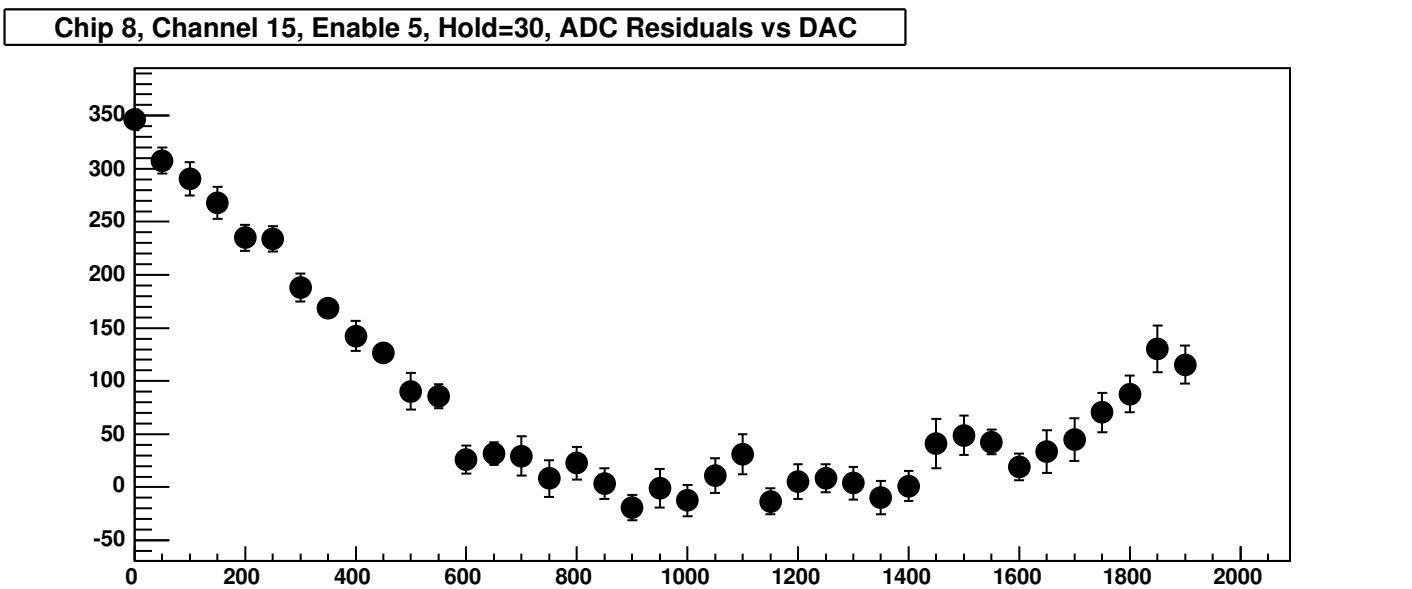


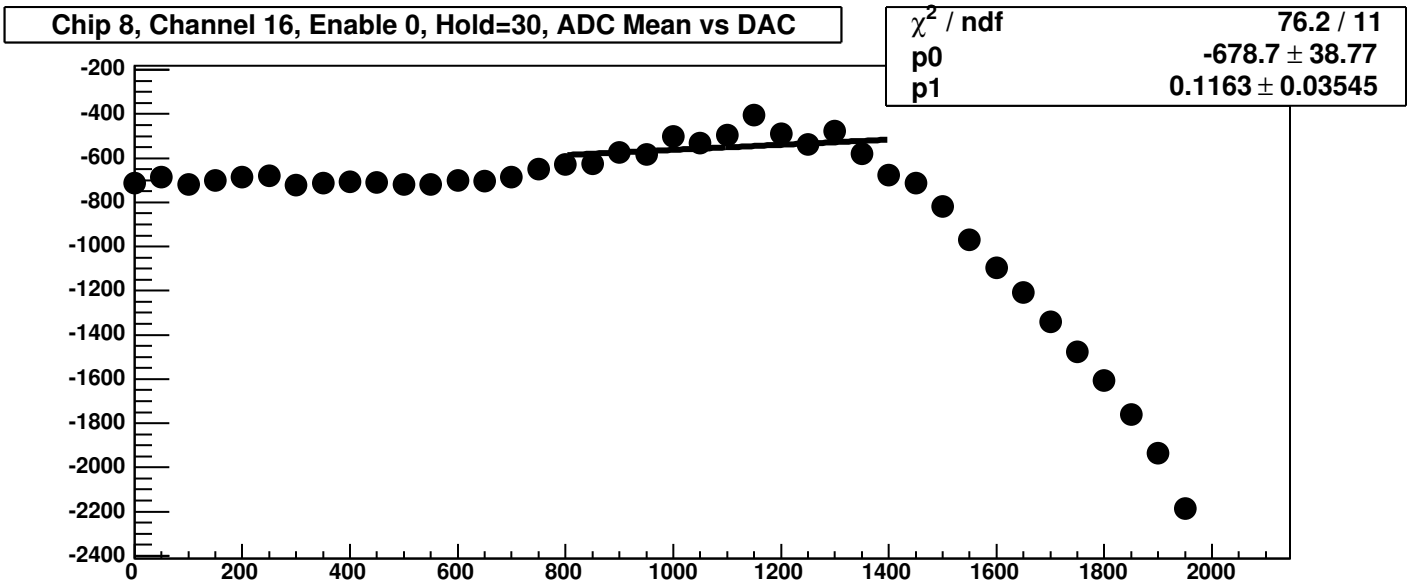
Chip 8, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC

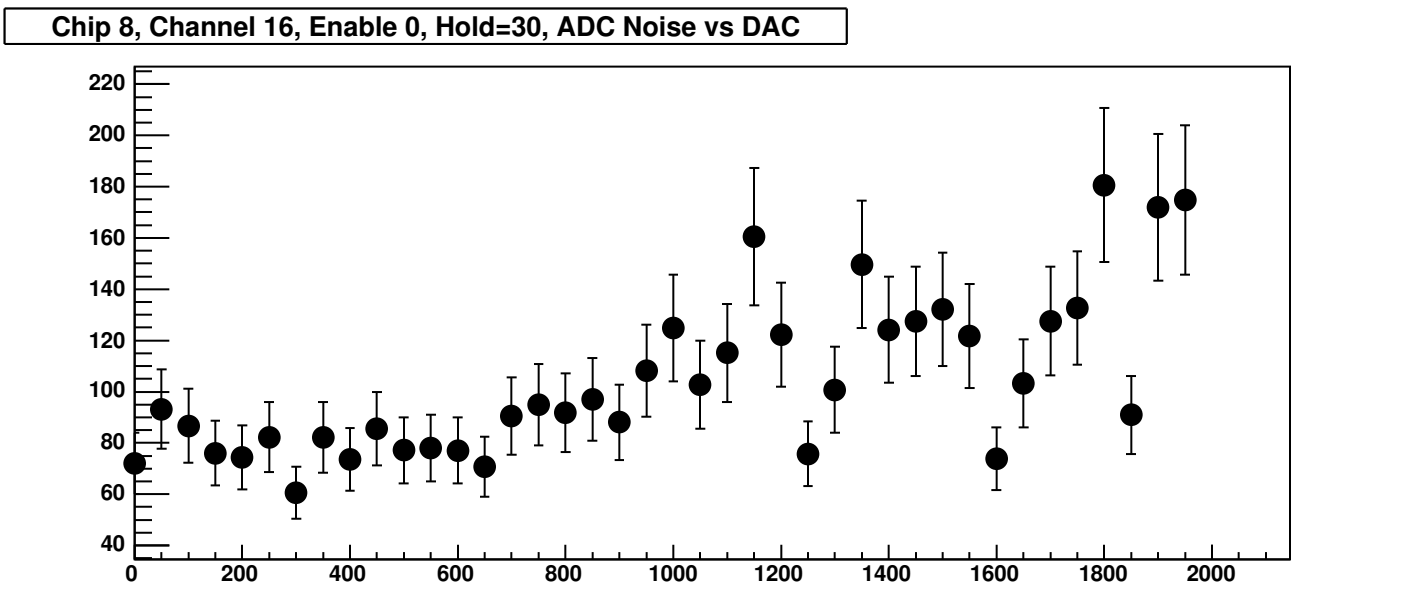


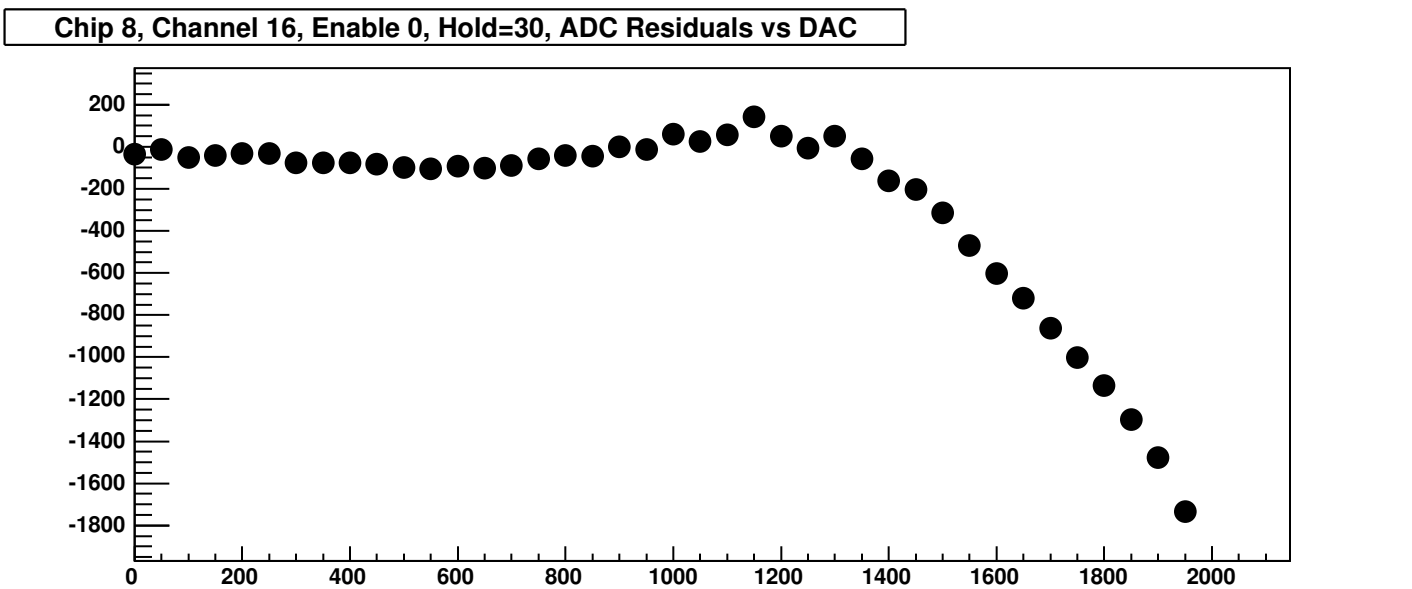

Chip 8, Channel 15, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 10.97 / 11
p0 -1015 ± 24.06
p1 0.4944 ± 0.0216
100
0
-100
-200
-300
-400
-500
-600
-700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

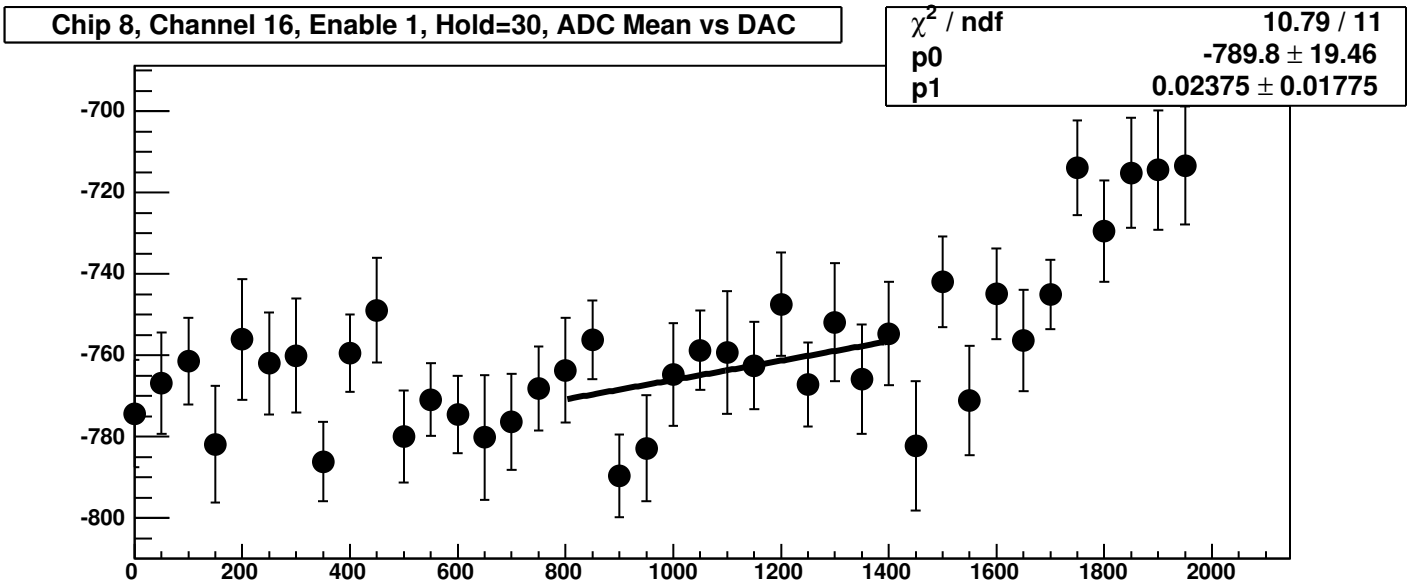

Chip 8, Channel 15, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

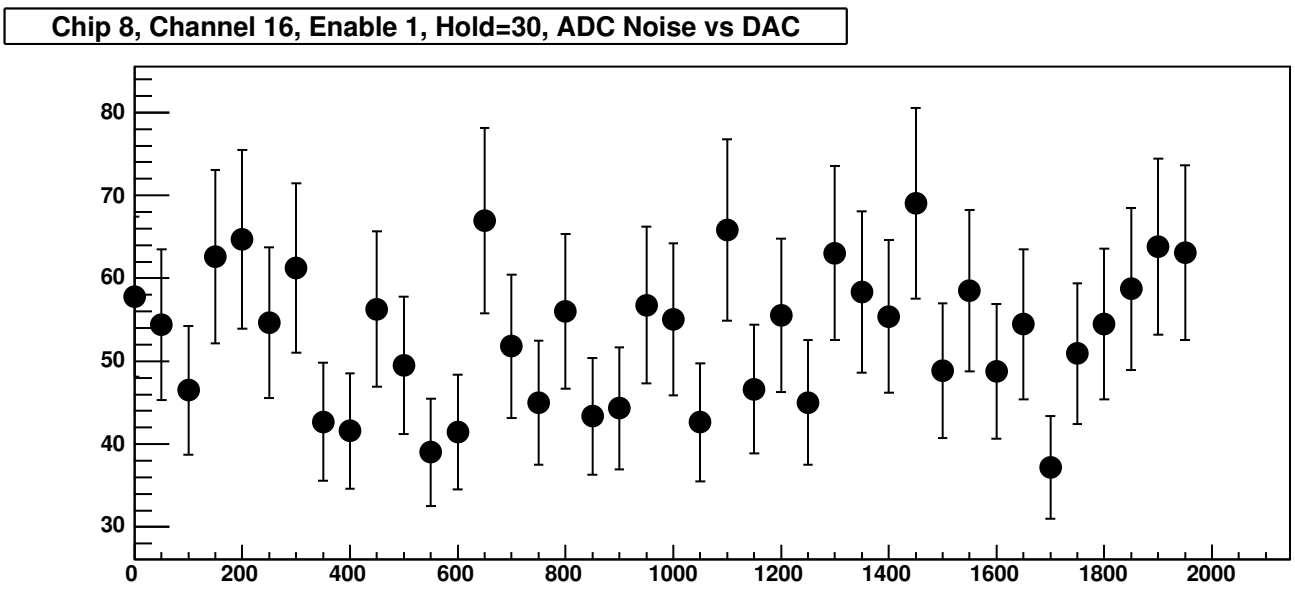

Chip 8, Channel 15, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

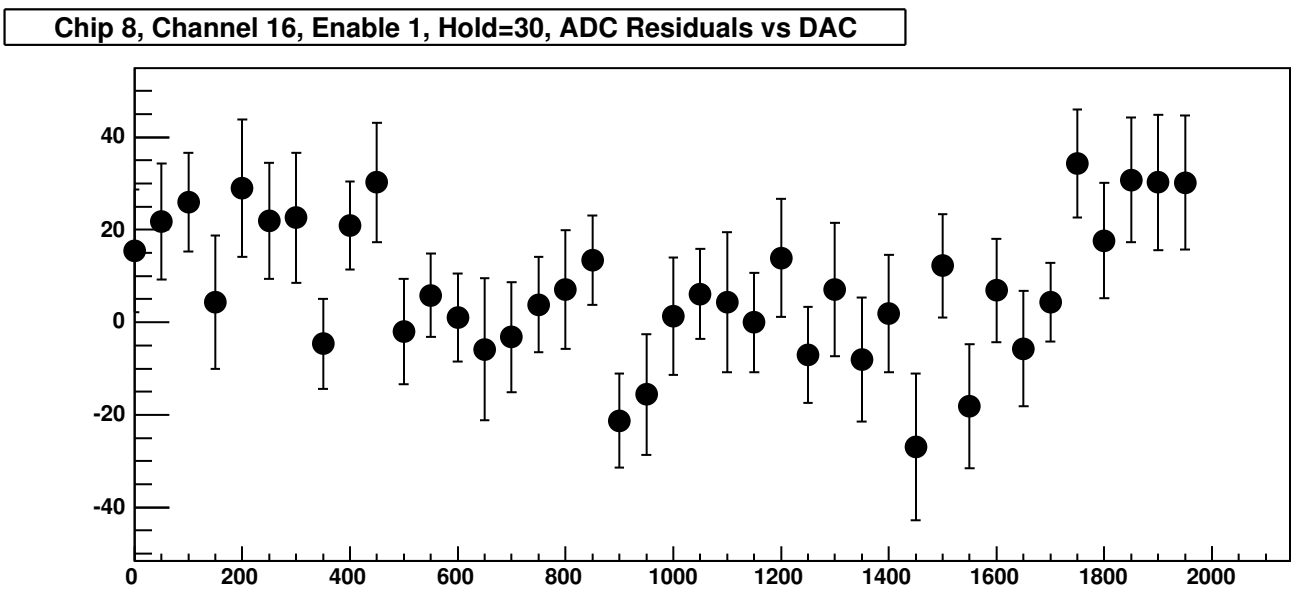

Chip 8, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 76.2 / 11
p0 -678.7 ± 38.77
p1 0.1163 ± 0.03545
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
-2000
-2200
-2400
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

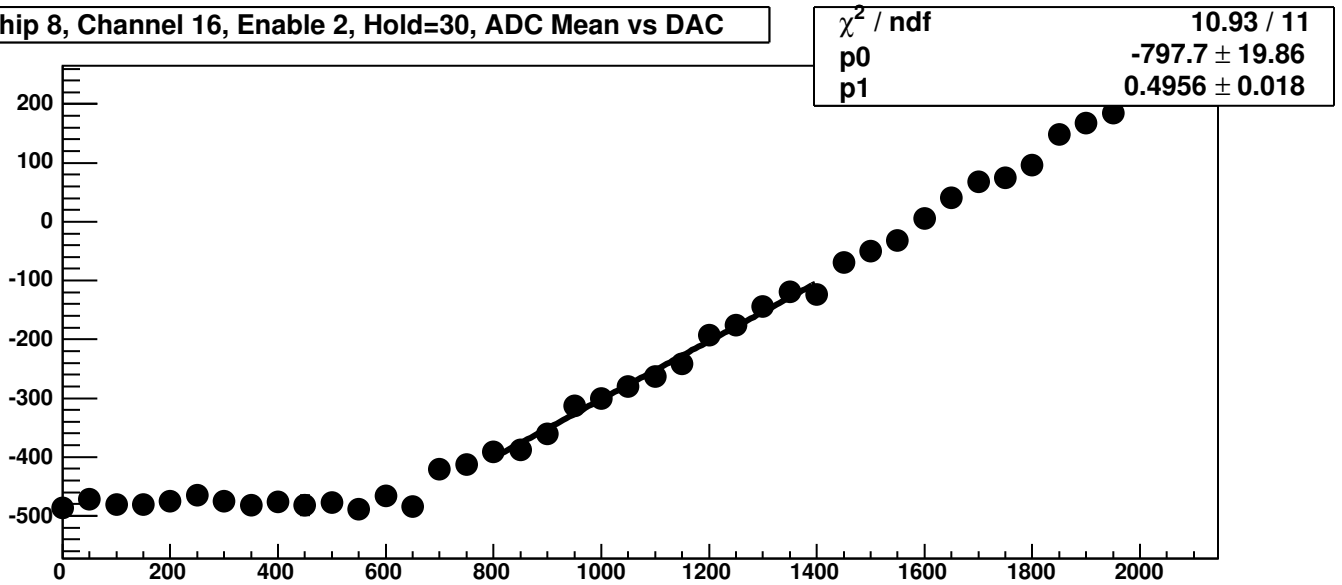

Chip 8, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 10.79 / 11
p0 -789.8 ± 19.46
p1 0.02375 ± 0.01775
-700
-720
-740
-760
-780
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

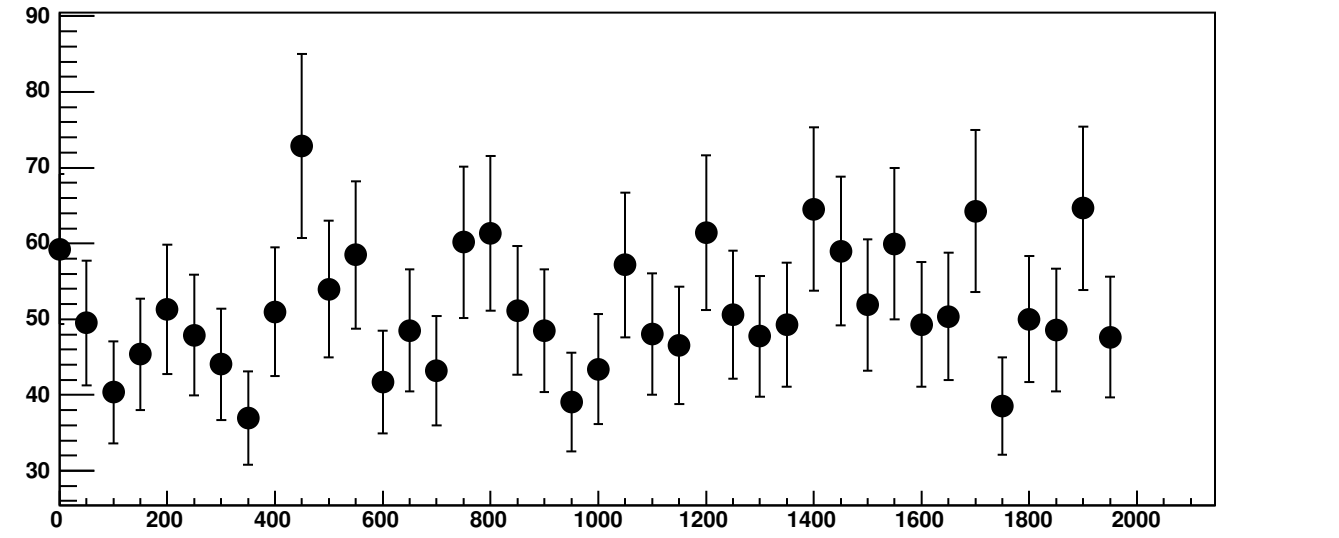

Chip 8, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

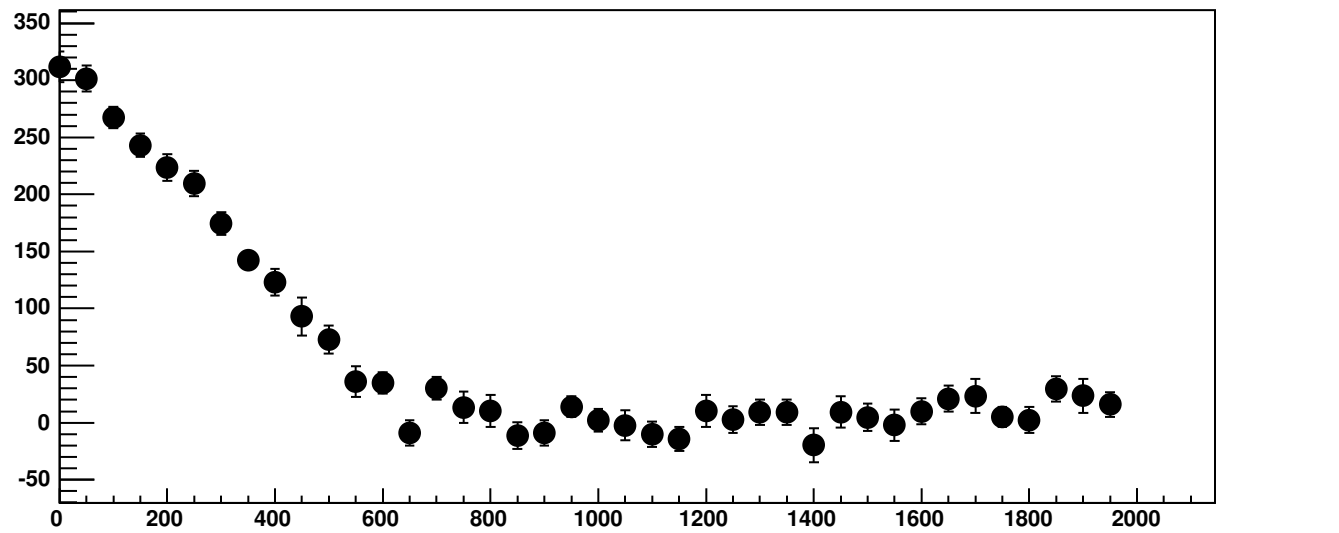
Chip 8, Channel 16, Enable 2, Hold=30, ADC Mean vs DAC

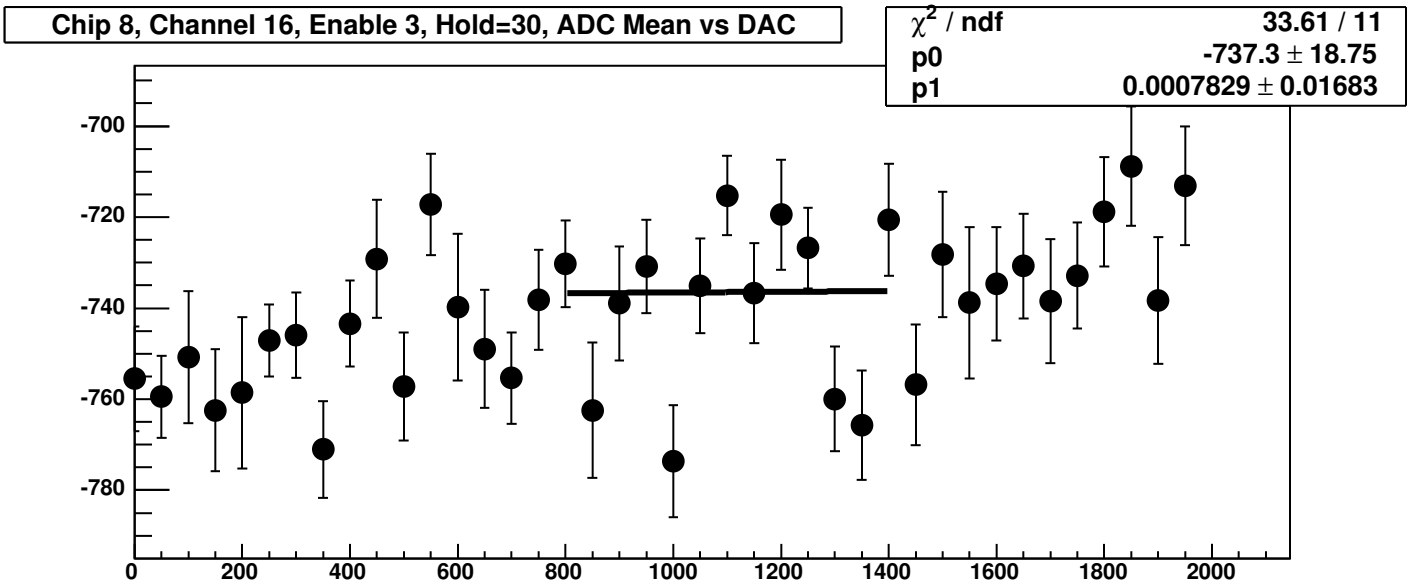


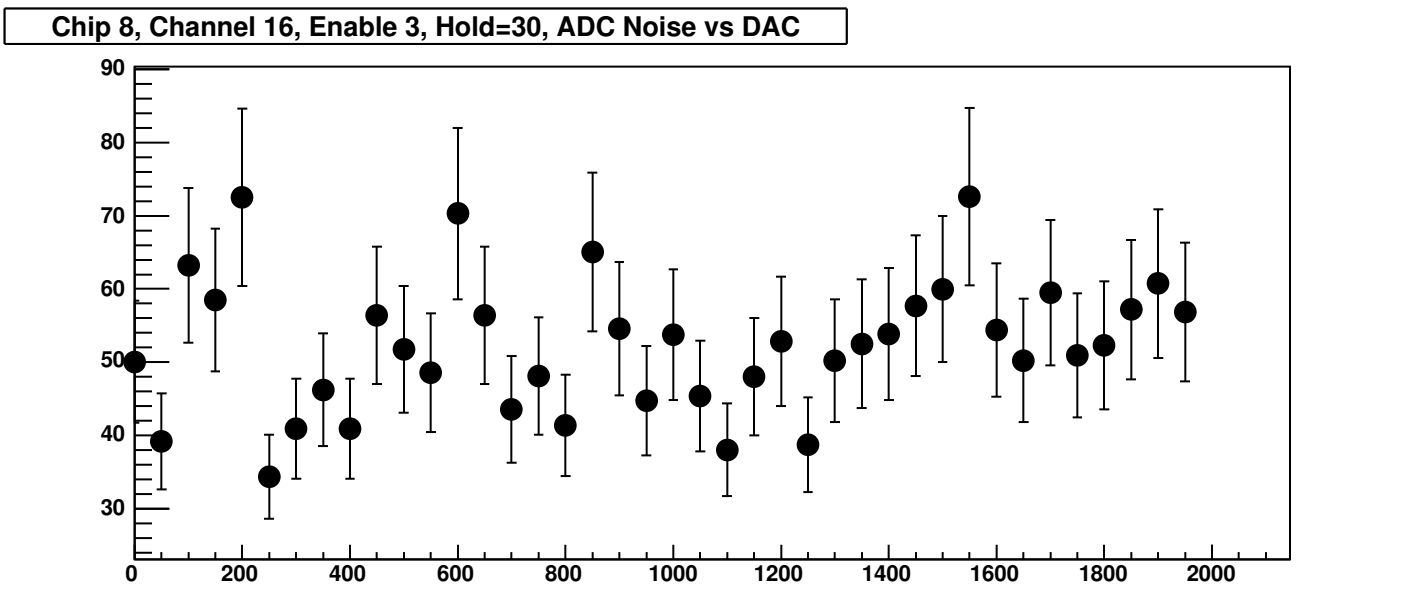
Chip 8, Channel 16, Enable 2, Hold=30, ADC Noise vs DAC

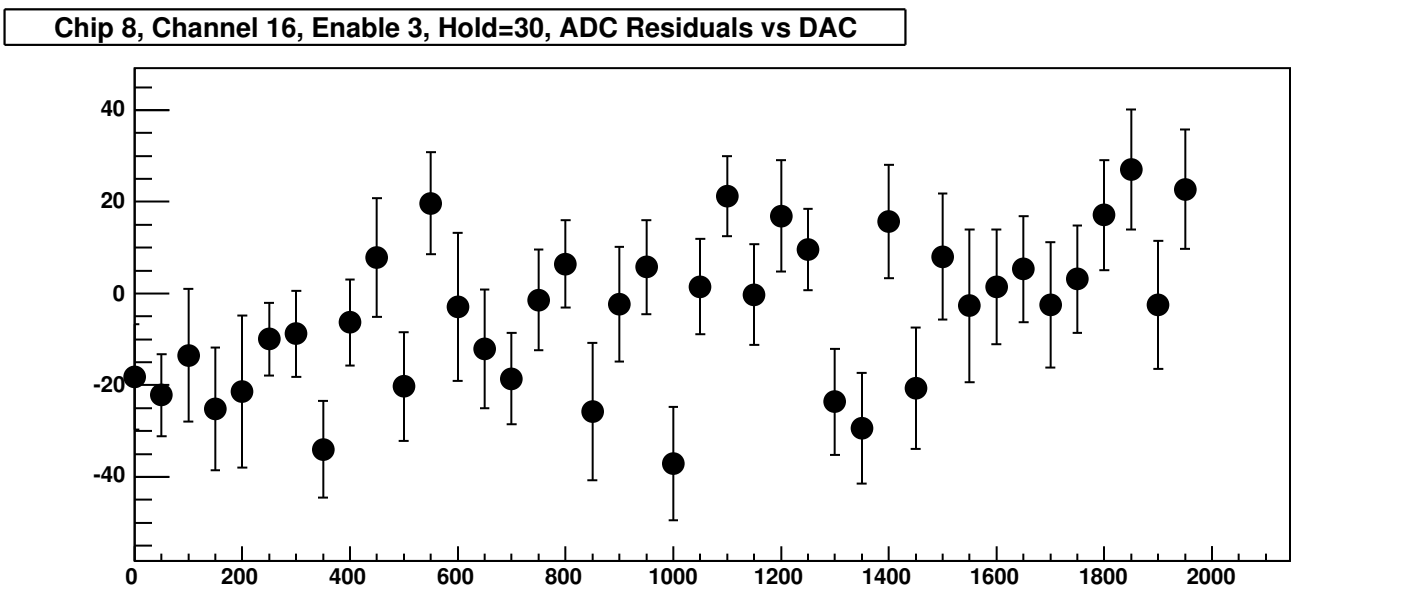


Chip 8, Channel 16, Enable 2, Hold=30, ADC Residuals vs DAC



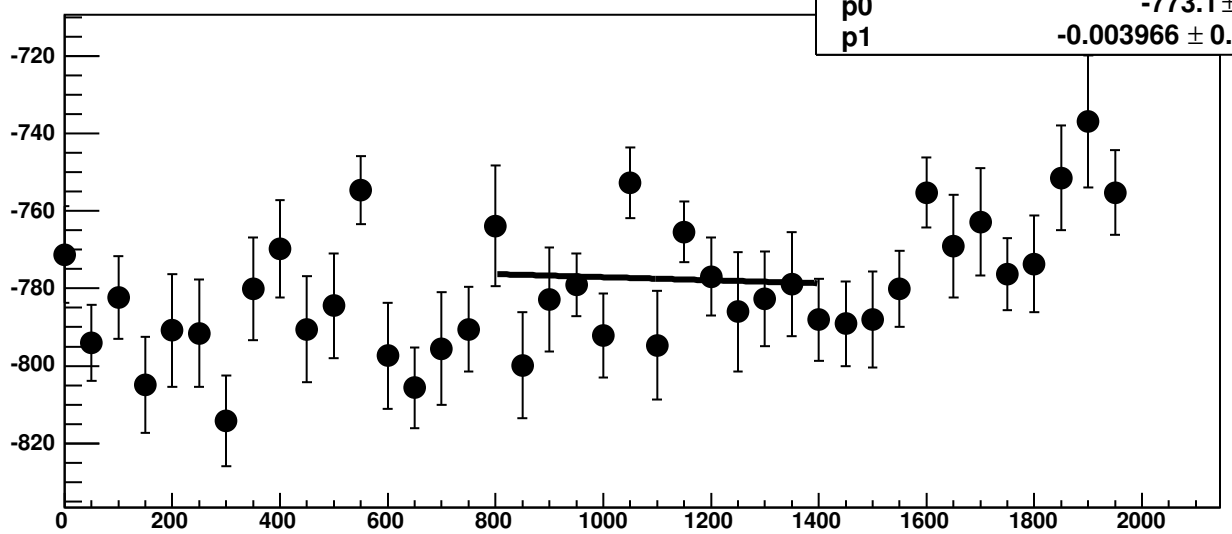

Chip 8, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 33.61 / 11
p0 -737.3 ± 18.75
p1 0.0007829 ± 0.01683
-700
-720
-740
-760
-780
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

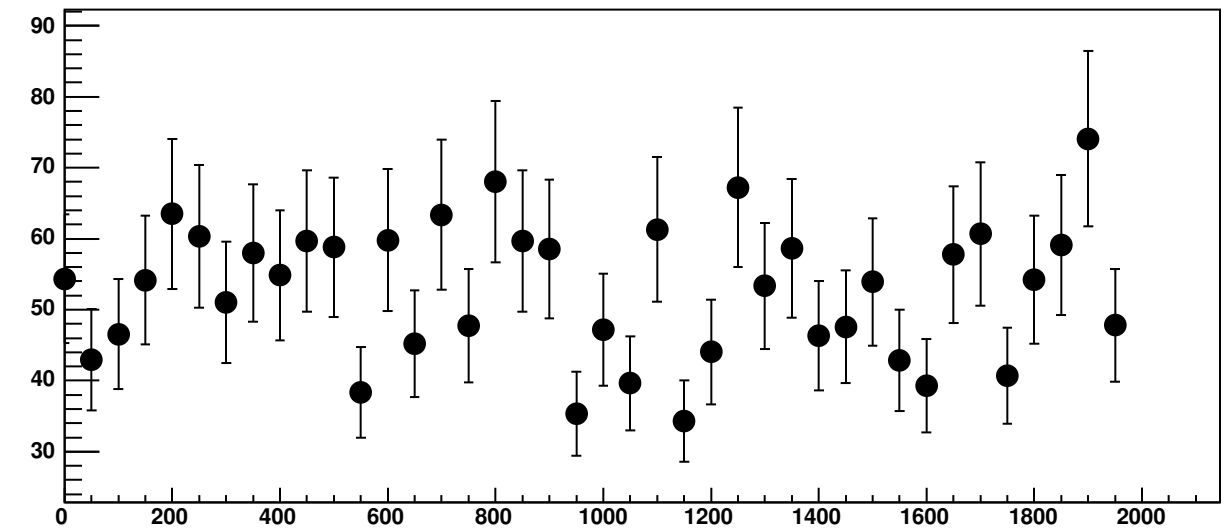

Chip 8, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

Chip 8, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC

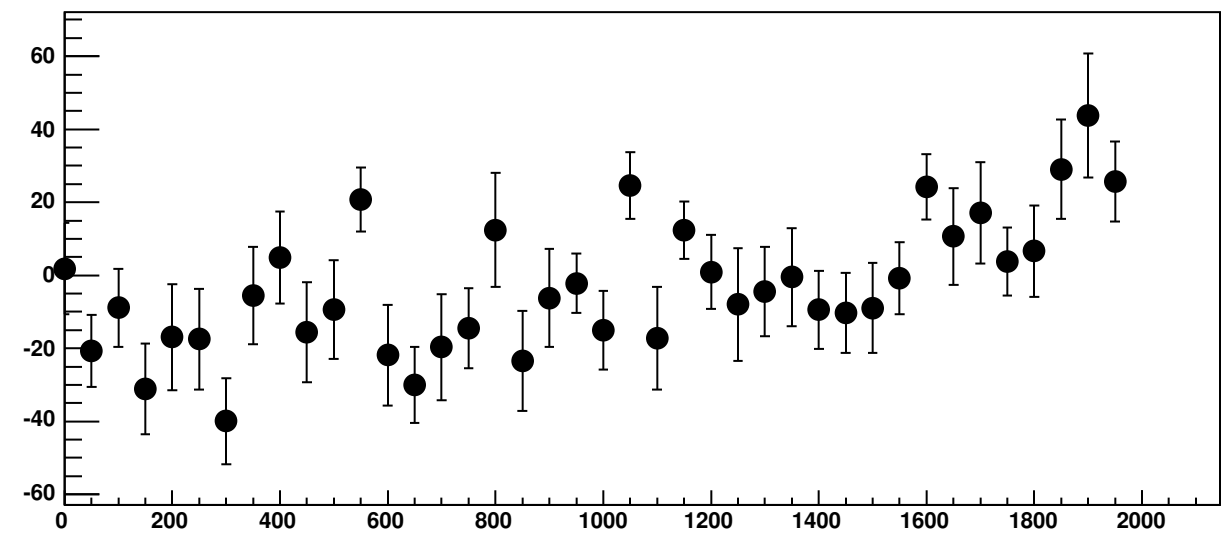
χ^2 / ndf	18.22 / 11
p0	-773.1 ± 20.31
p1	-0.003966 ± 0.01819

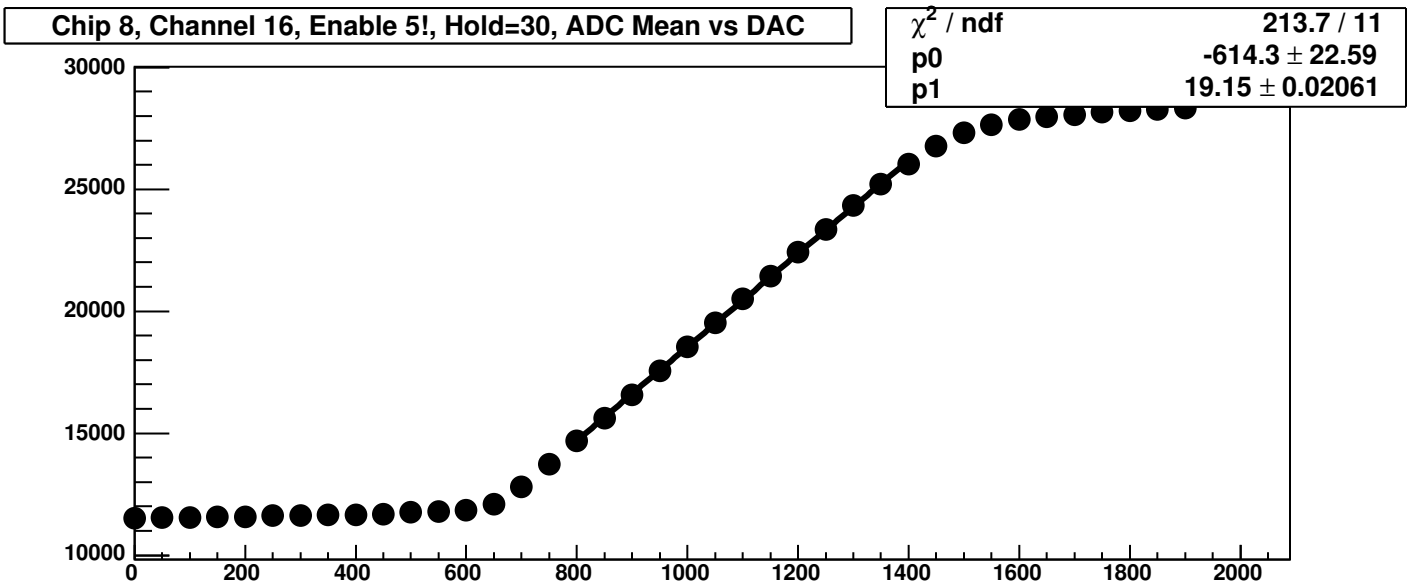


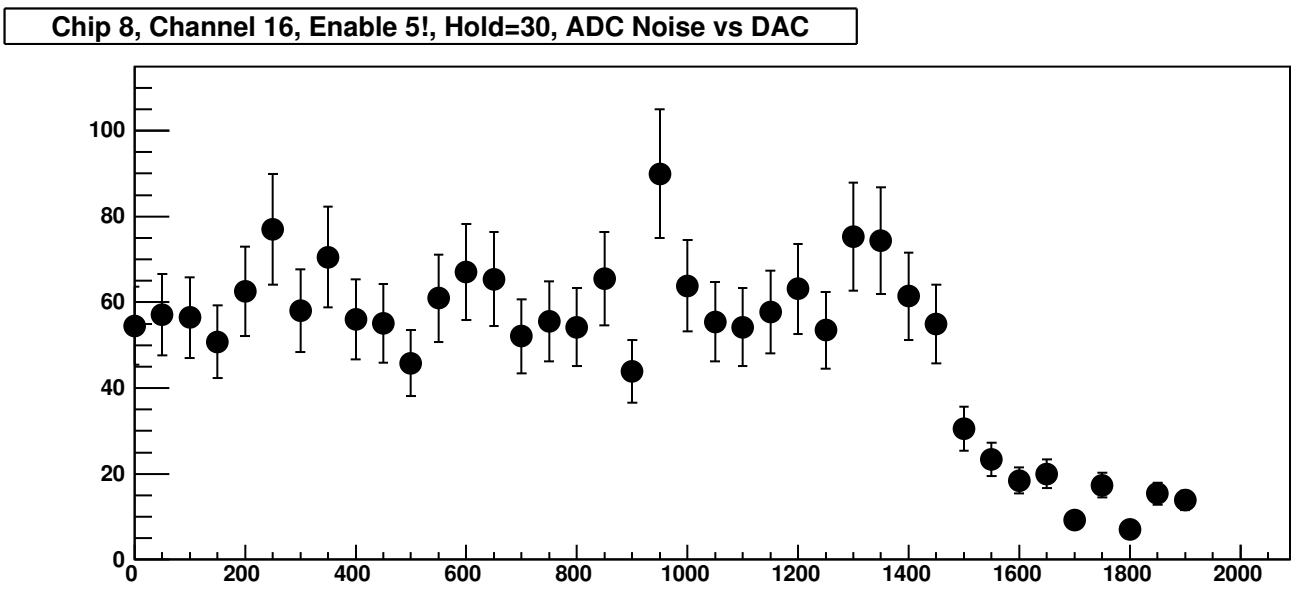
Chip 8, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC

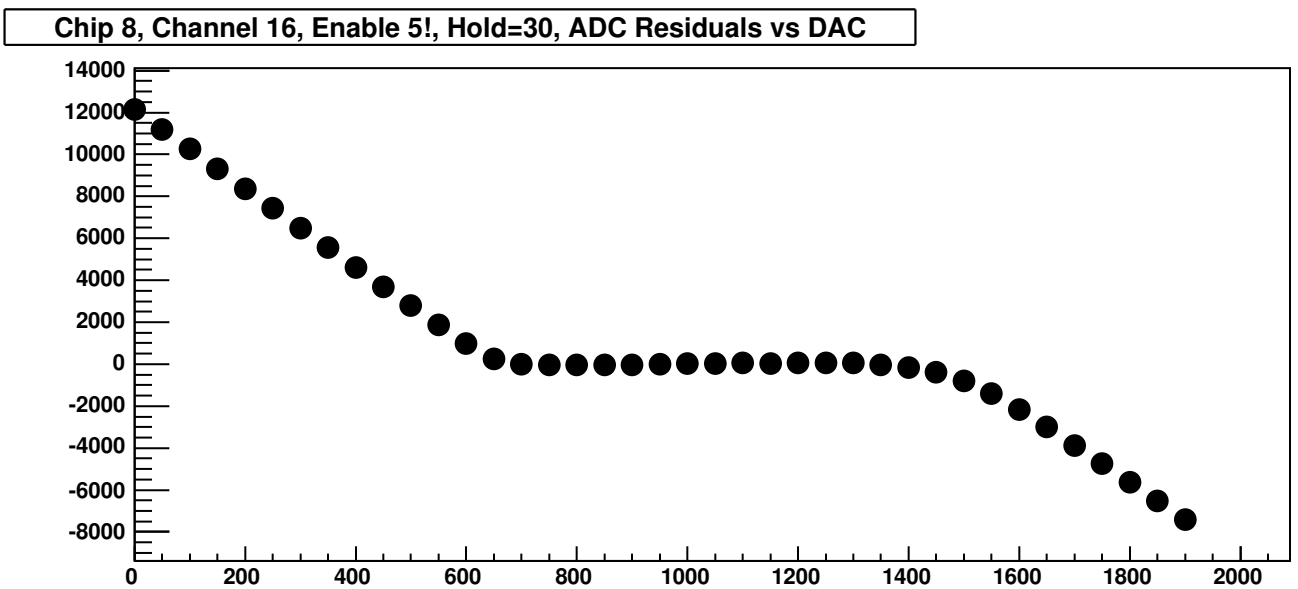


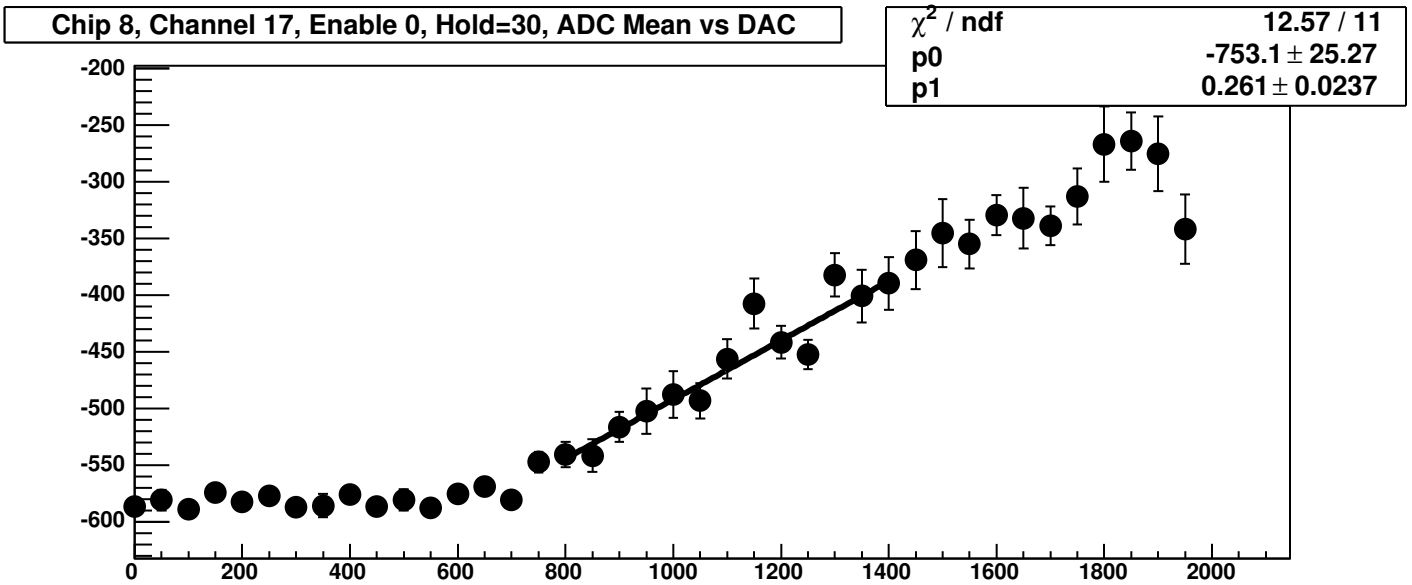
Chip 8, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC

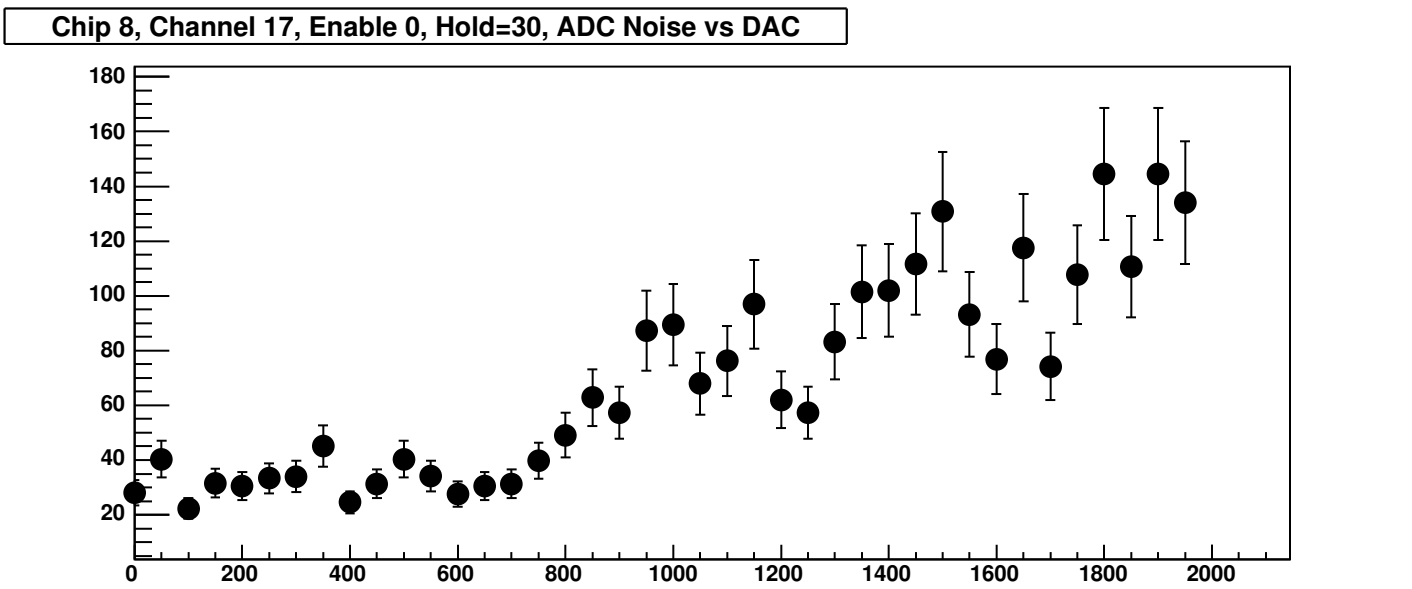


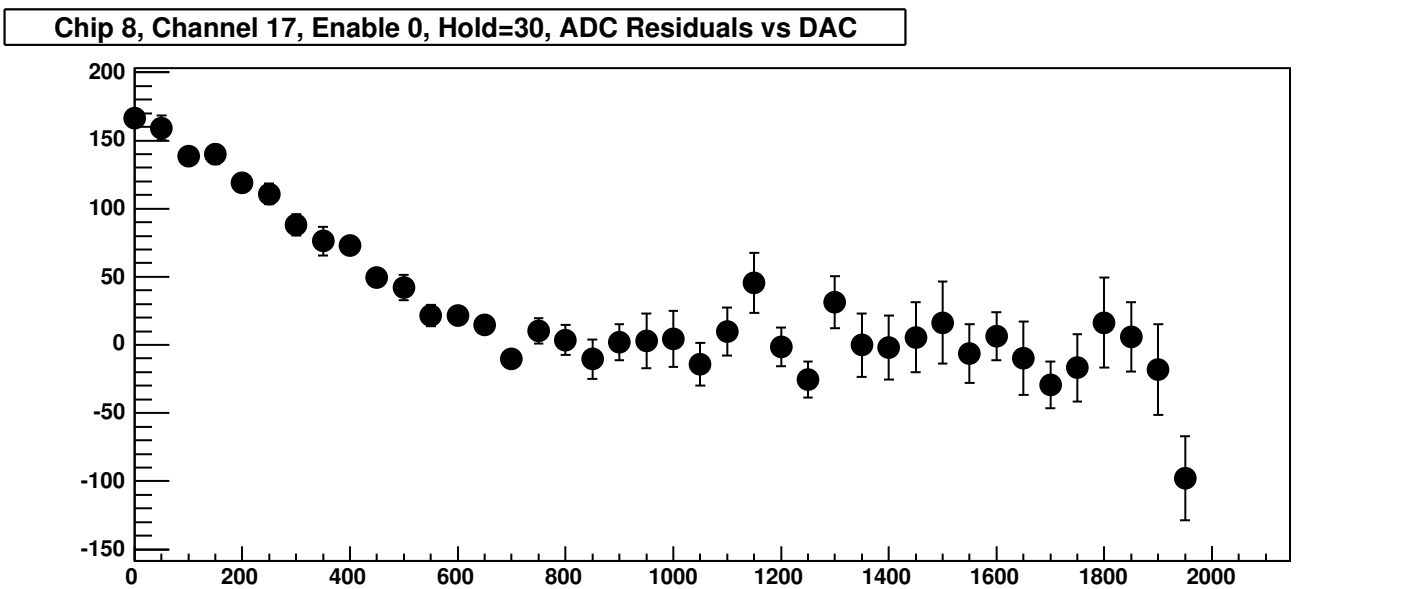

Chip 8, Channel 16, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 213.7 / 11
p0 -614.3 ± 22.59
p1 19.15 ± 0.02061
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

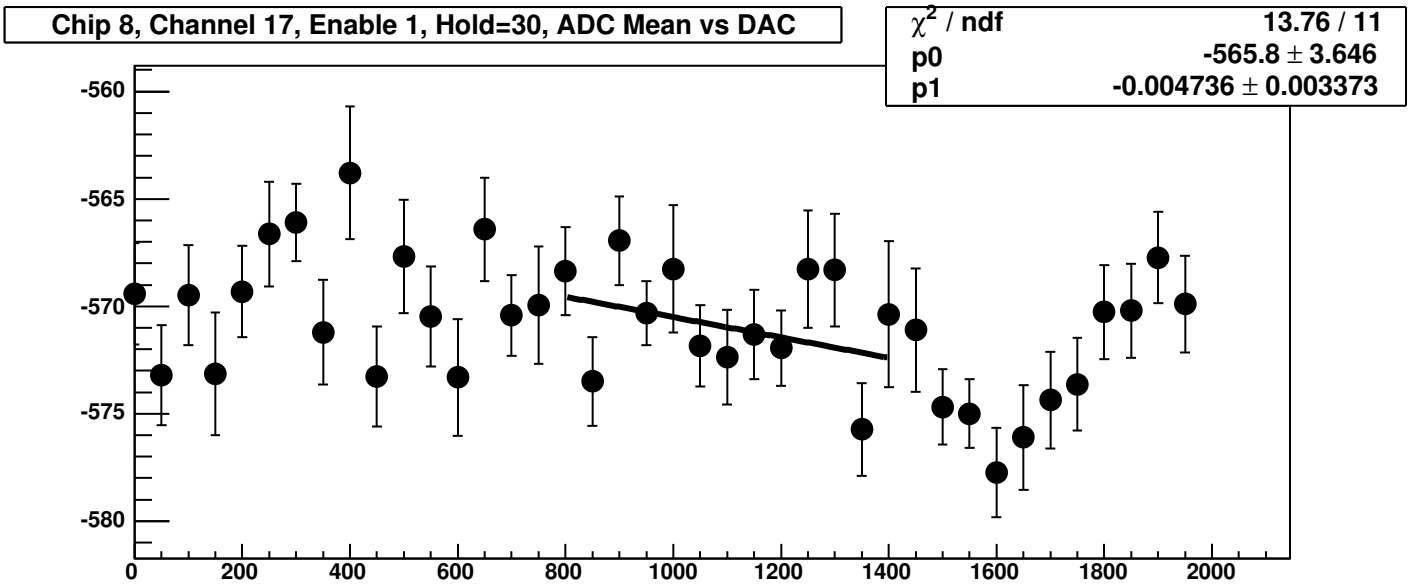

Chip 8, Channel 16, Enable 5!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

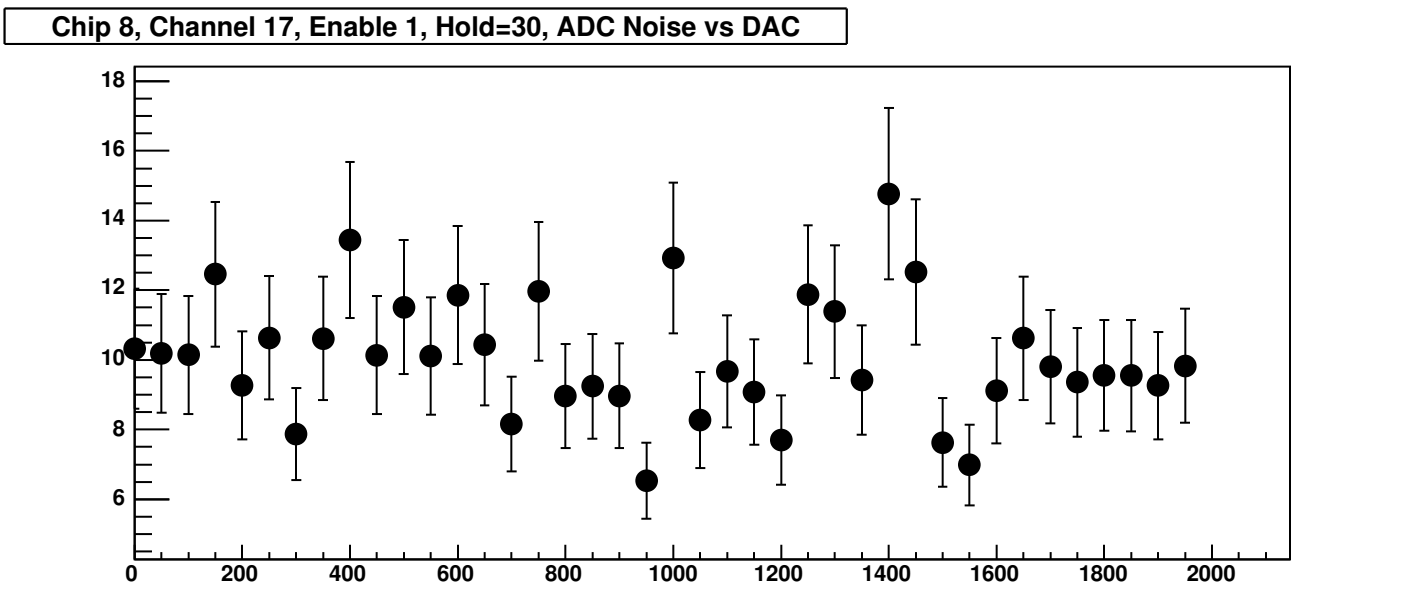

Chip 8, Channel 16, Enable 5!, Hold=30, ADC Residuals vs DAC
14000
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

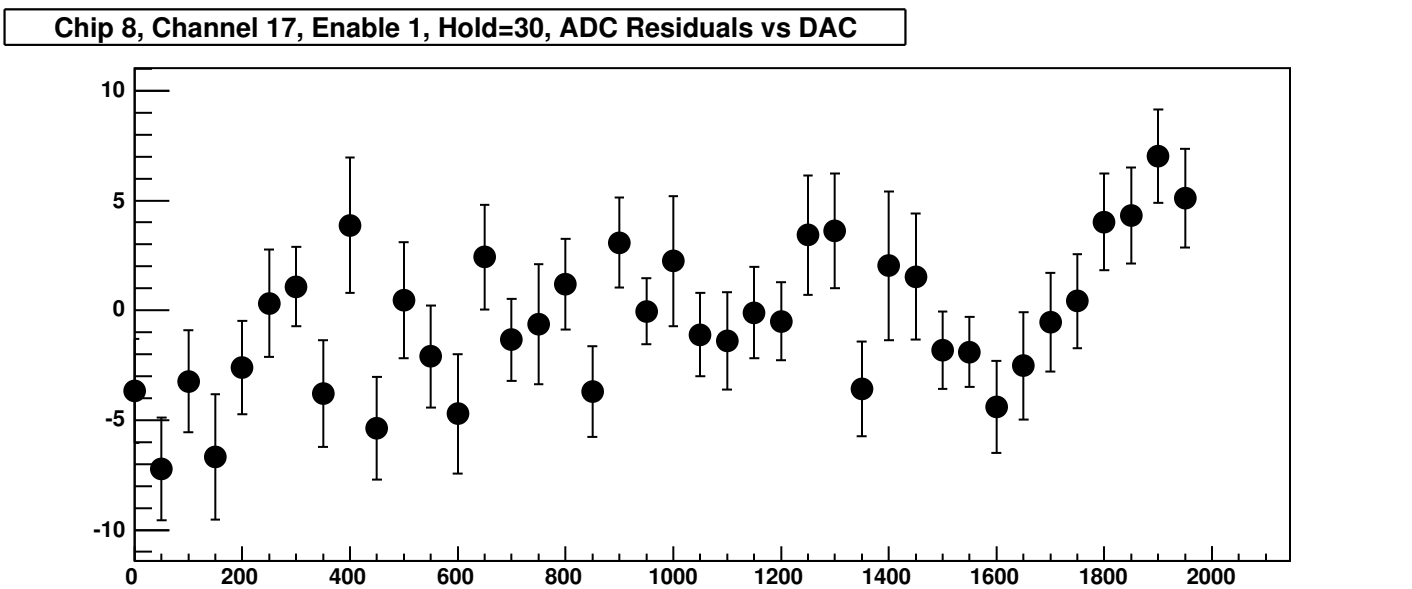

Chip 8, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 12.57 / 11
p0 -753.1 ± 25.27
p1 0.261 ± 0.0237
-200
-250
-300
-350
-400
-450
-500
-550
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

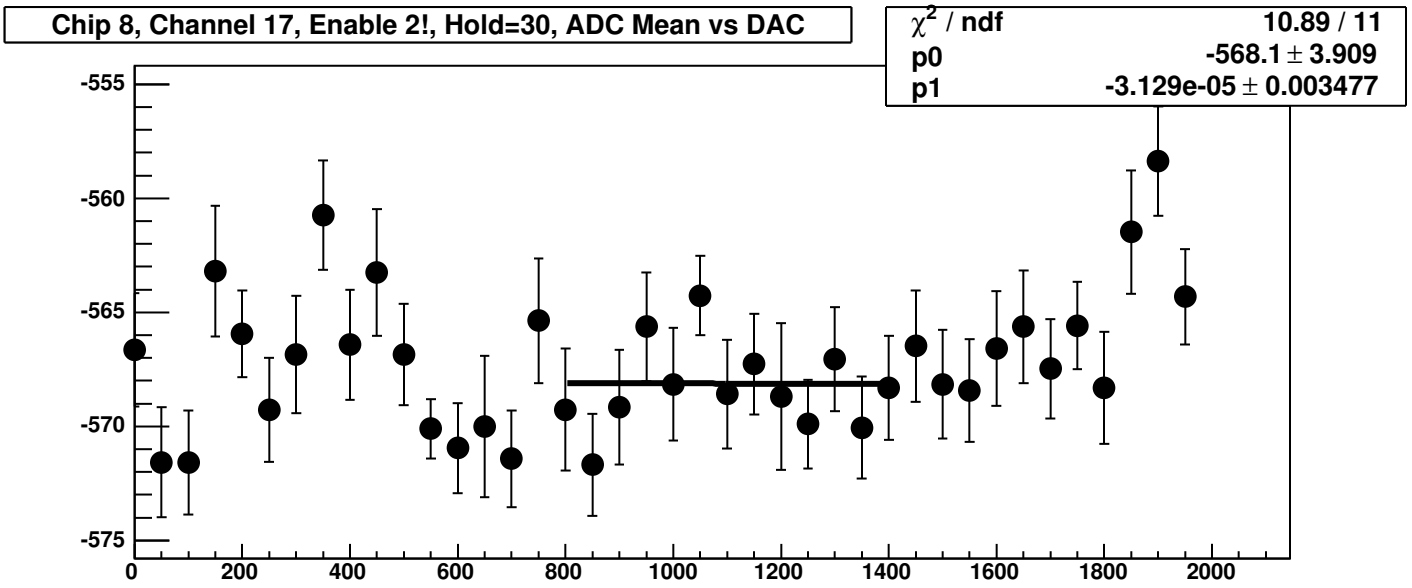

Chip 8, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

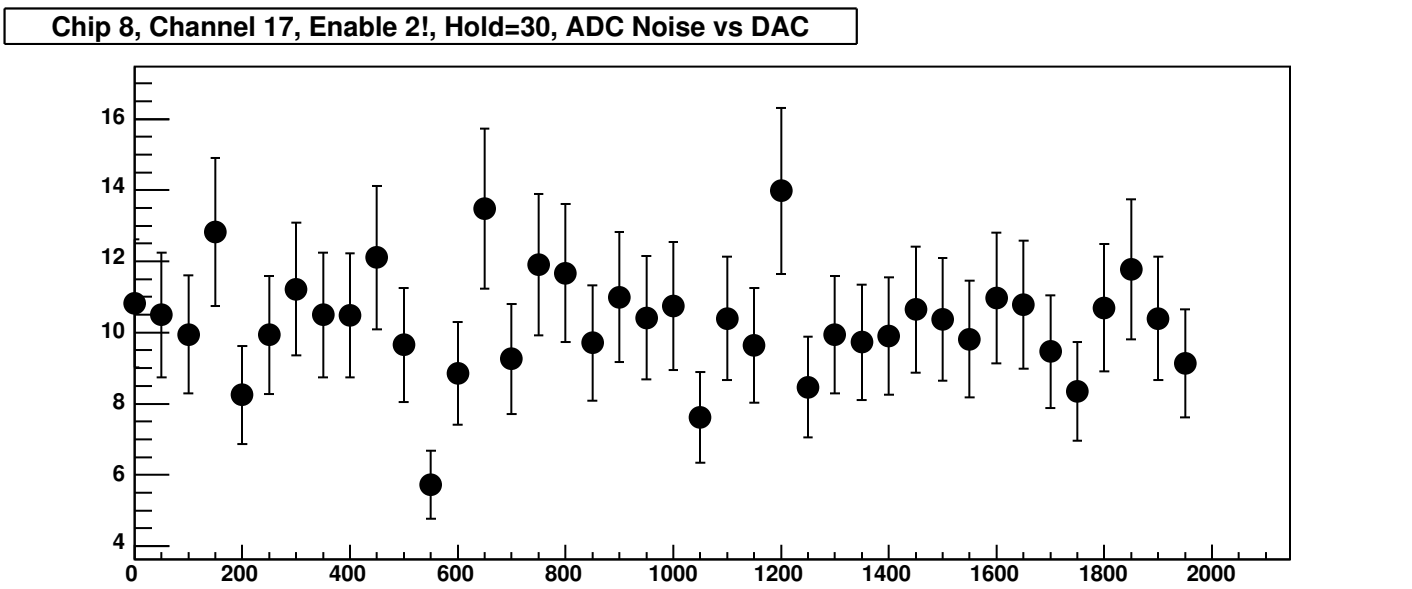

Chip 8, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
0
200
400
600
800
1000
1200
1400
1600
1800
2000

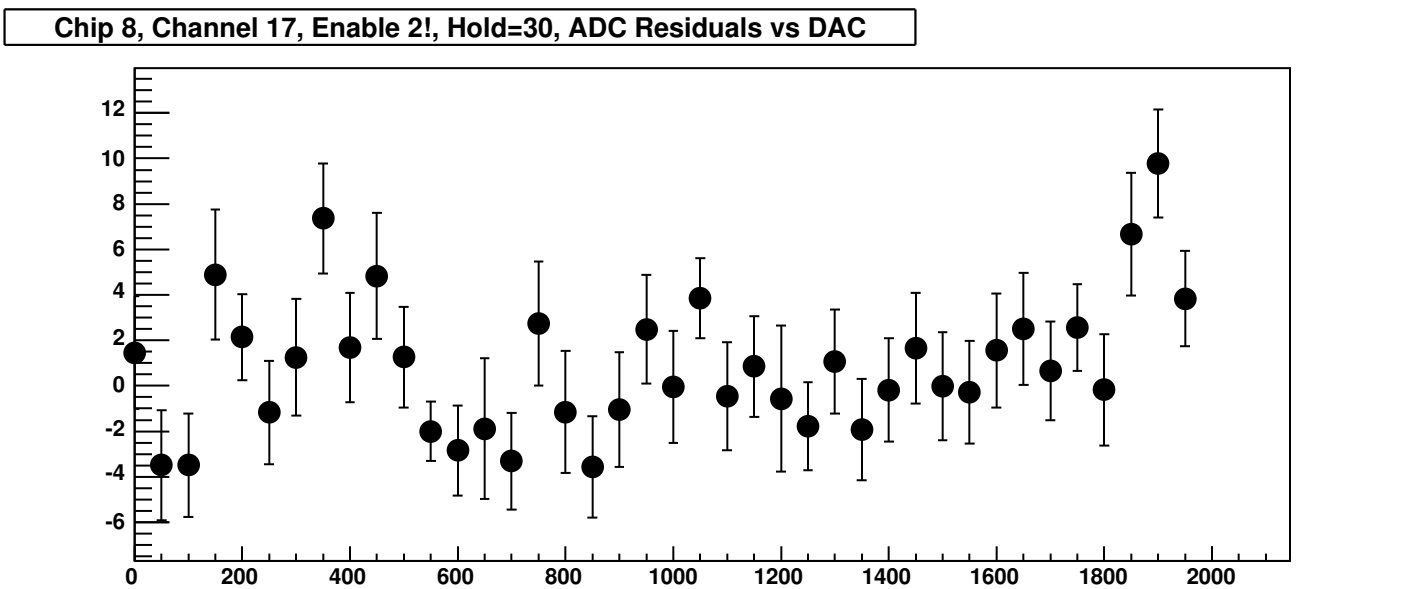

Chip 8, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 13.76 / 11
p0 -565.8 ± 3.646
p1 -0.004736 ± 0.003373
-560
-565
-570
-575
-580
0
200
400
600
800
1000
1200
1400
1600
1800
2000

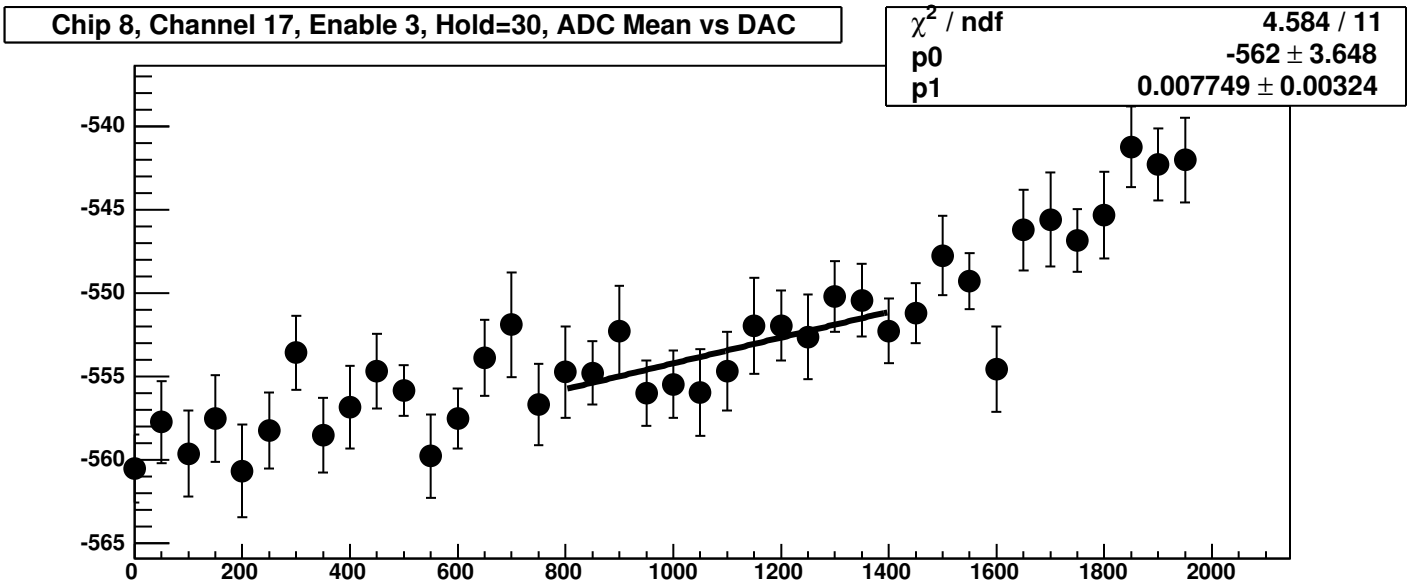

Chip 8, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
18
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

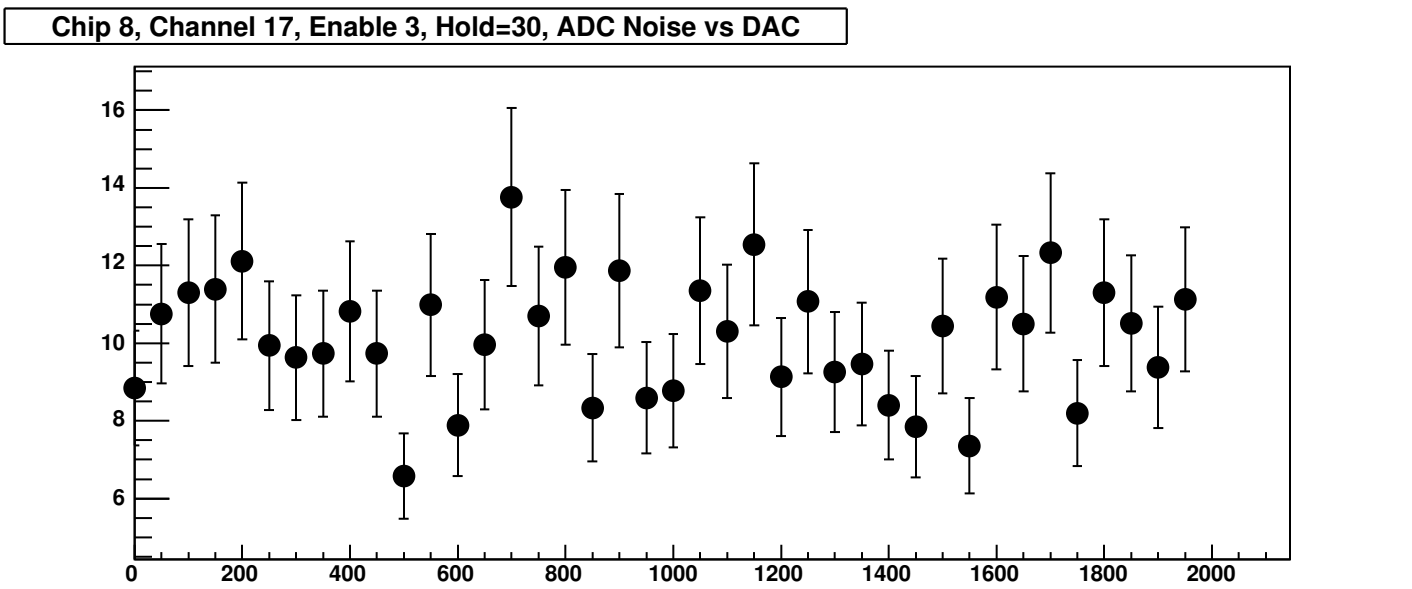

Chip 8, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

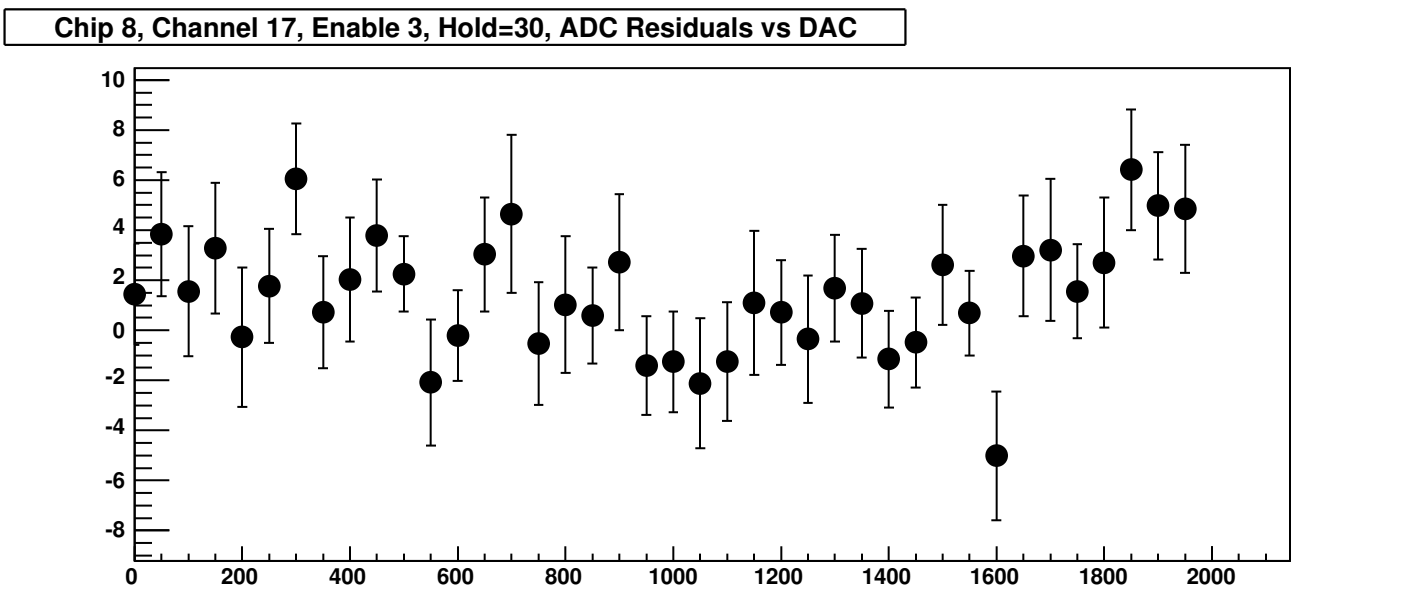

Chip 8, Channel 17, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 10.89 / 11
p0 -568.1 ± 3.909
p1 -3.129e-05 ± 0.003477
-555
-560
-565
-570
-575
0
200
400
600
800
1000
1200
1400
1600
1800
2000

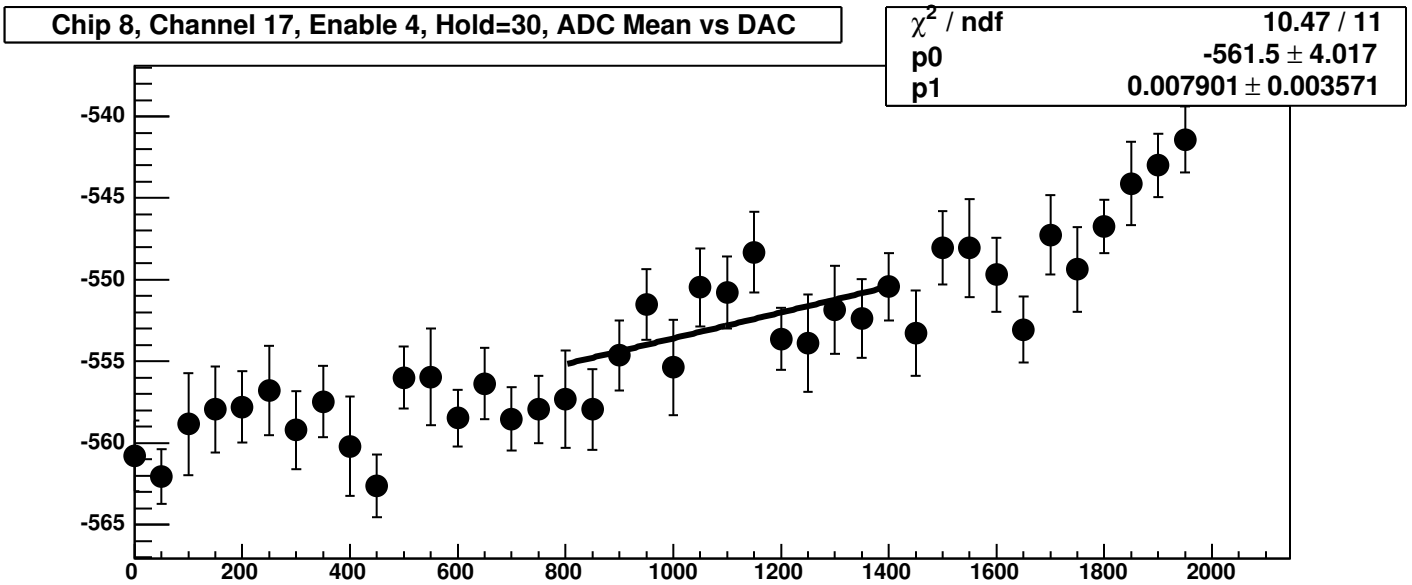

Chip 8, Channel 17, Enable 2!, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
4
0
200
400
600
800
1000
1200
1400
1600
1800
2000

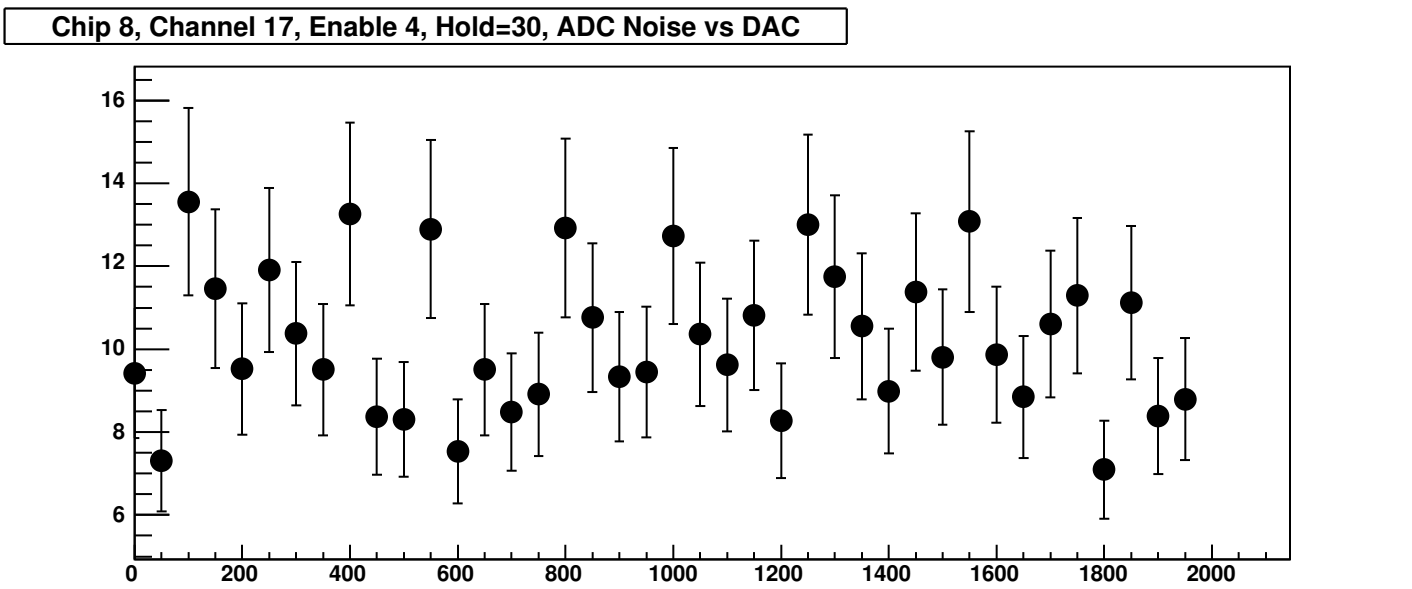

Chip 8, Channel 17, Enable 2!, Hold=30, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

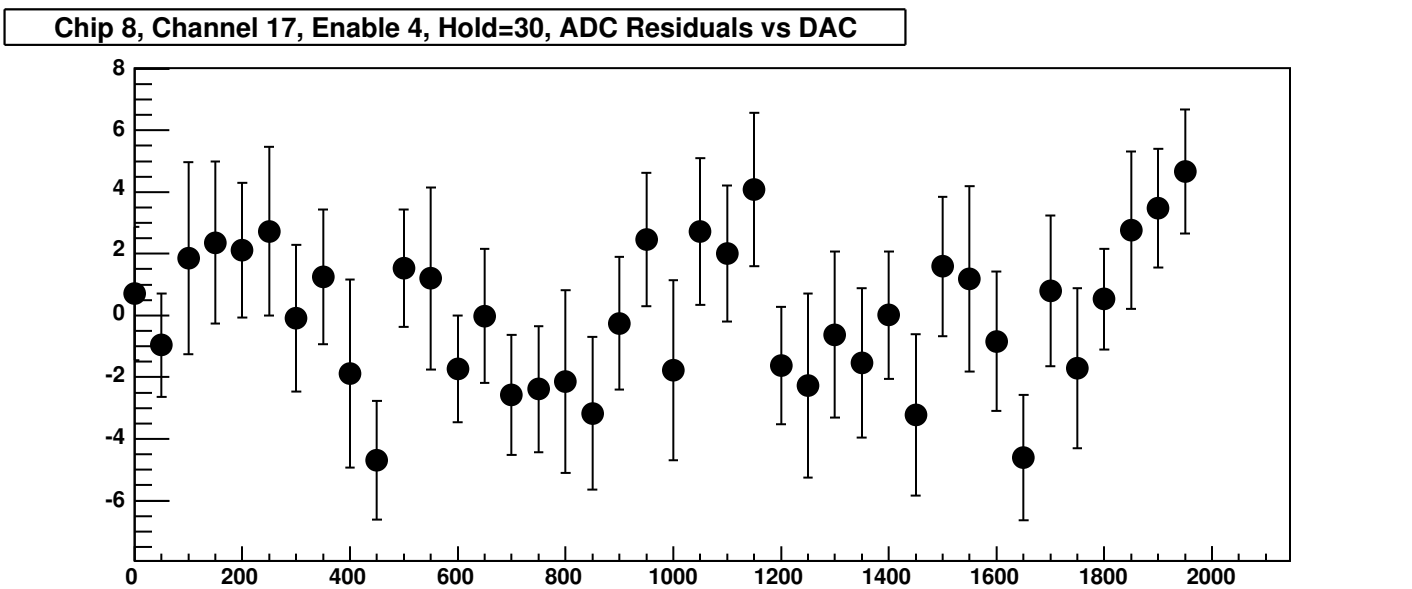

Chip 8, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4.584 / 11
p0 -562 ± 3.648
p1 0.007749 ± 0.00324
-540
-545
-550
-555
-560
-565
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

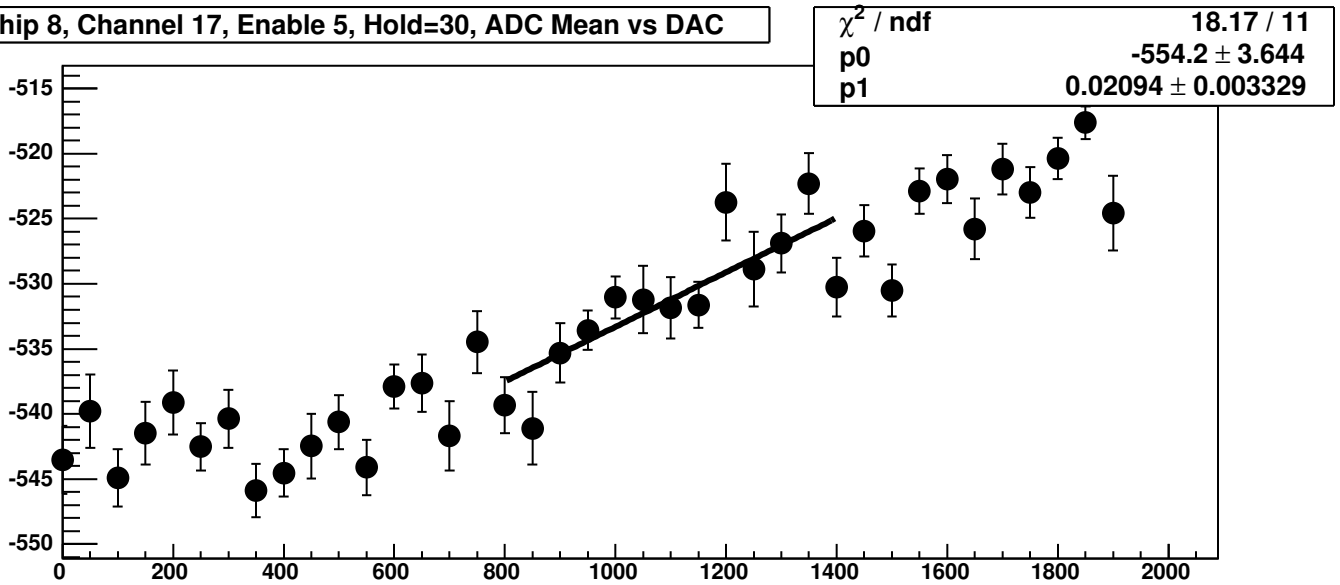

Chip 8, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 10.47 / 11
p0 -561.5 ± 4.017
p1 0.007901 ± 0.003571
-540
-545
-550
-555
-560
-565
0
200
400
600
800
1000
1200
1400
1600
1800
2000

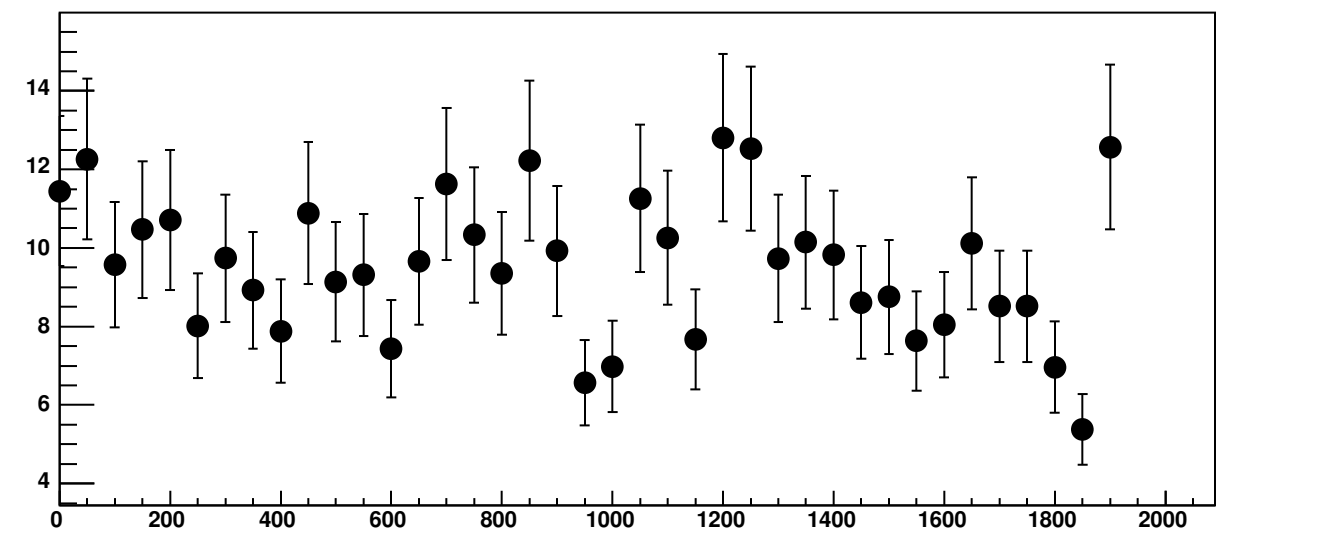

Chip 8, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 8, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

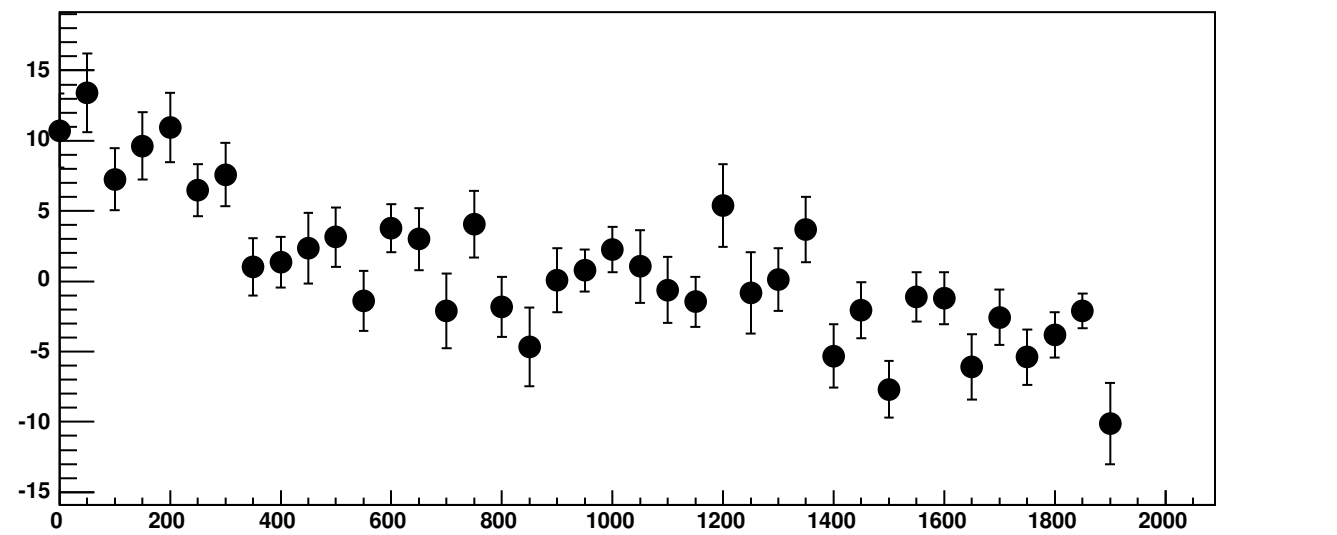
Chip 8, Channel 17, Enable 5, Hold=30, ADC Mean vs DAC

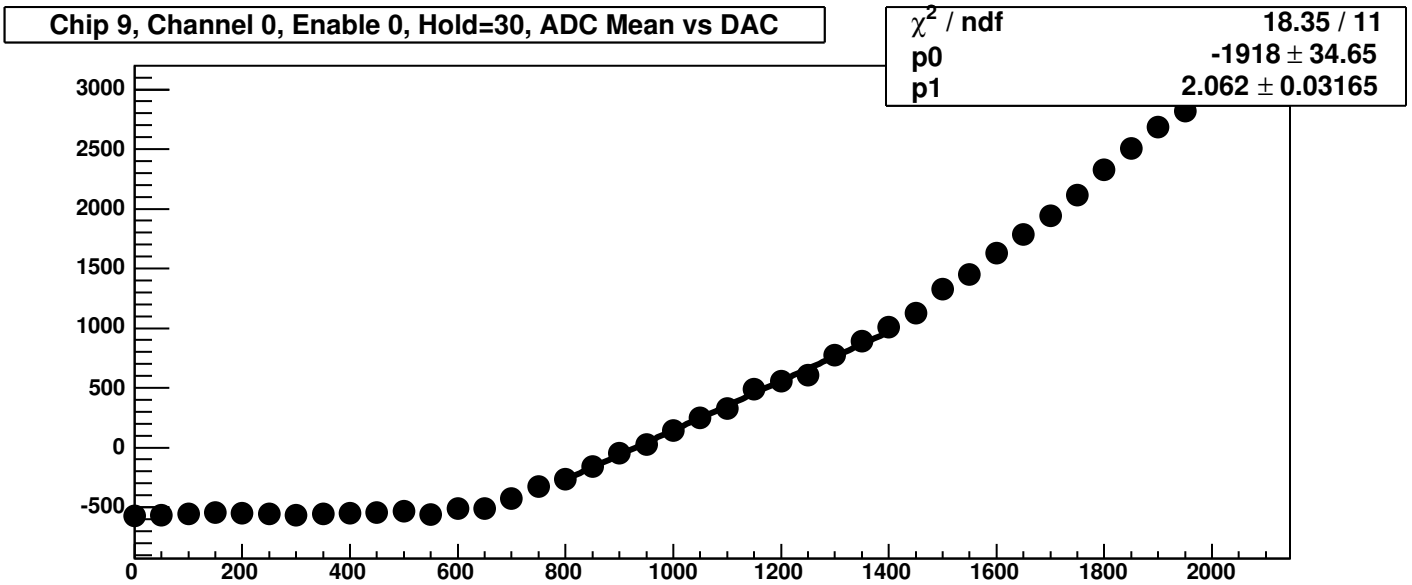


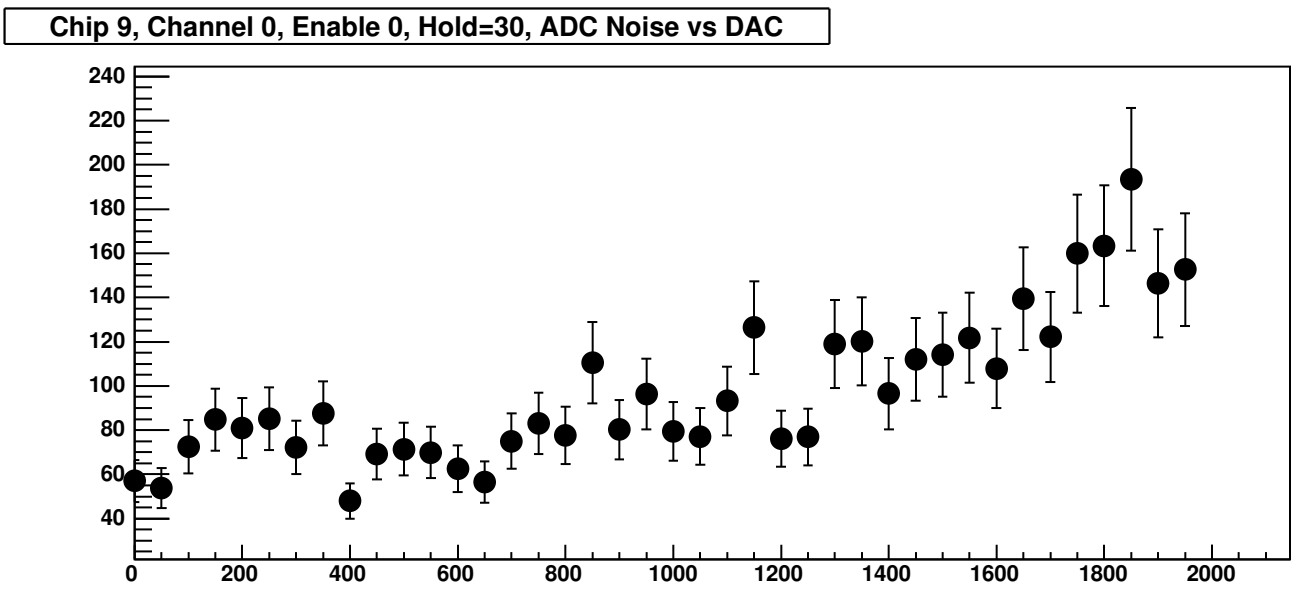
Chip 8, Channel 17, Enable 5, Hold=30, ADC Noise vs DAC

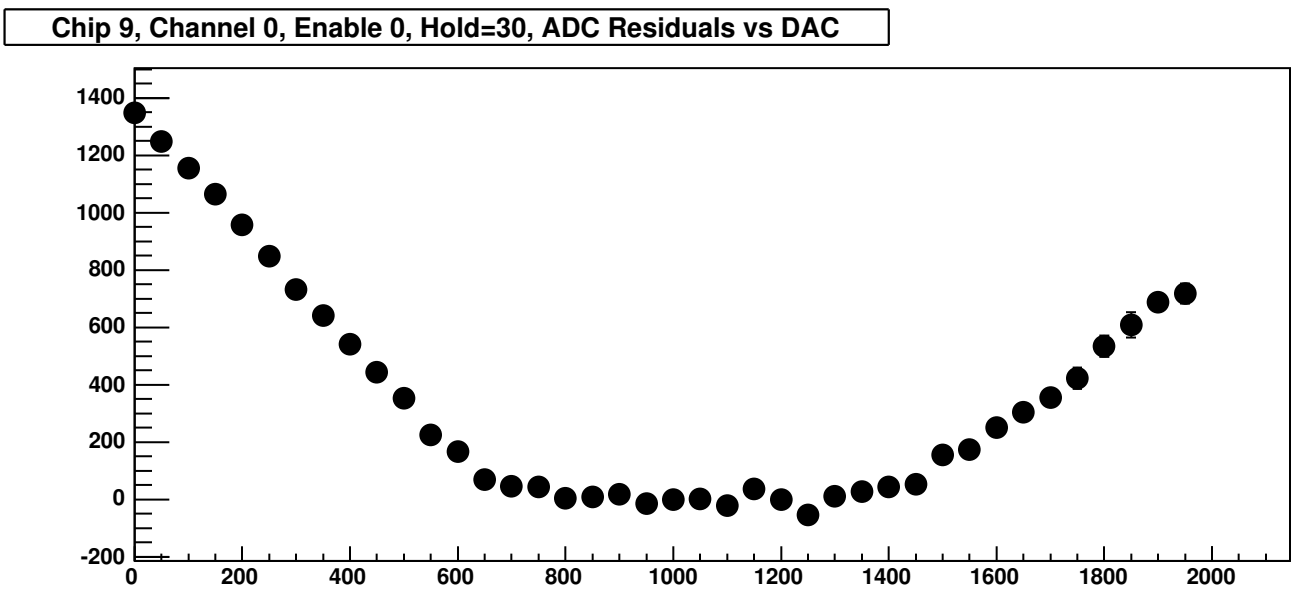


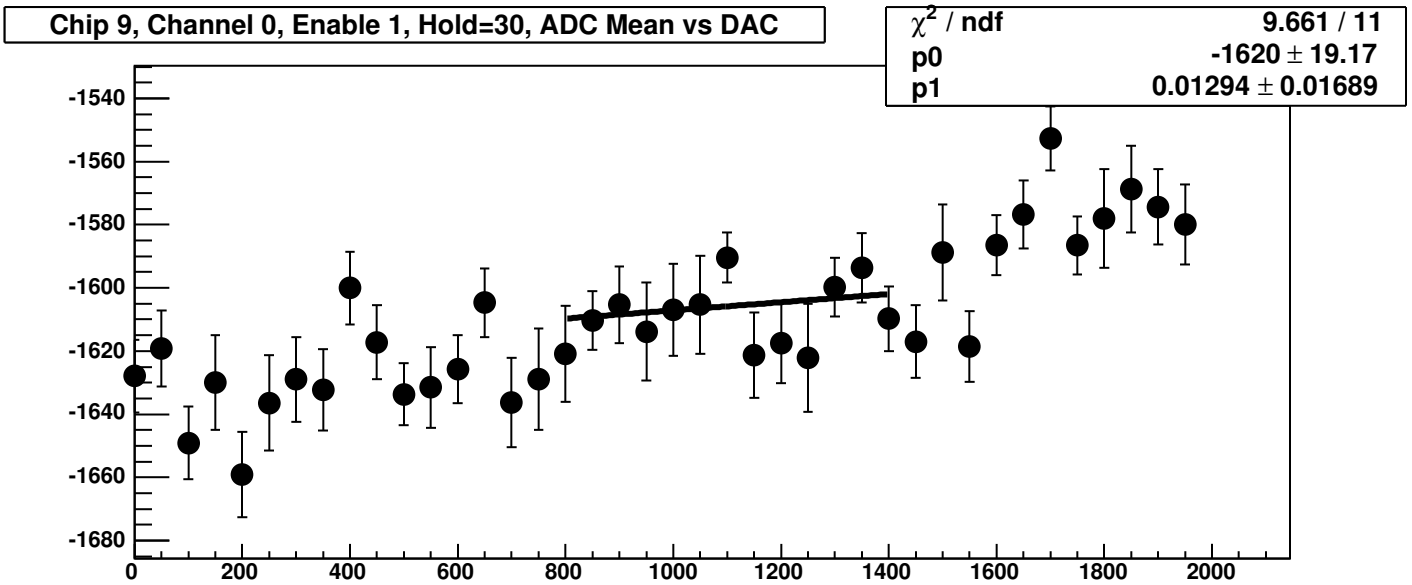
Chip 8, Channel 17, Enable 5, Hold=30, ADC Residuals vs DAC

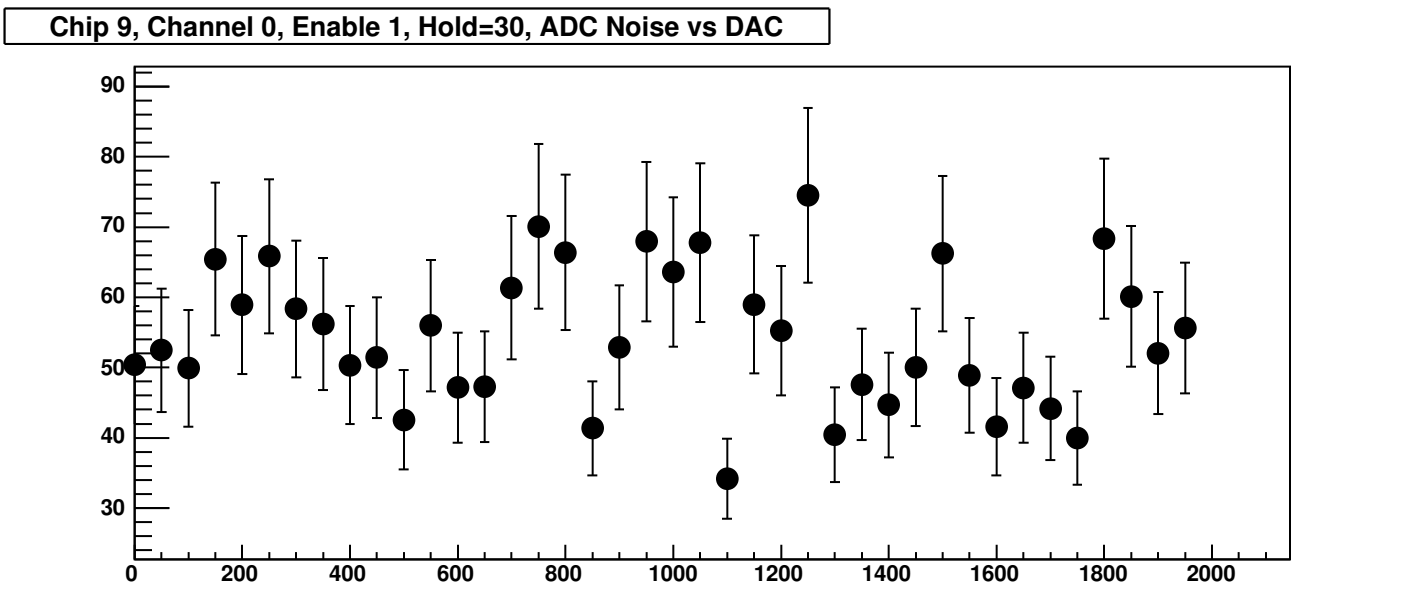


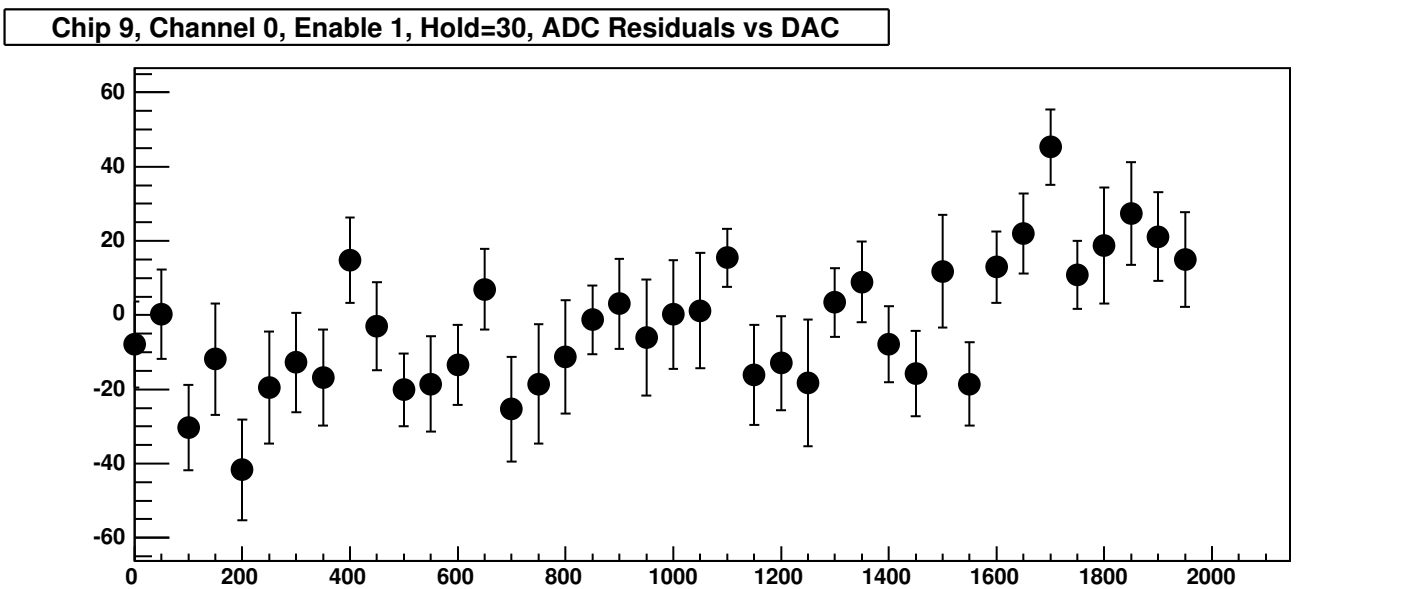

Chip 9, Channel 0, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 18.35 / 11
p0 -1918 ± 34.65
p1 2.062 ± 0.03165
3000
2500
2000
1500
1000
500
0
-500
0 200 400 600 800 1000 1200 1400 1600 1800 2000

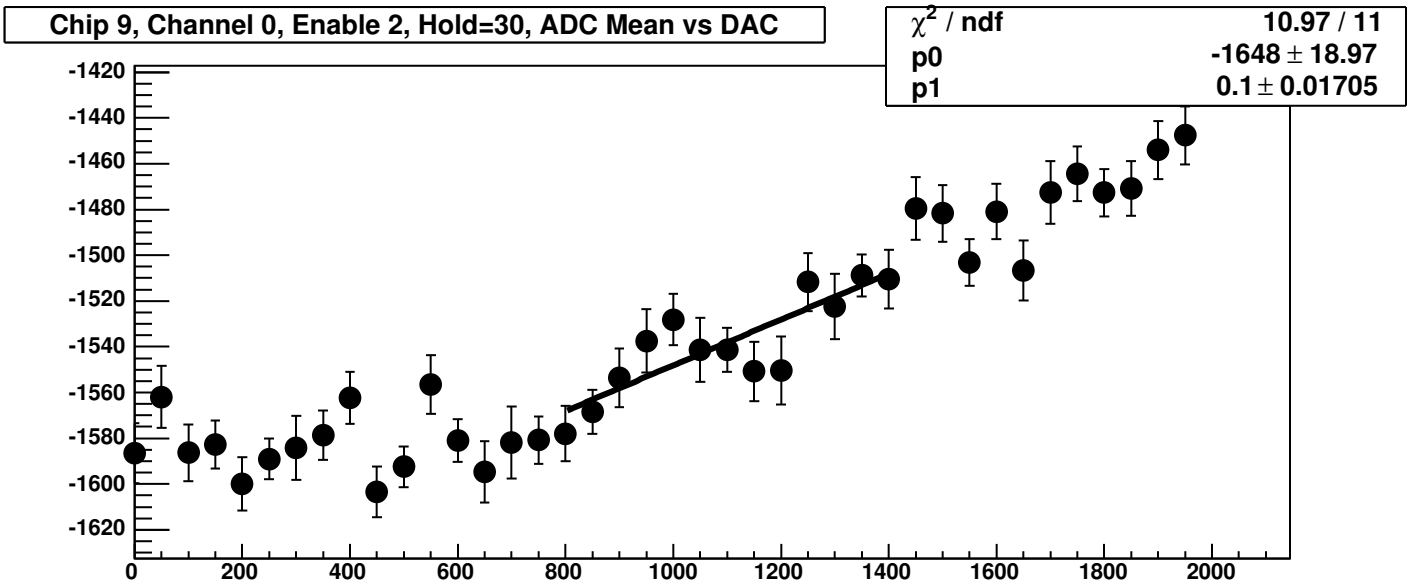

Chip 9, Channel 0, Enable 0, Hold=30, ADC Noise vs DAC
240
220
200
180
160
140
120
100
80
60
40
0 200 400 600 800 1000 1200 1400 1600 1800 2000

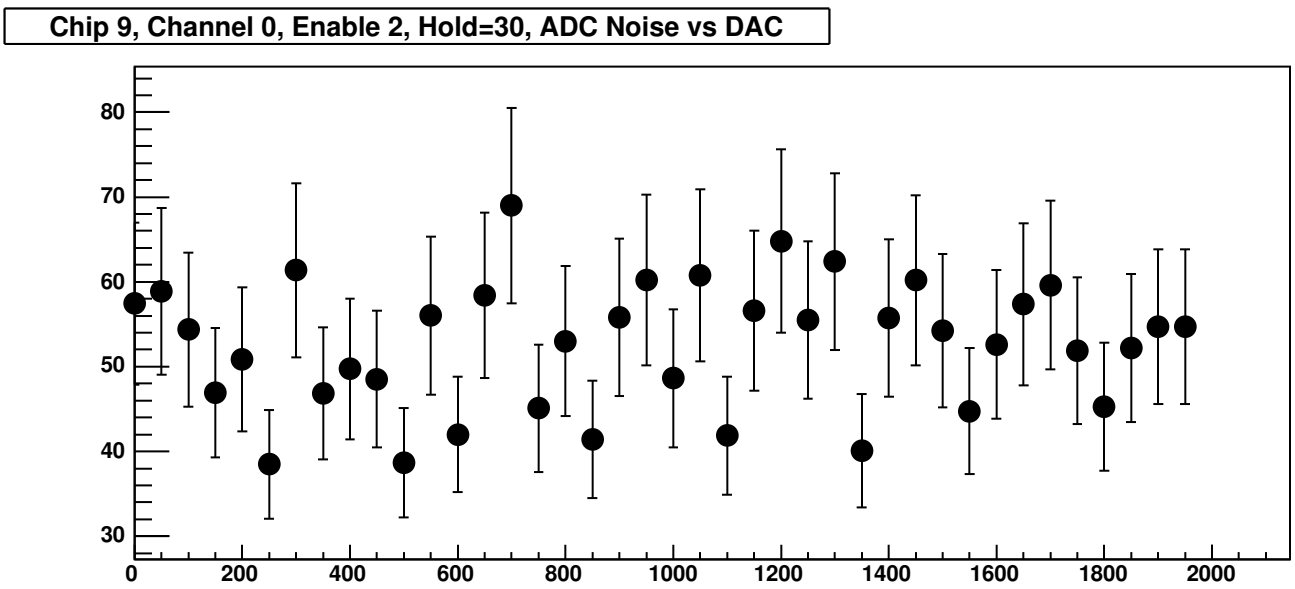

Chip 9, Channel 0, Enable 0, Hold=30, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
0 200 400 600 800 1000 1200 1400 1600 1800 2000

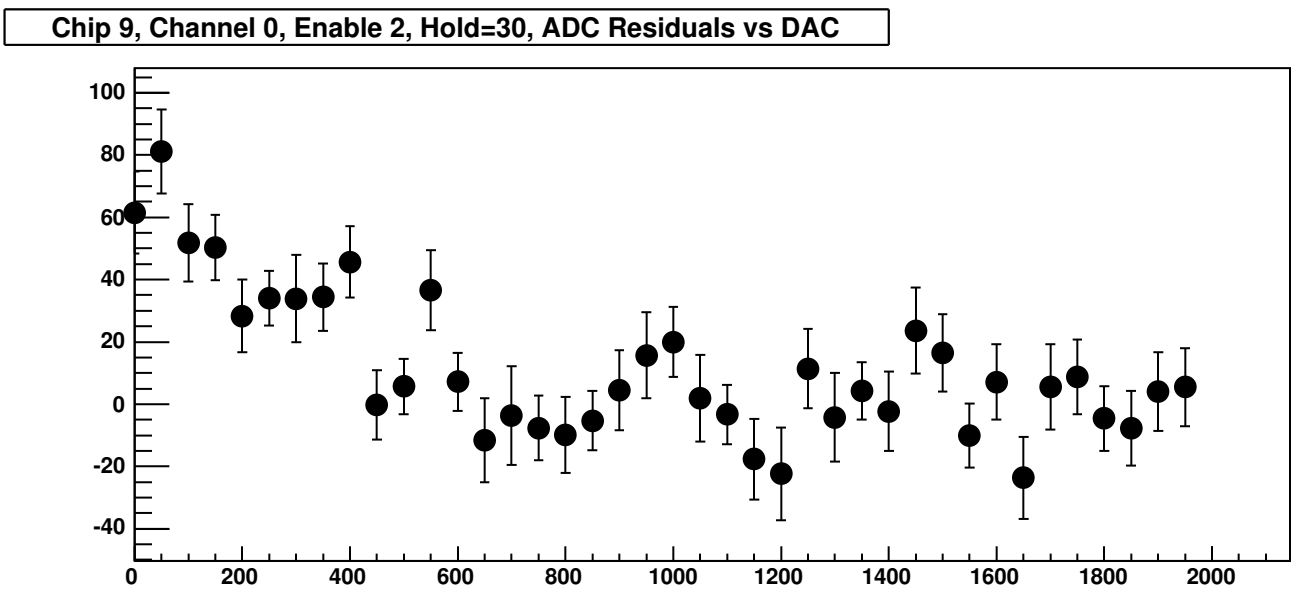

Chip 9, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 9.661 / 11
p0 -1620 ± 19.17
p1 0.01294 ± 0.01689
-1540
-1560
-1580
-1600
-1620
-1640
-1660
-1680
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

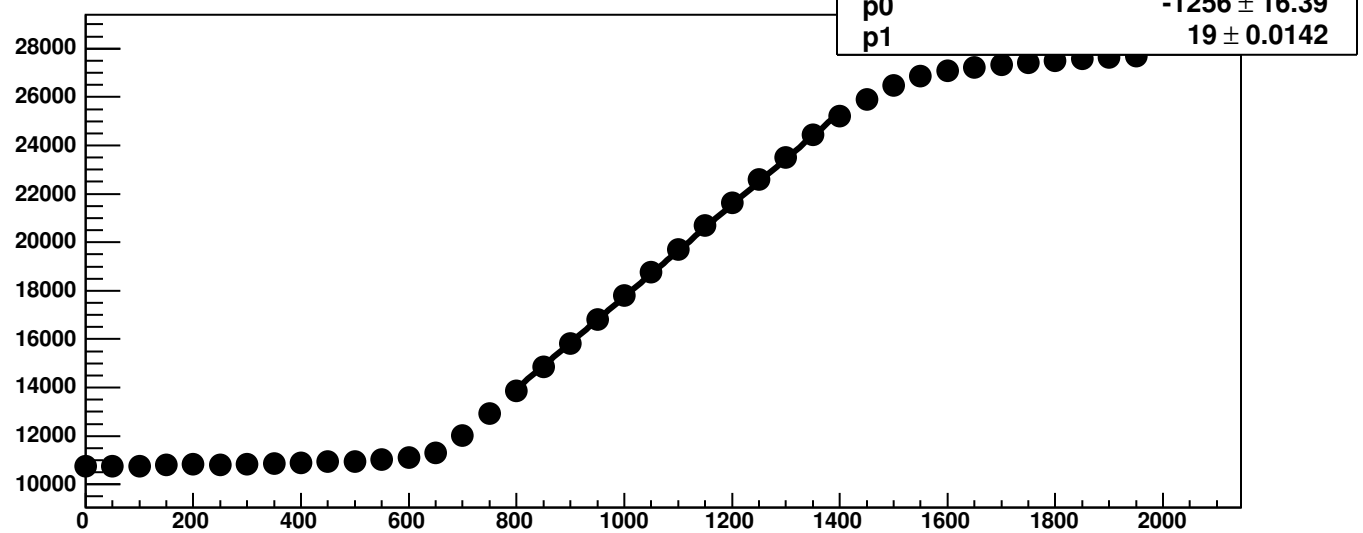

Chip 9, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 10.97 / 11
p0 -1648 ± 18.97
p1 0.1 ± 0.01705
-1420
-1440
-1460
-1480
-1500
-1520
-1540
-1560
-1580
-1600
-1620
0
200
400
600
800
1000
1200
1400
1600
1800
2000

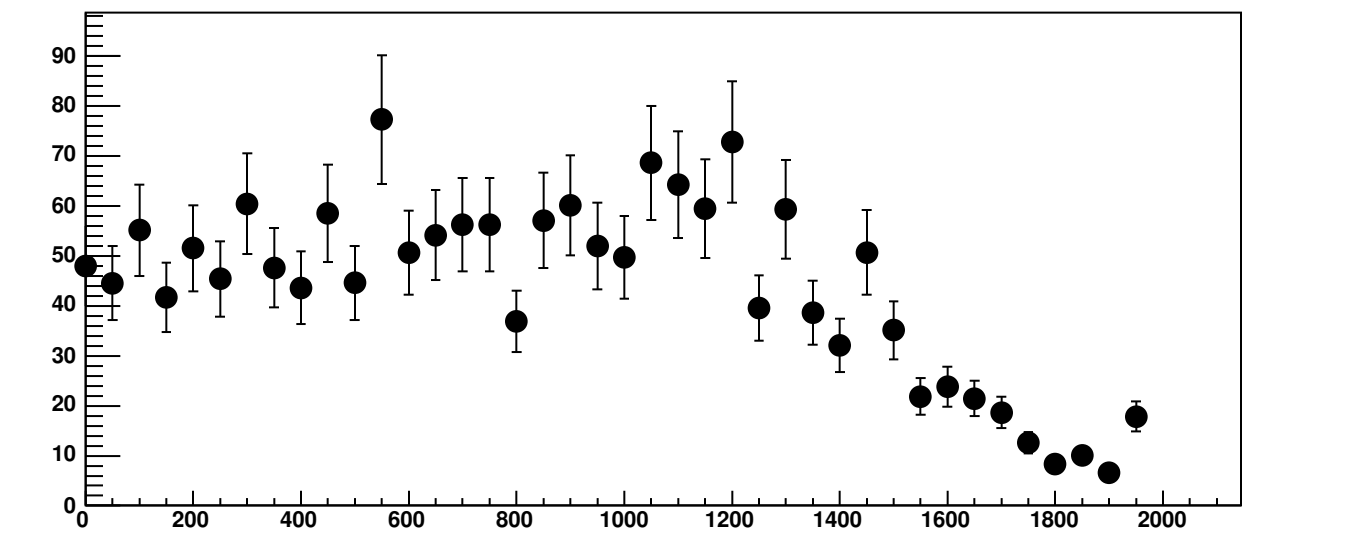

Chip 9, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

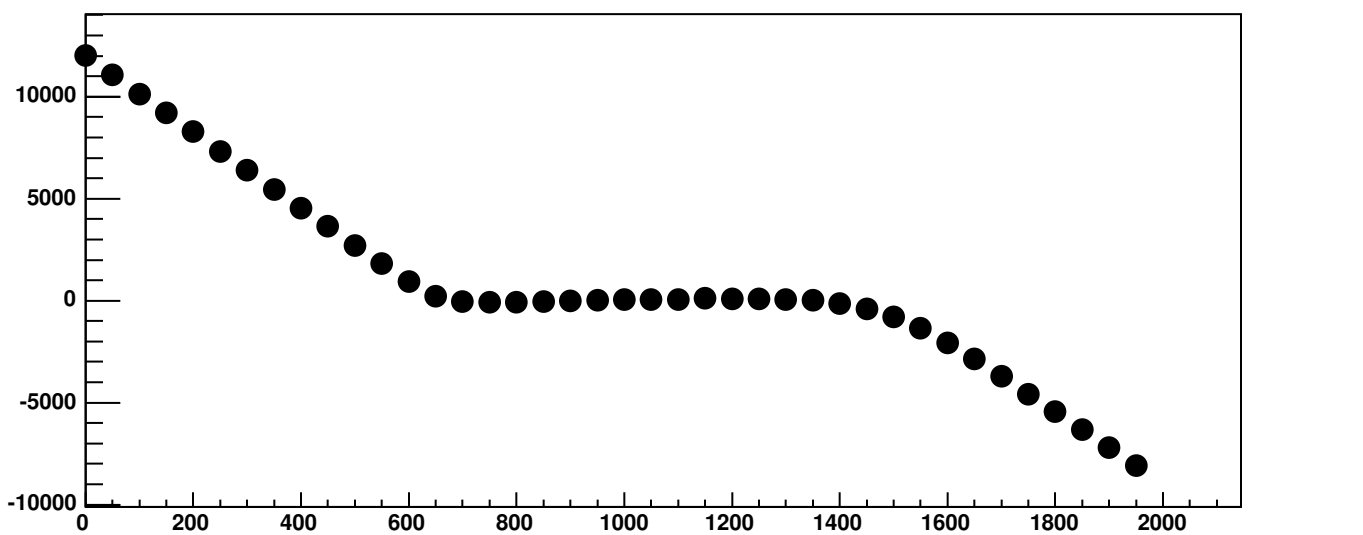
Chip 9, Channel 0, Enable 3!, Hold=30, ADC Mean vs DAC

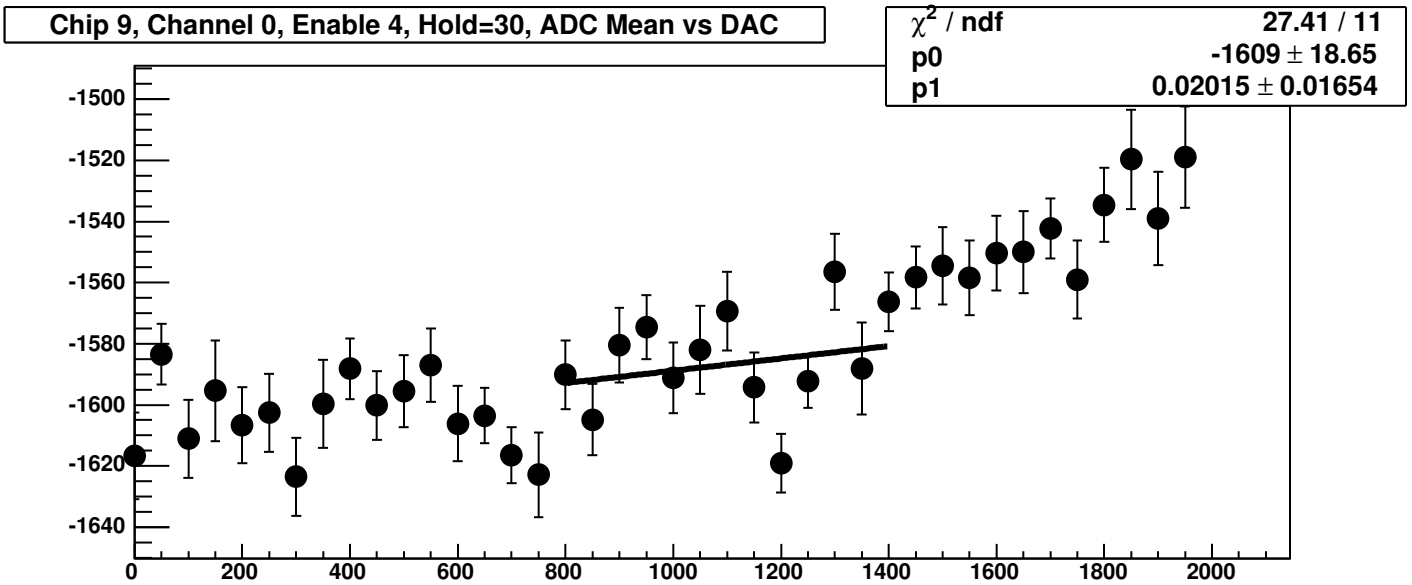


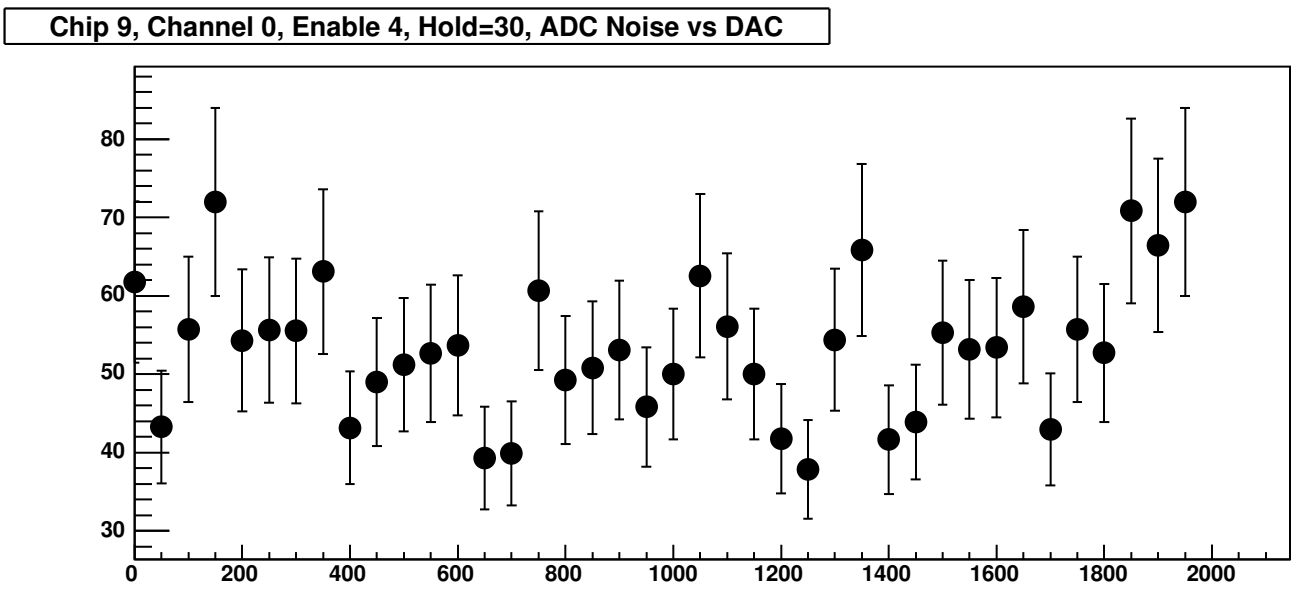
Chip 9, Channel 0, Enable 3!, Hold=30, ADC Noise vs DAC

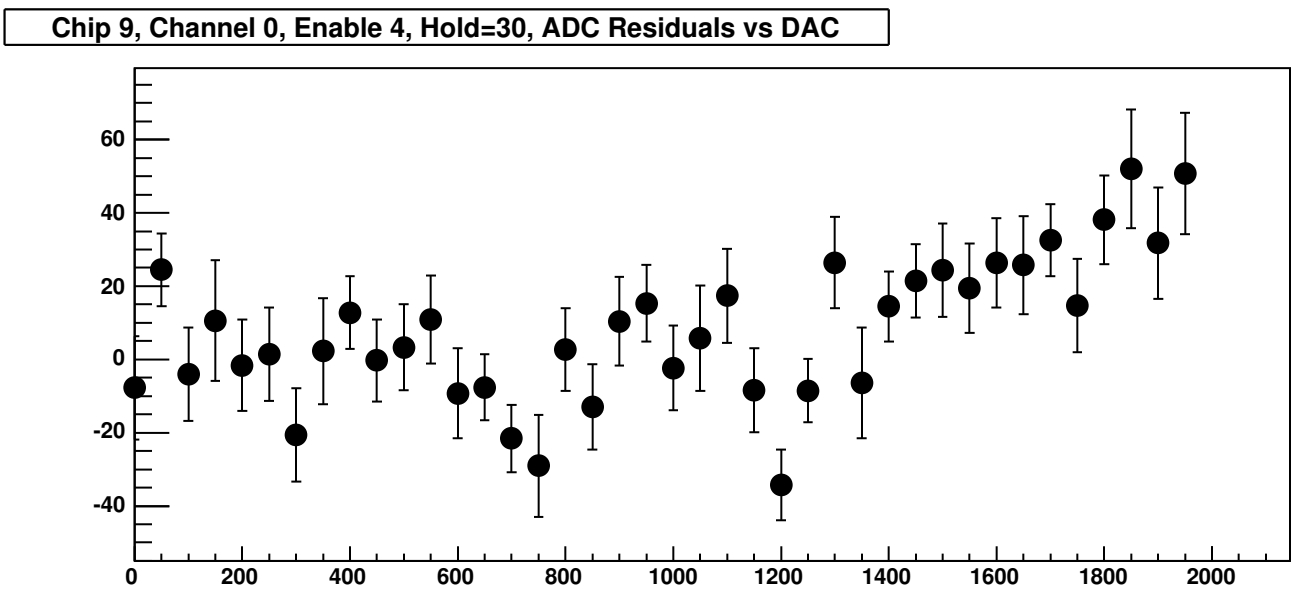


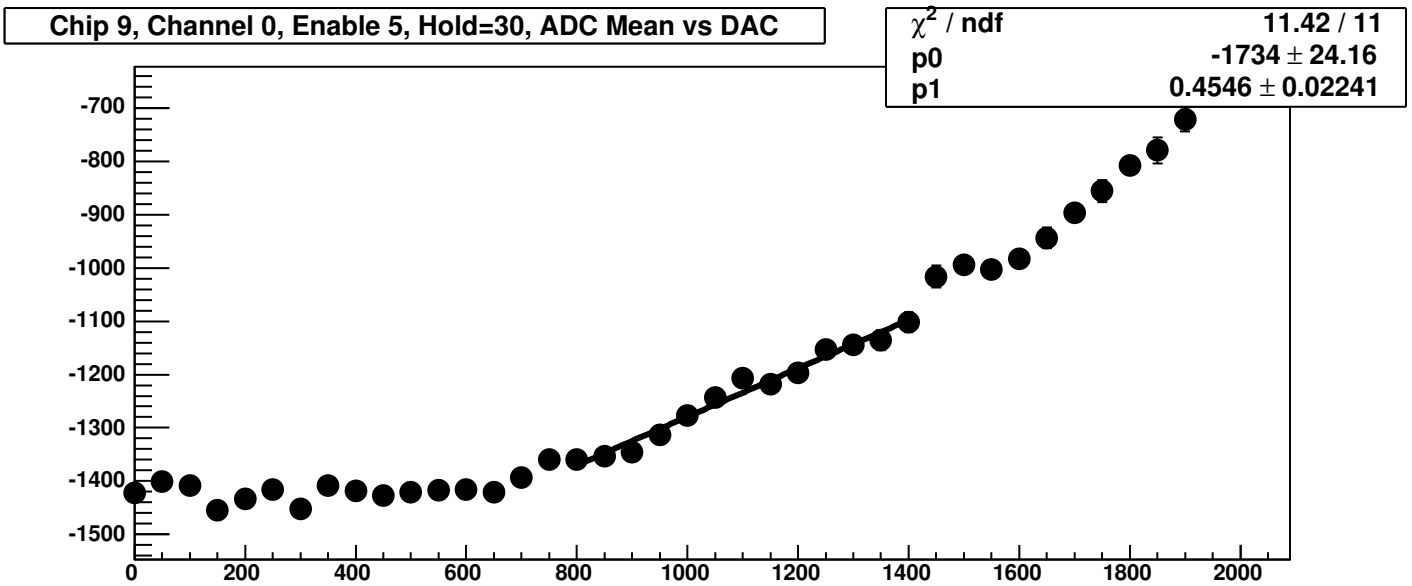
Chip 9, Channel 0, Enable 3!, Hold=30, ADC Residuals vs DAC

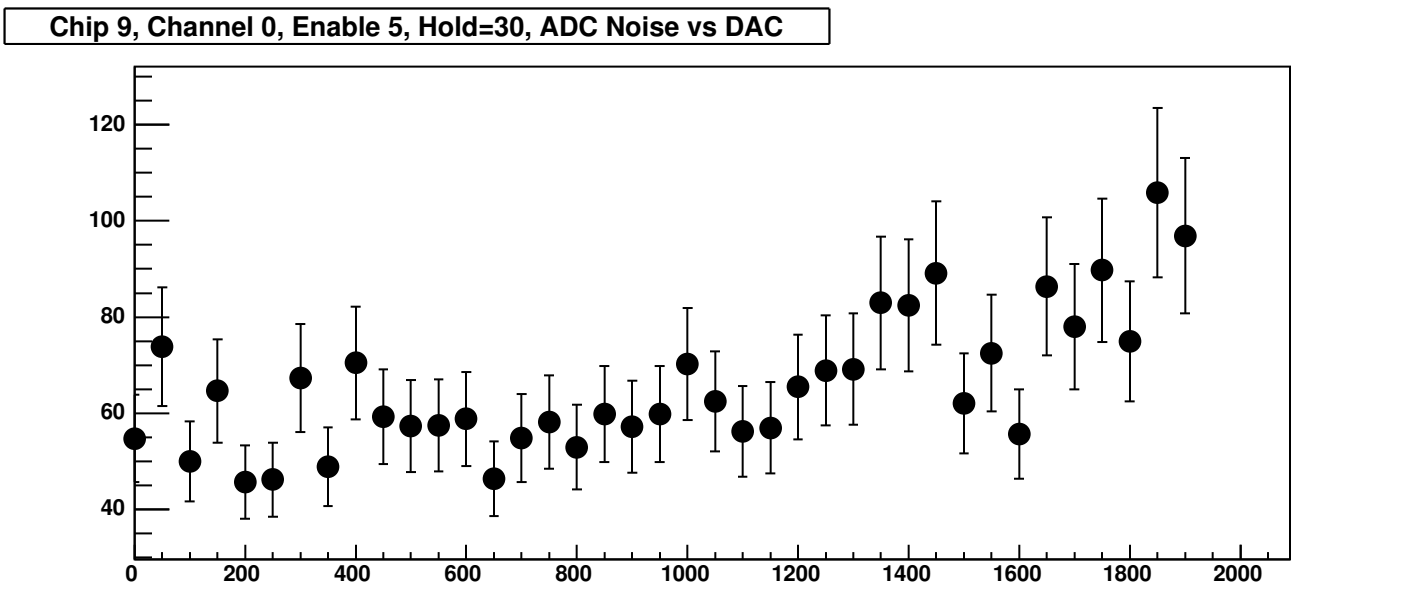


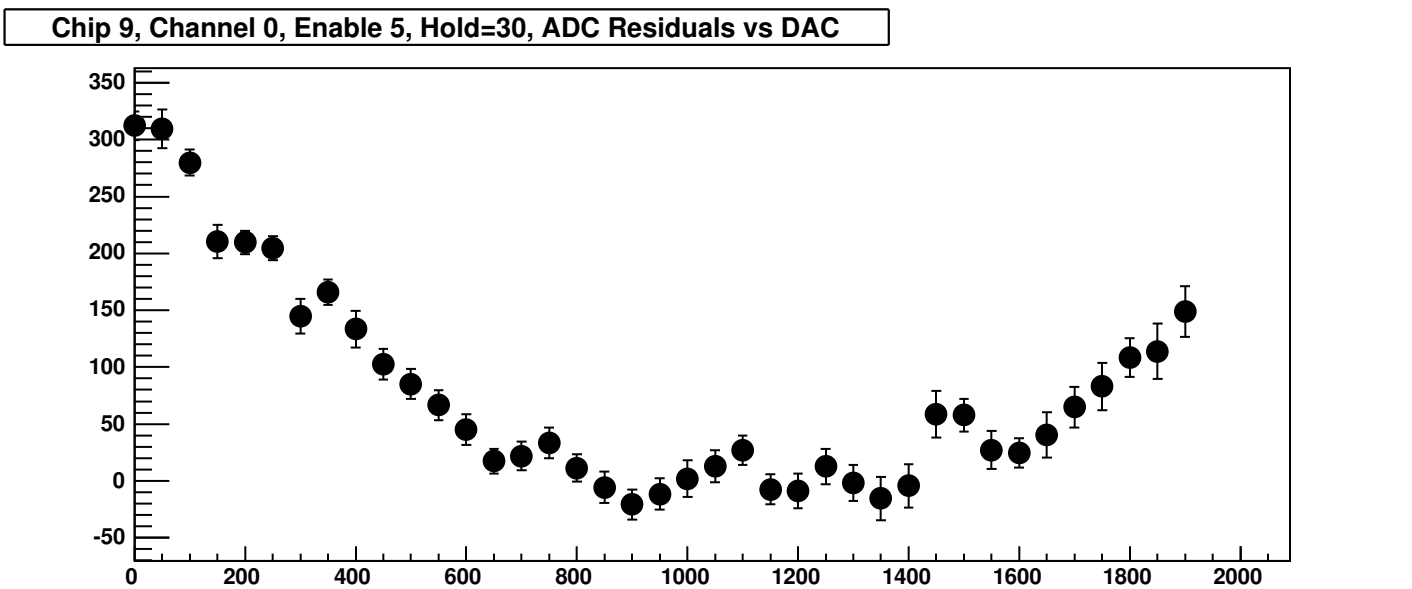

Chip 9, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 27.41 / 11
p0 -1609 ± 18.65
p1 0.02015 ± 0.01654
-1500
-1520
-1540
-1560
-1580
-1600
-1620
-1640
0
200
400
600
800
1000
1200
1400
1600
1800
2000

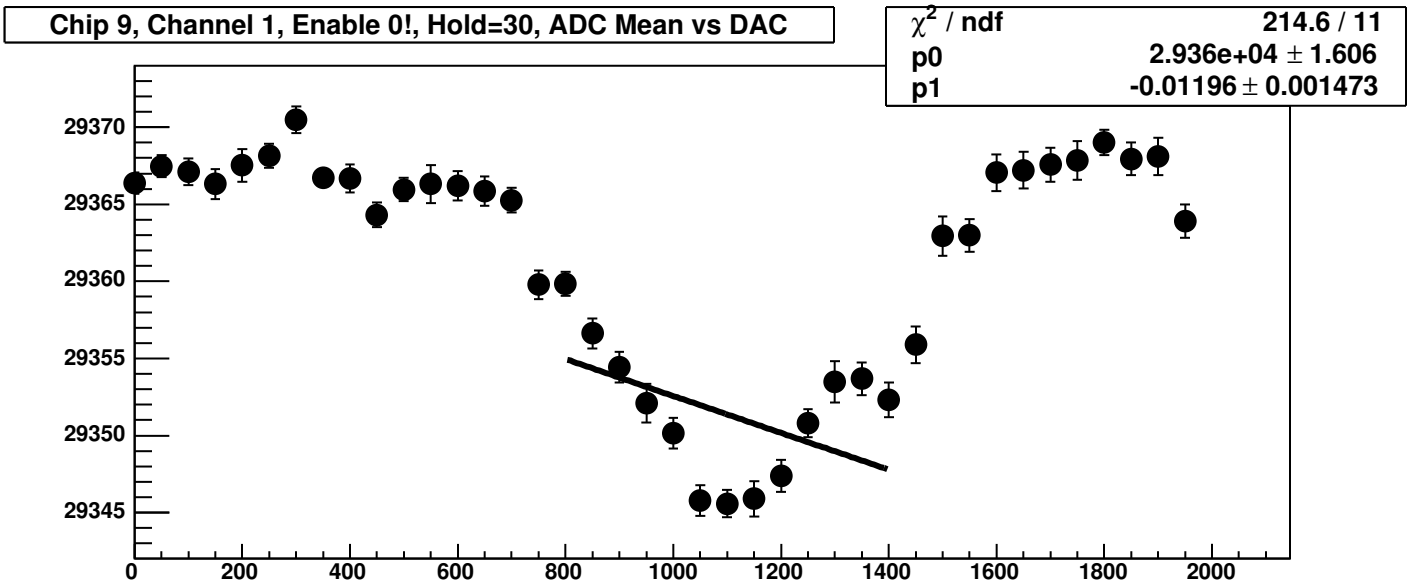

Chip 9, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

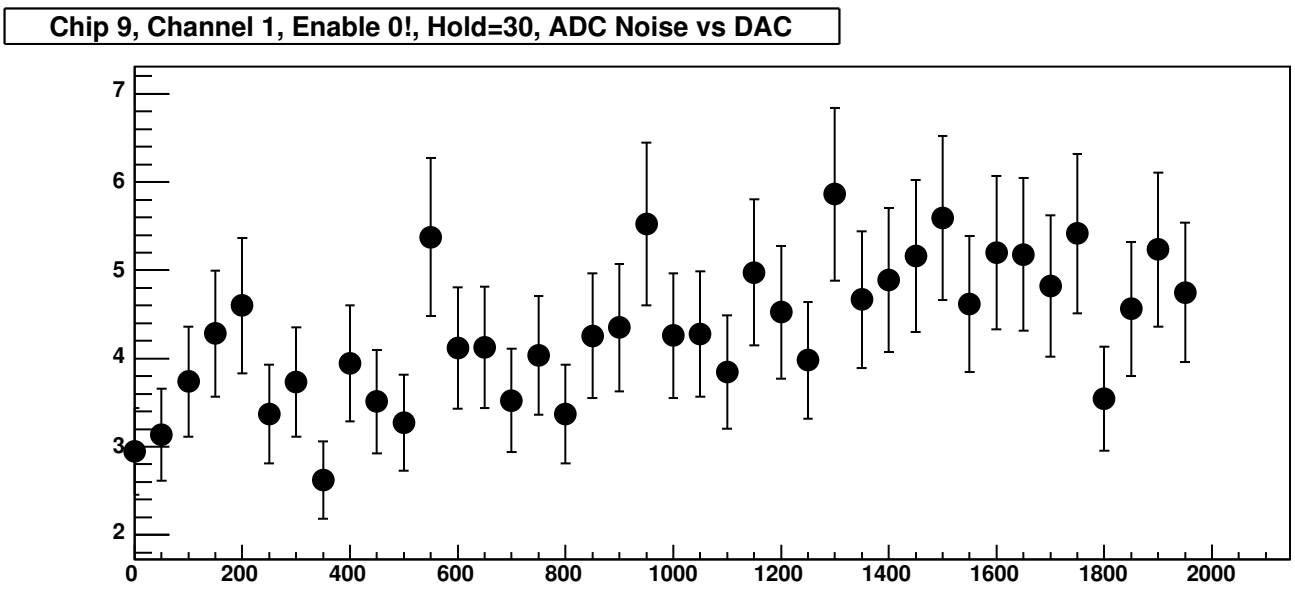

Chip 9, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

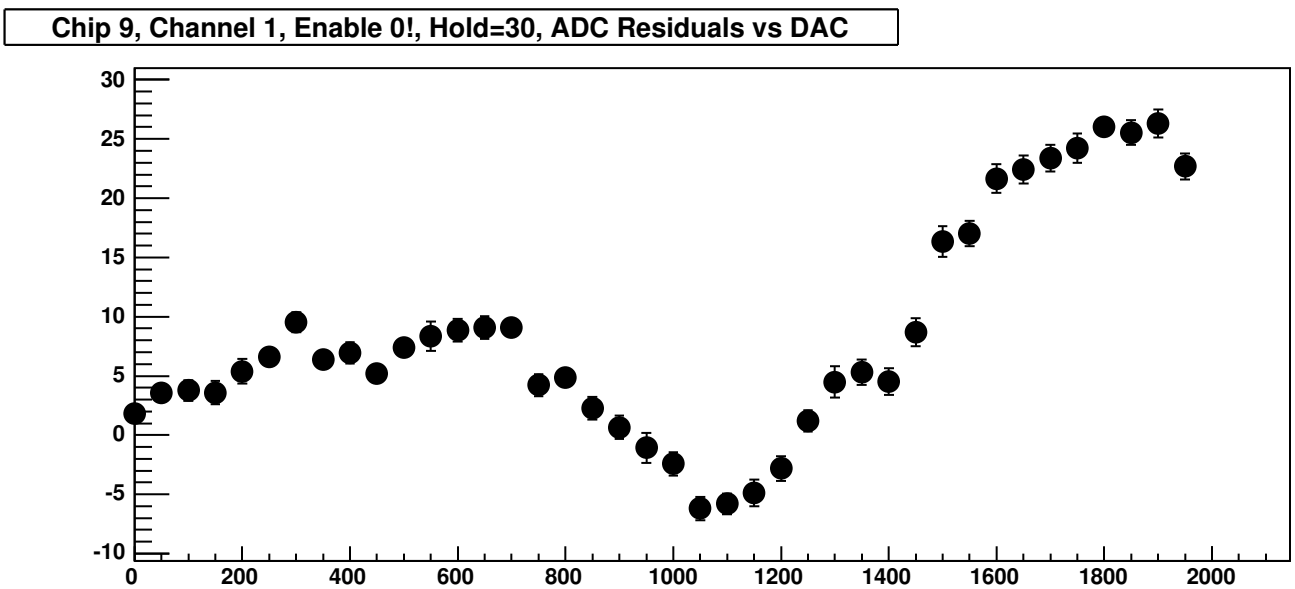

Chip 9, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 11.42 / 11
p0 -1734 ± 24.16
p1 0.4546 ± 0.02241
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

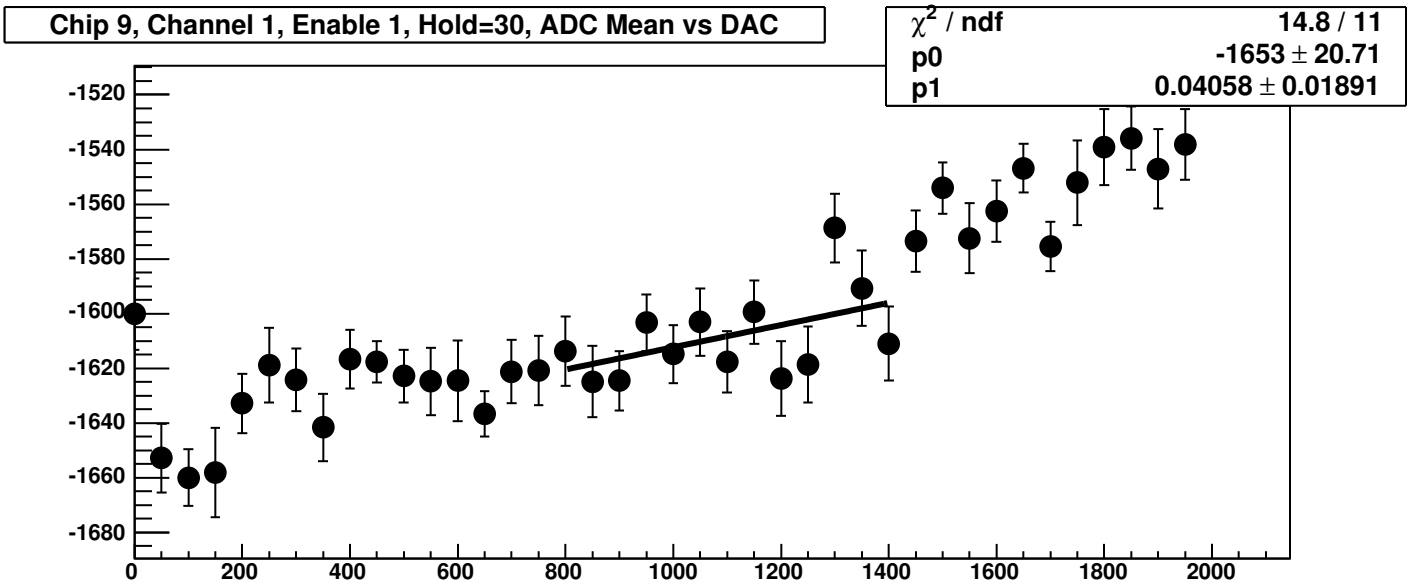

Chip 9, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

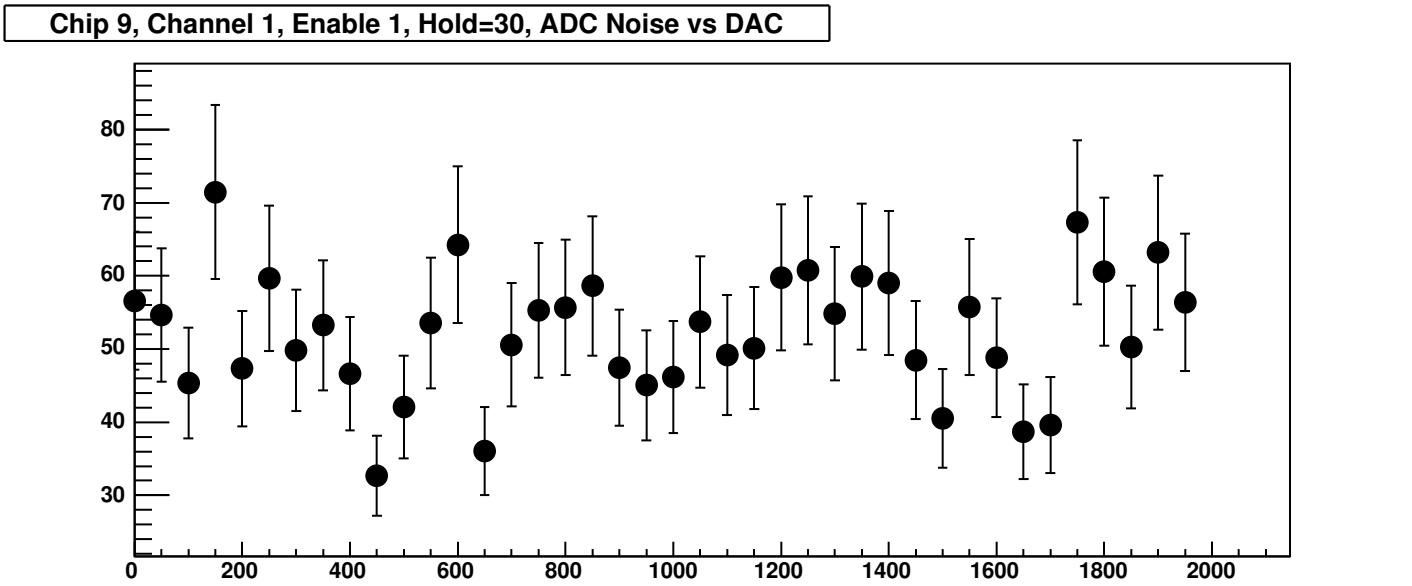

Chip 9, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

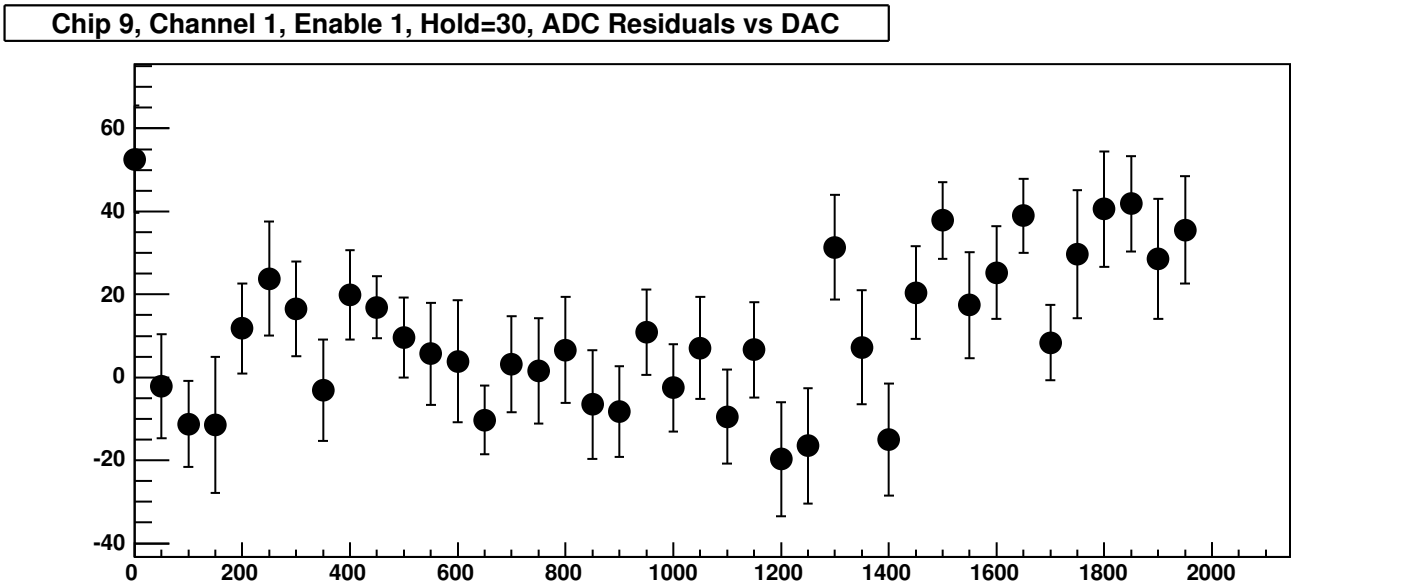

Chip 9, Channel 1, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 214.6 / 11
p0 2.936e+04 ± 1.606
p1 -0.01196 ± 0.001473
29370
29365
29360
29355
29350
29345
0
200
400
600
800
1000
1200
1400
1600
1800
2000

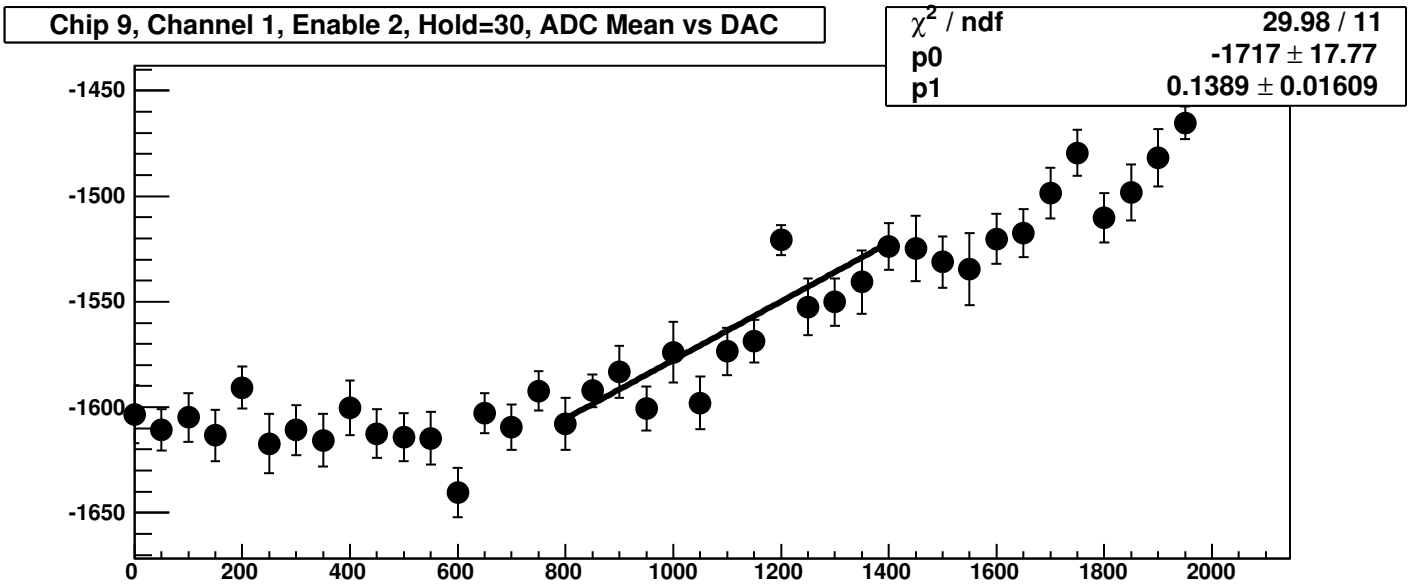

Chip 9, Channel 1, Enable 0!, Hold=30, ADC Noise vs DAC
7
6
5
4
3
2
0
200
400
600
800
1000
1200
1400
1600
1800
2000

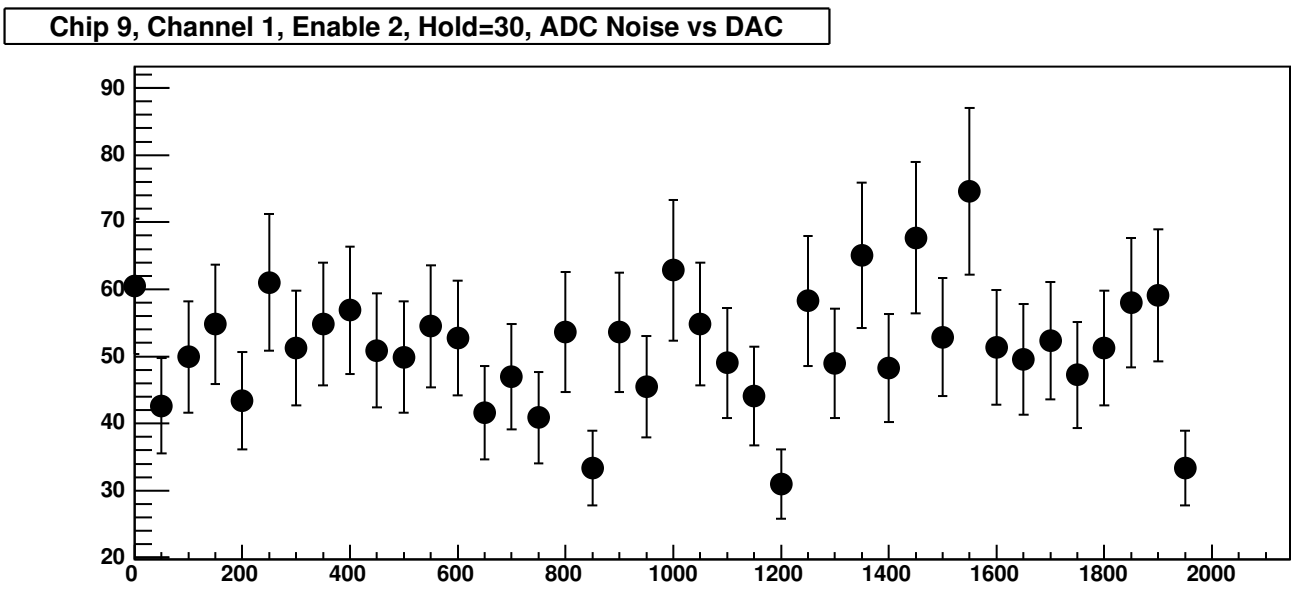

Chip 9, Channel 1, Enable 0!, Hold=30, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

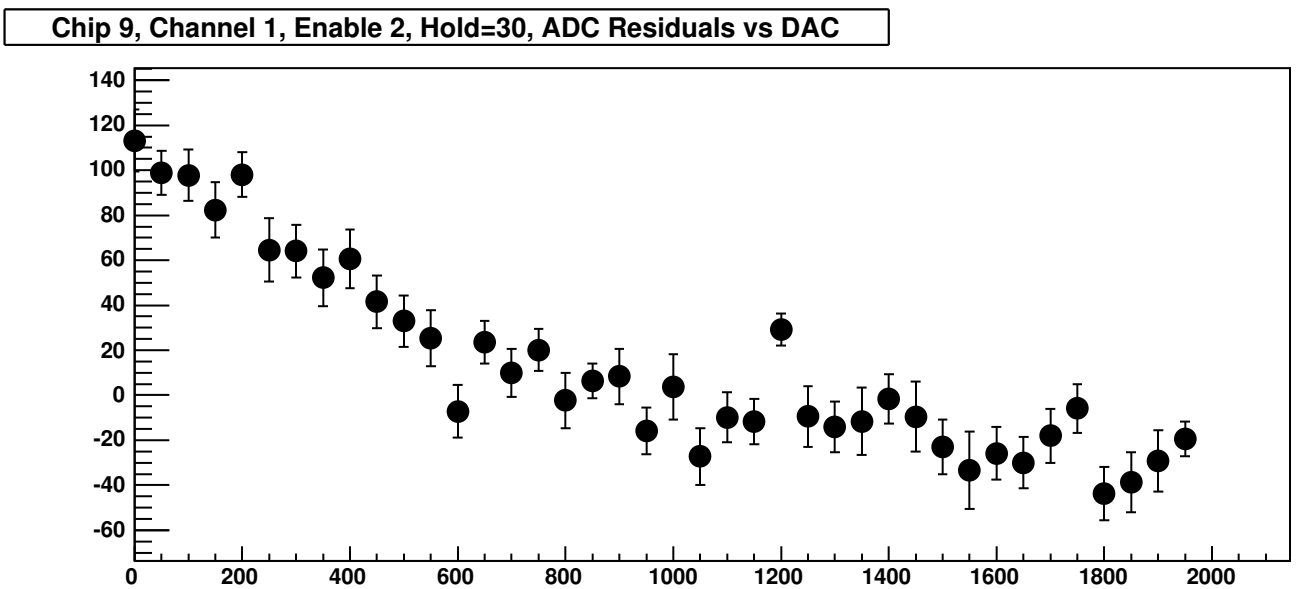

Chip 9, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 14.8 / 11
p0 -1653 ± 20.71
p1 0.04058 ± 0.01891
-1520
-1540
-1560
-1580
-1600
-1620
-1640
-1660
-1680
0
200
400
600
800
1000
1200
1400
1600
1800
2000

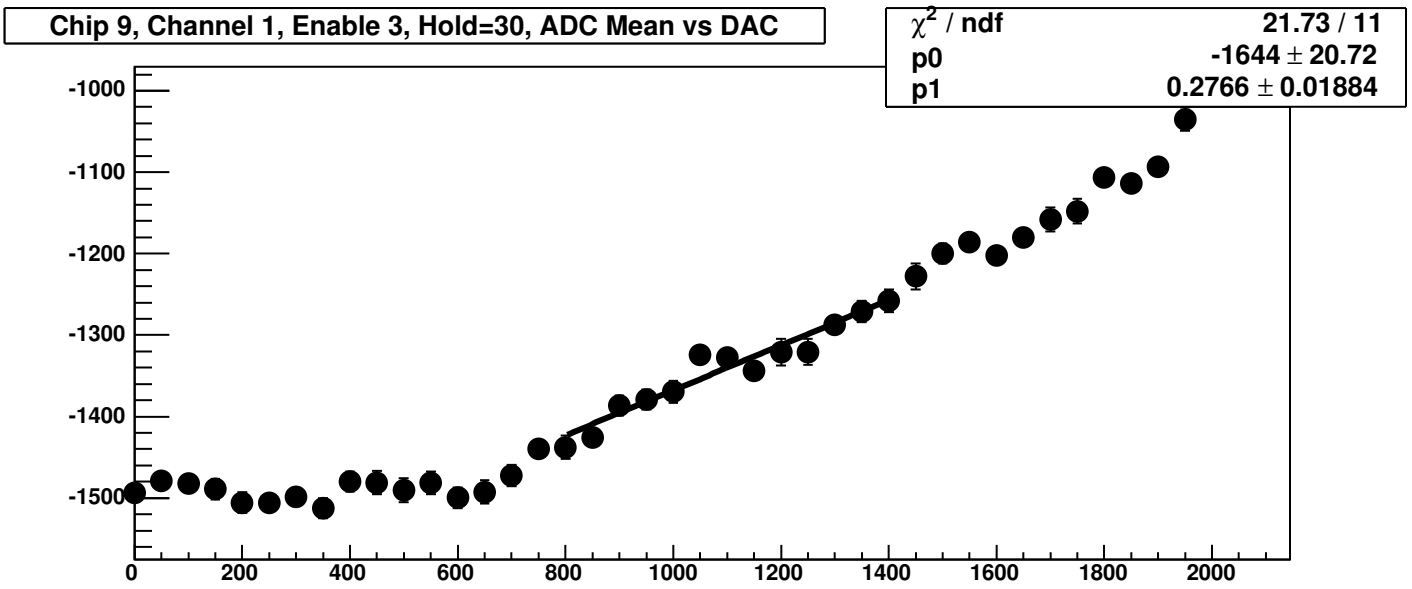

Chip 9, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

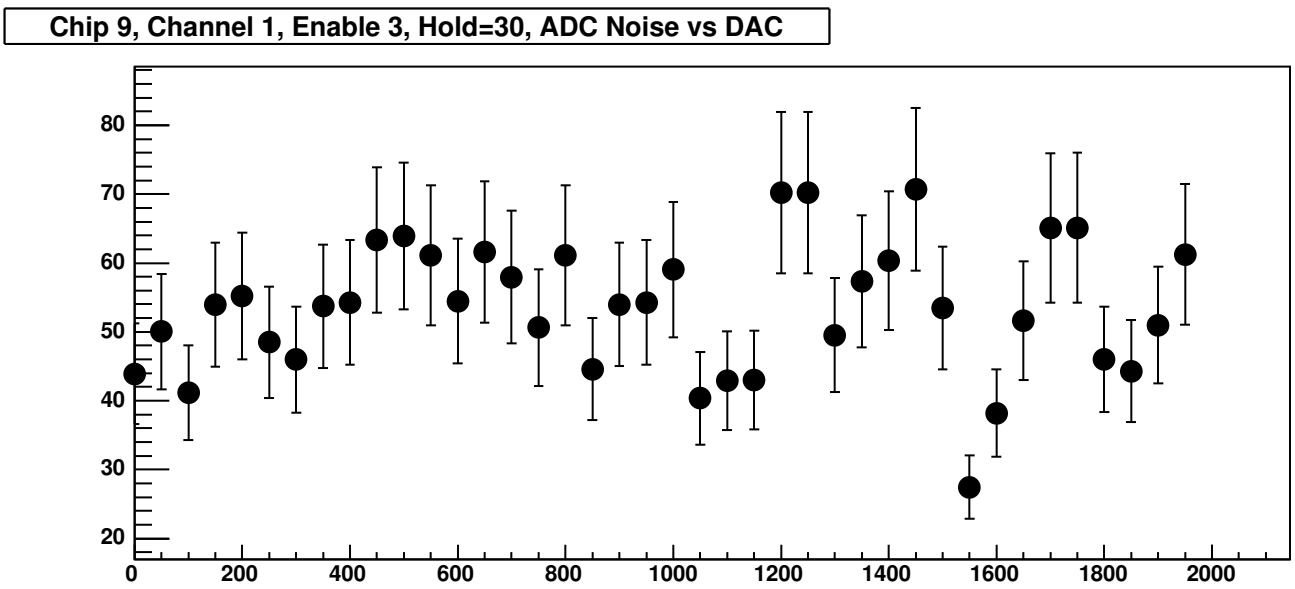

Chip 9, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

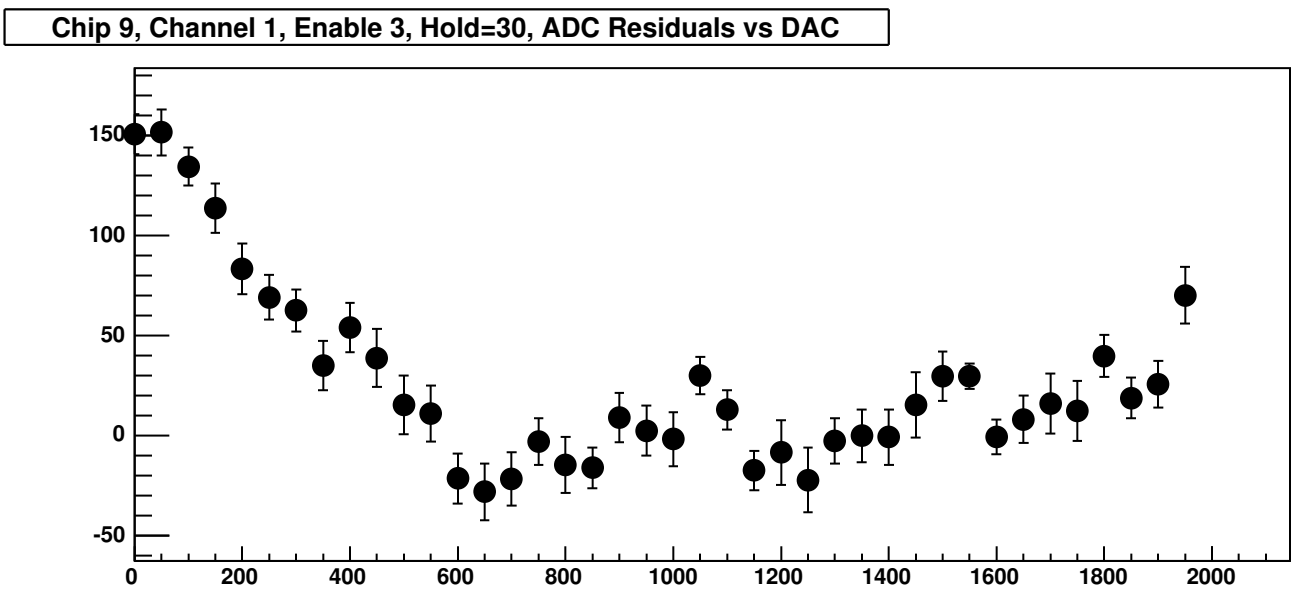

Chip 9, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 29.98 / 11
p0 -1717 ± 17.77
p1 0.1389 ± 0.01609
-1450
-1500
-1550
-1600
-1650
0
200
400
600
800
1000
1200
1400
1600
1800
2000

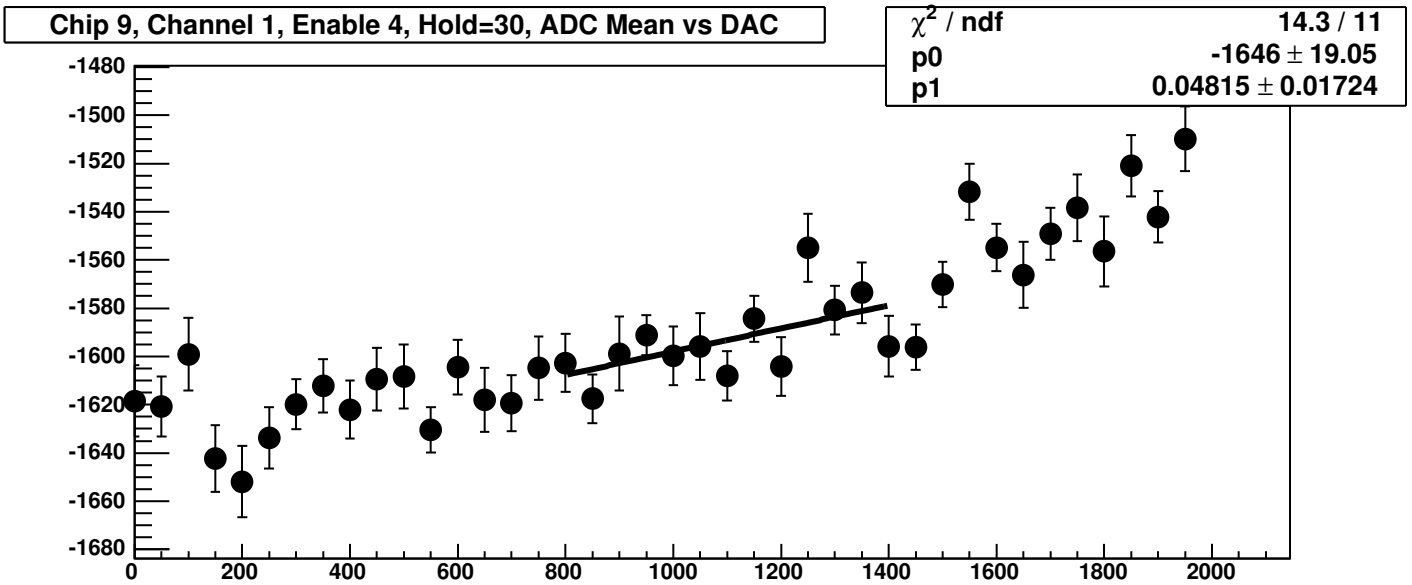

Chip 9, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

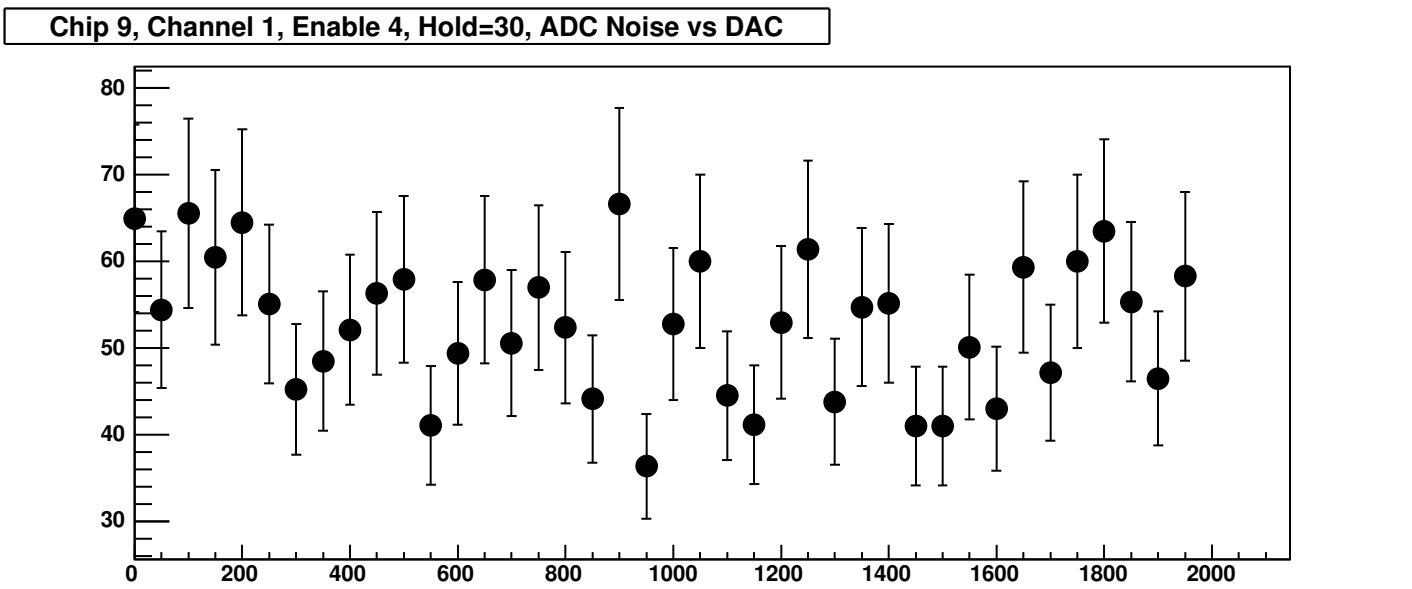

Chip 9, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

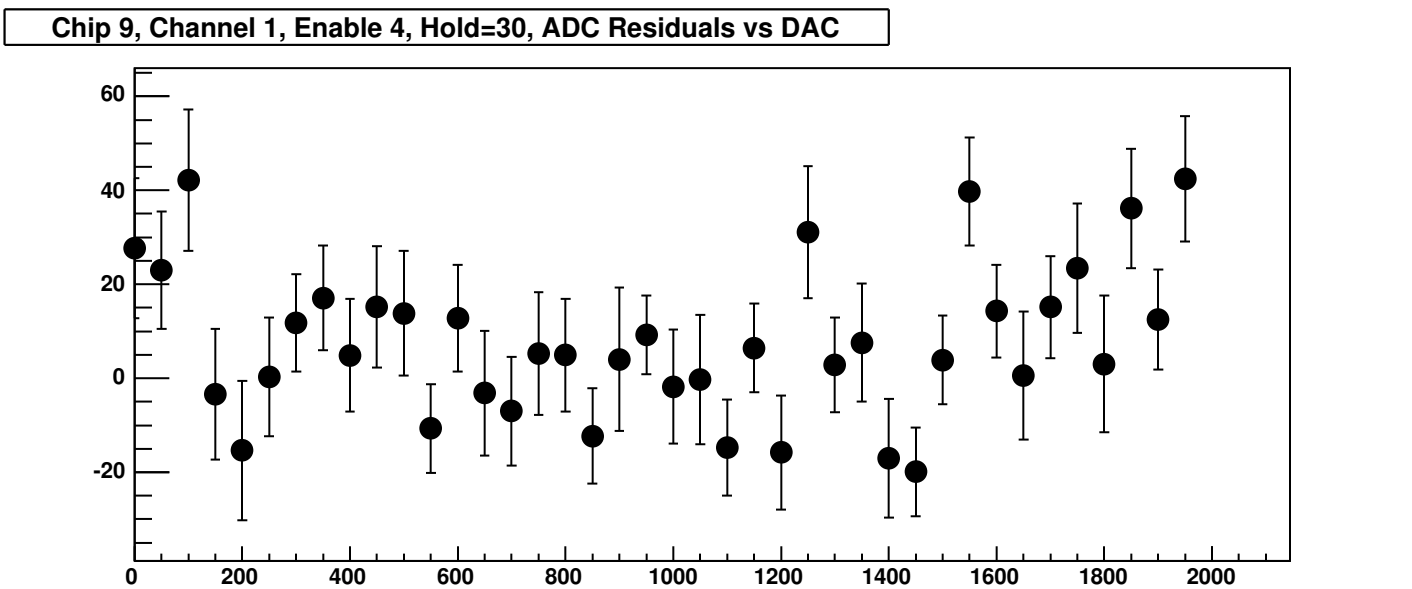

Chip 9, Channel 1, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 21.73 / 11
p0 -1644 ± 20.72
p1 0.2766 ± 0.01884
-1000
-1100
-1200
-1300
-1400
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

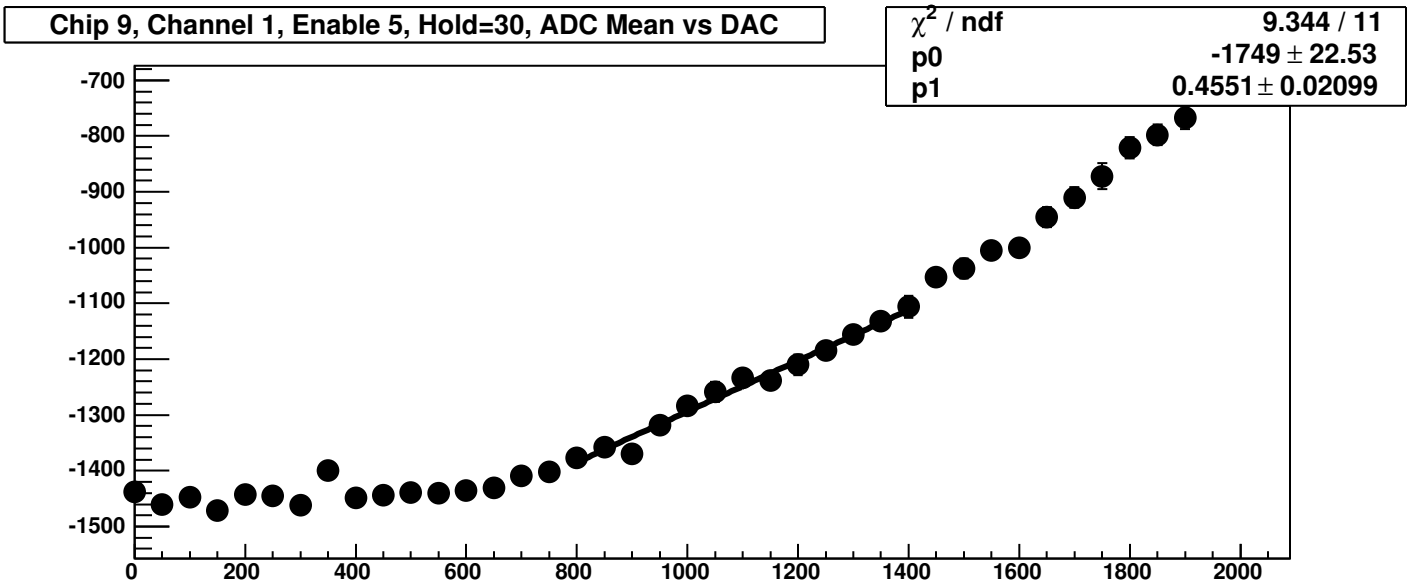

Chip 9, Channel 1, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

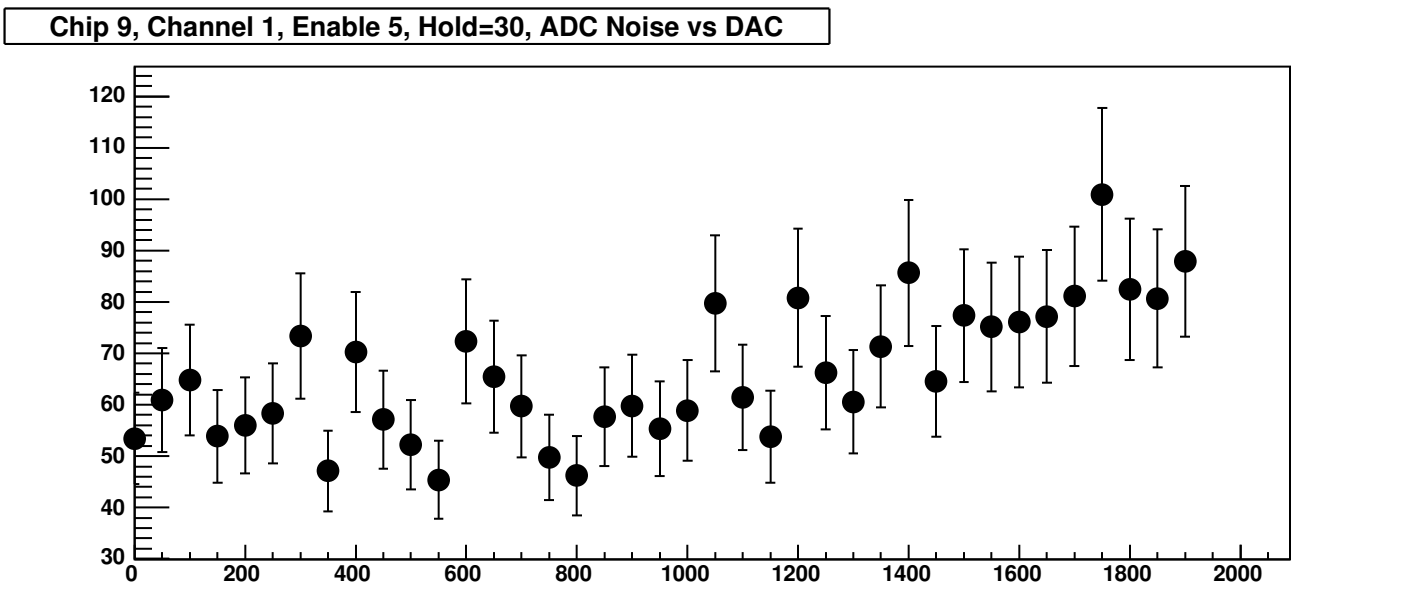

Chip 9, Channel 1, Enable 3, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

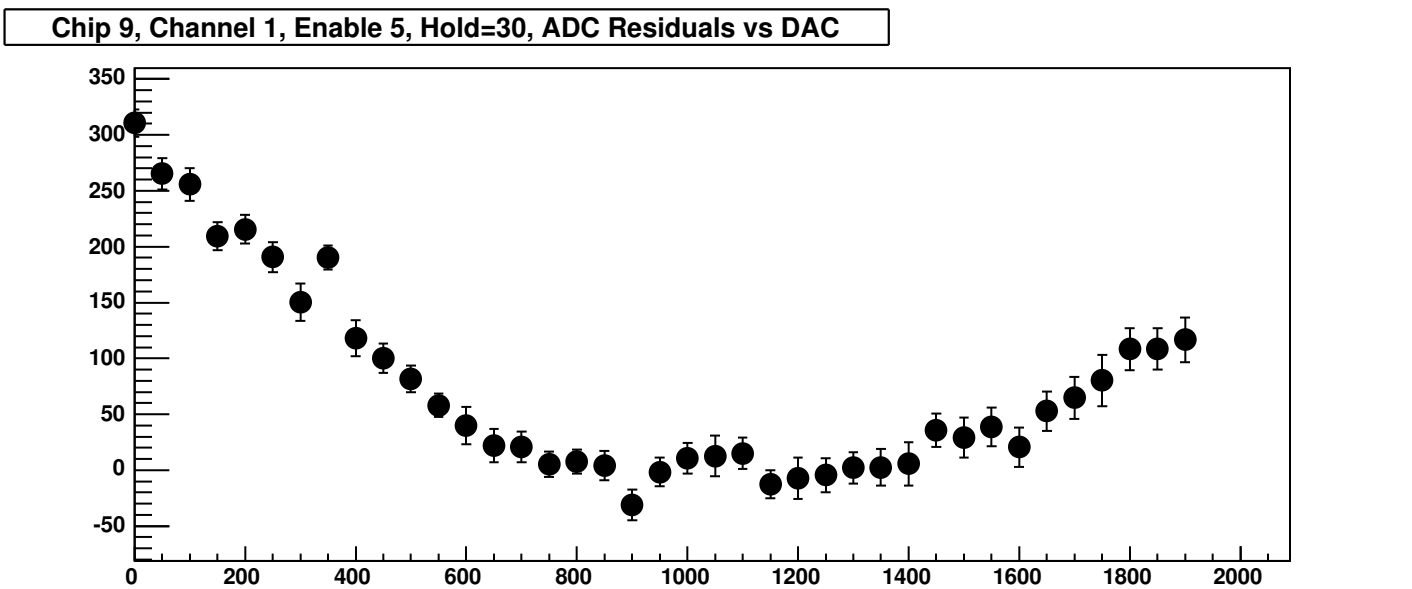

Chip 9, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 14.3 / 11
p0 -1646 ± 19.05
p1 0.04815 ± 0.01724
-1480
-1500
-1520
-1540
-1560
-1580
-1600
-1620
-1640
-1660
-1680
0
200
400
600
800
1000
1200
1400
1600
1800
2000

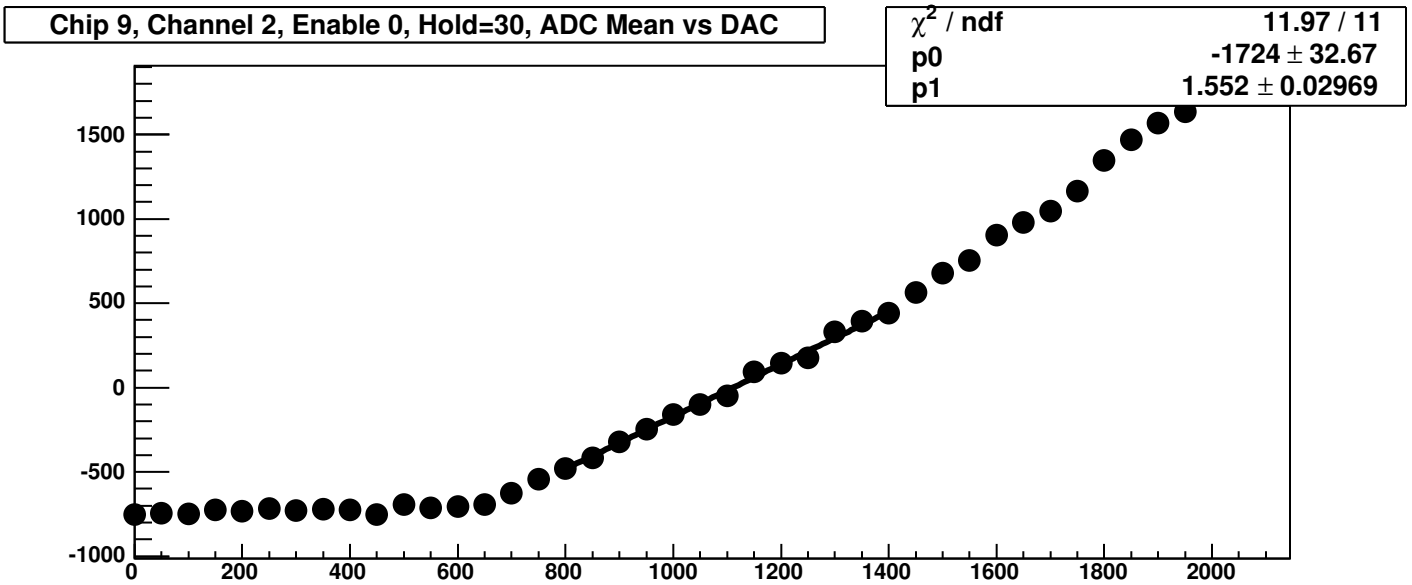

Chip 9, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

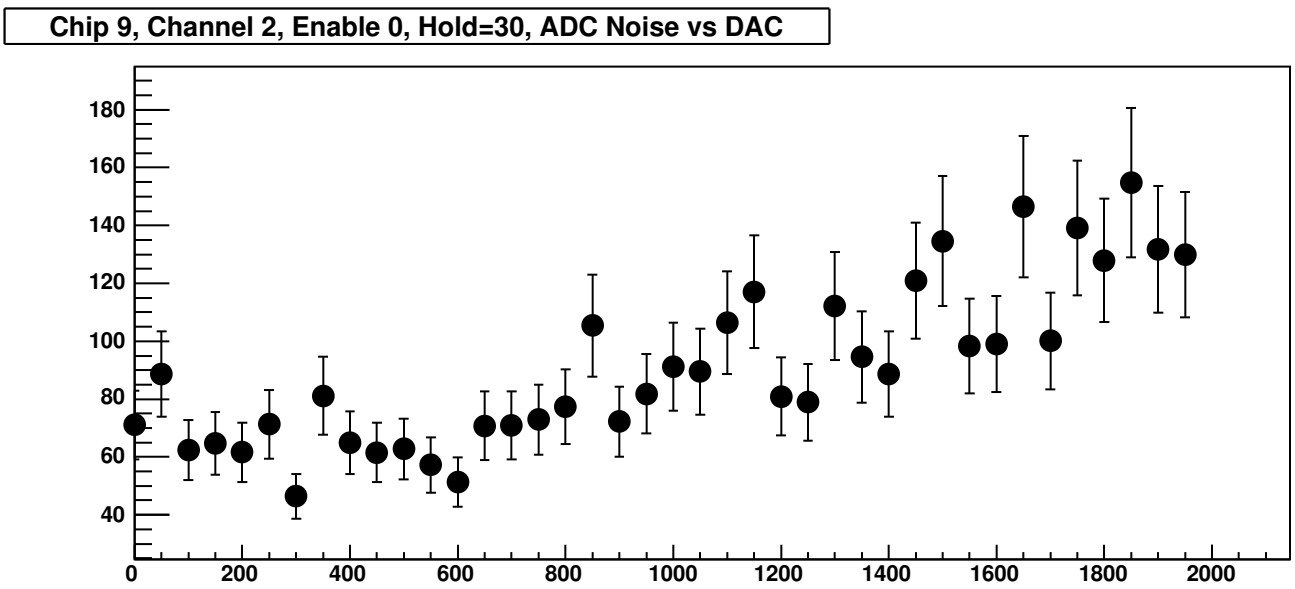

Chip 9, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

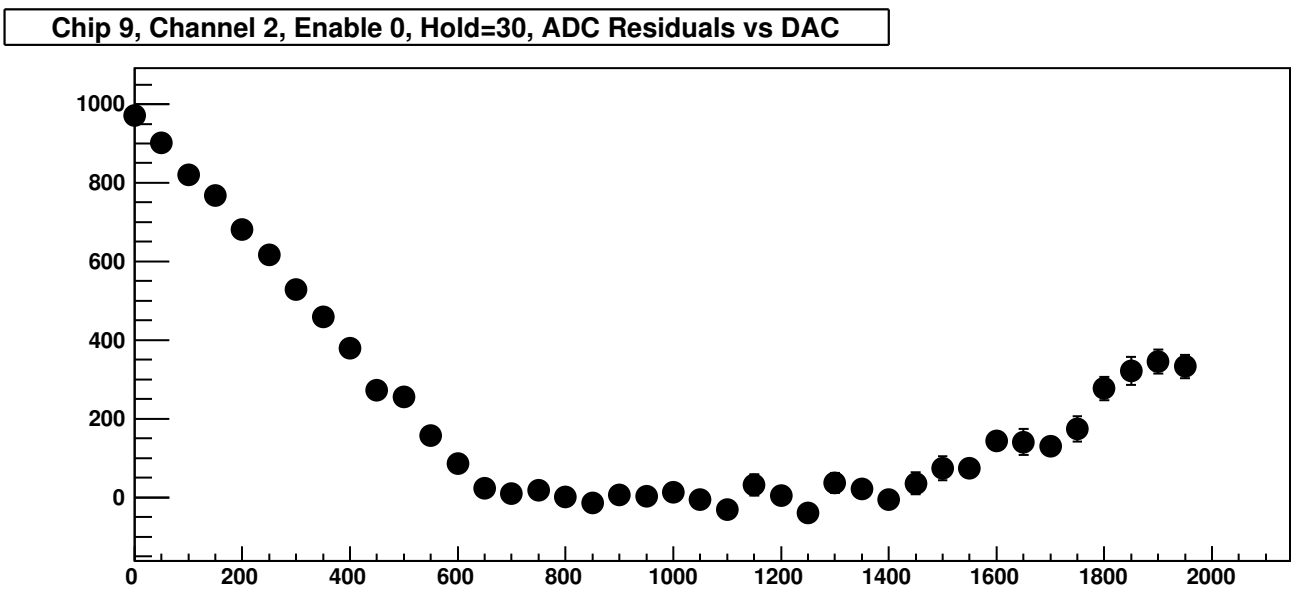

Chip 9, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9.344 / 11
p0 -1749 ± 22.53
p1 0.4551 ± 0.02099
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
120
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

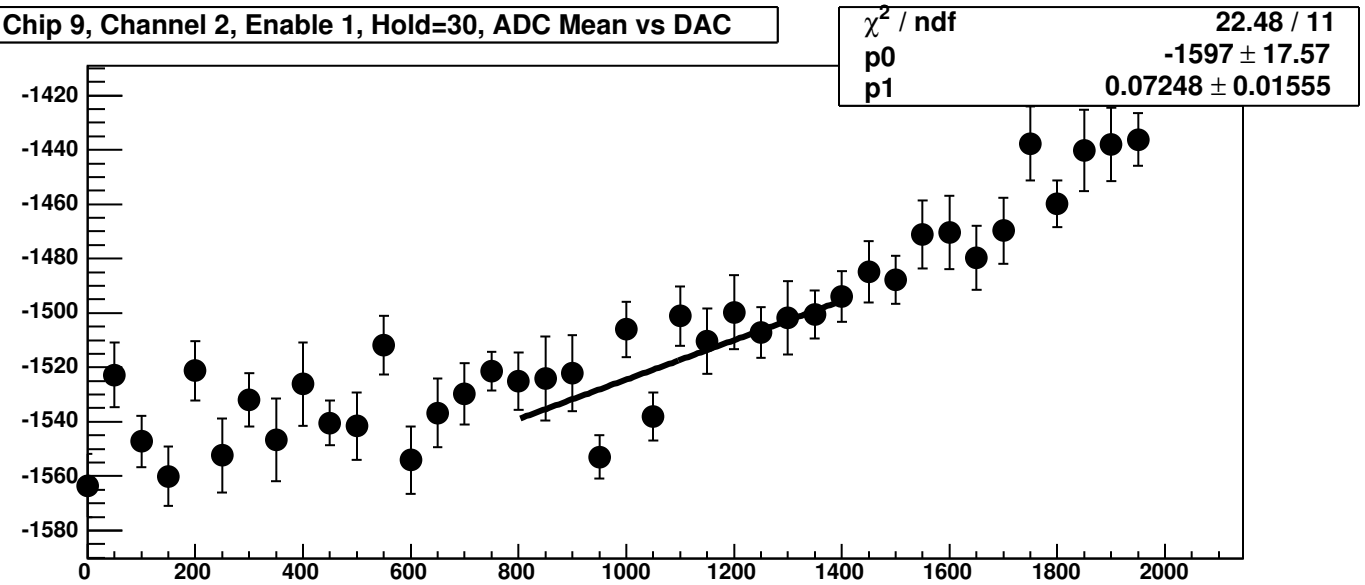

Chip 9, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 2, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 11.97 / 11
p0 -1724 ± 32.67
p1 1.552 ± 0.02969
1500
1000
500
0
-500
-1000
0 200 400 600 800 1000 1200 1400 1600 1800 2000

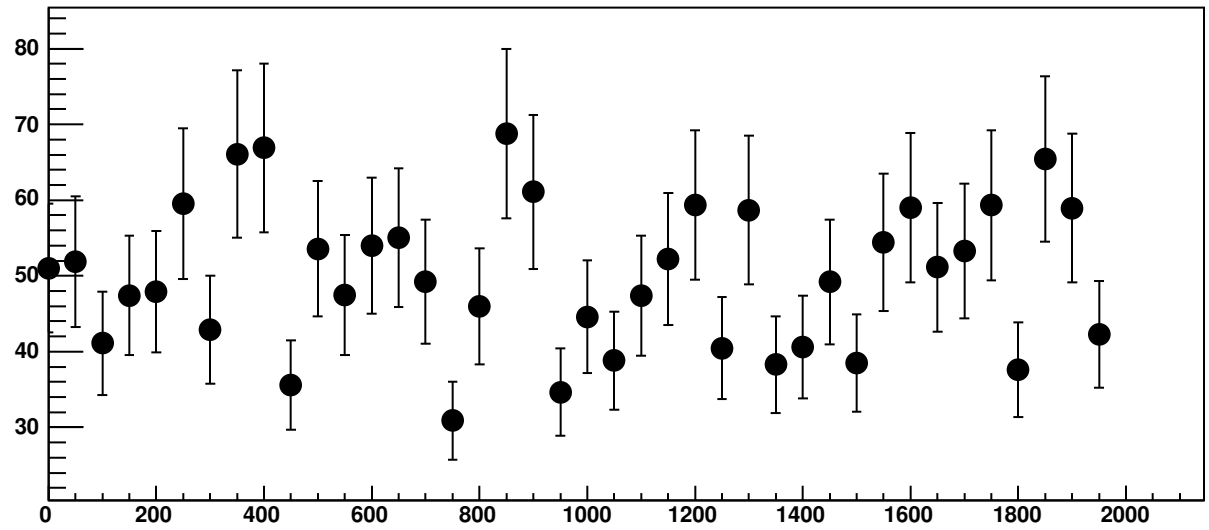

Chip 9, Channel 2, Enable 0, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0 200 400 600 800 1000 1200 1400 1600 1800 2000


Chip 9, Channel 2, Enable 0, Hold=30, ADC Residuals vs DAC
1000
800
600
400
200
0
0 200 400 600 800 1000 1200 1400 1600 1800 2000

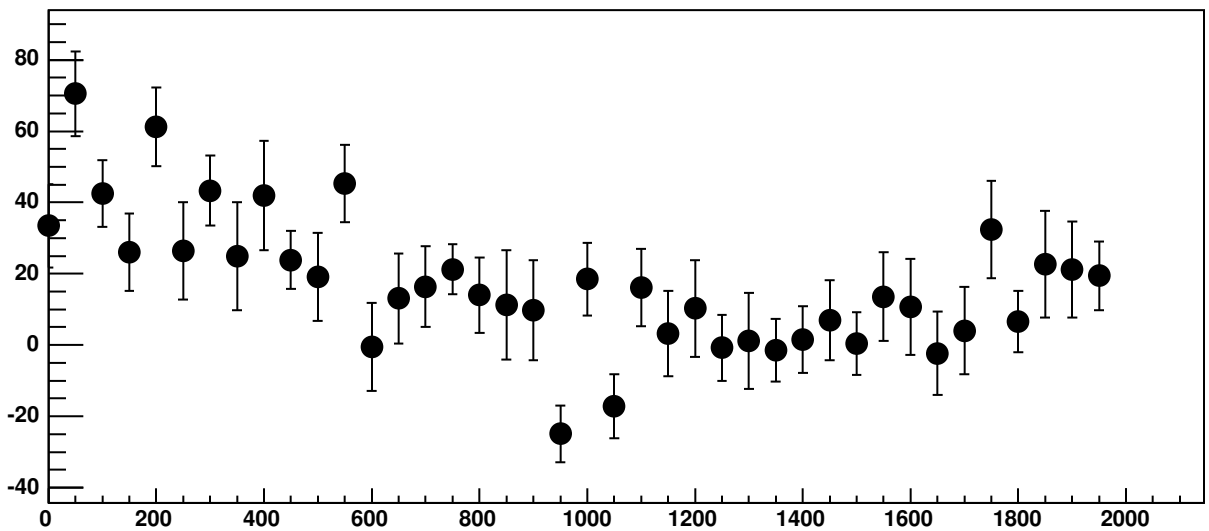
Chip 9, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC

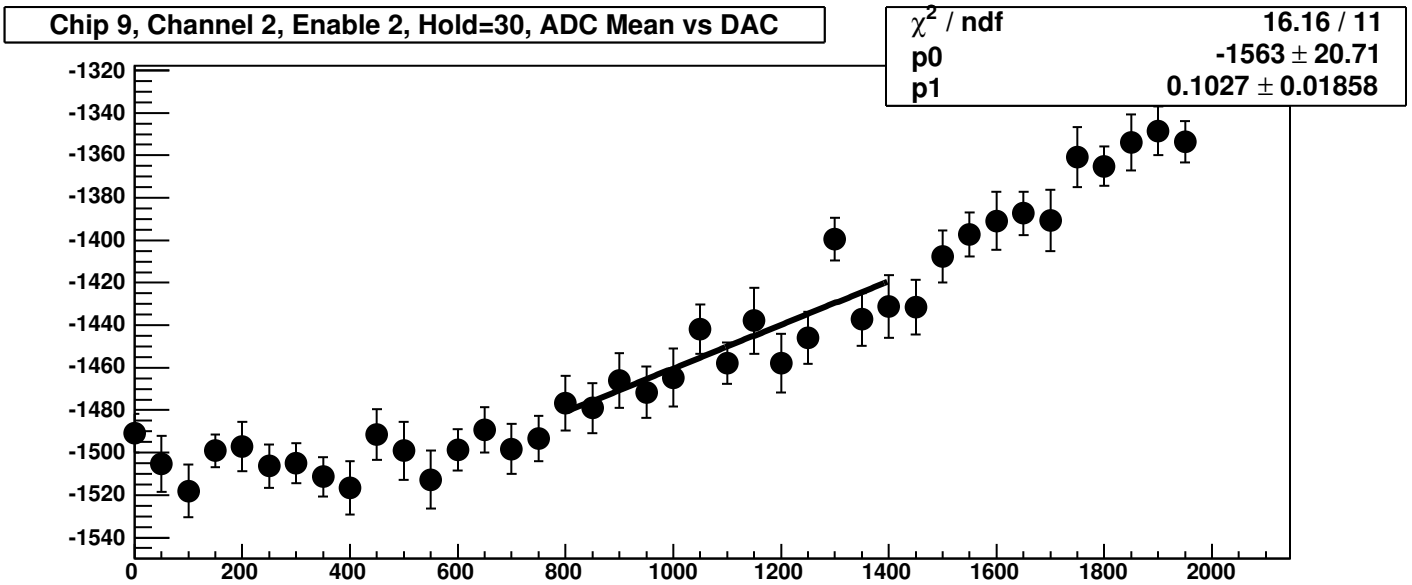


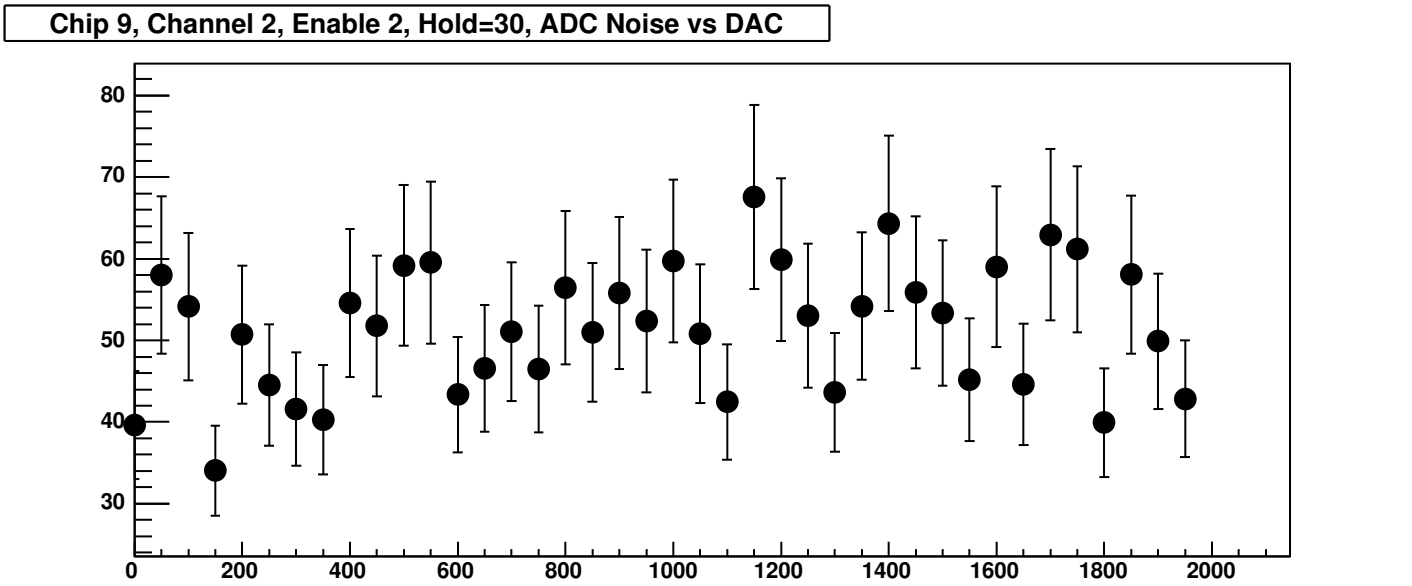
Chip 9, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC

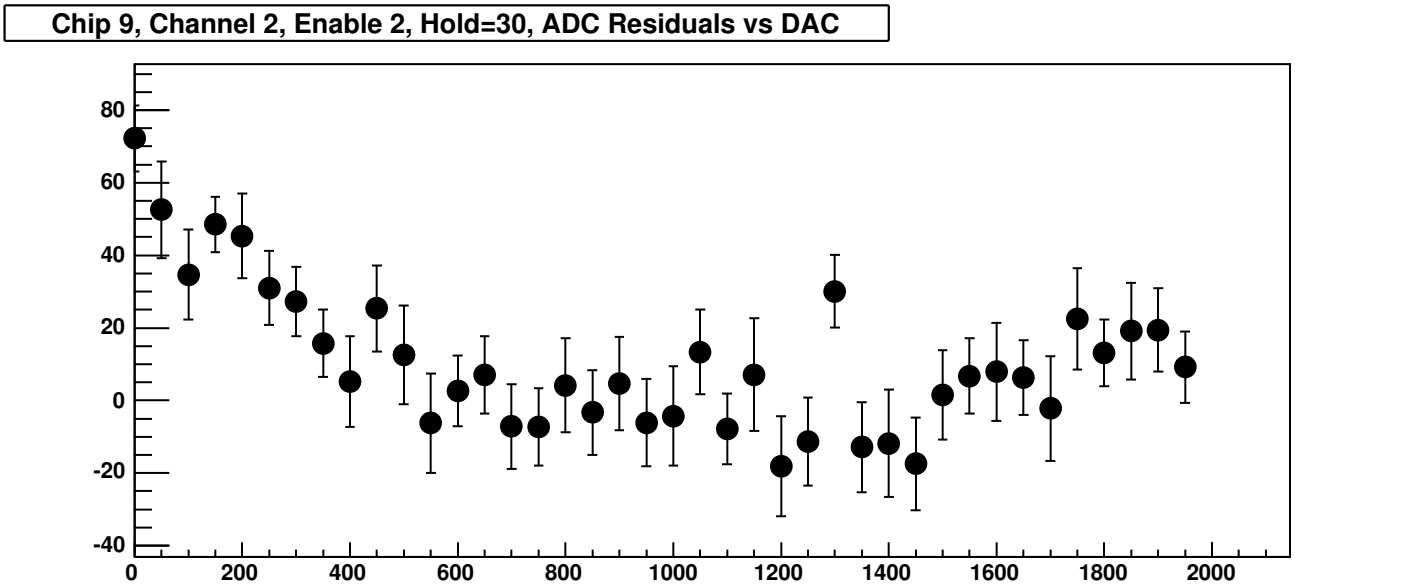


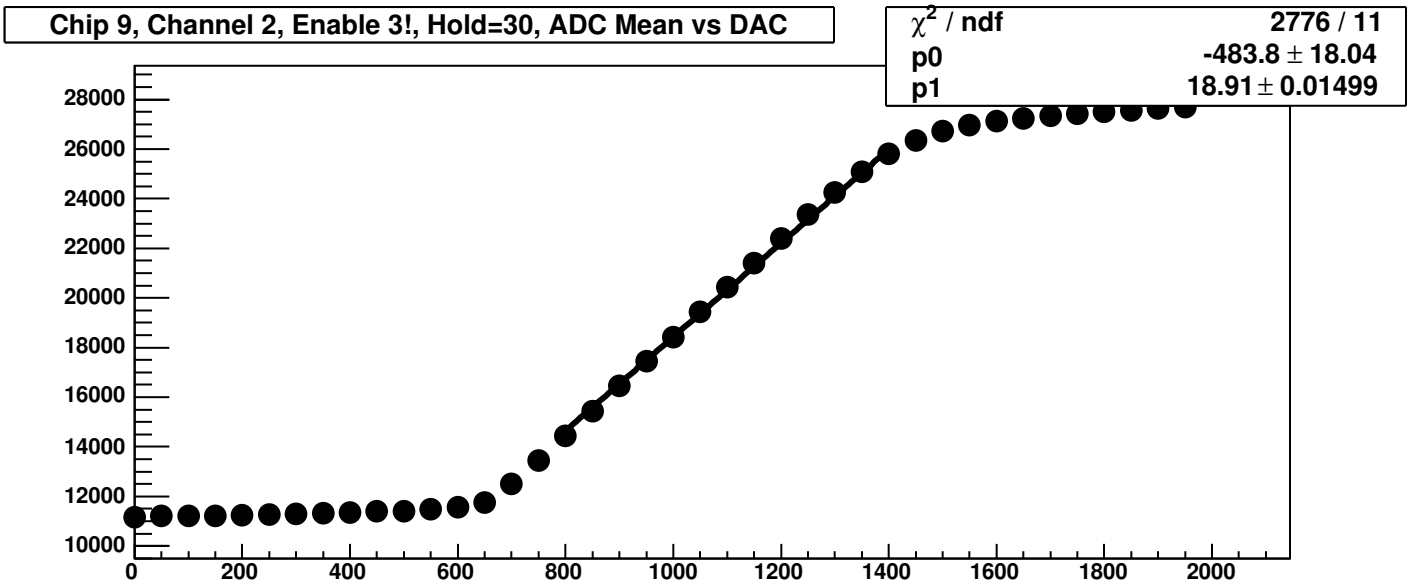
Chip 9, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC

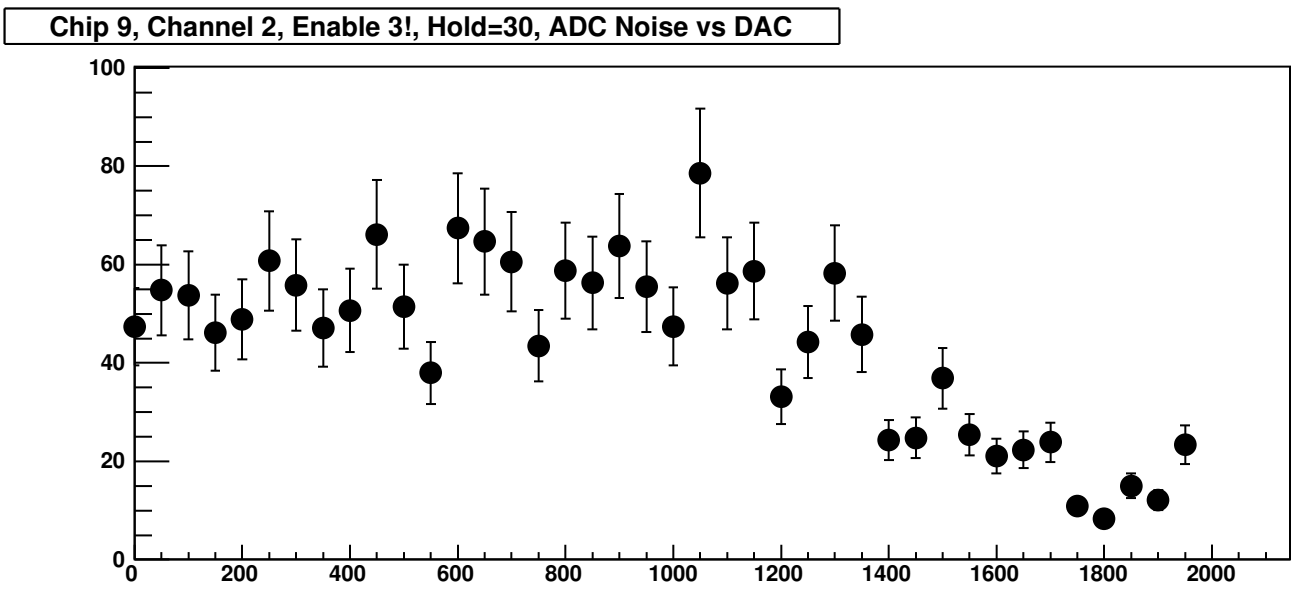


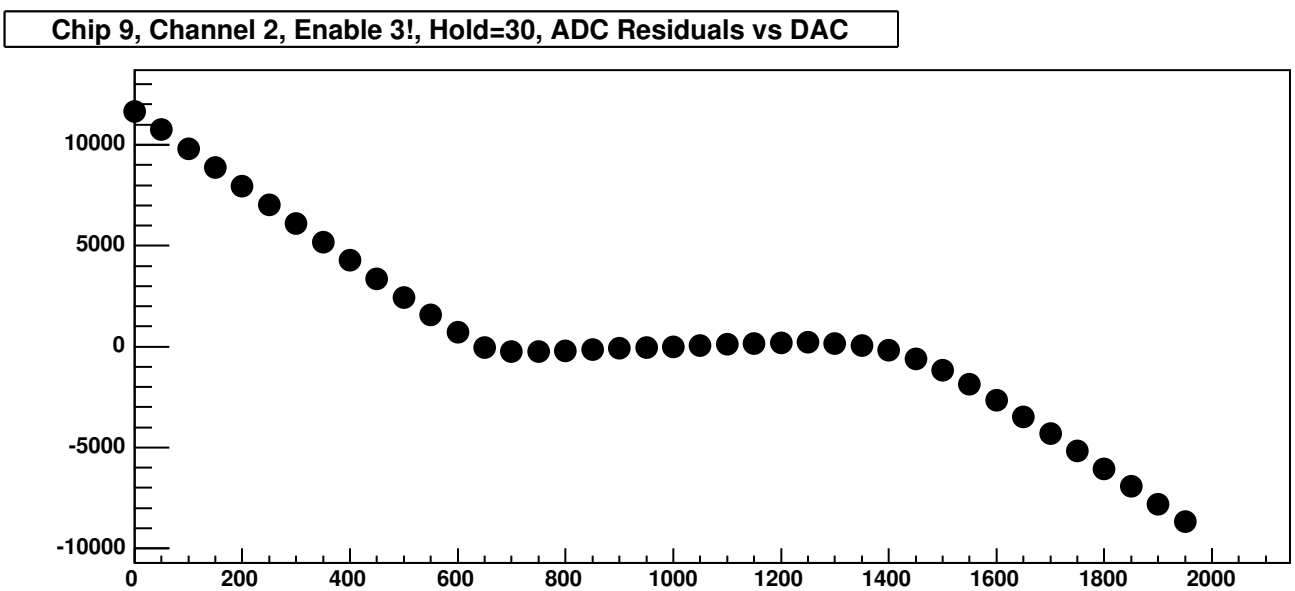

Chip 9, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 16.16 / 11
p0 -1563 ± 20.71
p1 0.1027 ± 0.01858
-1320
-1340
-1360
-1380
-1400
-1420
-1440
-1460
-1480
-1500
-1520
-1540
0
200
400
600
800
1000
1200
1400
1600
1800
2000

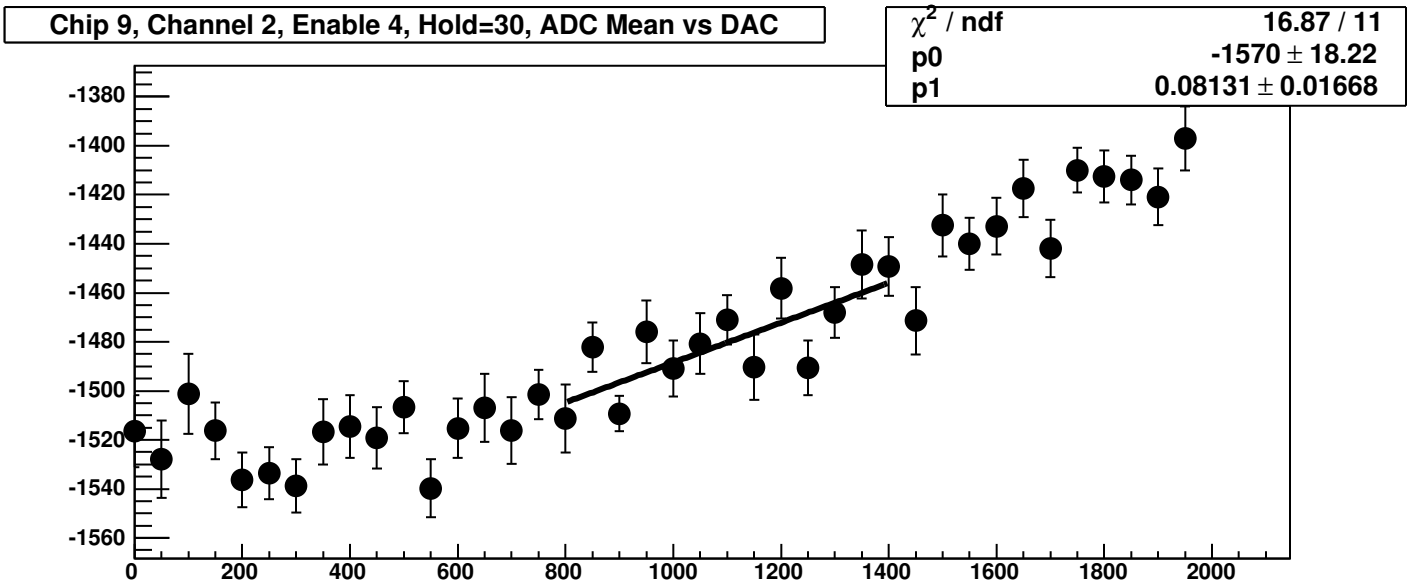

Chip 9, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

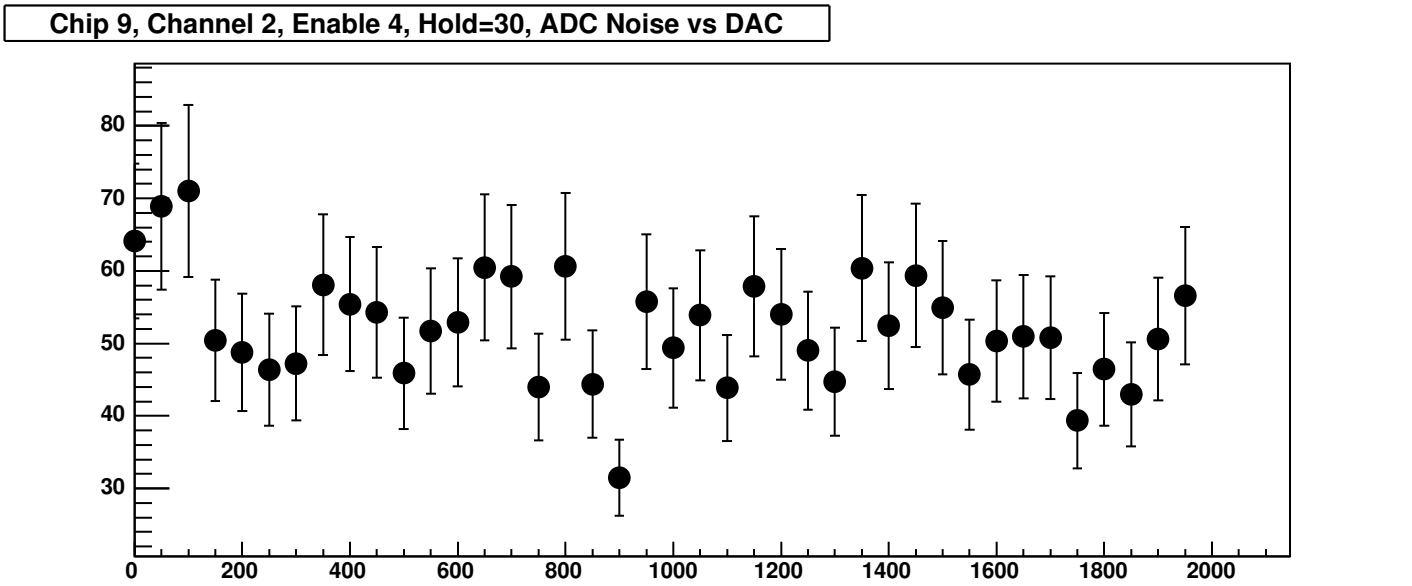

Chip 9, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

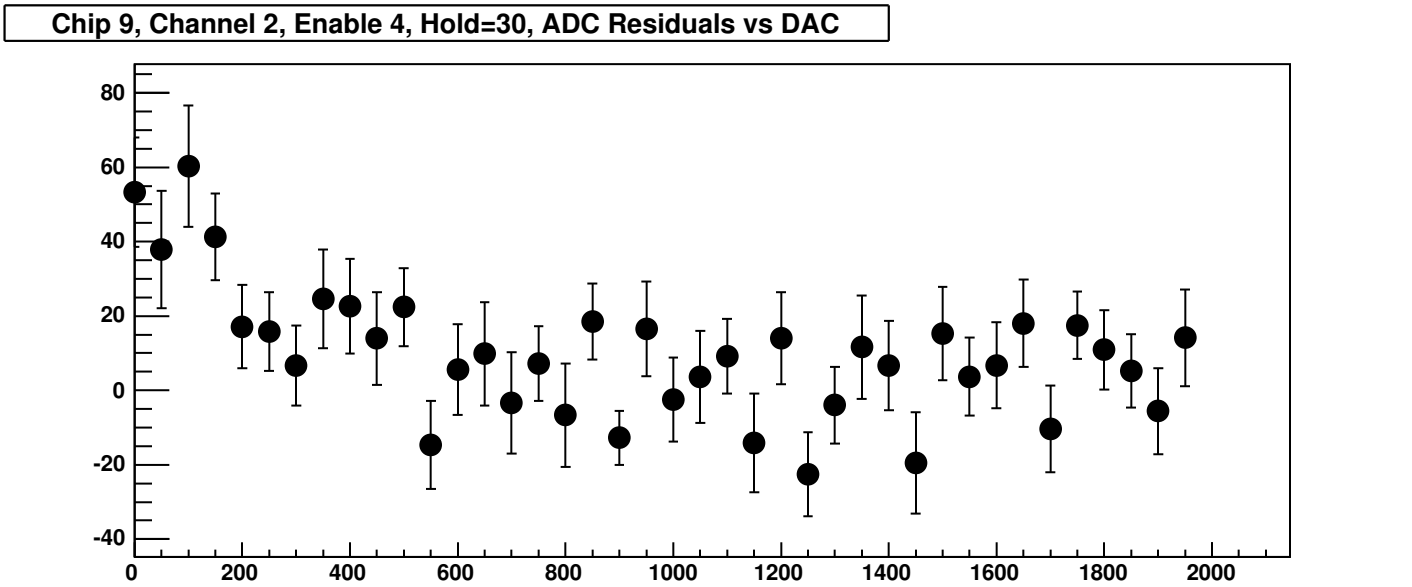

Chip 9, Channel 2, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 2776 / 11
p0 -483.8 ± 18.04
p1 18.91 ± 0.01499
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

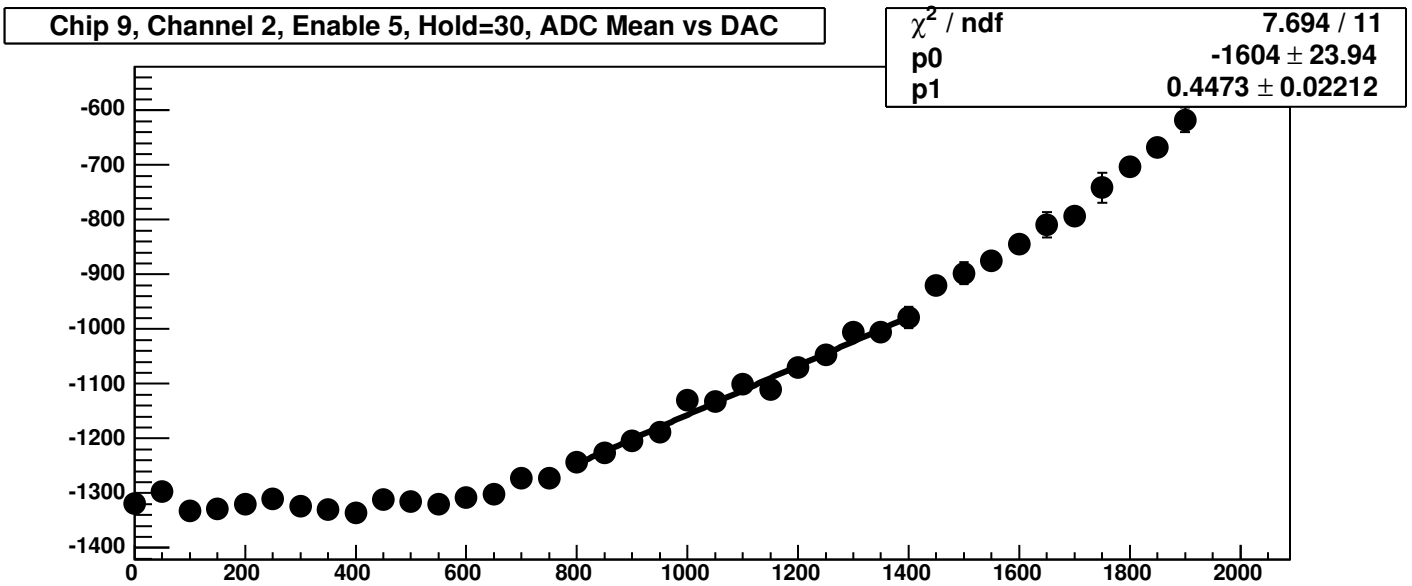

Chip 9, Channel 2, Enable 3!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

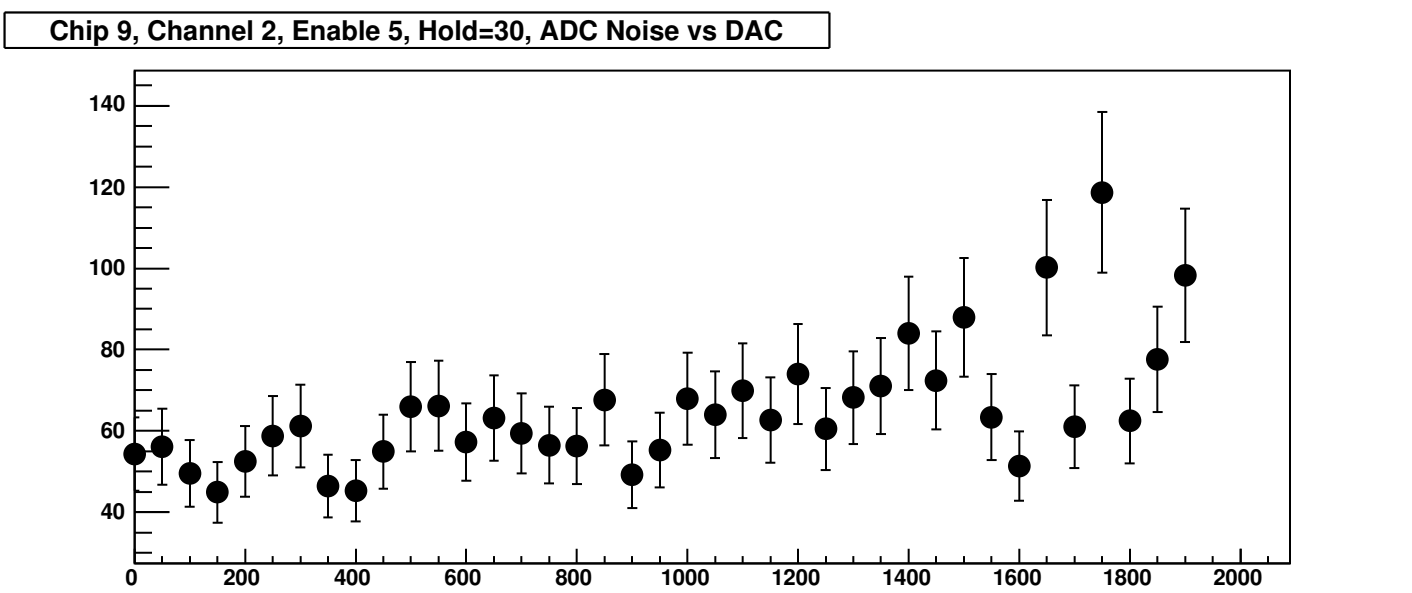

Chip 9, Channel 2, Enable 3!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

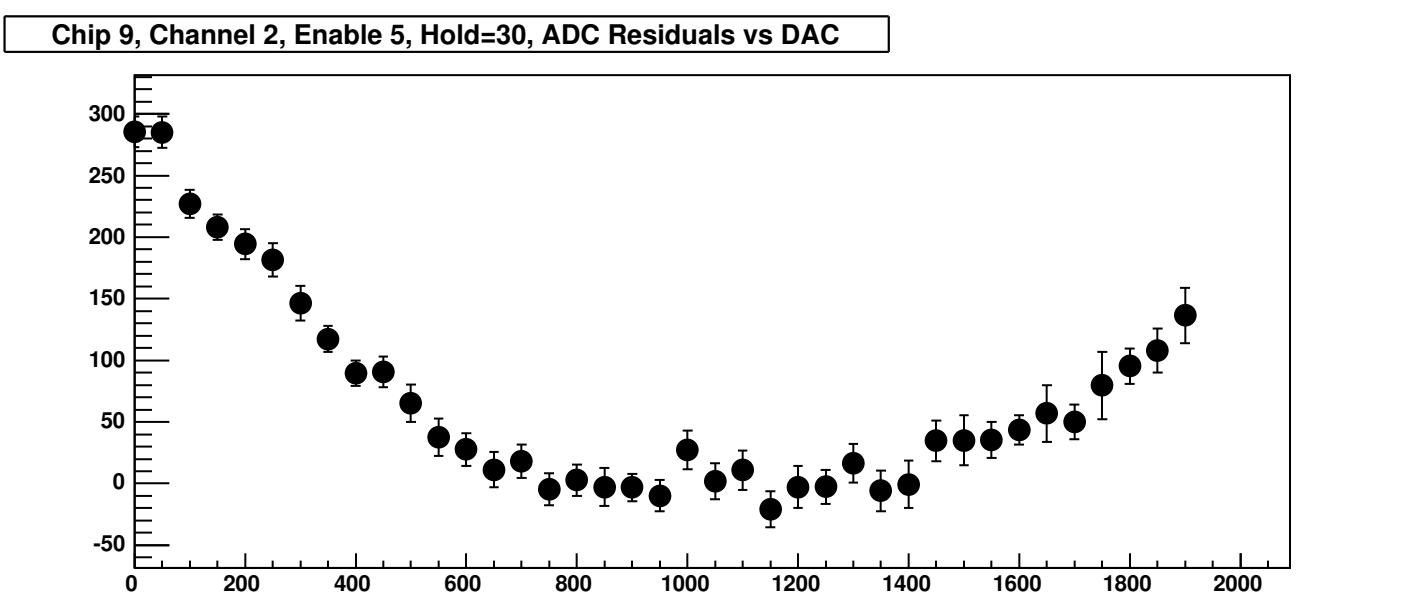

Chip 9, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 16.87 / 11
p0 -1570 ± 18.22
p1 0.08131 ± 0.01668
-1380
-1400
-1420
-1440
-1460
-1480
-1500
-1520
-1540
-1560
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

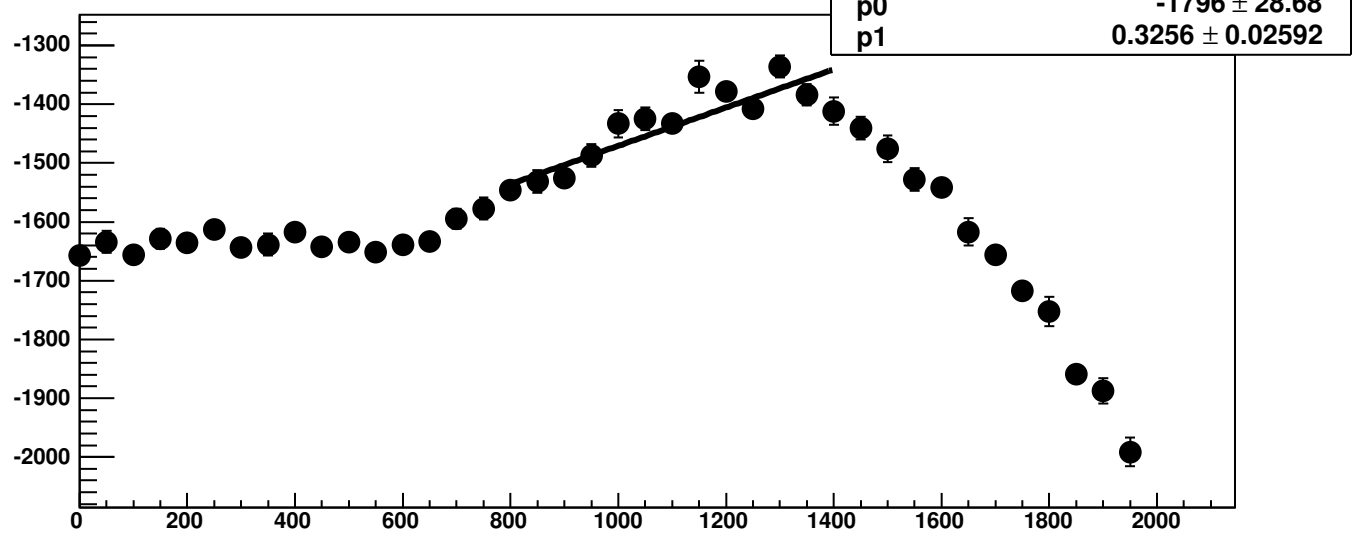

Chip 9, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7.694 / 11
p0 -1604 ± 23.94
p1 0.4473 ± 0.02212
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

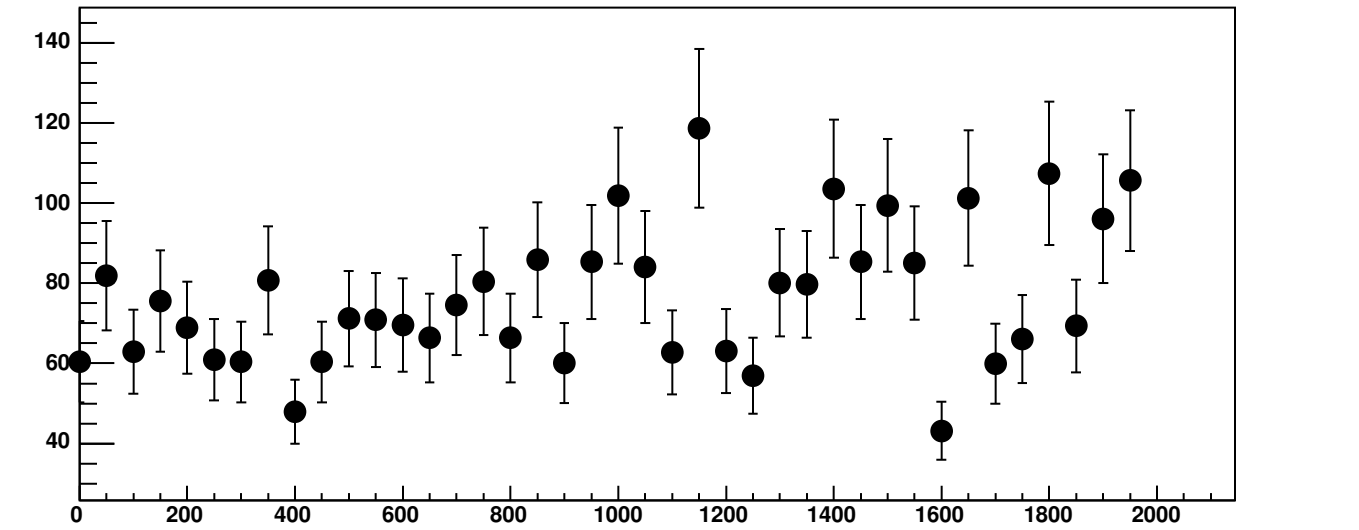

Chip 9, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

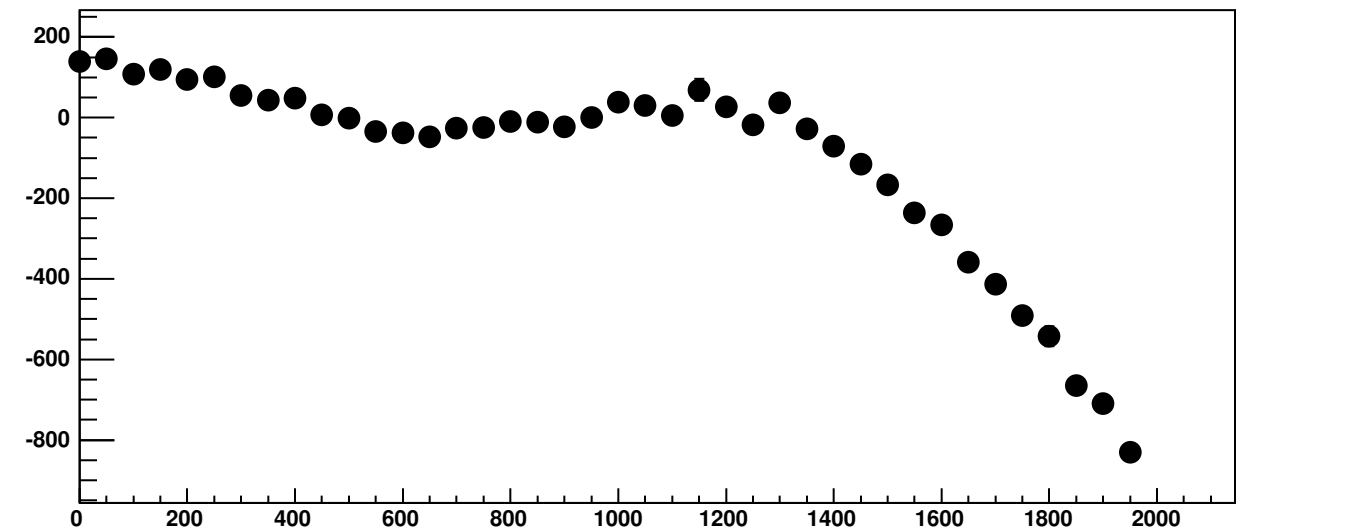
Chip 9, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC

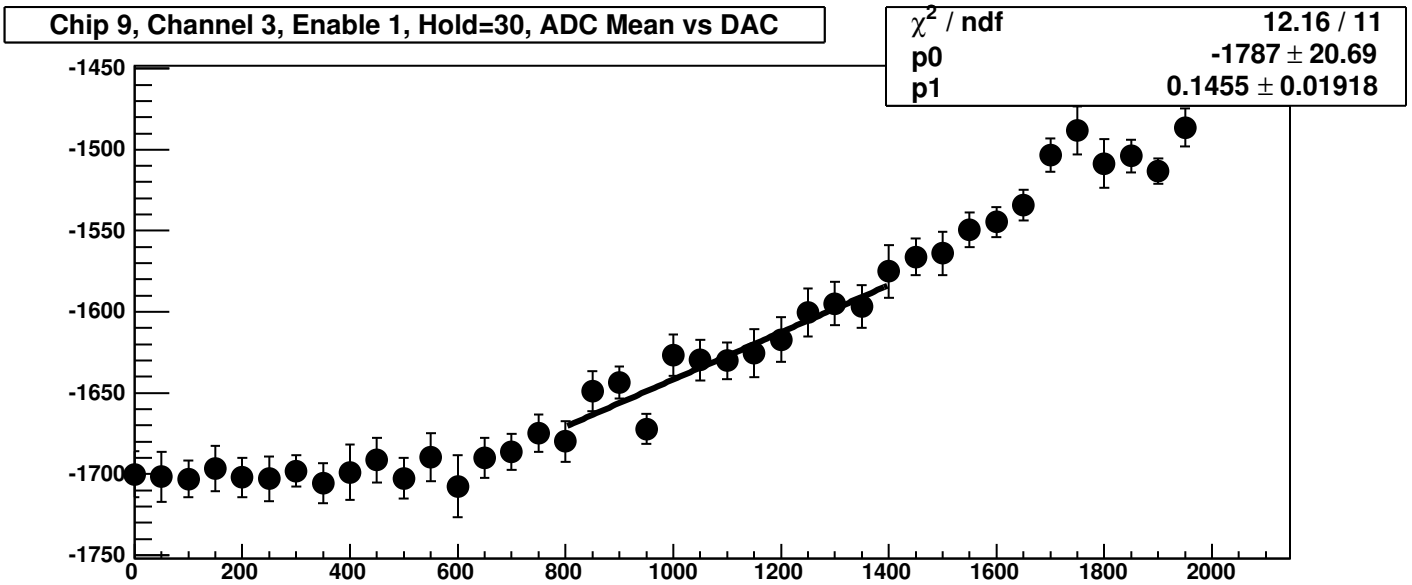


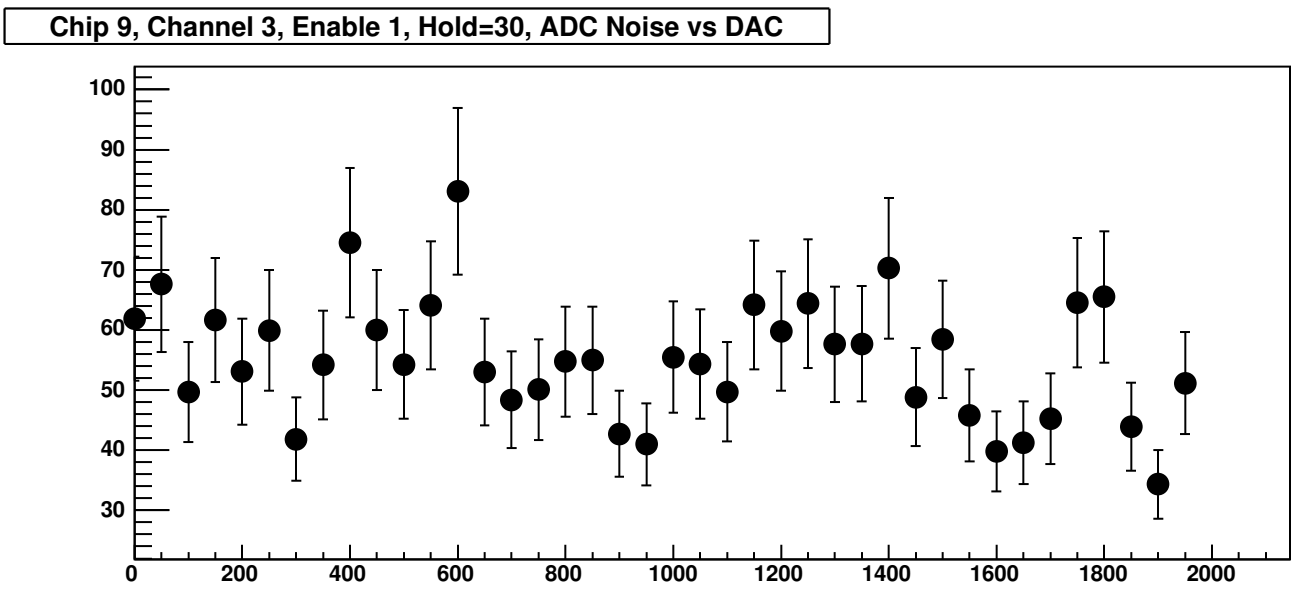
Chip 9, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC

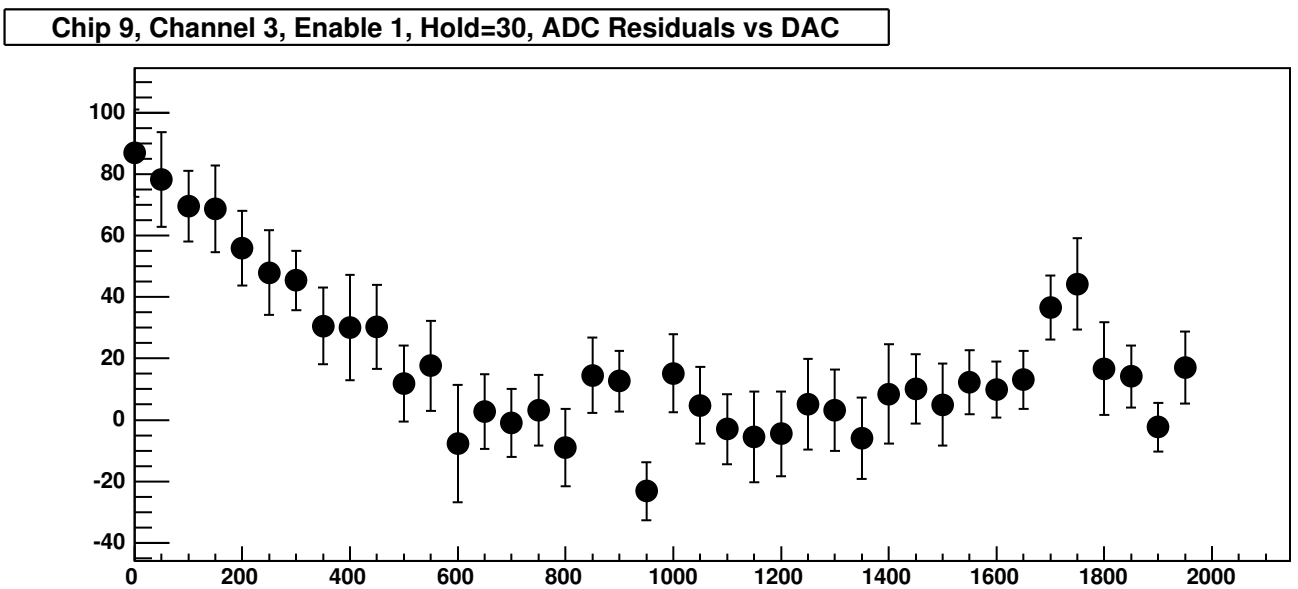


Chip 9, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC

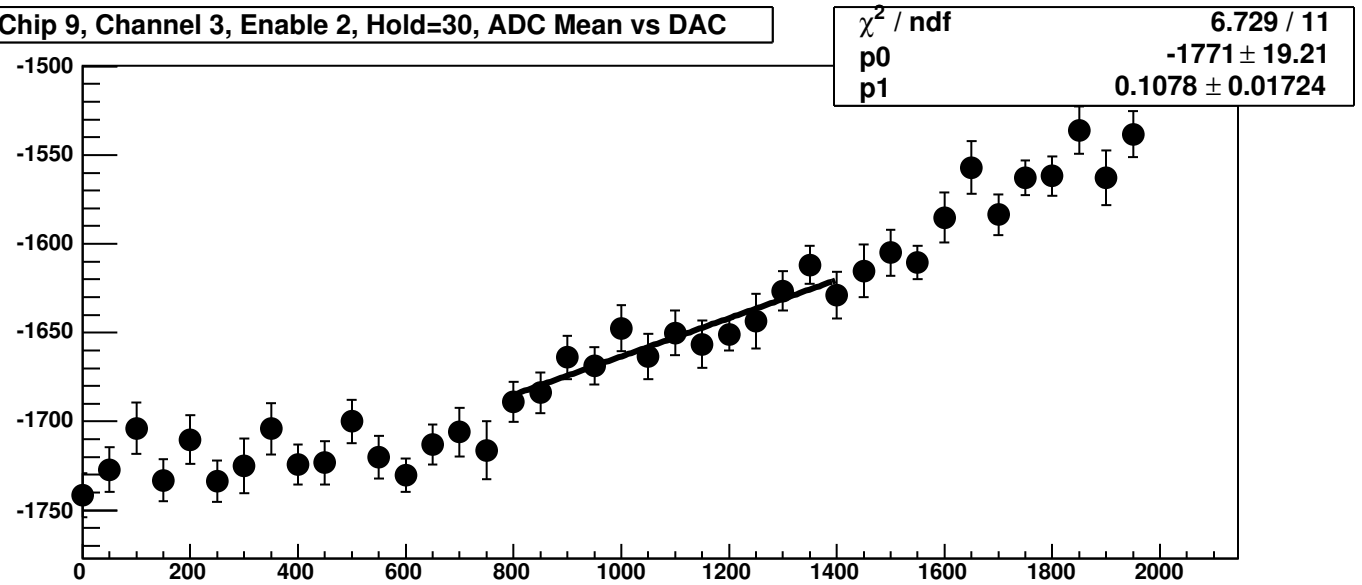



Chip 9, Channel 3, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 12.16 / 11
p0 -1787 ± 20.69
p1 0.1455 ± 0.01918
-1450
-1500
-1550
-1600
-1650
-1700
-1750
0
200
400
600
800
1000
1200
1400
1600
1800
2000

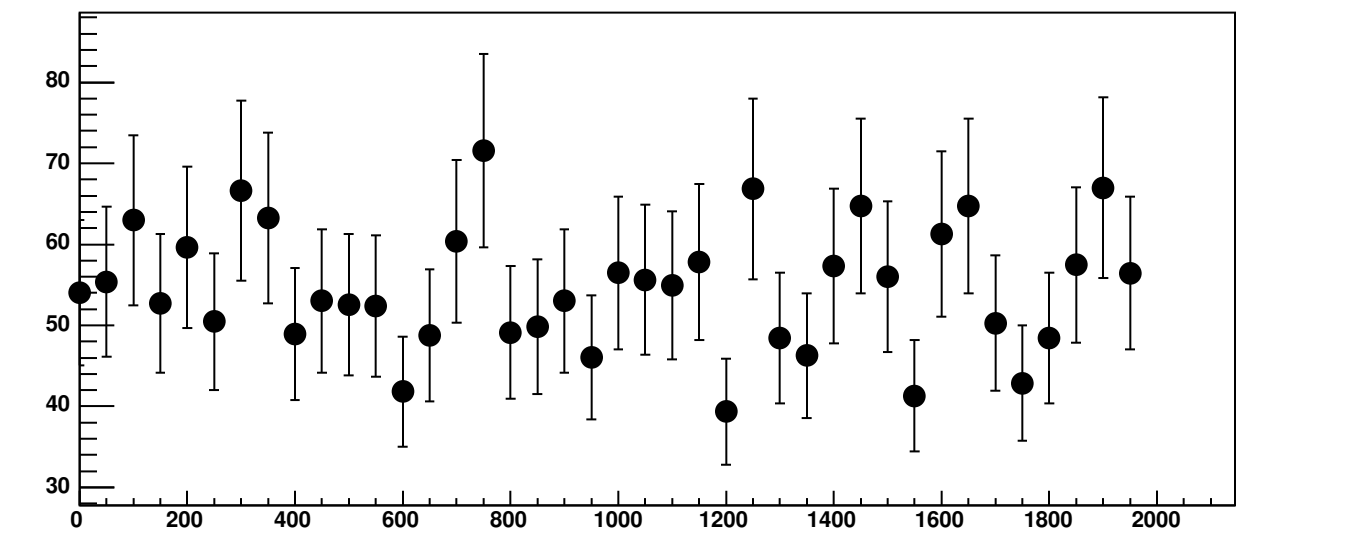

Chip 9, Channel 3, Enable 1, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 3, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

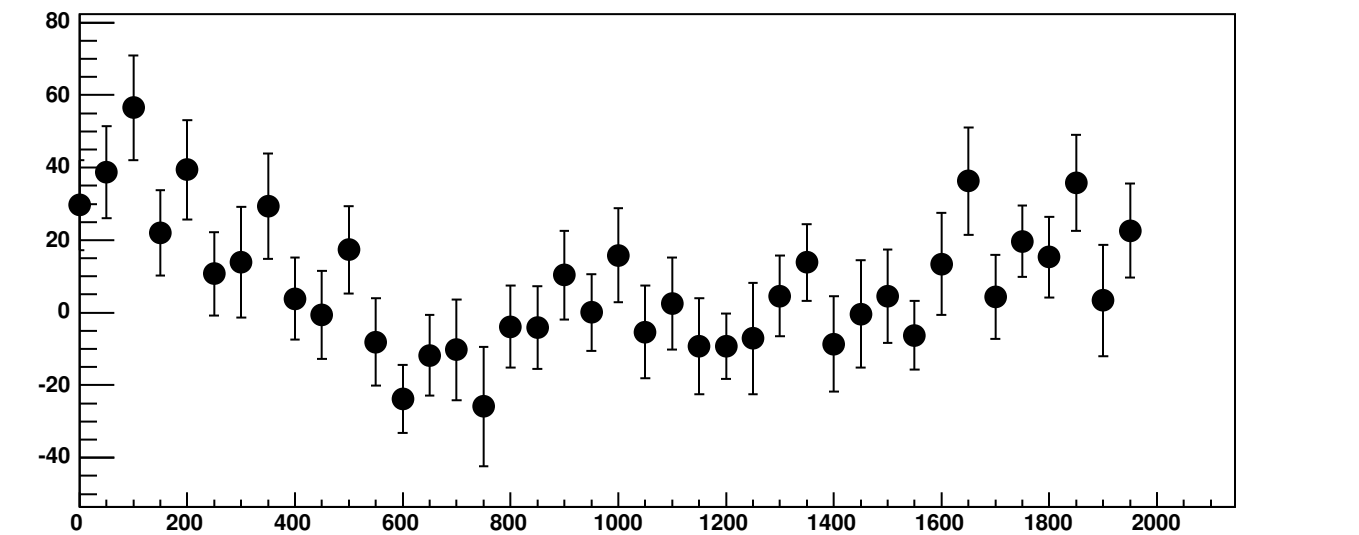
Chip 9, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC



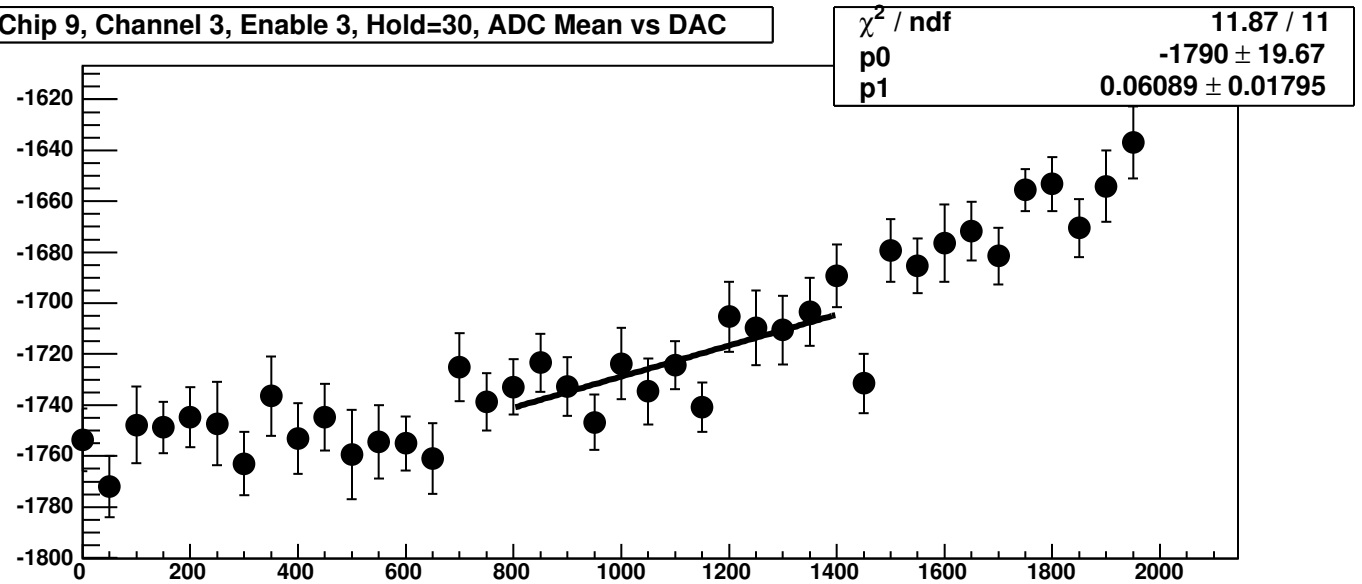
Chip 9, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC



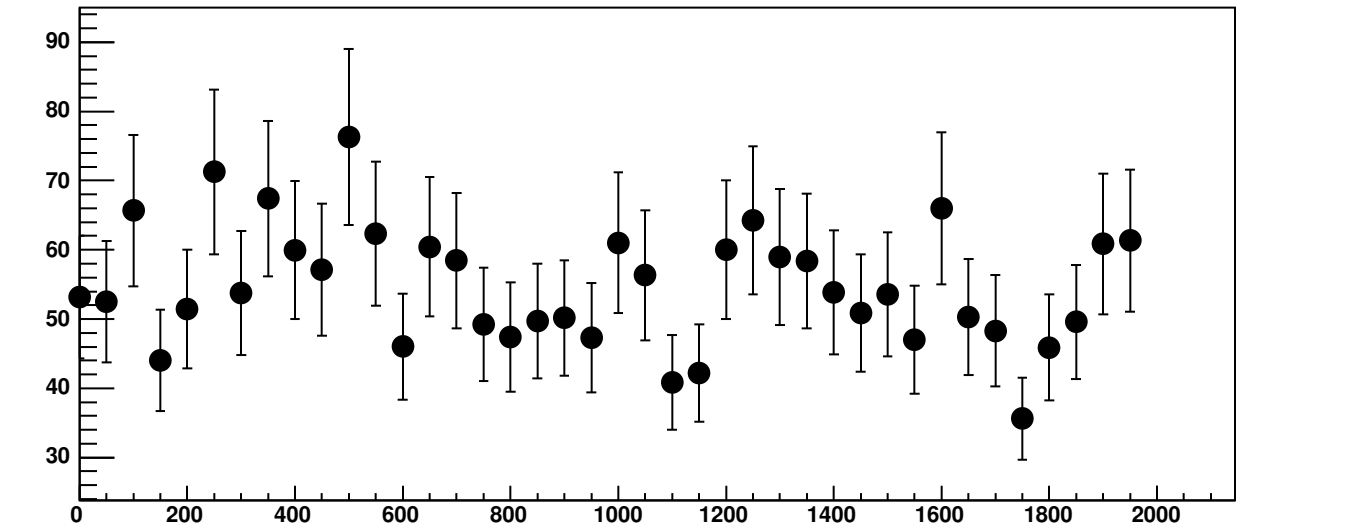
Chip 9, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC



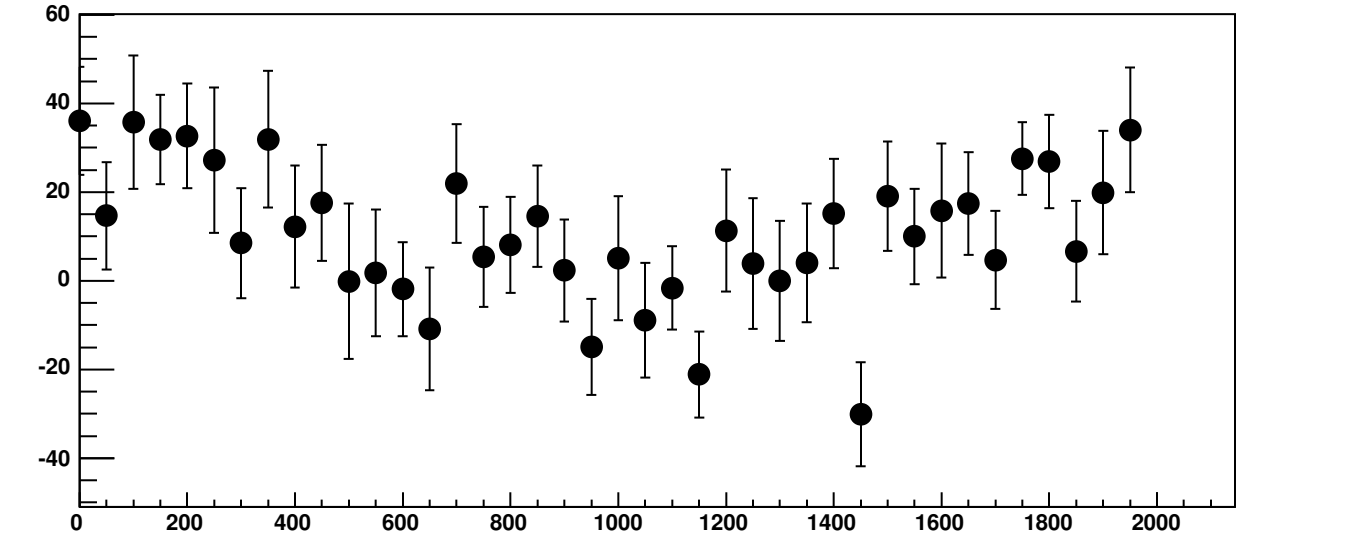
Chip 9, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC



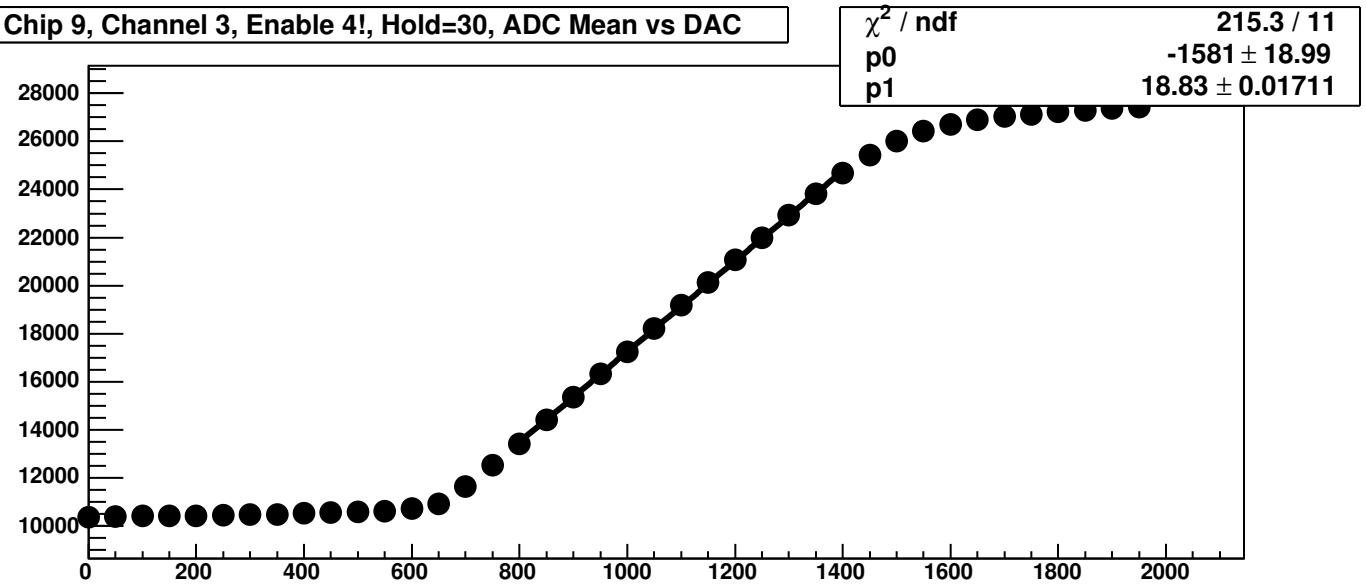
Chip 9, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC



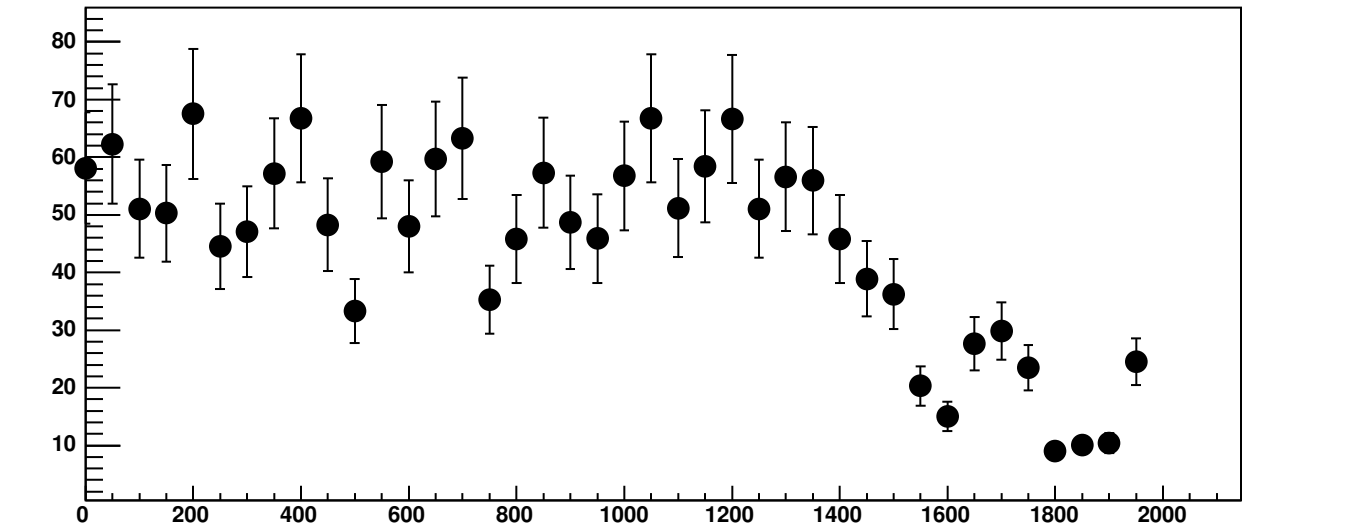
Chip 9, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC



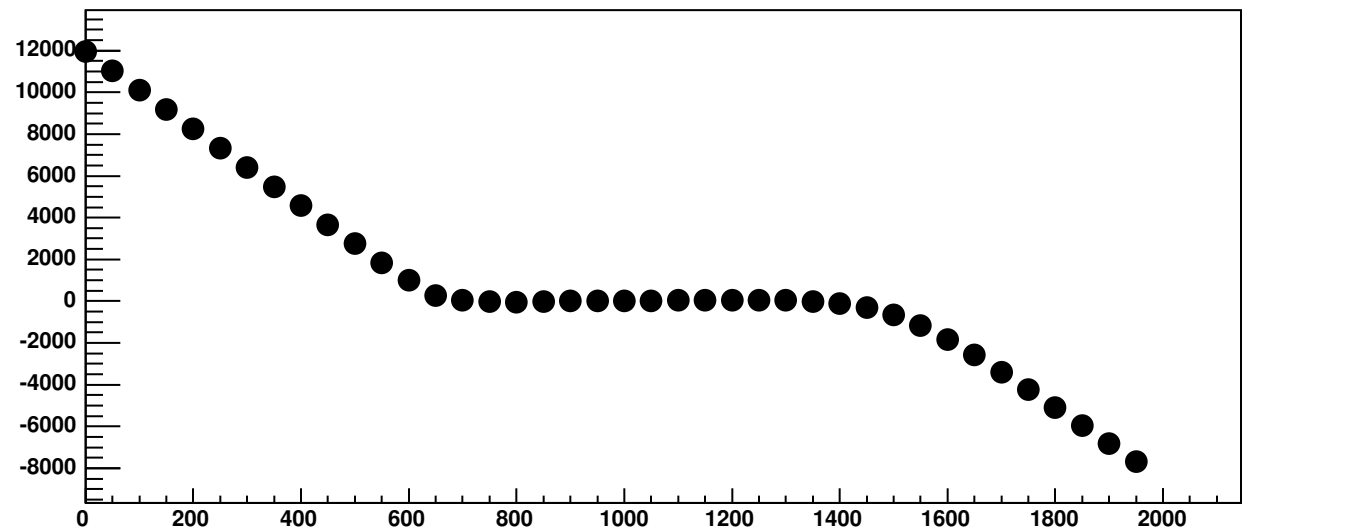
Chip 9, Channel 3, Enable 4!, Hold=30, ADC Mean vs DAC

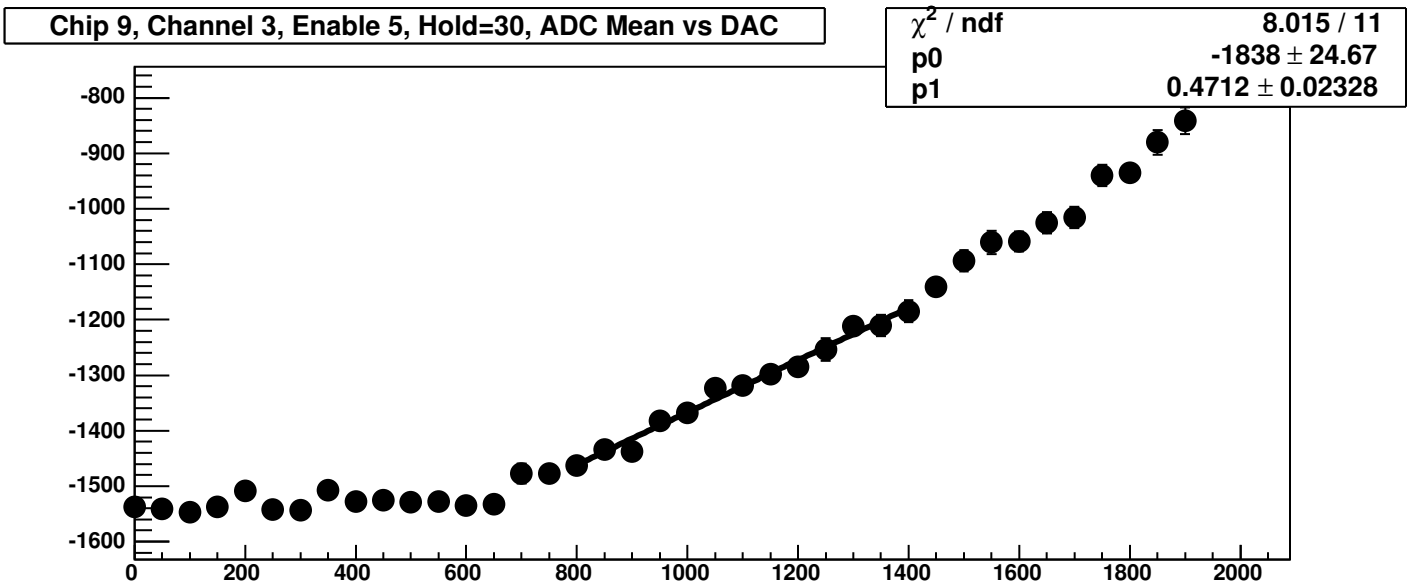


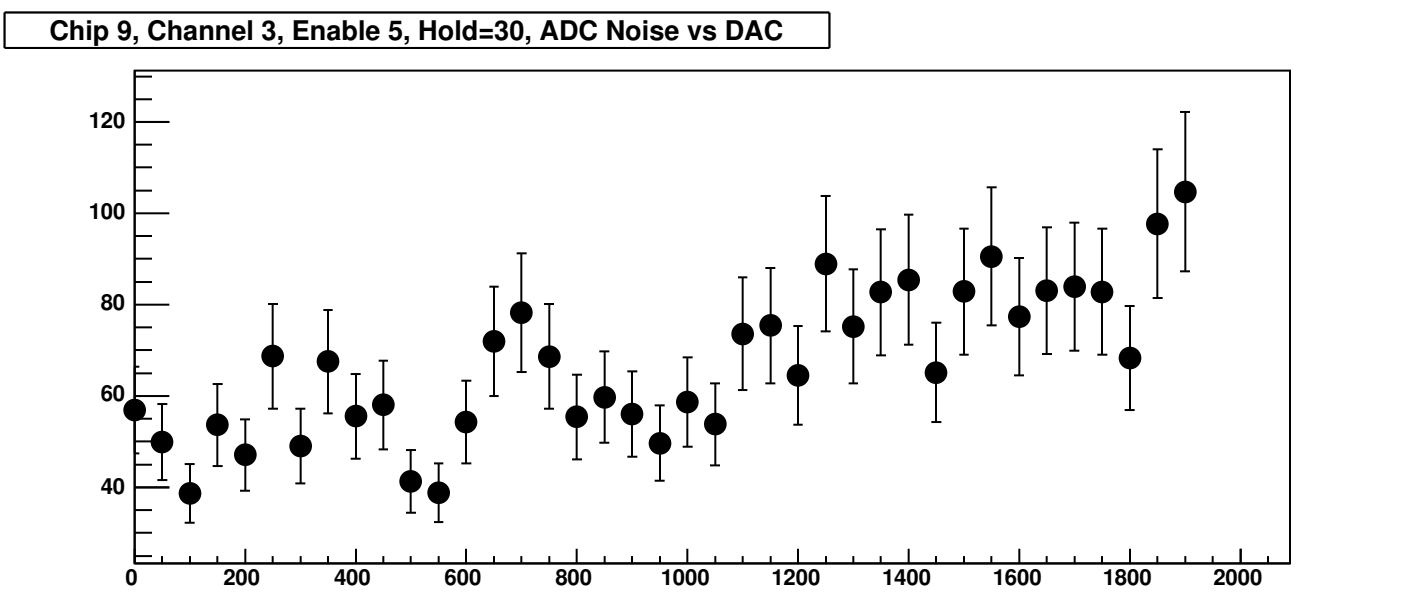
Chip 9, Channel 3, Enable 4!, Hold=30, ADC Noise vs DAC

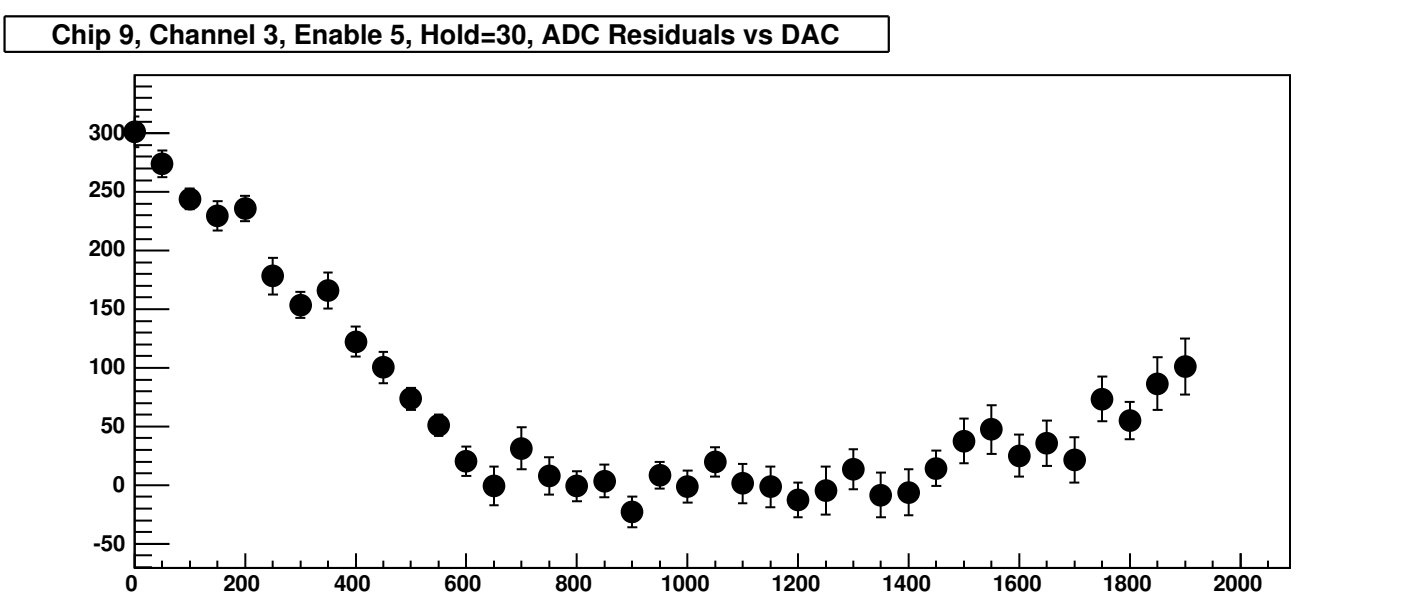


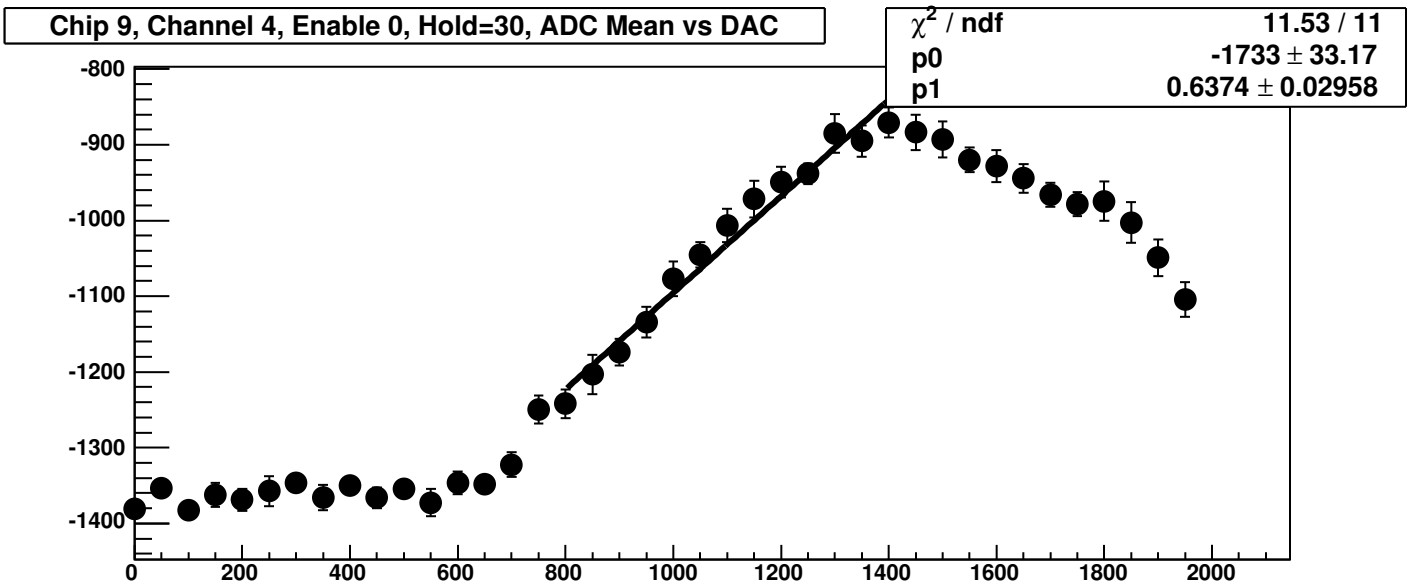
Chip 9, Channel 3, Enable 4!, Hold=30, ADC Residuals vs DAC

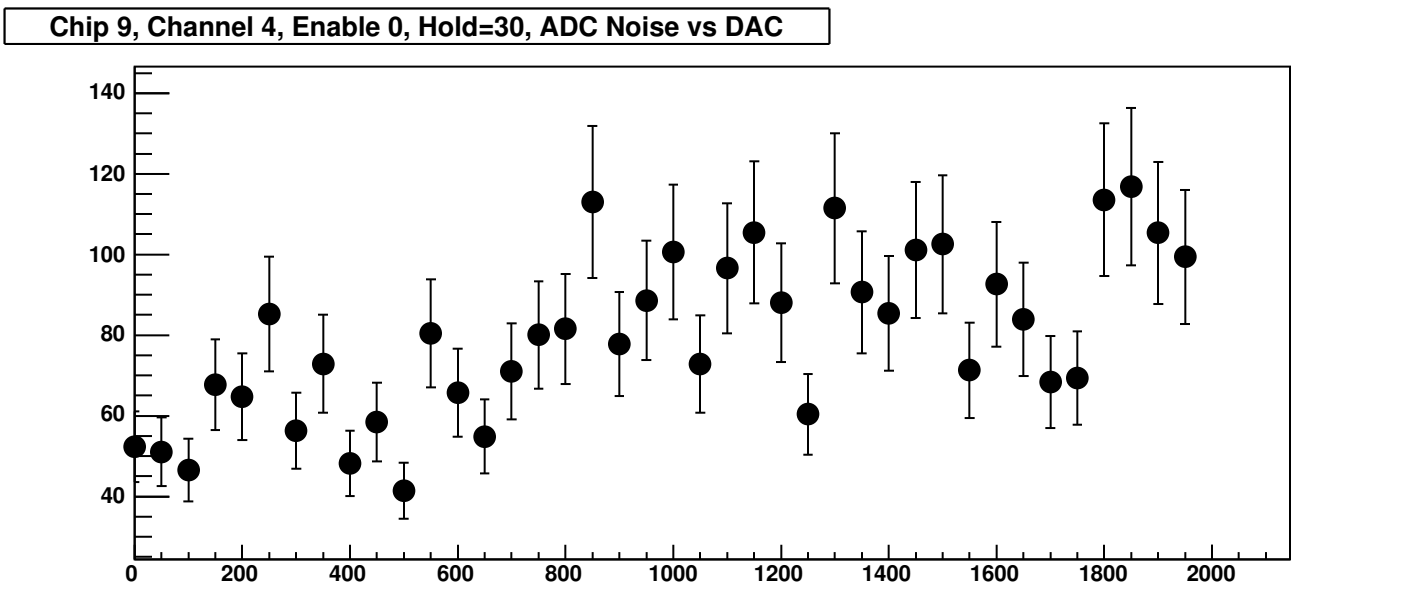


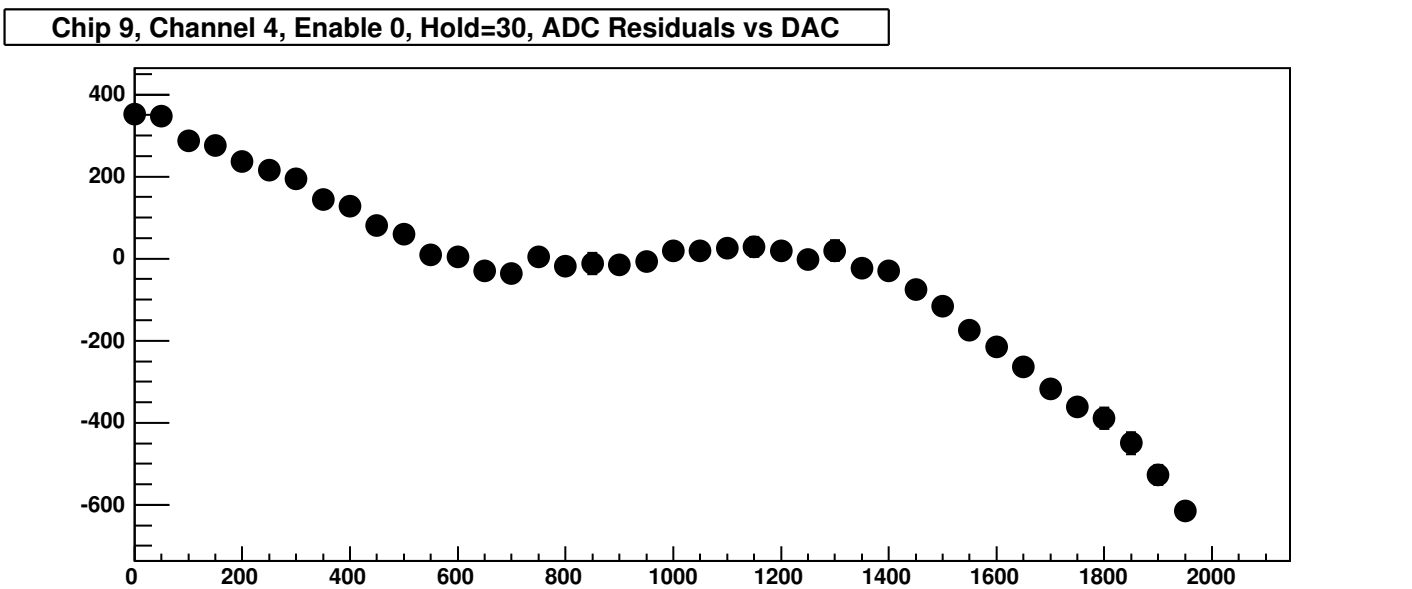

Chip 9, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8.015 / 11
p0 -1838 ± 24.67
p1 0.4712 ± 0.02328
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

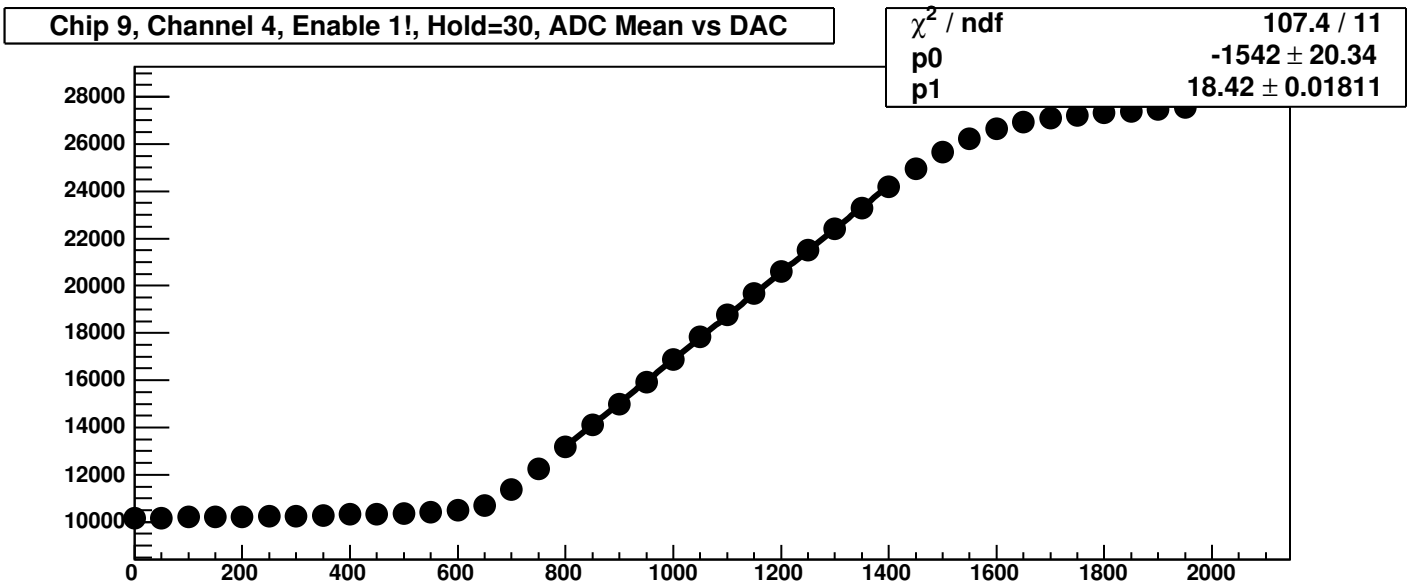

Chip 9, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

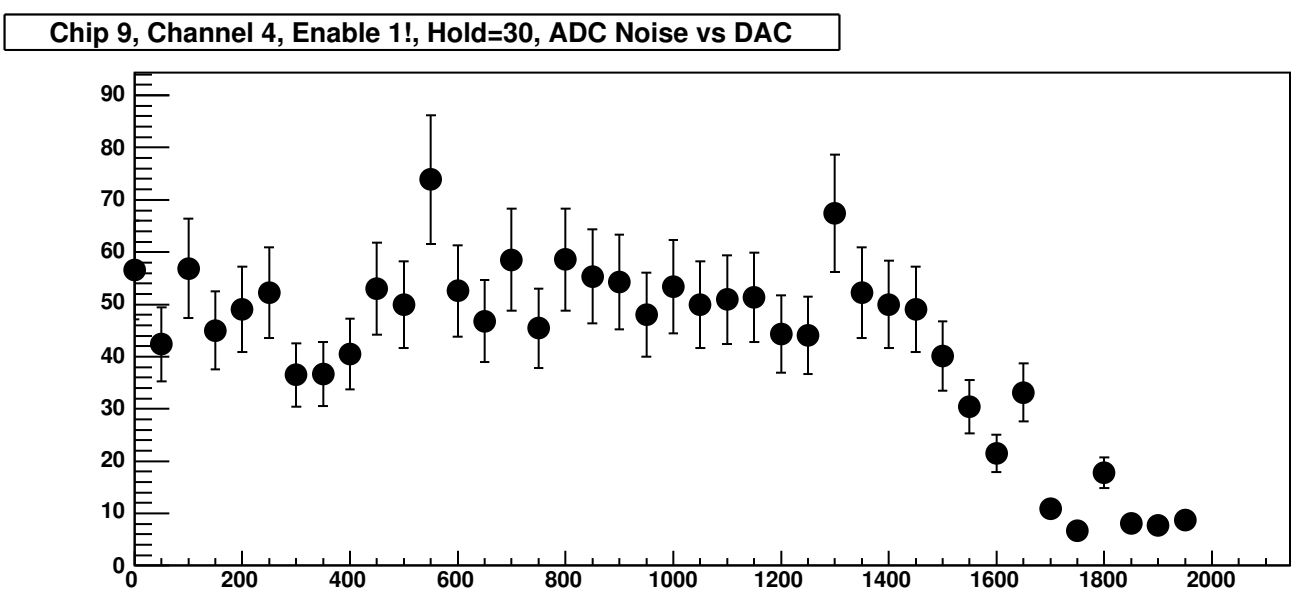

Chip 9, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

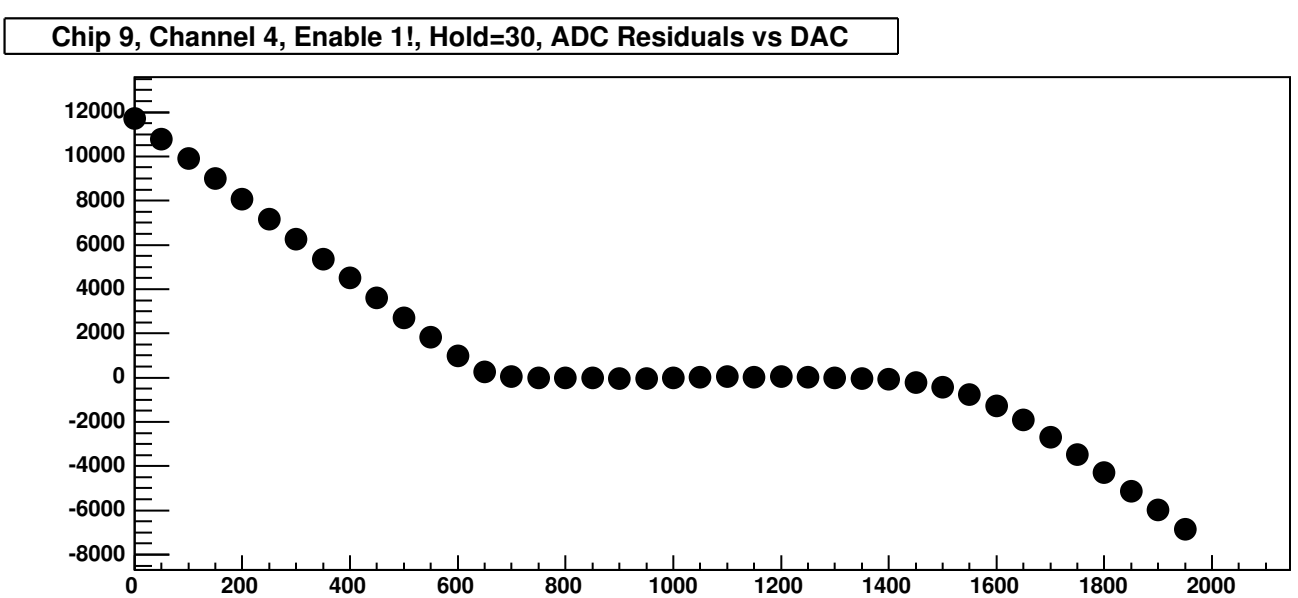

Chip 9, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 11.53 / 11
p0 -1733 ± 33.17
p1 0.6374 ± 0.02958
-800
-900
-1000
-1100
-1200
-1300
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

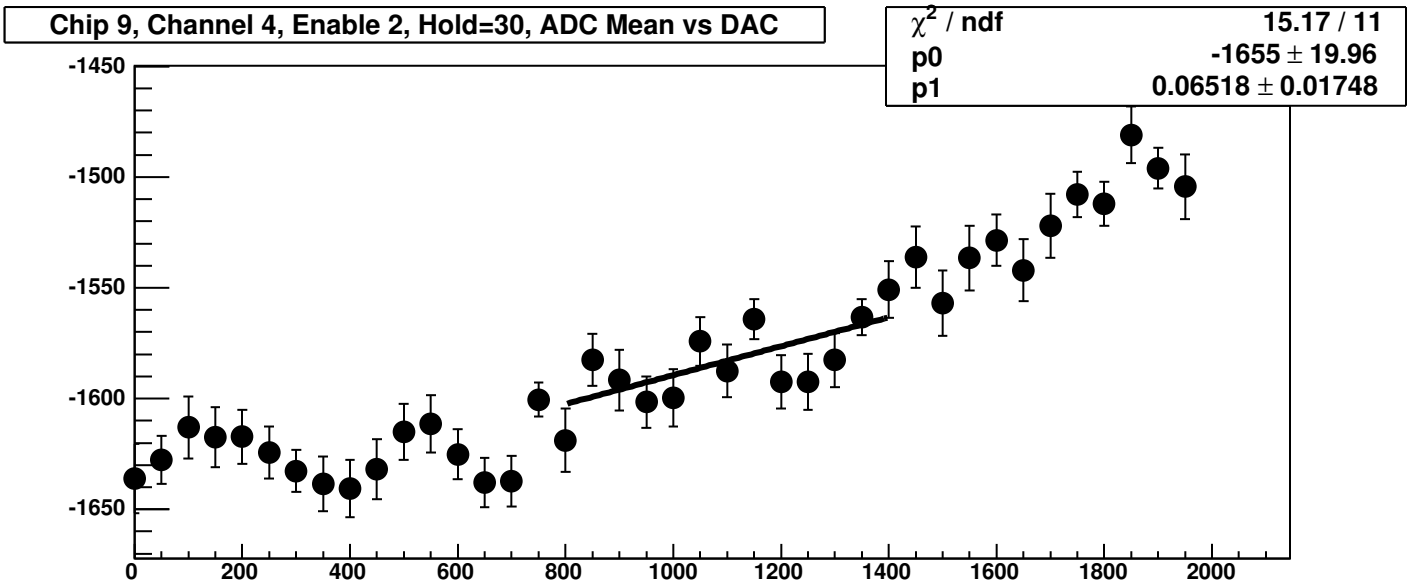

Chip 9, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

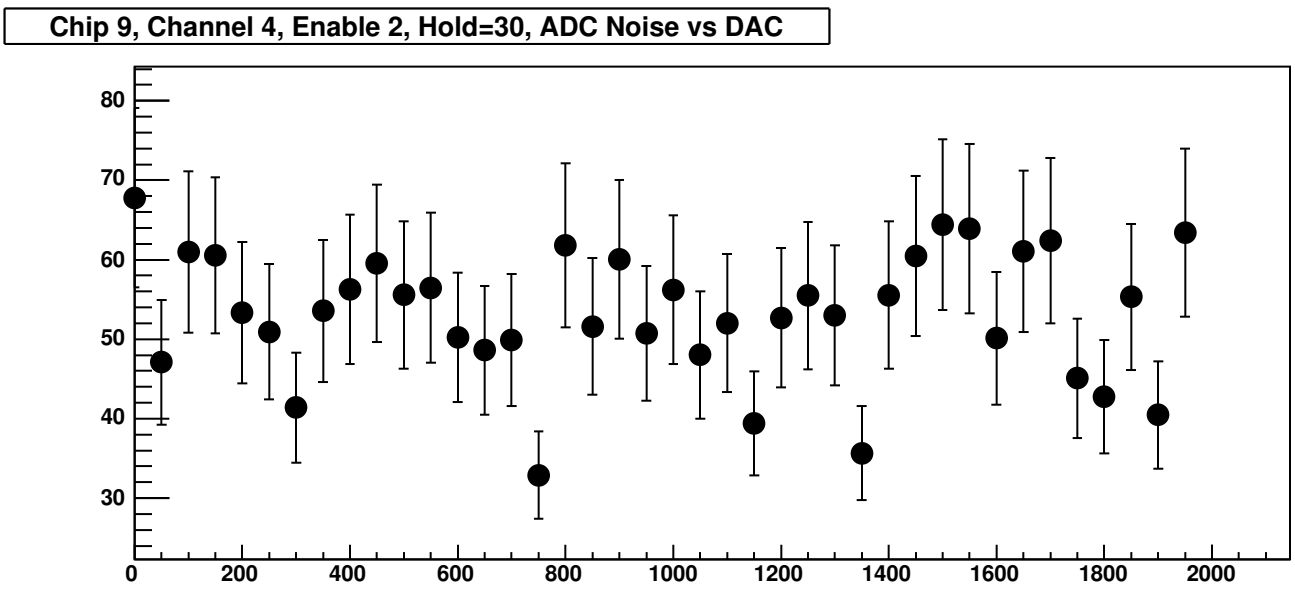

Chip 9, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

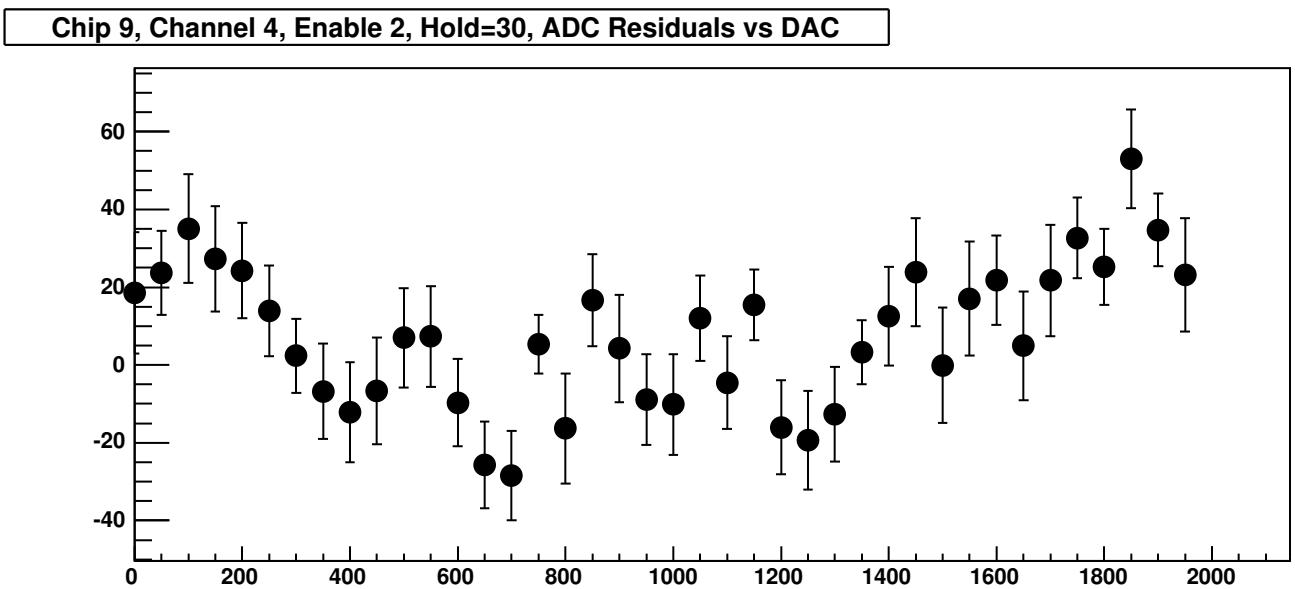

Chip 9, Channel 4, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 107.4 / 11
p0 -1542 ± 20.34
p1 18.42 ± 0.01811
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

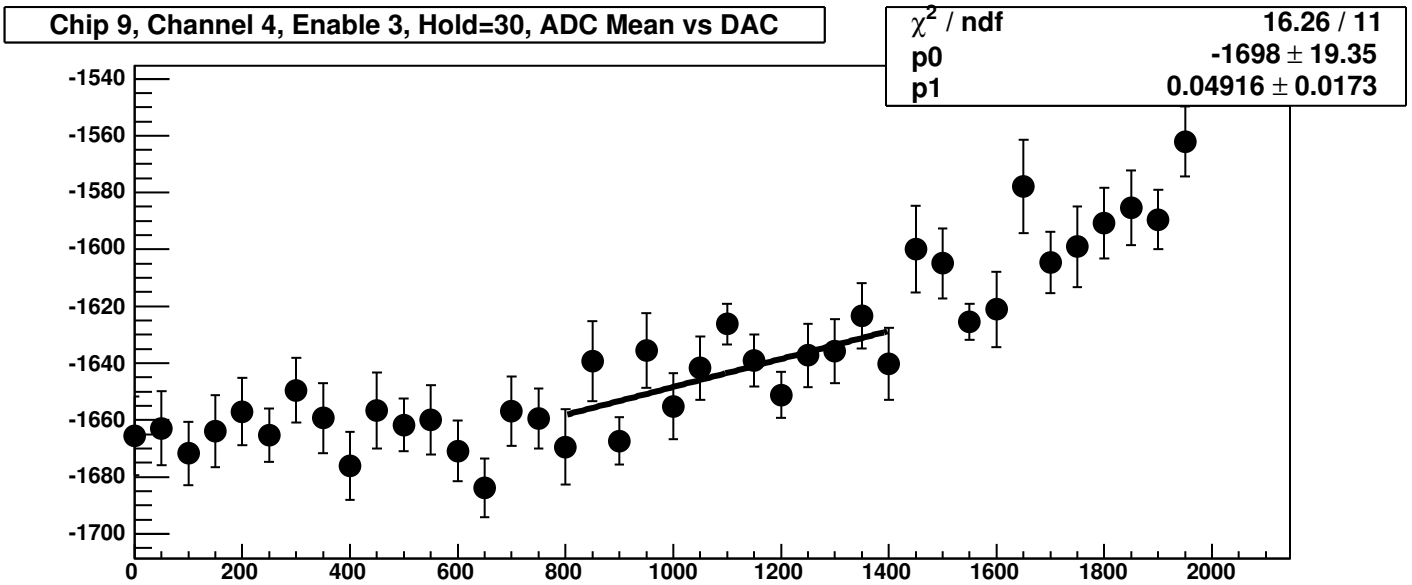

Chip 9, Channel 4, Enable 1!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

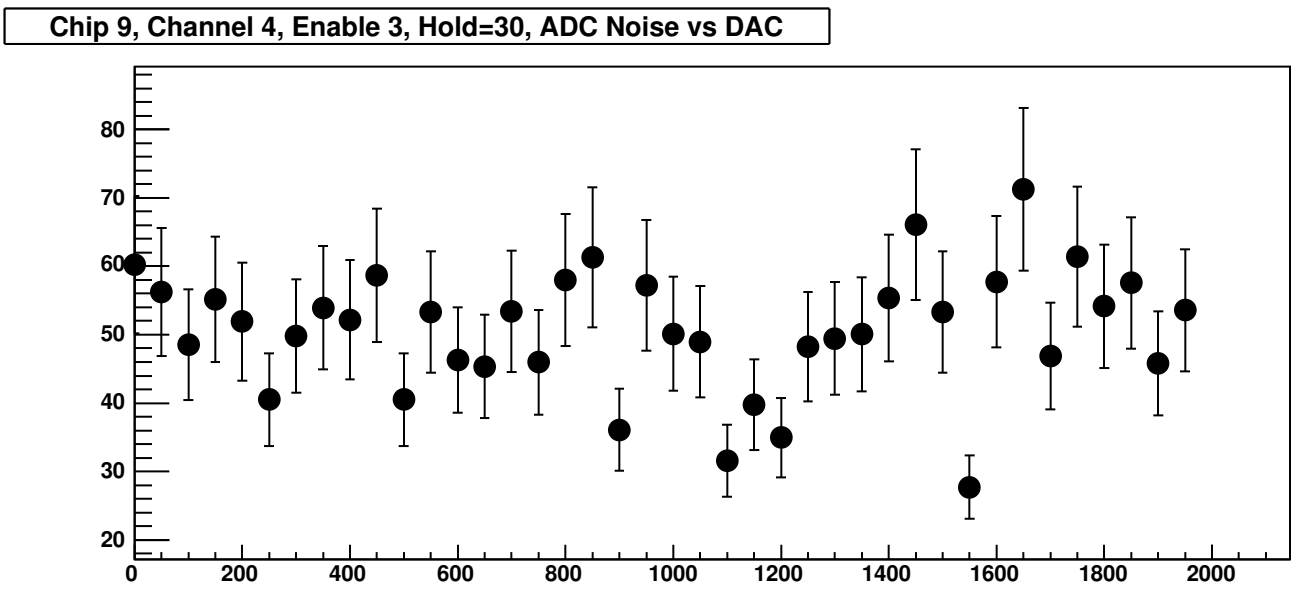

Chip 9, Channel 4, Enable 1!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

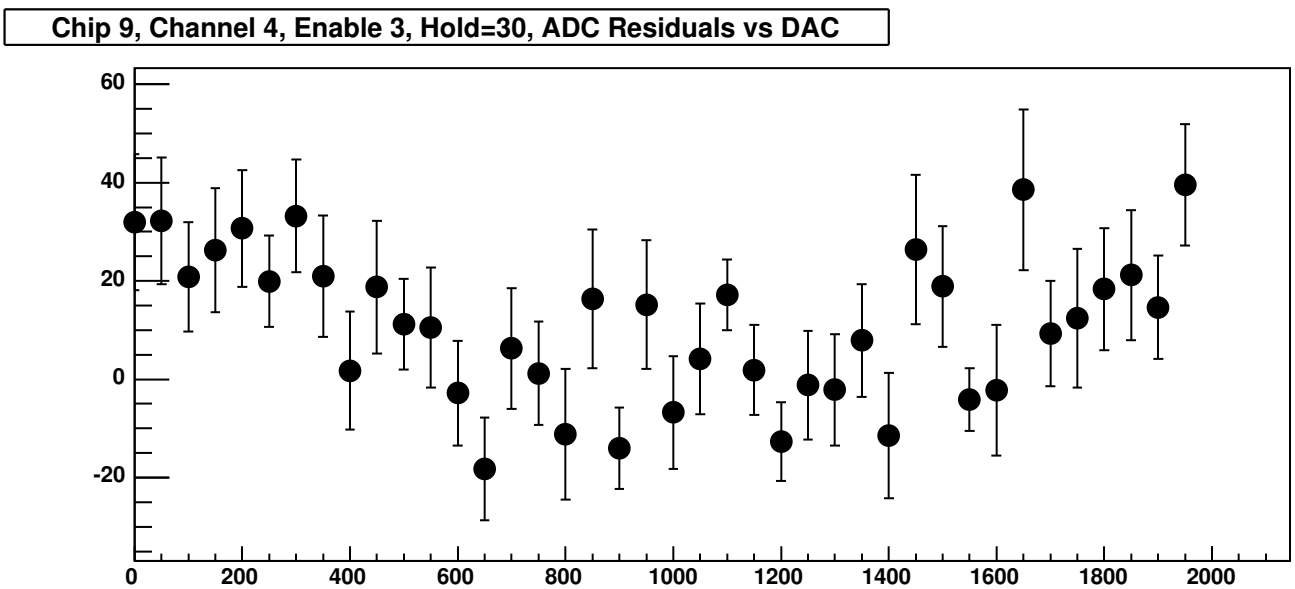

Chip 9, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 15.17 / 11
p0 -1655 ± 19.96
p1 0.06518 ± 0.01748
-1450
-1500
-1550
-1600
-1650
0
200
400
600
800
1000
1200
1400
1600
1800
2000

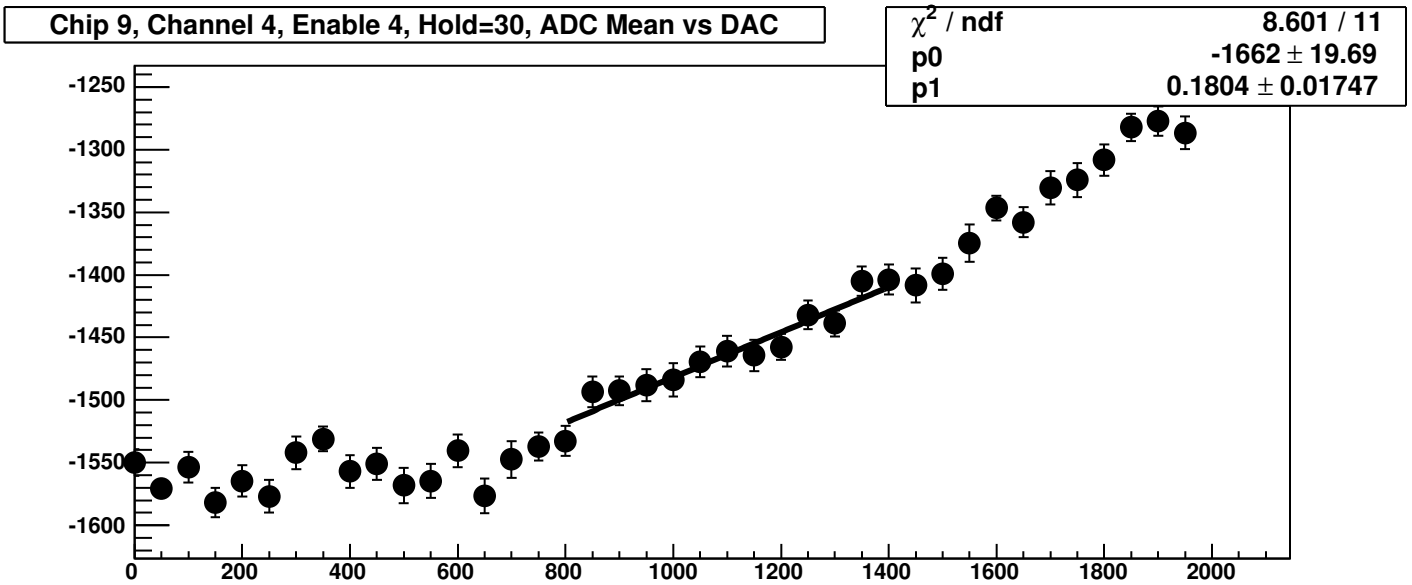

Chip 9, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

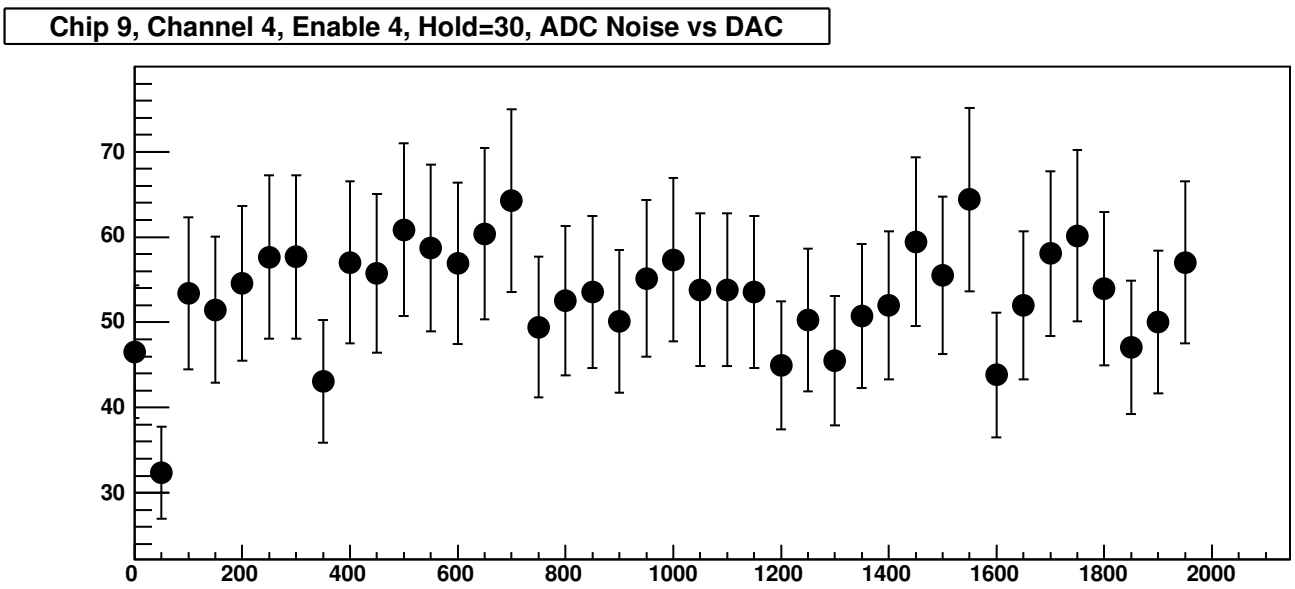

Chip 9, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

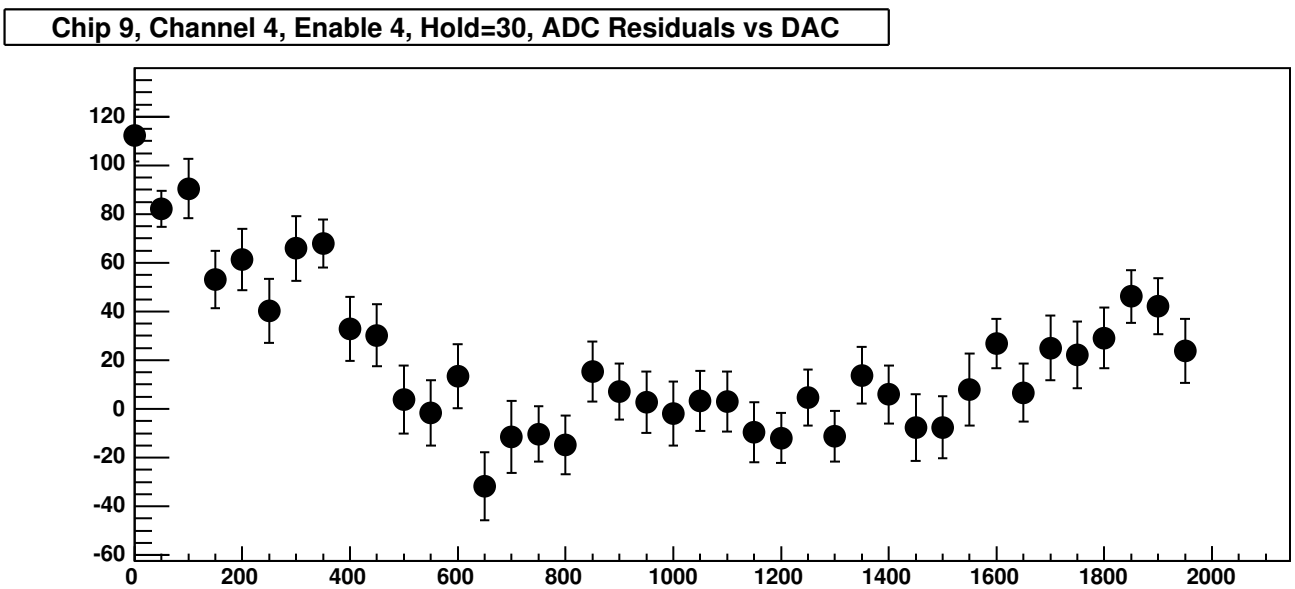

Chip 9, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 16.26 / 11
p0 -1698 ± 19.35
p1 0.04916 ± 0.0173
-1540
-1560
-1580
-1600
-1620
-1640
-1660
-1680
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

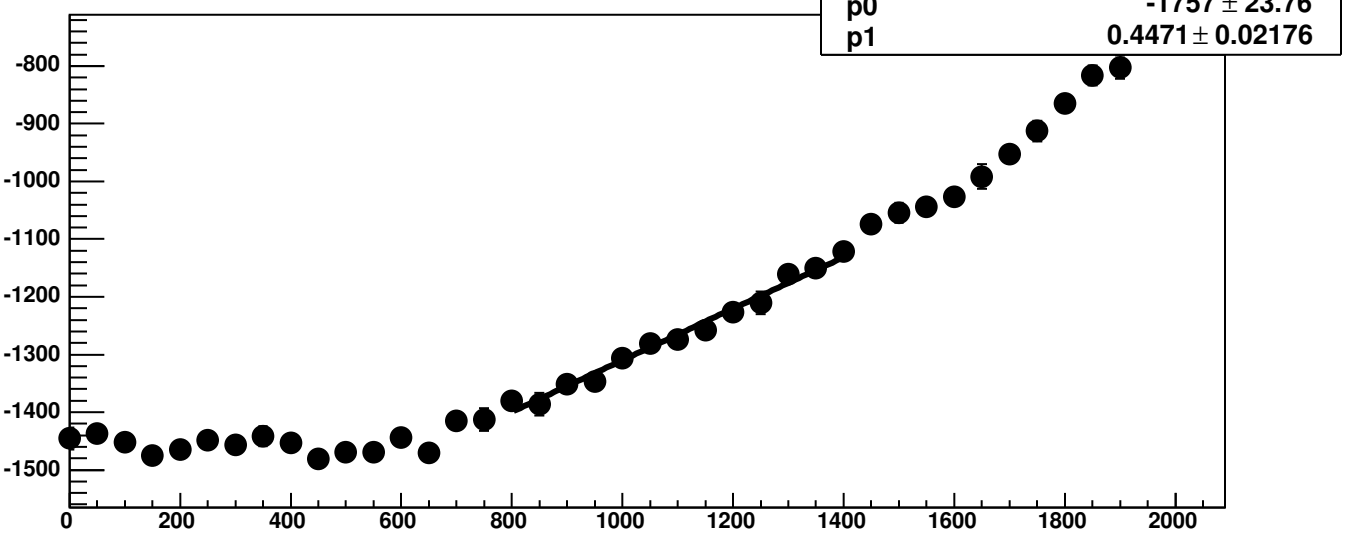

Chip 9, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 4, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 8.601 / 11
p0 -1662 ± 19.69
p1 0.1804 ± 0.01747
-1250
-1300
-1350
-1400
-1450
-1500
-1550
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

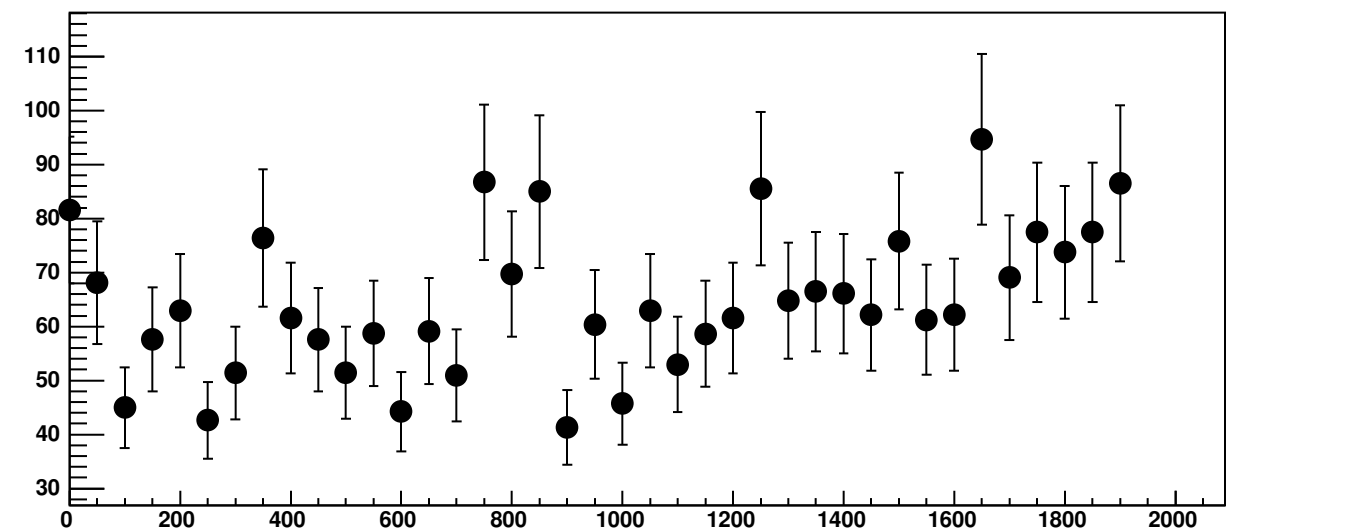

Chip 9, Channel 4, Enable 4, Hold=30, ADC Noise vs DAC
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 4, Enable 4, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

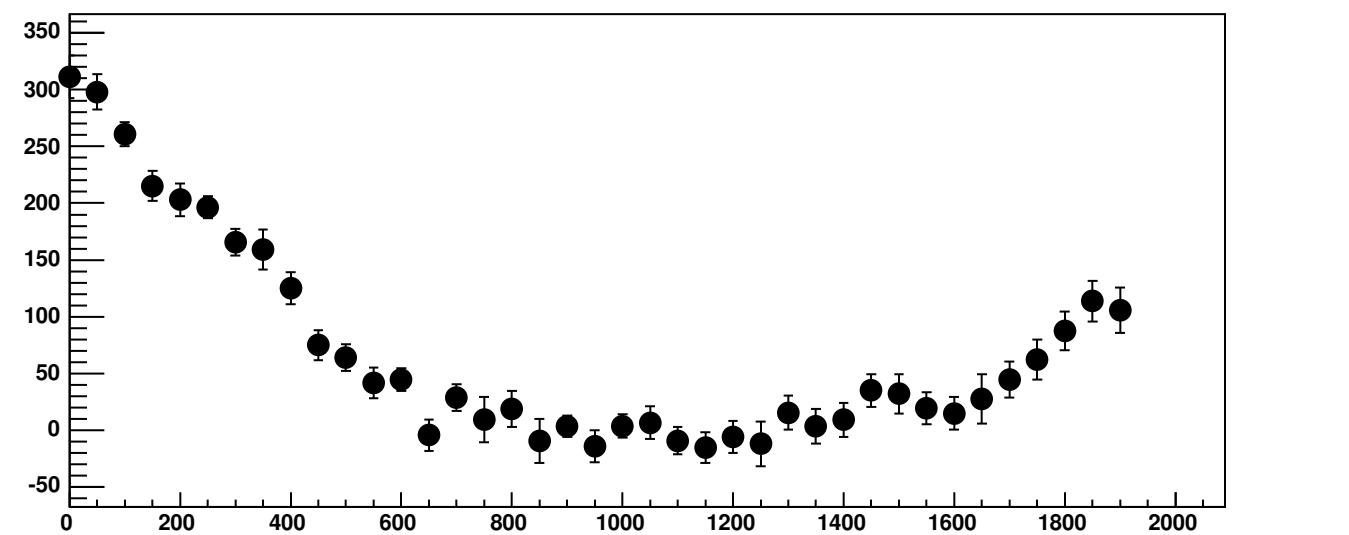
Chip 9, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC

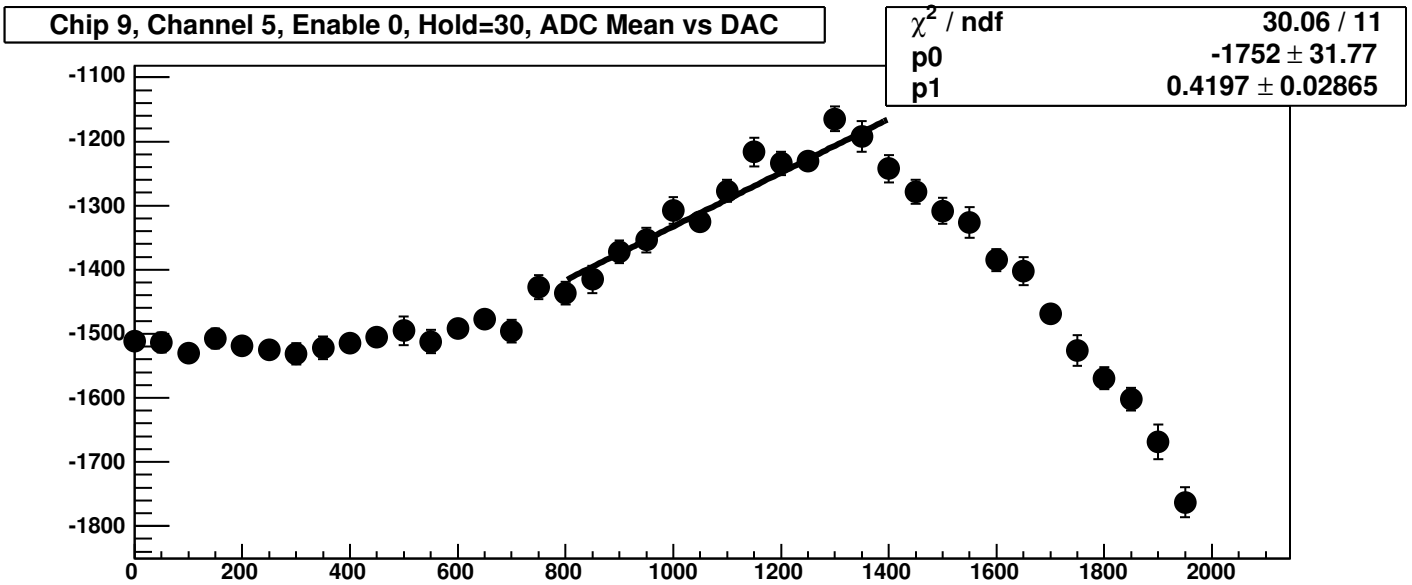


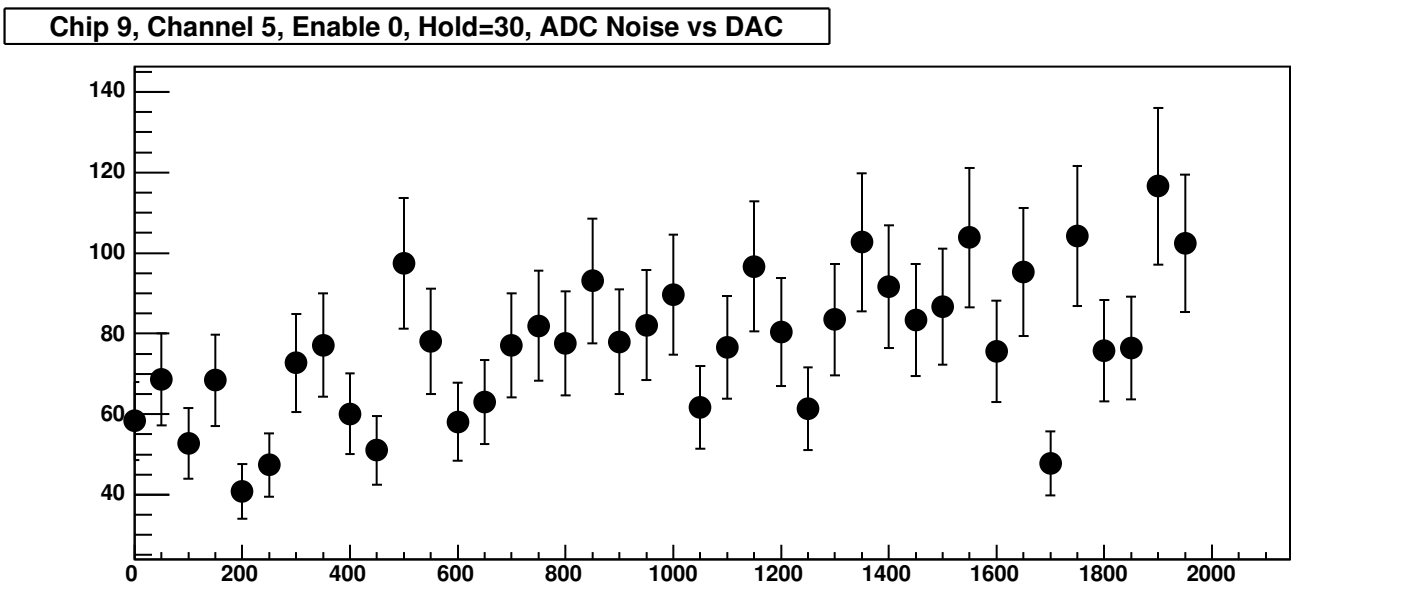
Chip 9, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC

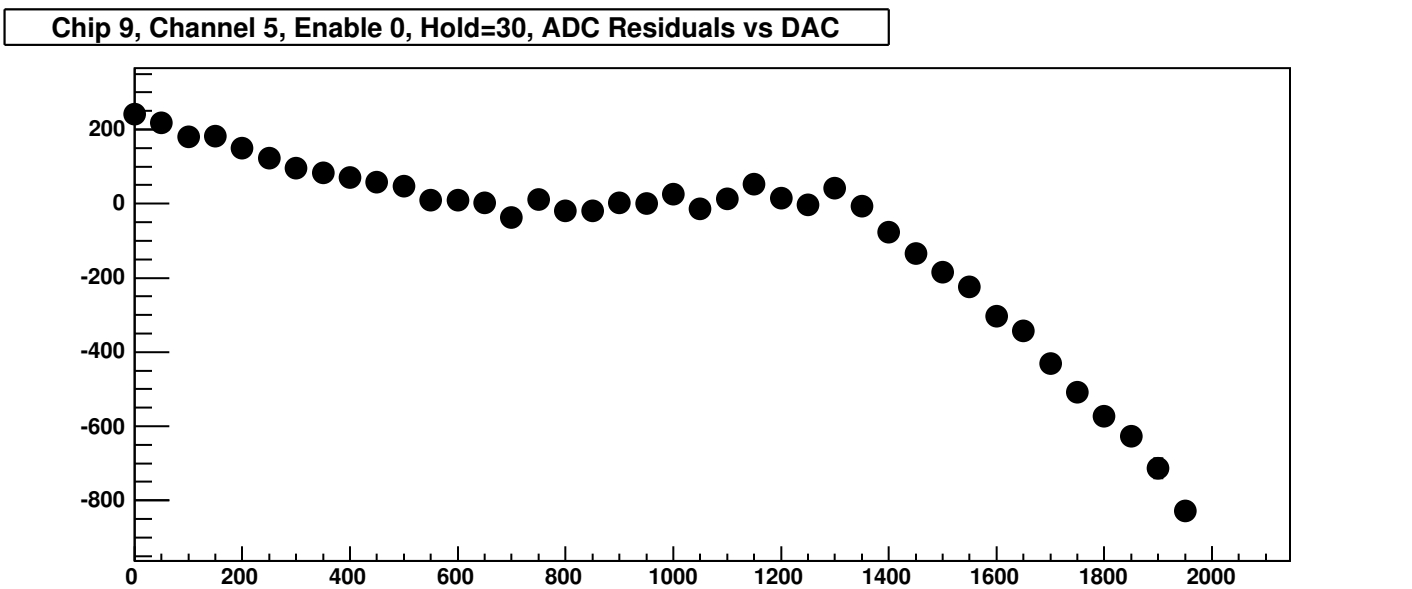


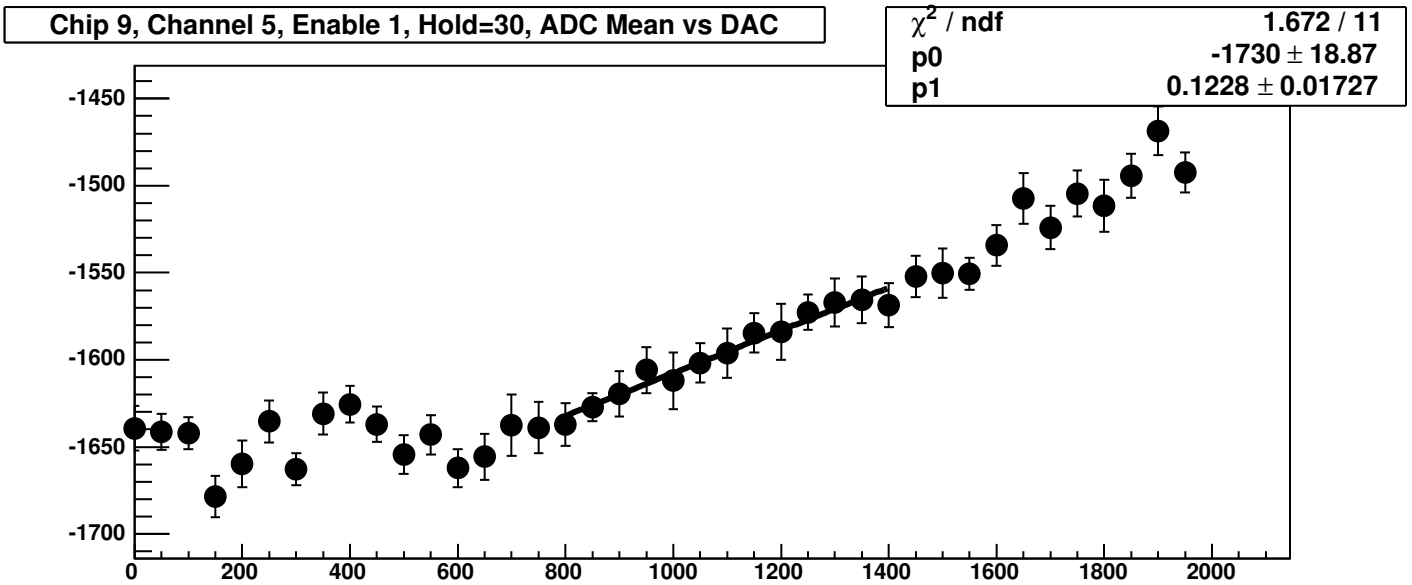
Chip 9, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC

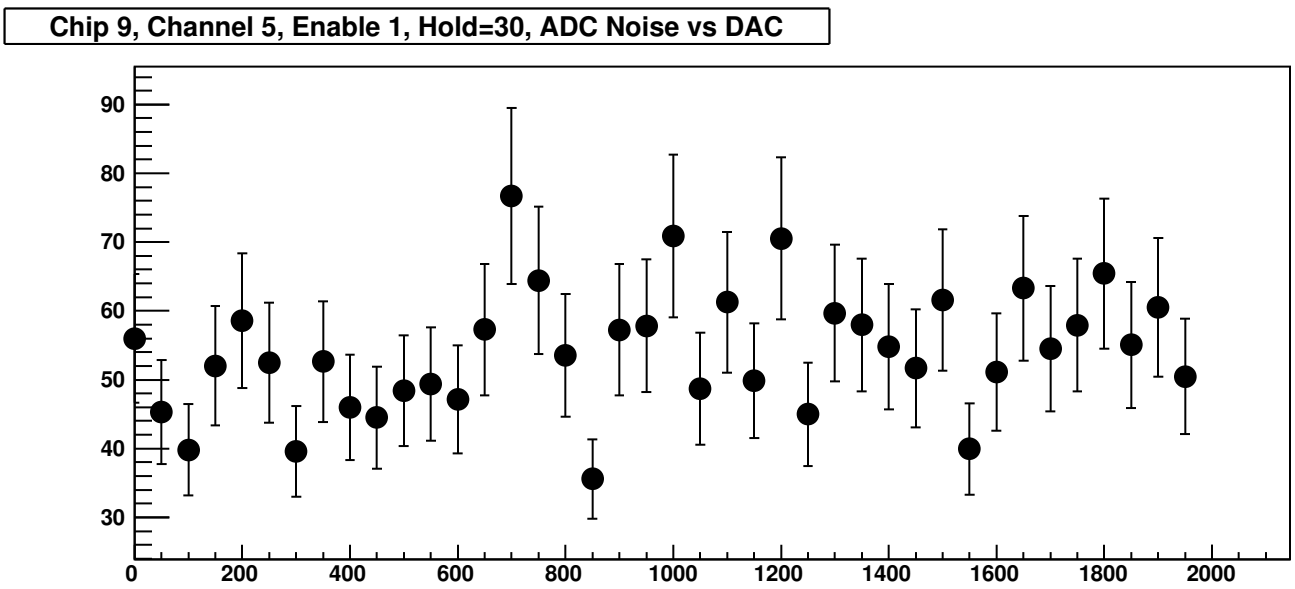


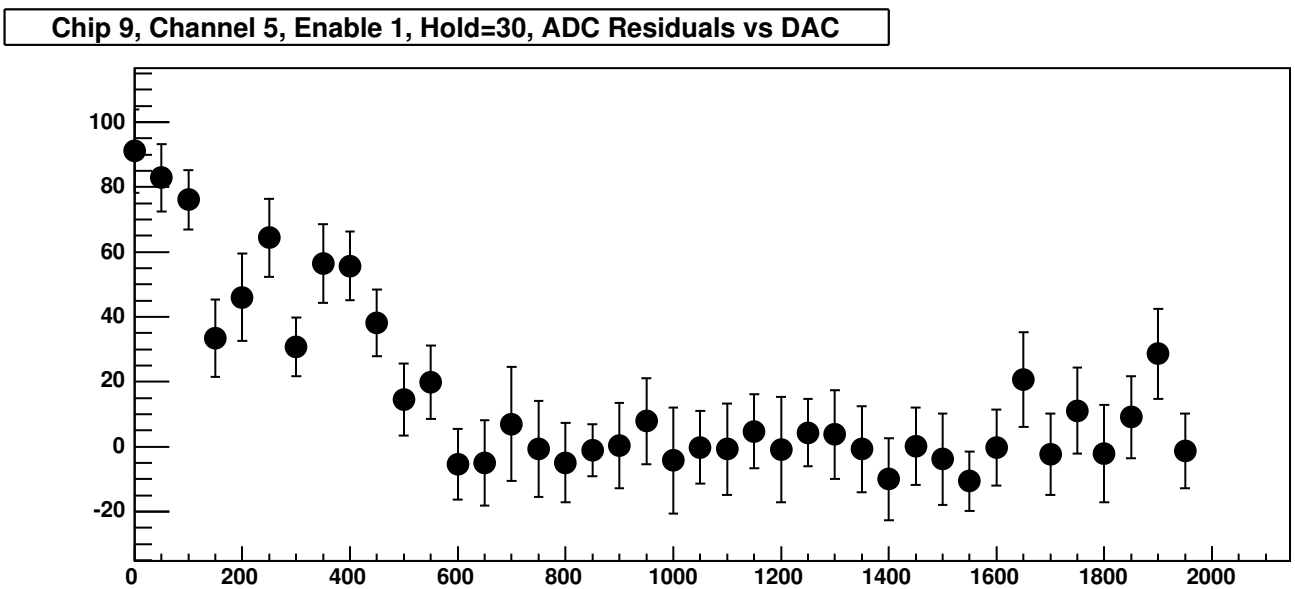

Chip 9, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 30.06 / 11
p0 -1752 ± 31.77
p1 0.4197 ± 0.02865
-1100
-1200
-1300
-1400
-1500
-1600
-1700
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

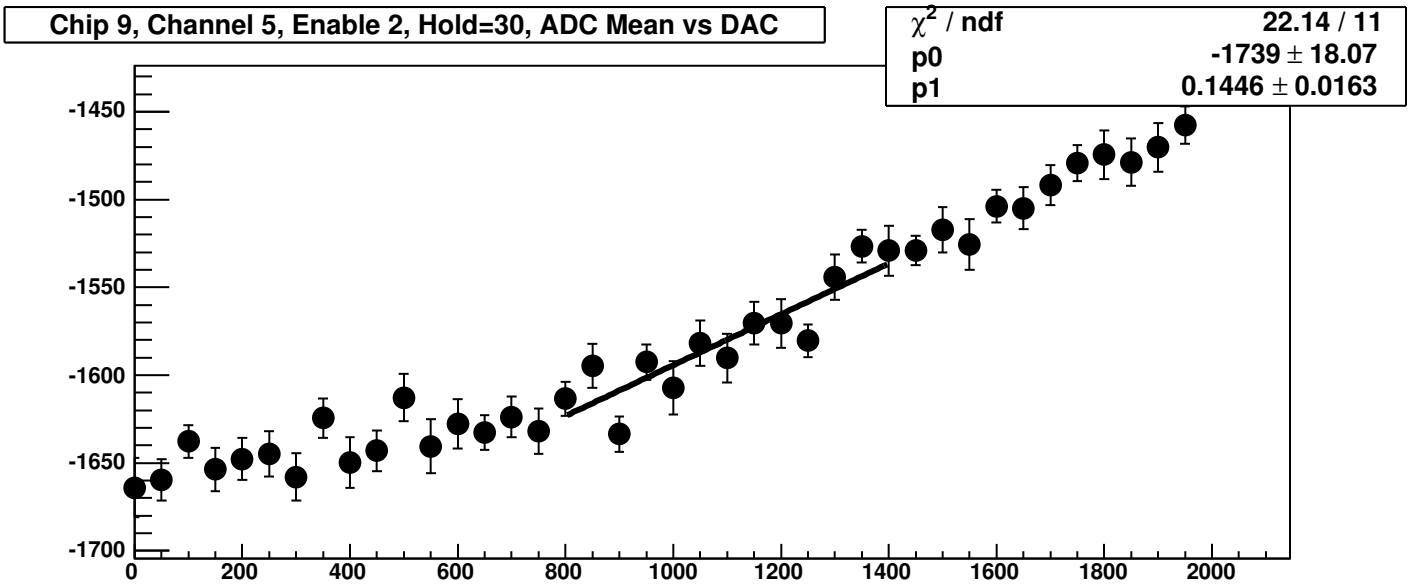

Chip 9, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

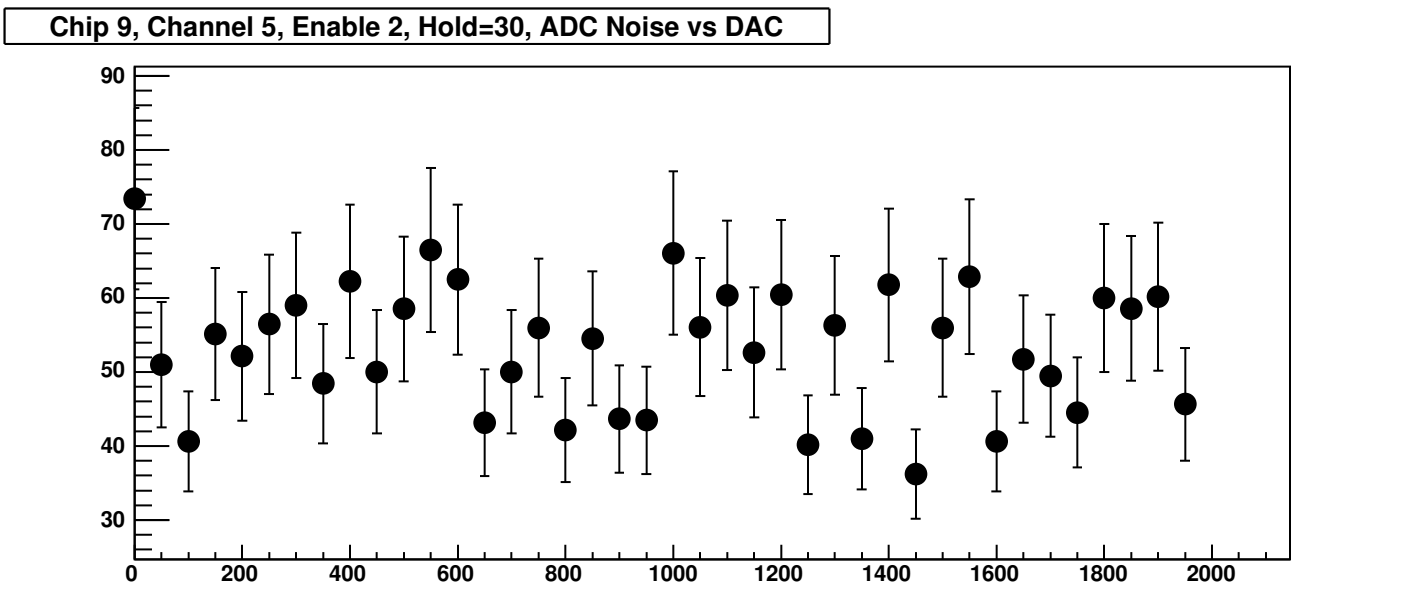

Chip 9, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

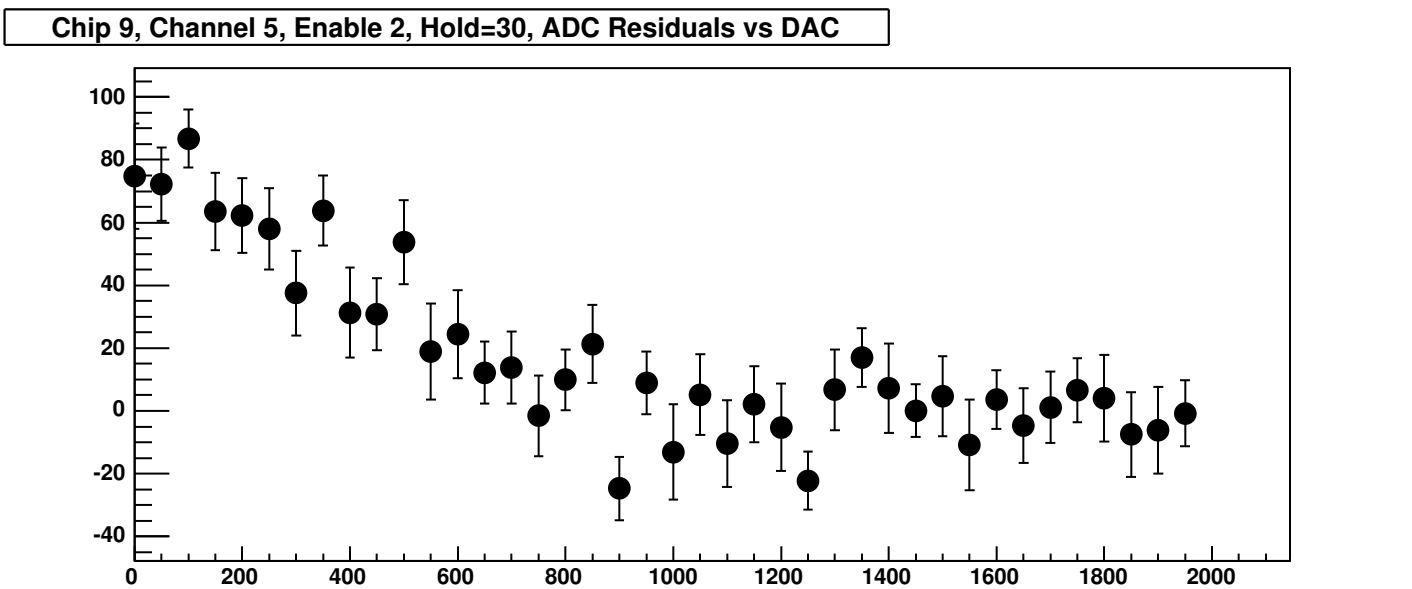

Chip 9, Channel 5, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 1.672 / 11
p0 -1730 ± 18.87
p1 0.1228 ± 0.01727
-1450
-1500
-1550
-1600
-1650
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

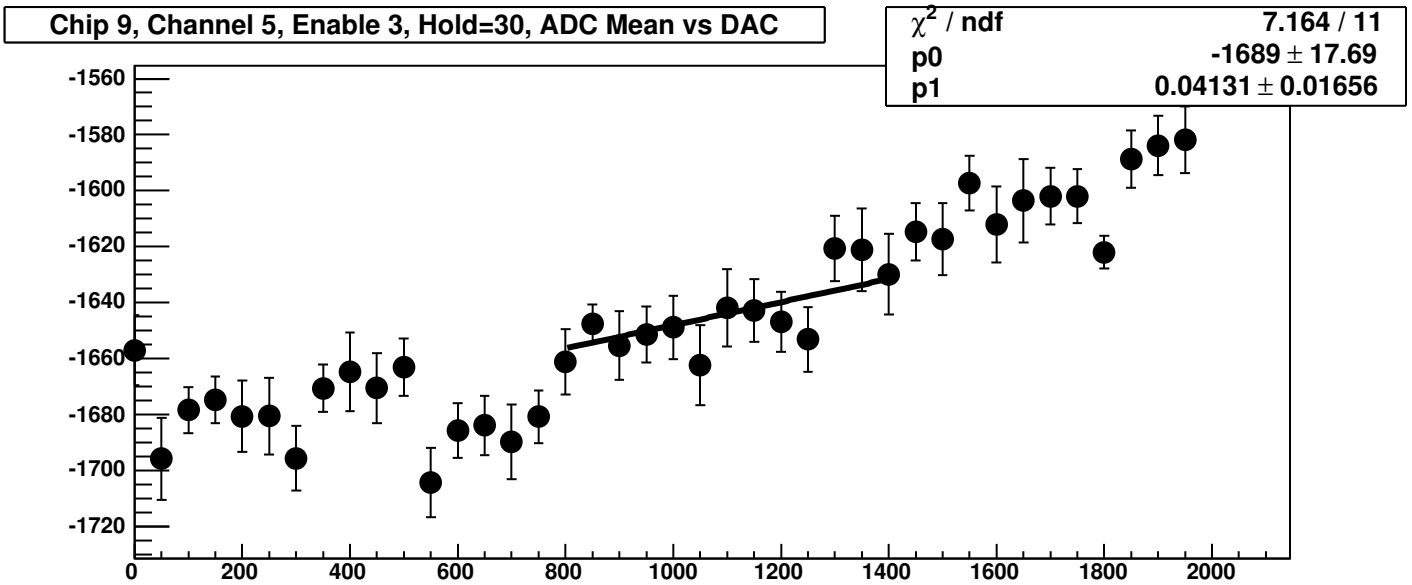

Chip 9, Channel 5, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

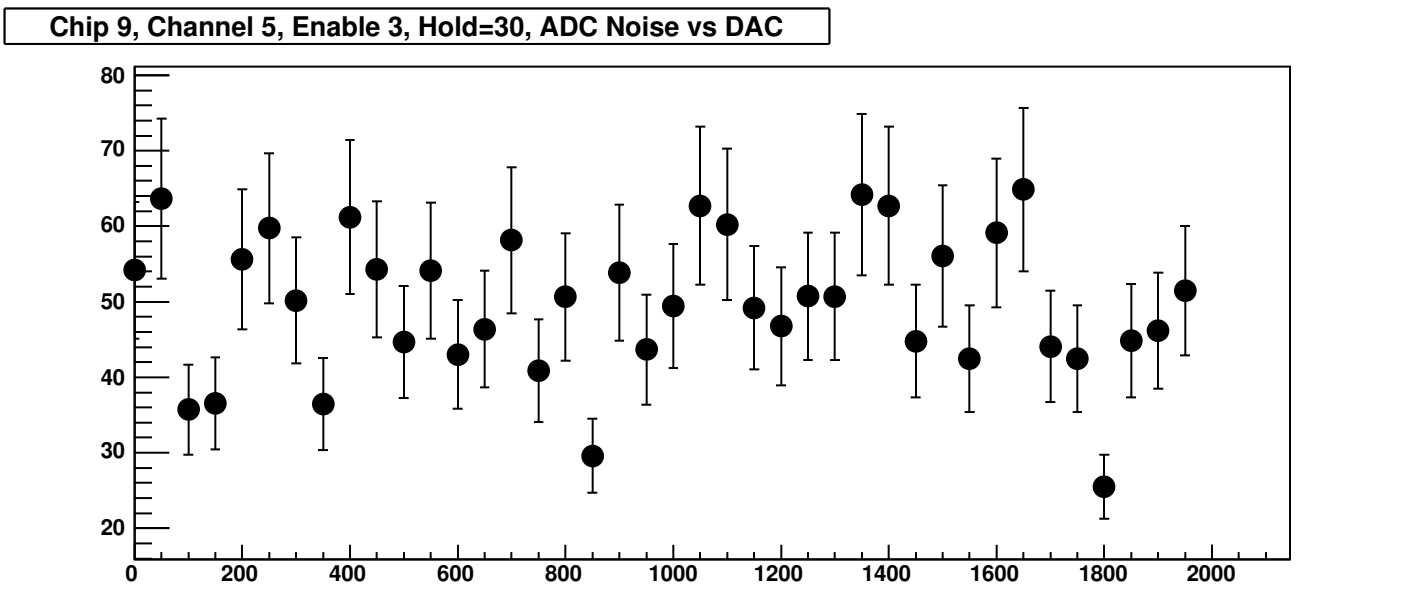

Chip 9, Channel 5, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

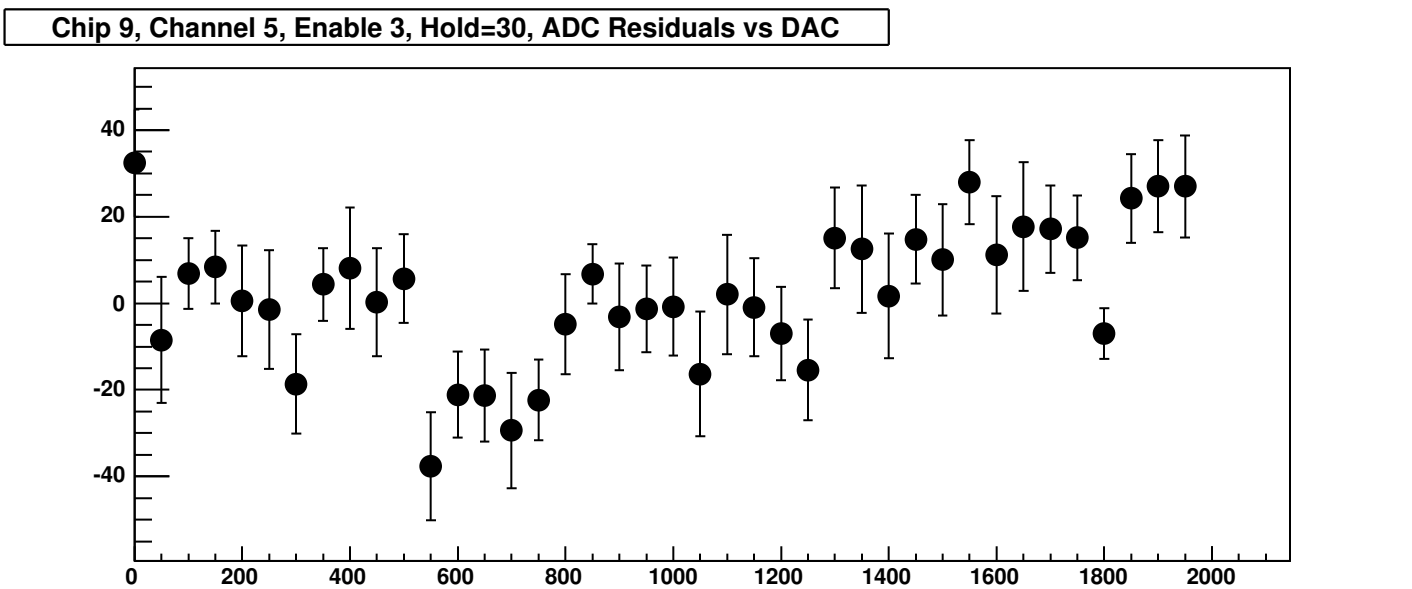

Chip 9, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 22.14 / 11
p0 -1739 ± 18.07
p1 0.1446 ± 0.0163
-1450
-1500
-1550
-1600
-1650
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

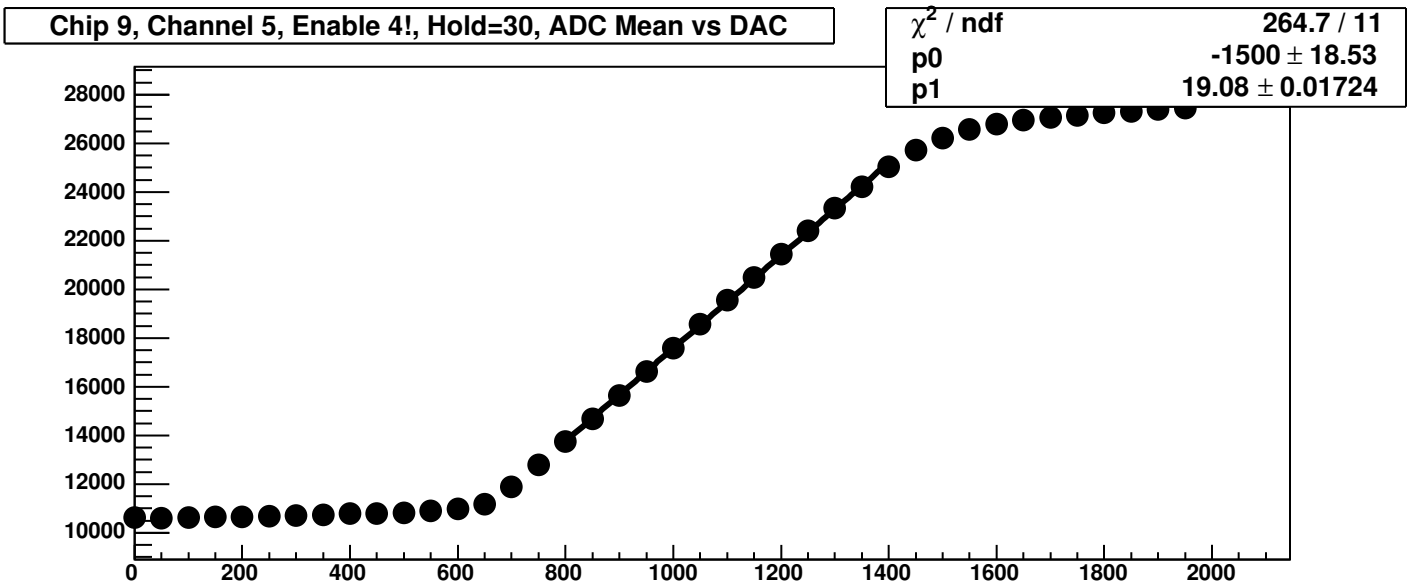

Chip 9, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

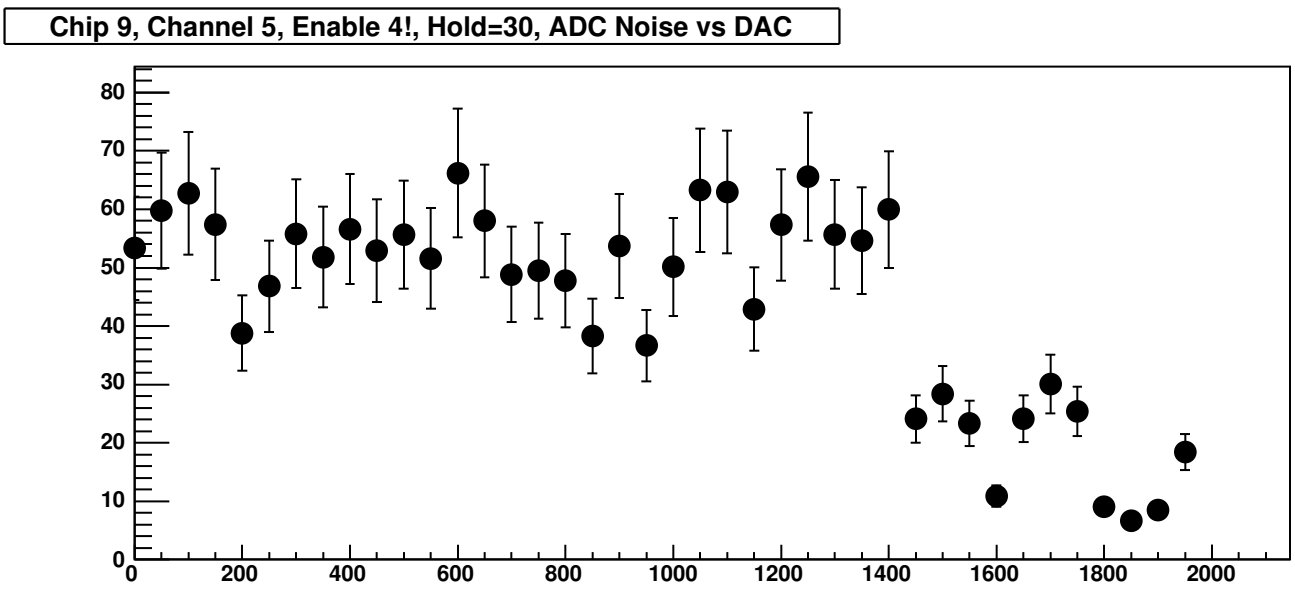

Chip 9, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

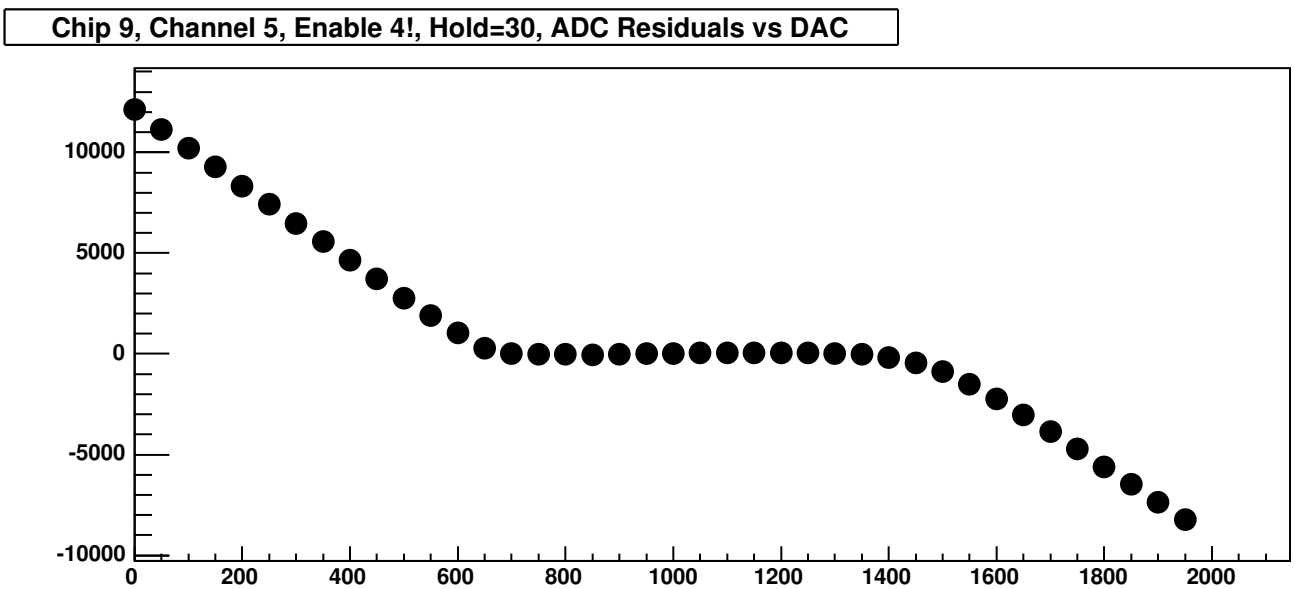

Chip 9, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC
χ^2 / ndf 7.164 / 11
p0 -1689 ± 17.69
p1 0.04131 ± 0.01656
-1560
-1580
-1600
-1620
-1640
-1660
-1680
-1700
-1720
0
200
400
600
800
1000
1200
1400
1600
1800
2000

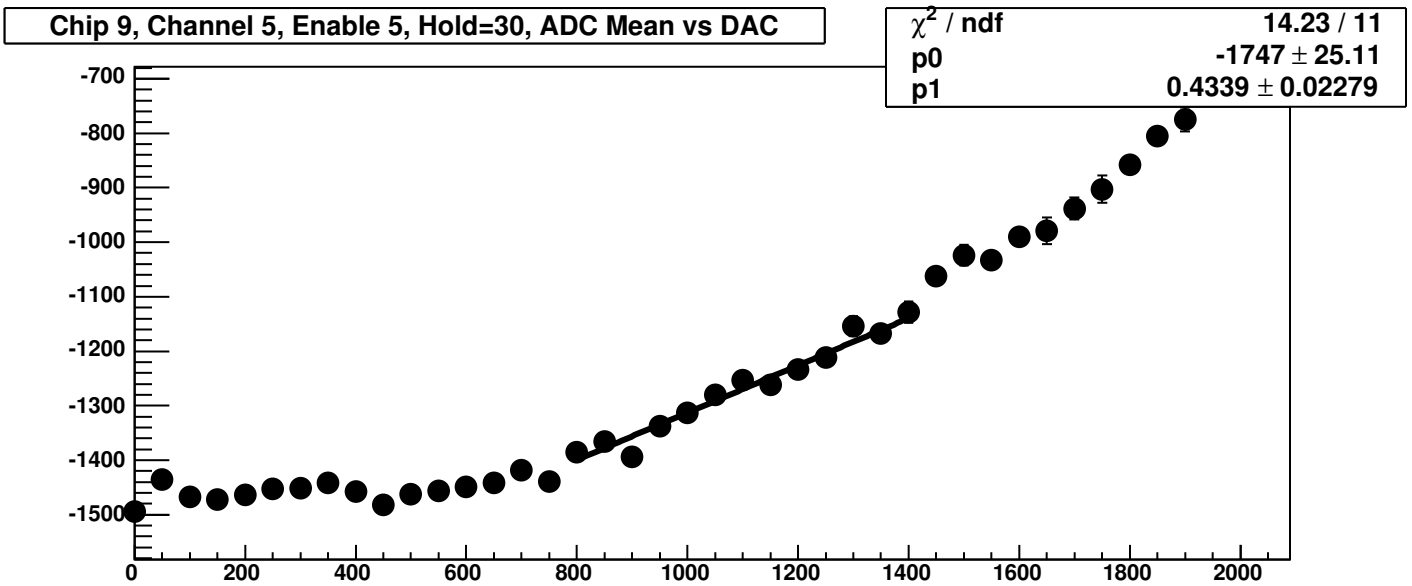

Chip 9, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

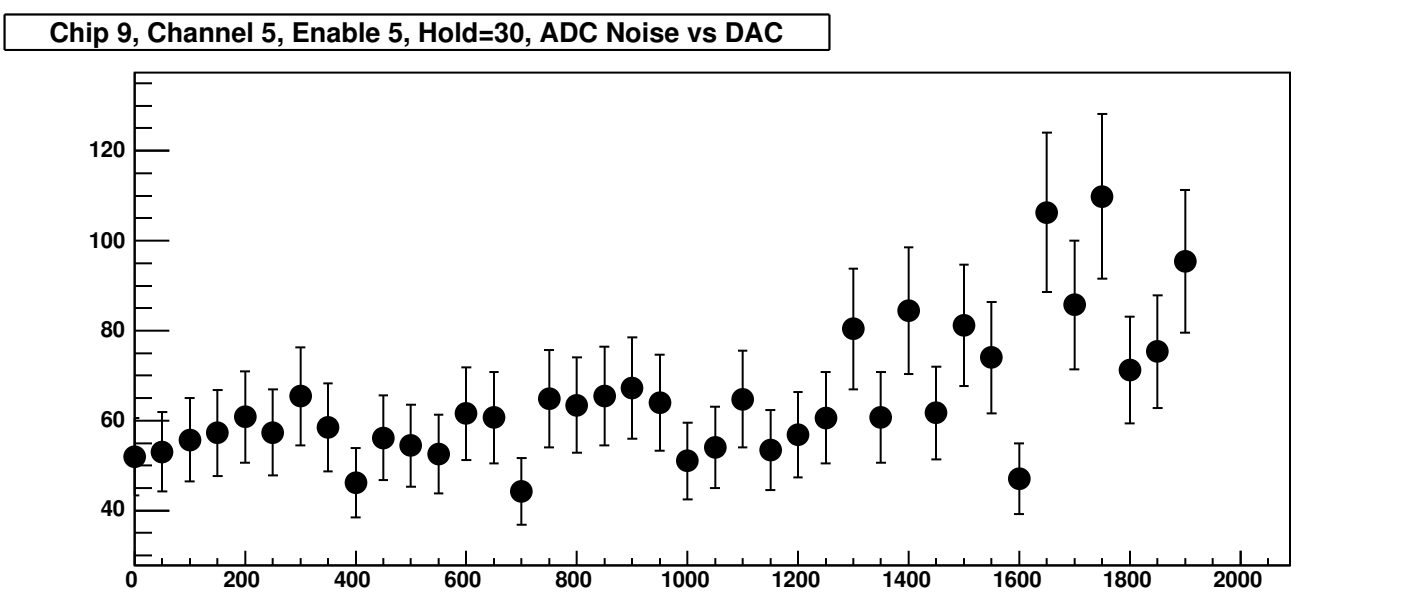

Chip 9, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

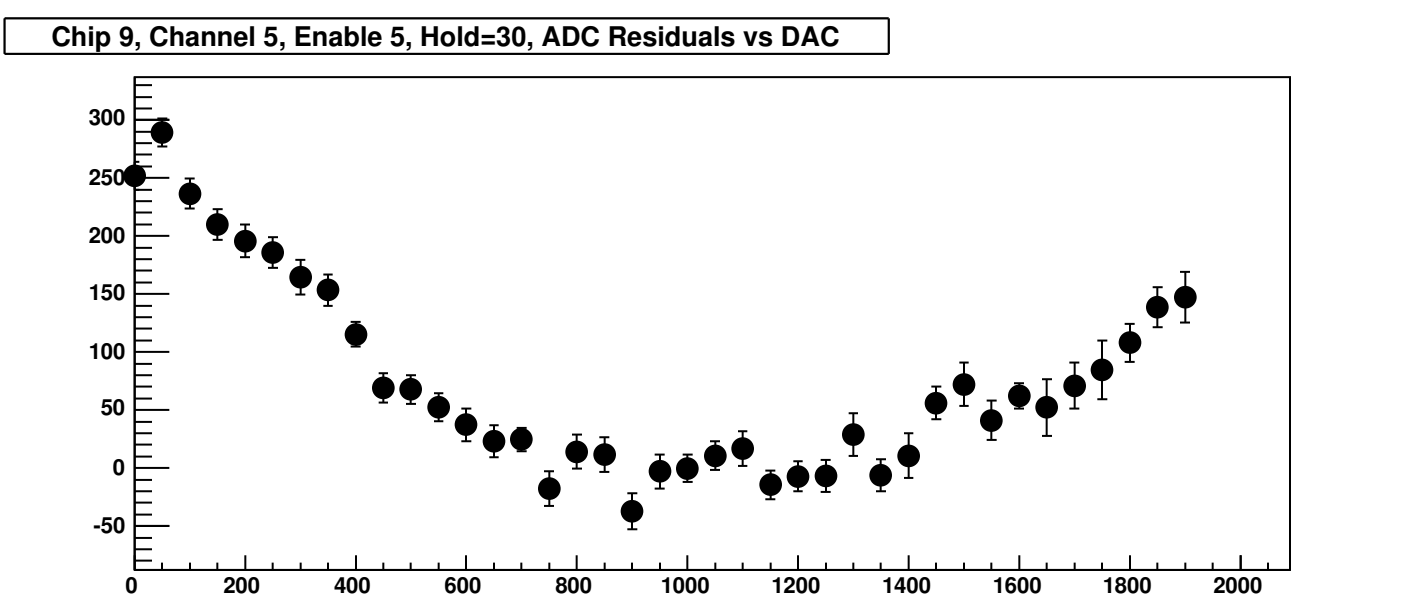

Chip 9, Channel 5, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 264.7 / 11
p0 -1500 ± 18.53
p1 19.08 ± 0.01724
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

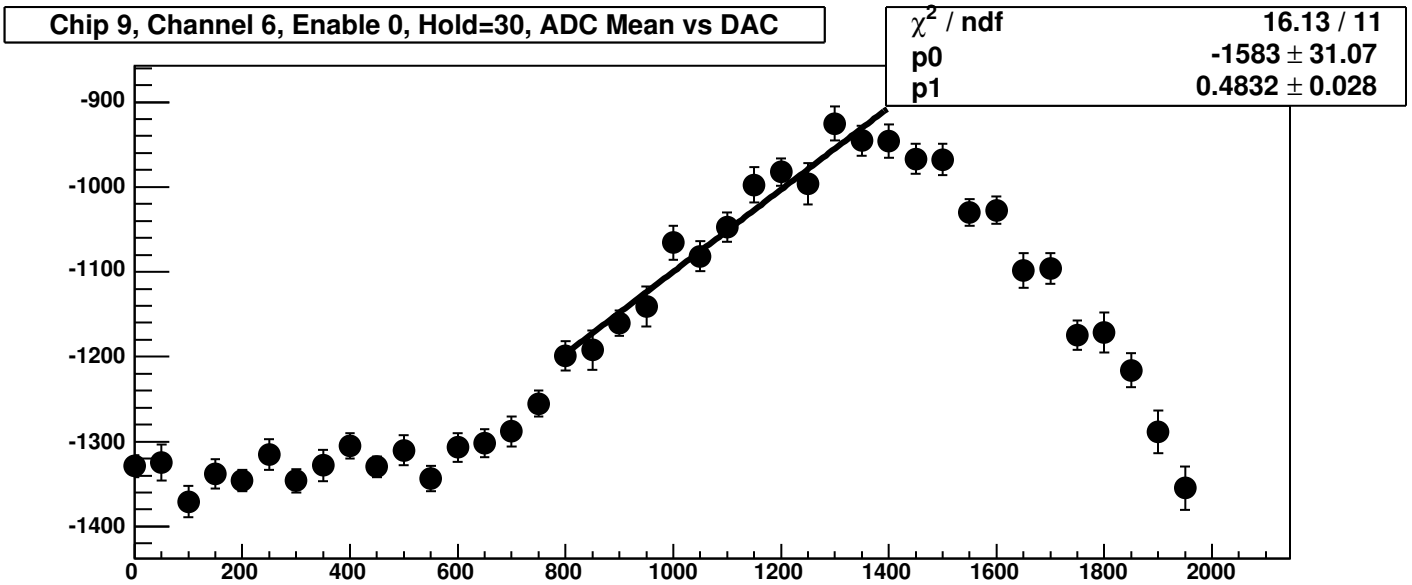

Chip 9, Channel 5, Enable 4!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

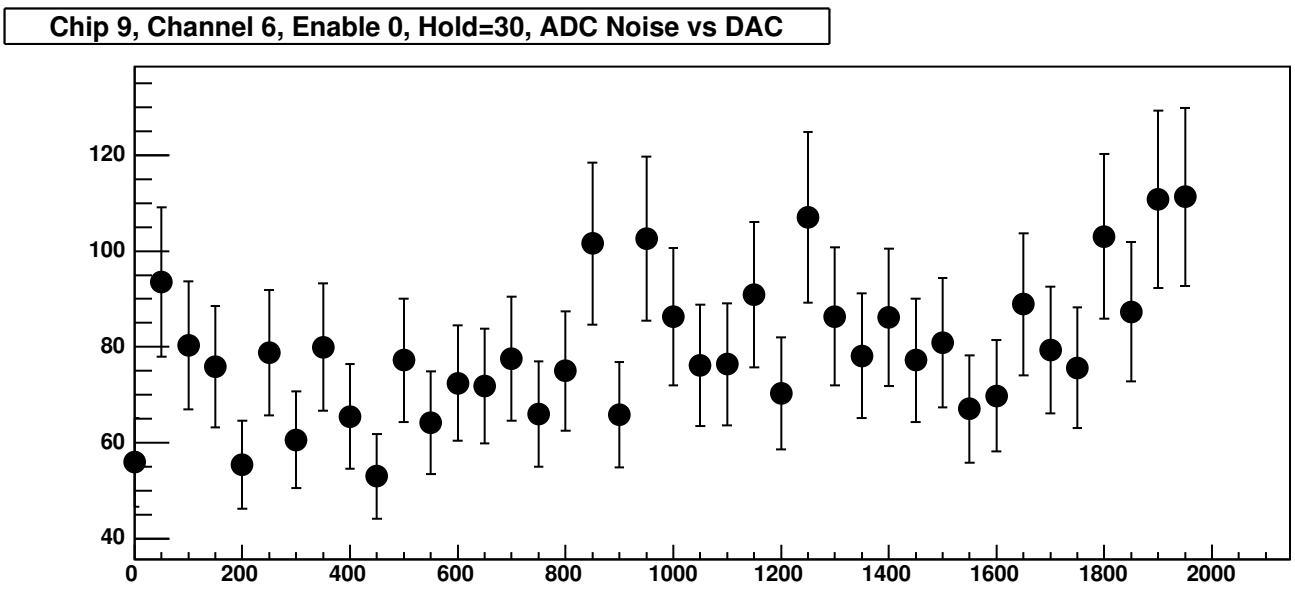

Chip 9, Channel 5, Enable 4!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

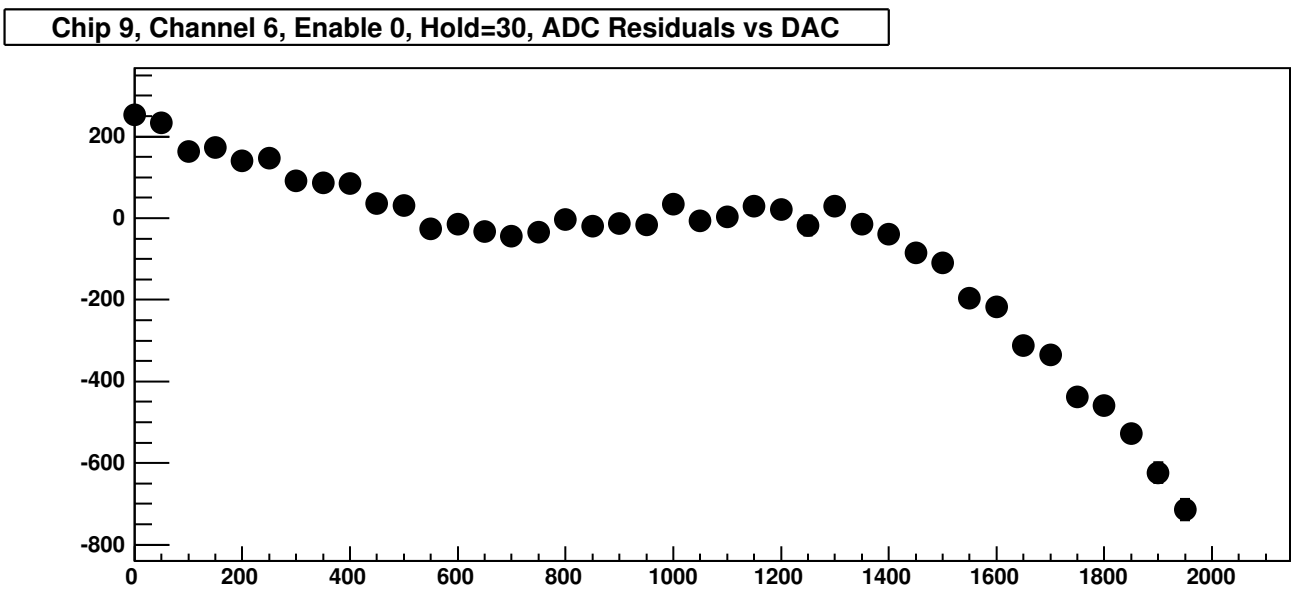

Chip 9, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 14.23 / 11
p0 -1747 ± 25.11
p1 0.4339 ± 0.02279
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

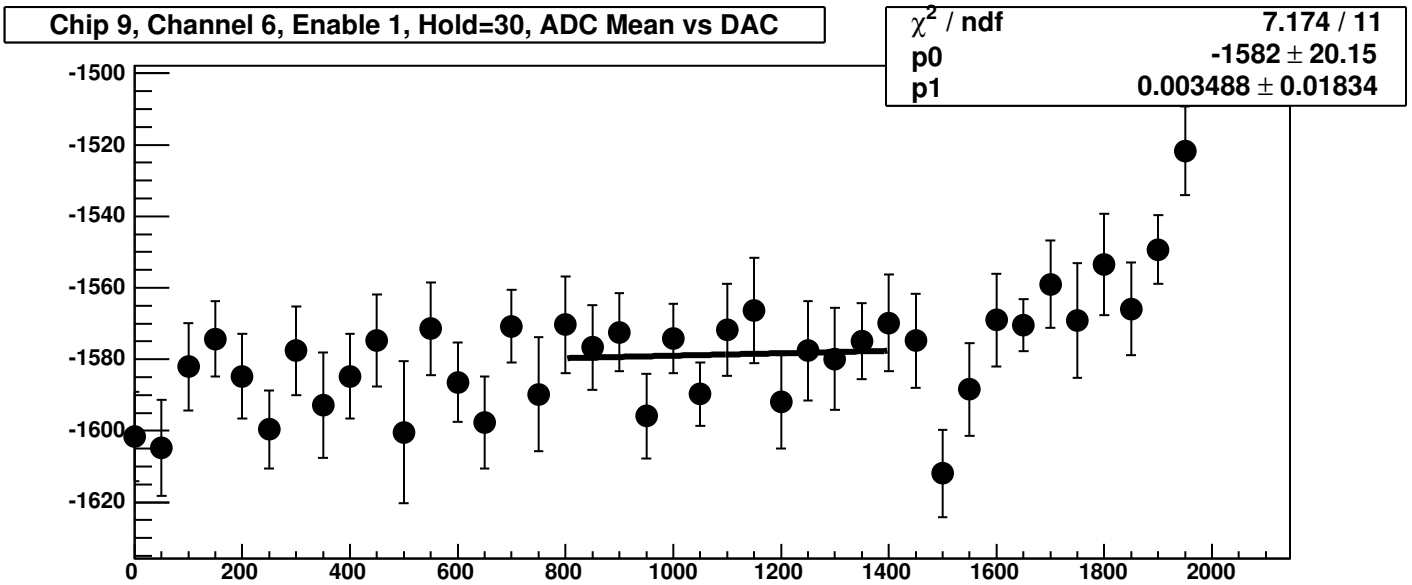

Chip 9, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

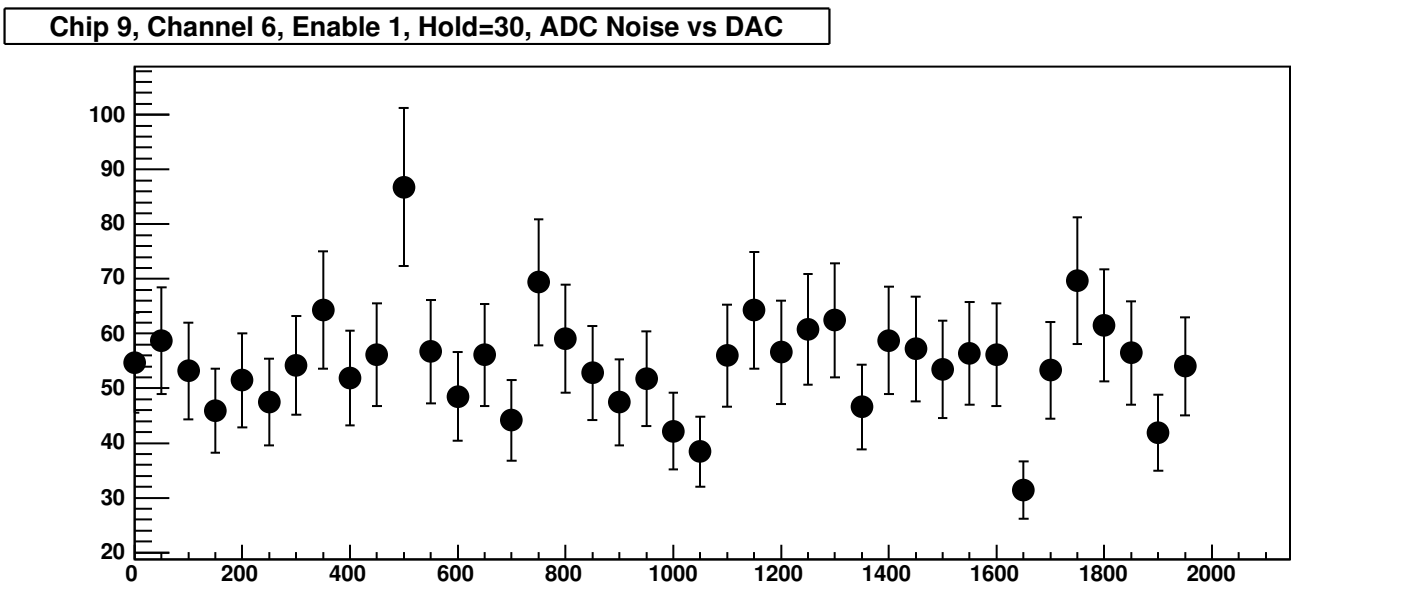

Chip 9, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

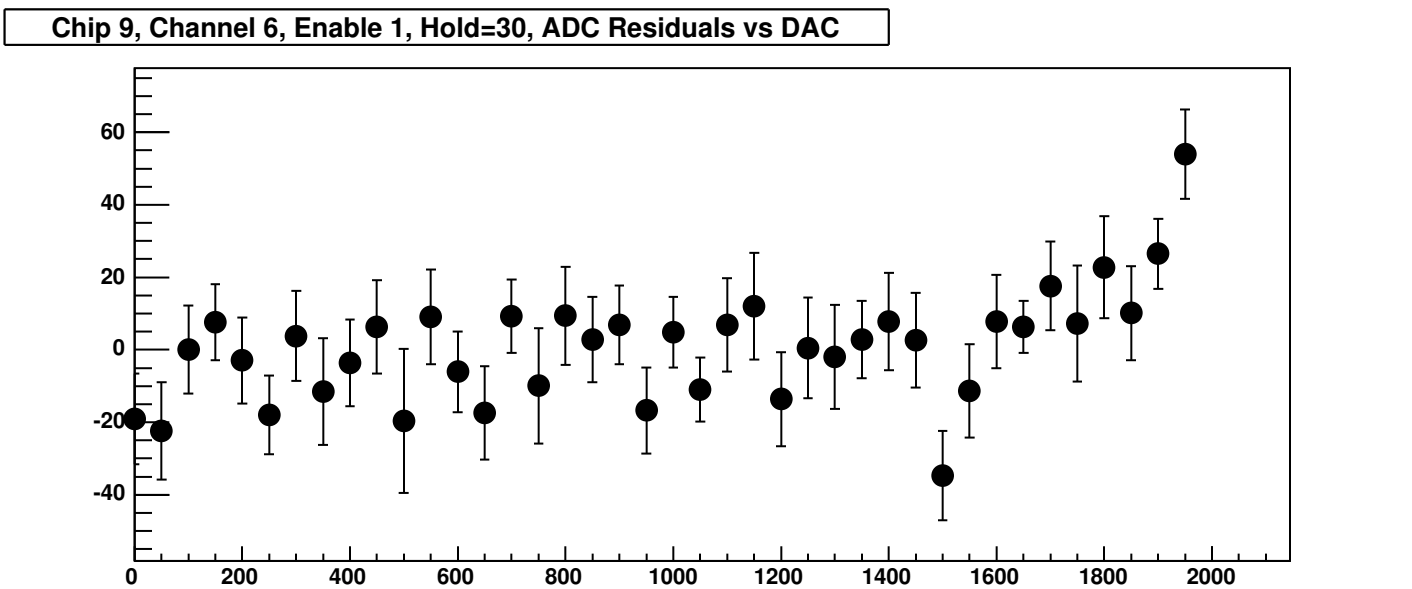

Chip 9, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 16.13 / 11
p0 -1583 ± 31.07
p1 0.4832 ± 0.028
-900
-1000
-1100
-1200
-1300
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

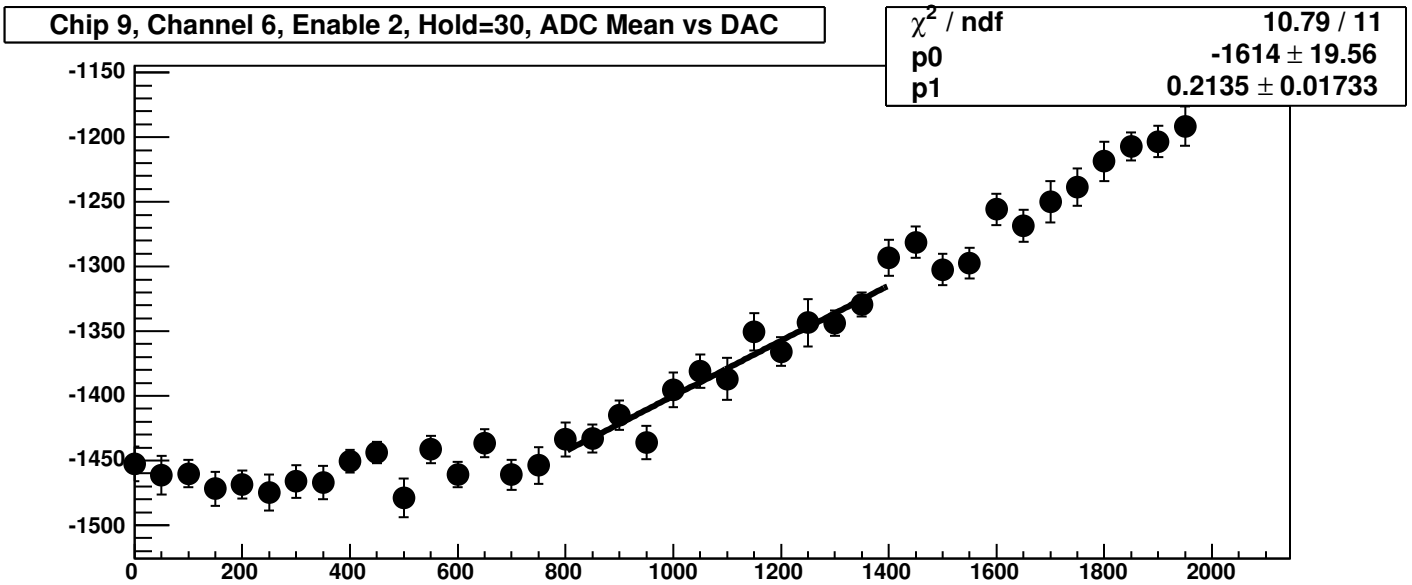

Chip 9, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

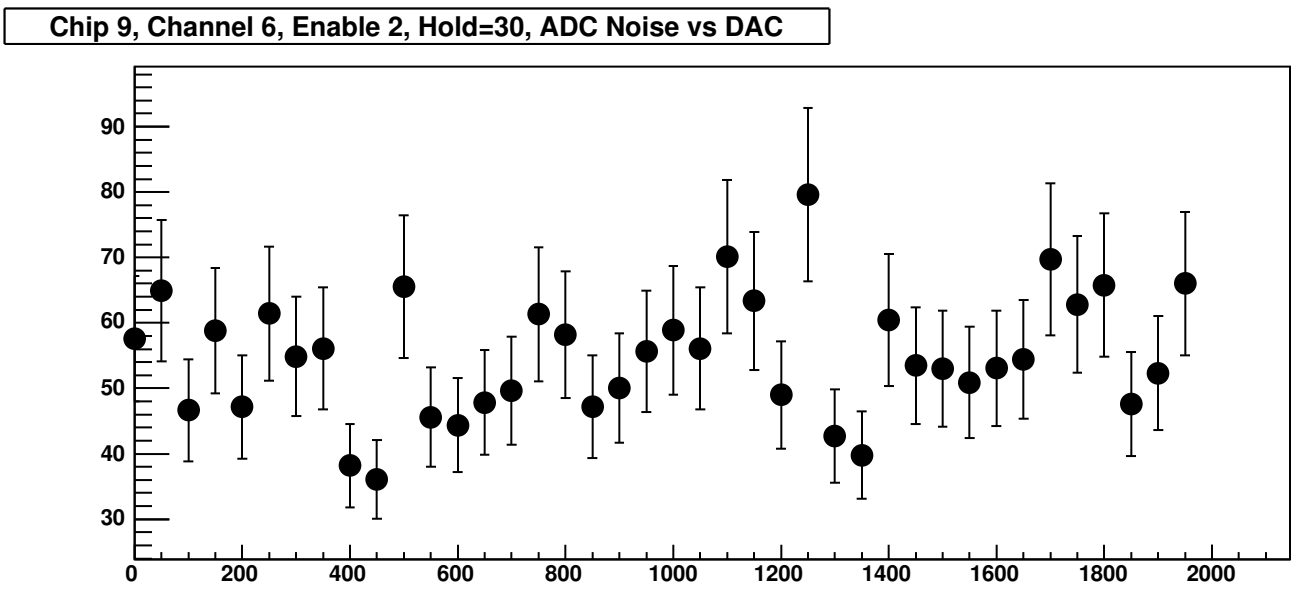

Chip 9, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

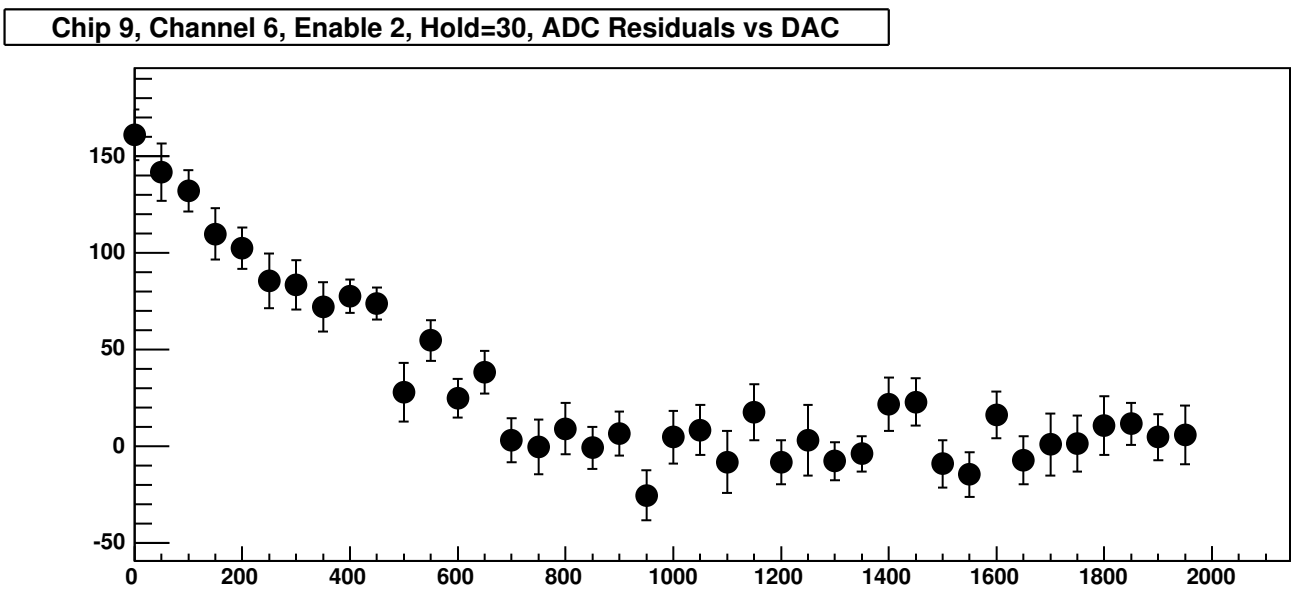

Chip 9, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC
χ^2 / ndf 7.174 / 11
p0 -1582 ± 20.15
p1 0.003488 ± 0.01834
-1500
-1520
-1540
-1560
-1580
-1600
-1620
0
200
400
600
800
1000
1200
1400
1600
1800
2000

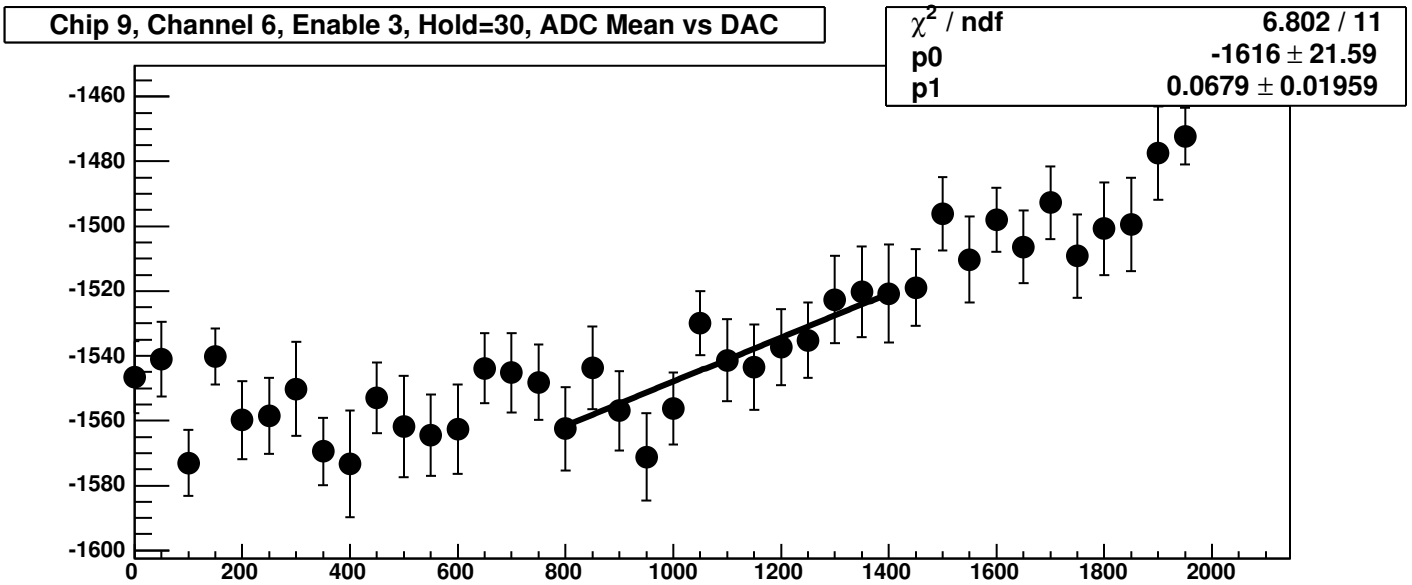

Chip 9, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

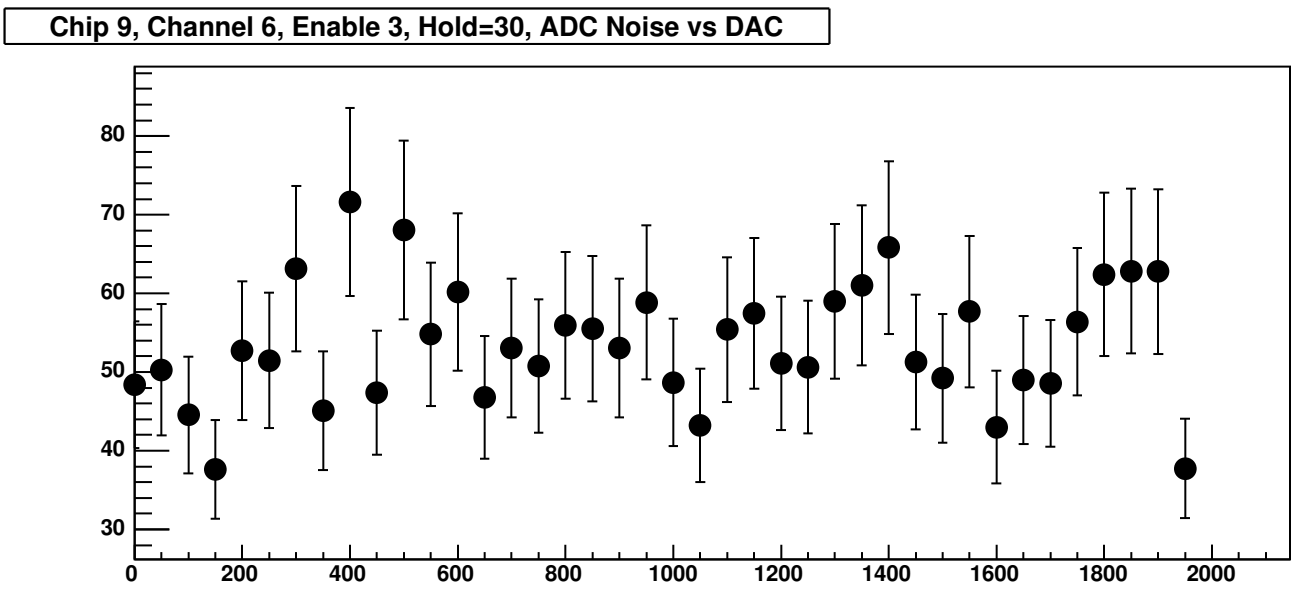

Chip 9, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

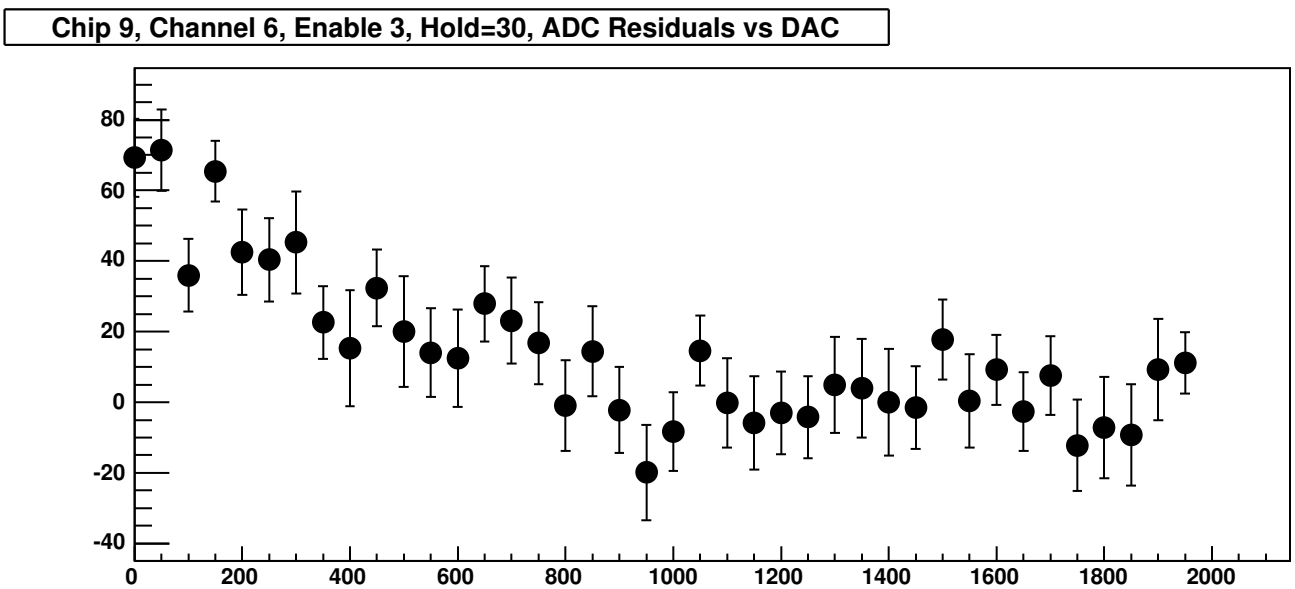

Chip 9, Channel 6, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 10.79 / 11
p0 -1614 ± 19.56
p1 0.2135 ± 0.01733
-1150
-1200
-1250
-1300
-1350
-1400
-1450
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

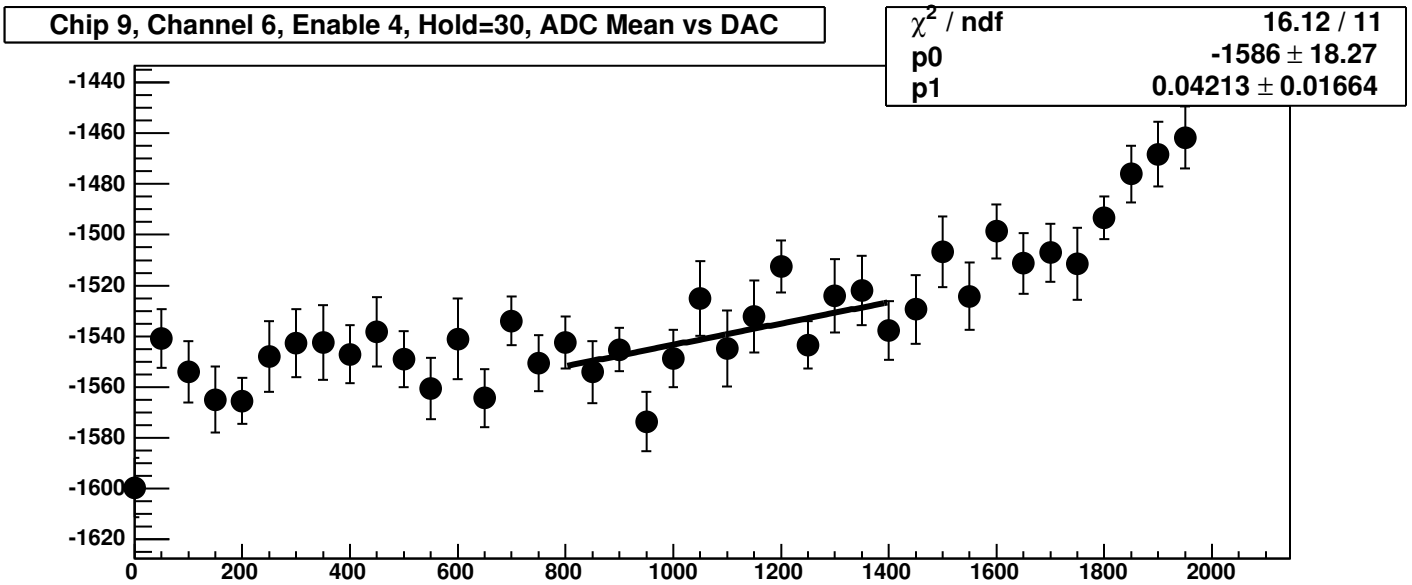

Chip 9, Channel 6, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

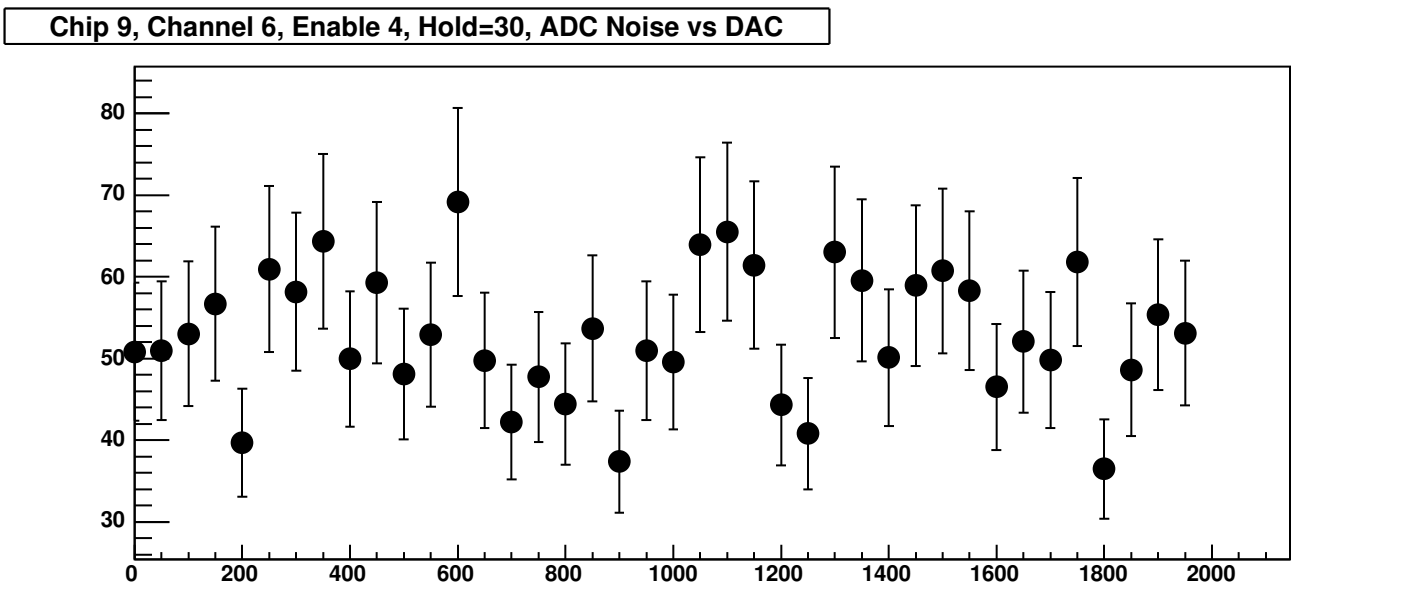

Chip 9, Channel 6, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

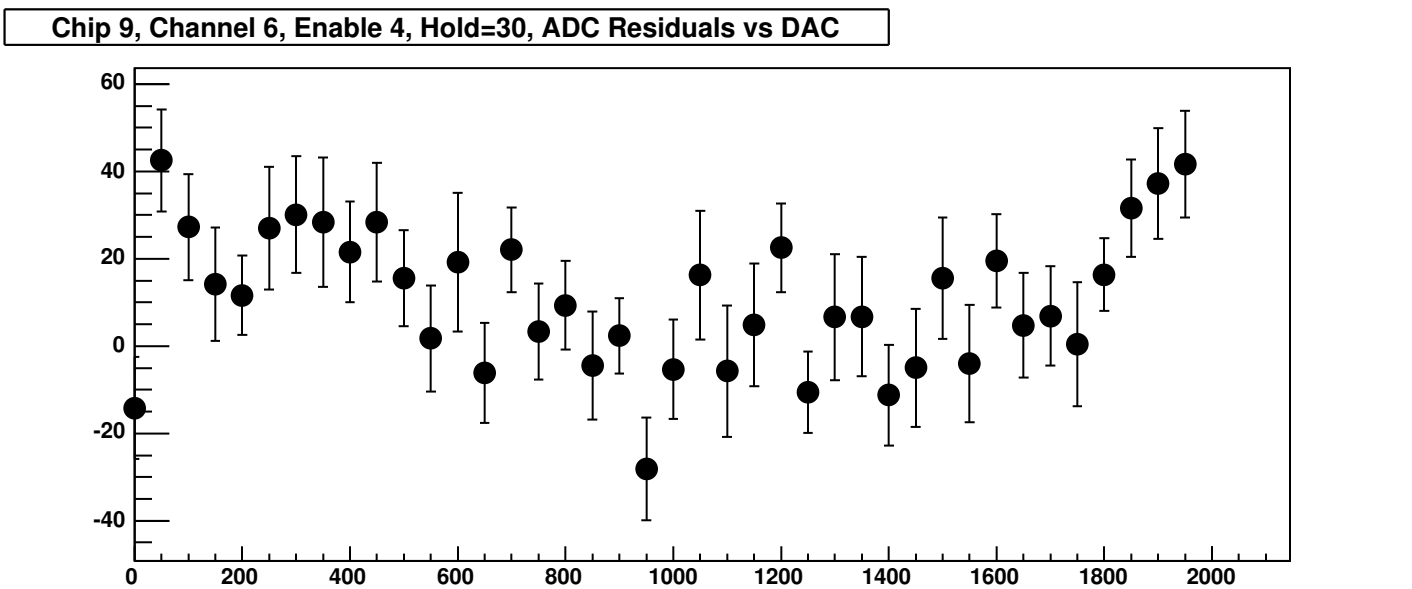

Chip 9, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 6.802 / 11
p0 -1616 ± 21.59
p1 0.0679 ± 0.01959
-1460
-1480
-1500
-1520
-1540
-1560
-1580
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

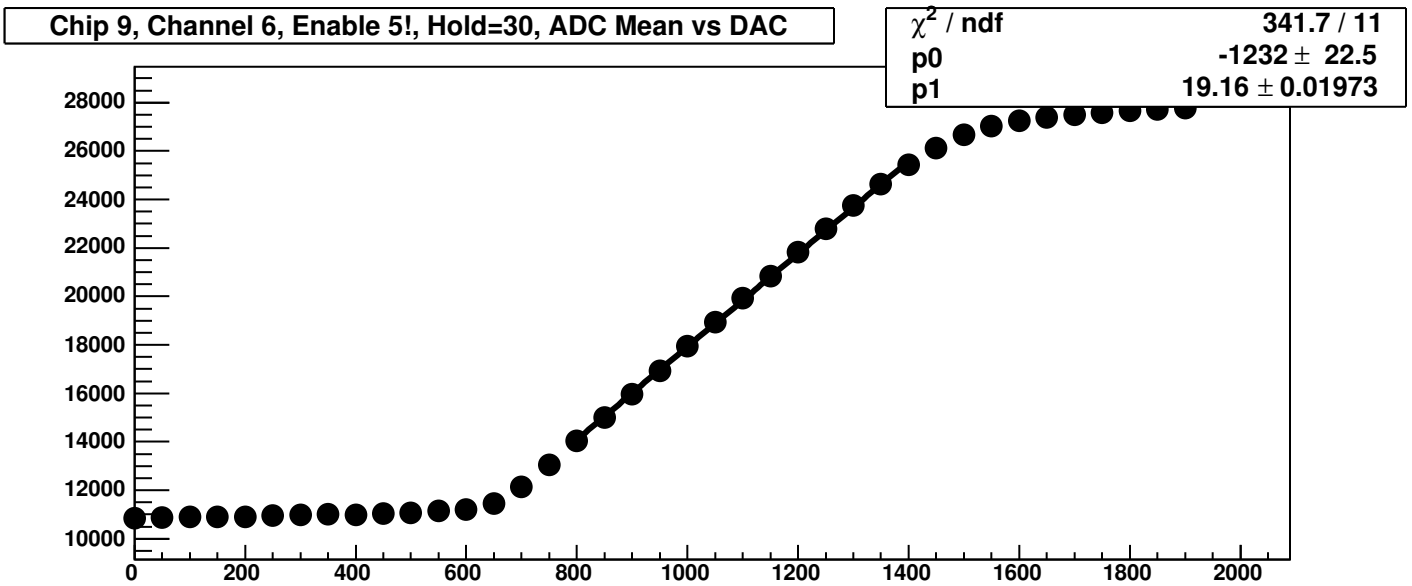

Chip 9, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

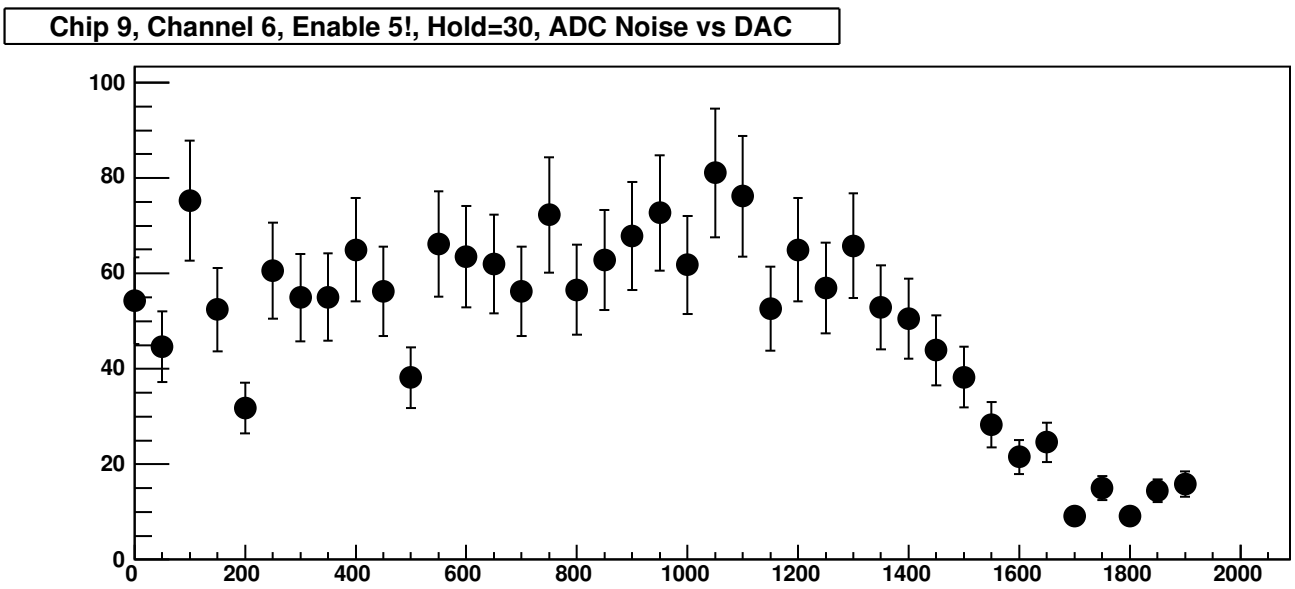

Chip 9, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

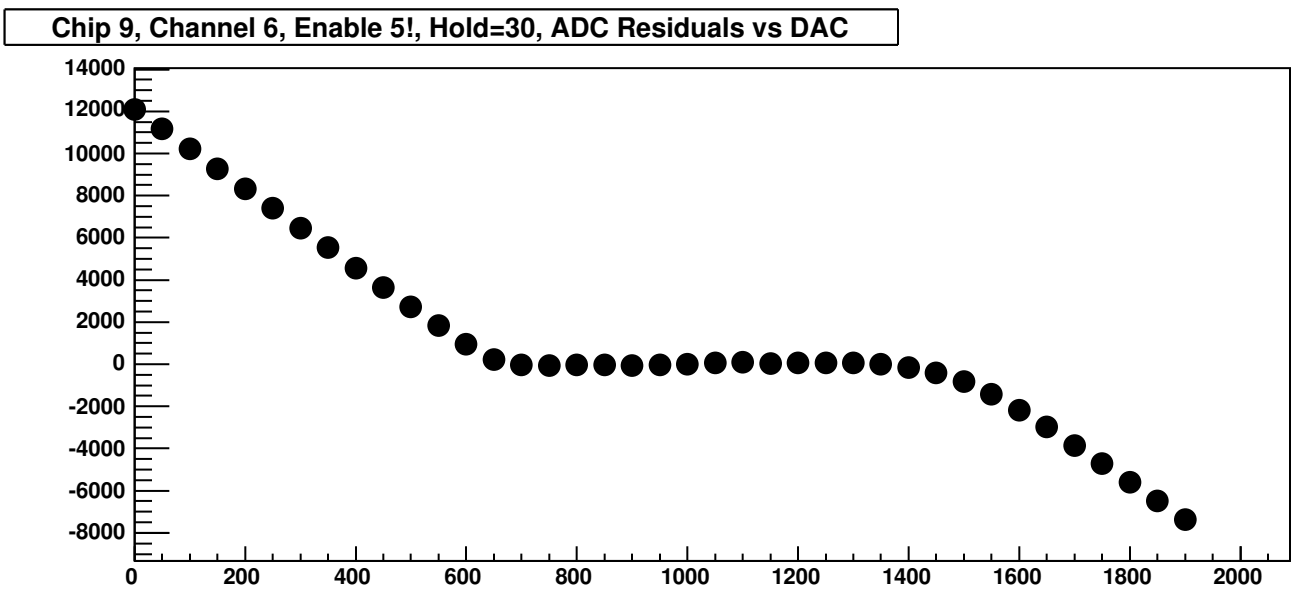

Chip 9, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 16.12 / 11
p0 -1586 ± 18.27
p1 0.04213 ± 0.01664
-1440
-1460
-1480
-1500
-1520
-1540
-1560
-1580
-1600
-1620
0
200
400
600
800
1000
1200
1400
1600
1800
2000

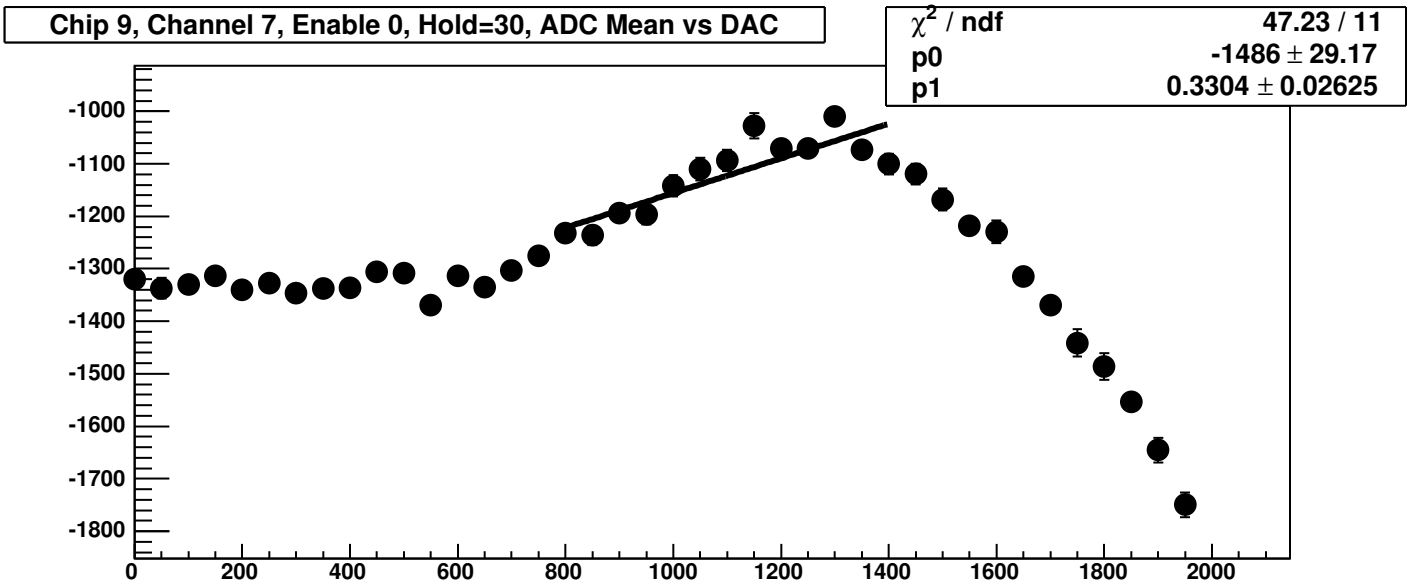

Chip 9, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

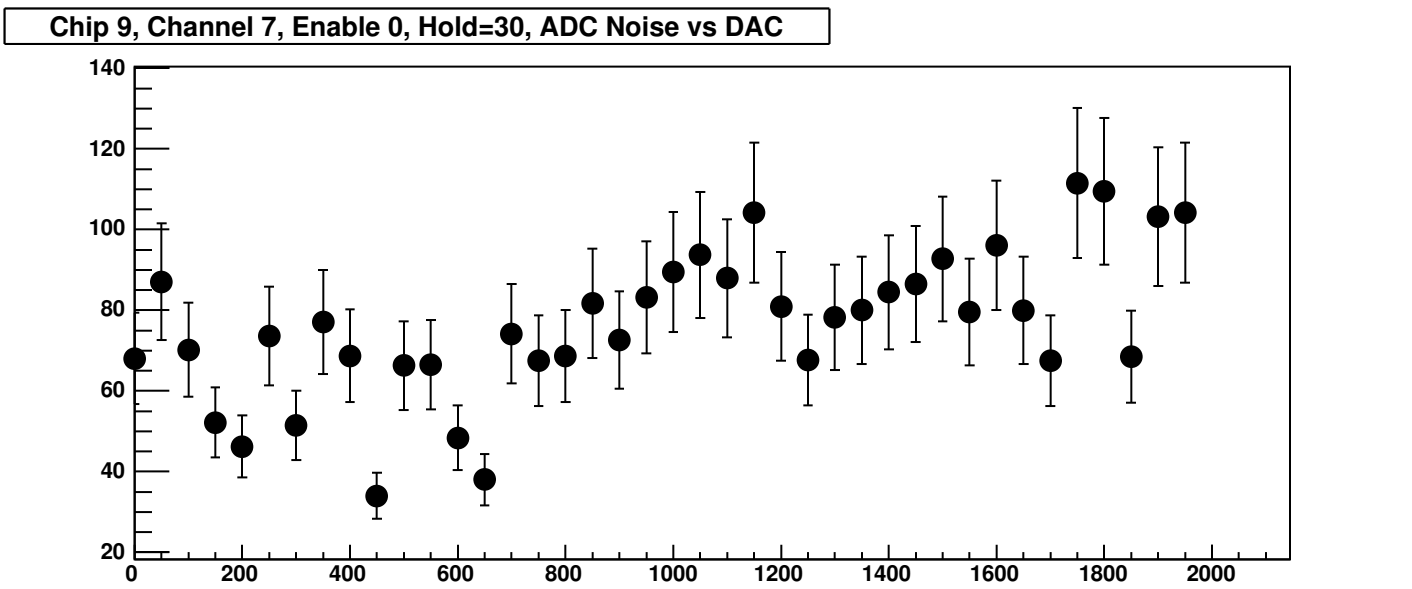

Chip 9, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

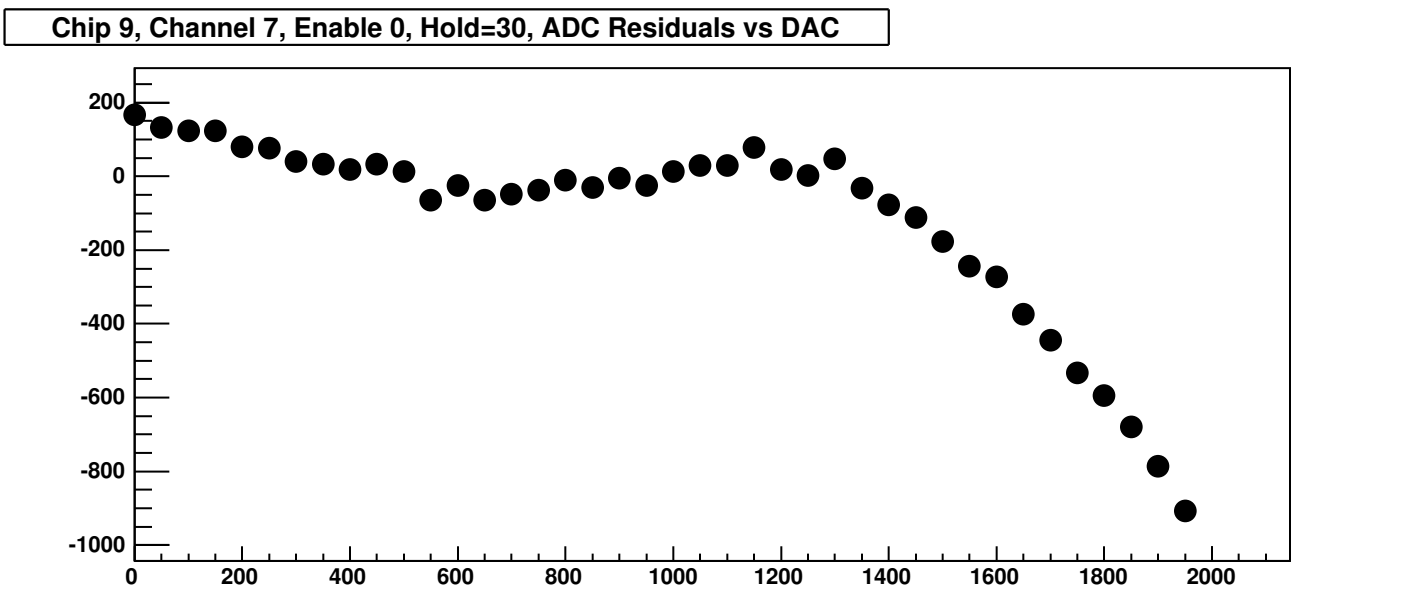

Chip 9, Channel 6, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 341.7 / 11
p0 -1232 ± 22.5
p1 19.16 ± 0.01973
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 6, Enable 5!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

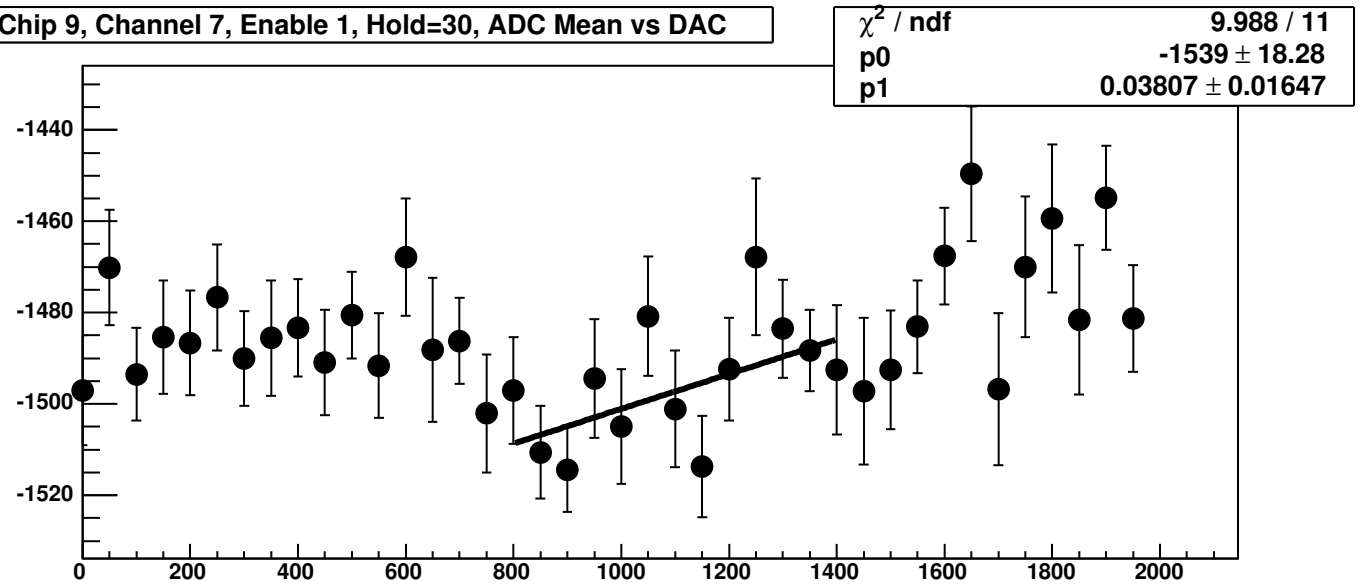

Chip 9, Channel 6, Enable 5!, Hold=30, ADC Residuals vs DAC
14000
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 47.23 / 11
p0 -1486 ± 29.17
p1 0.3304 ± 0.02625
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

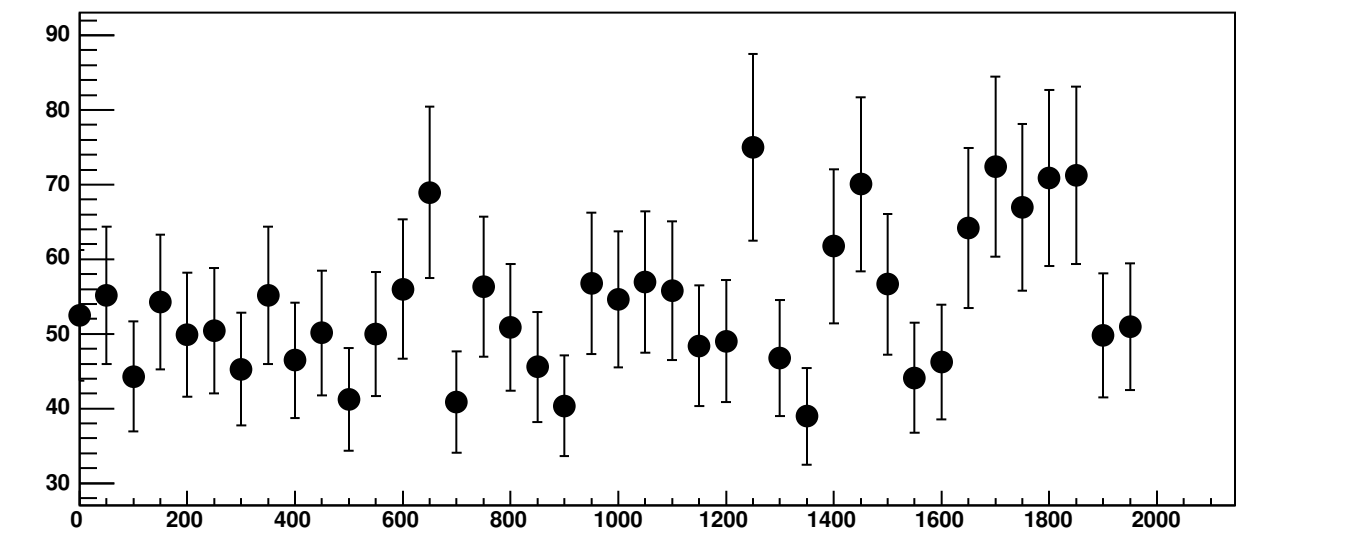

Chip 9, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

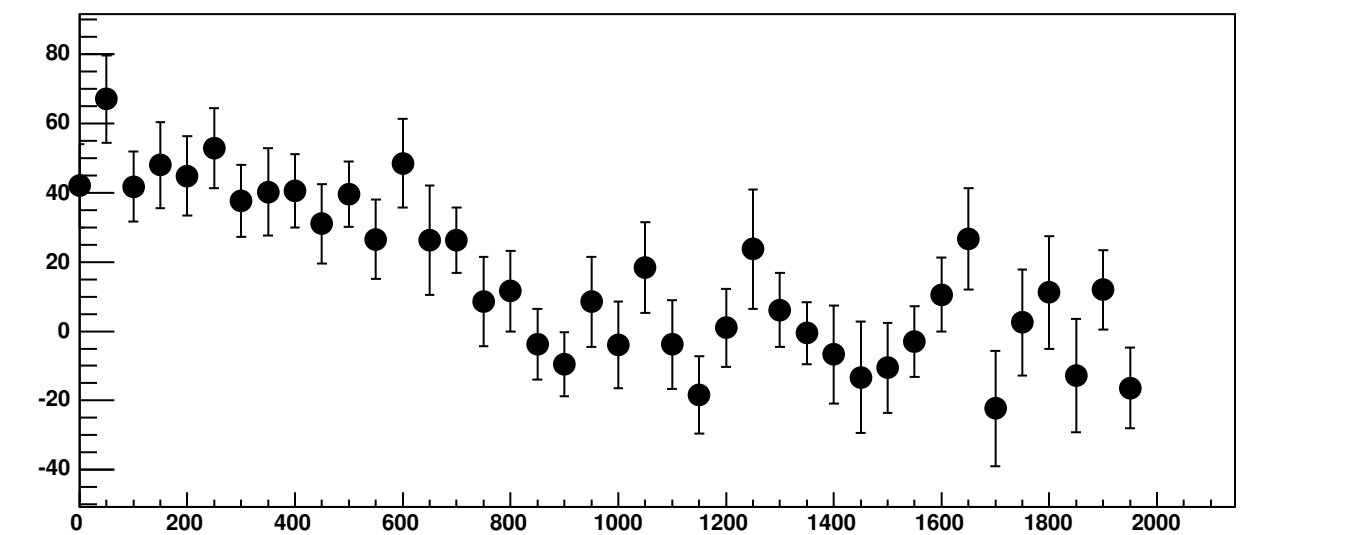
Chip 9, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC

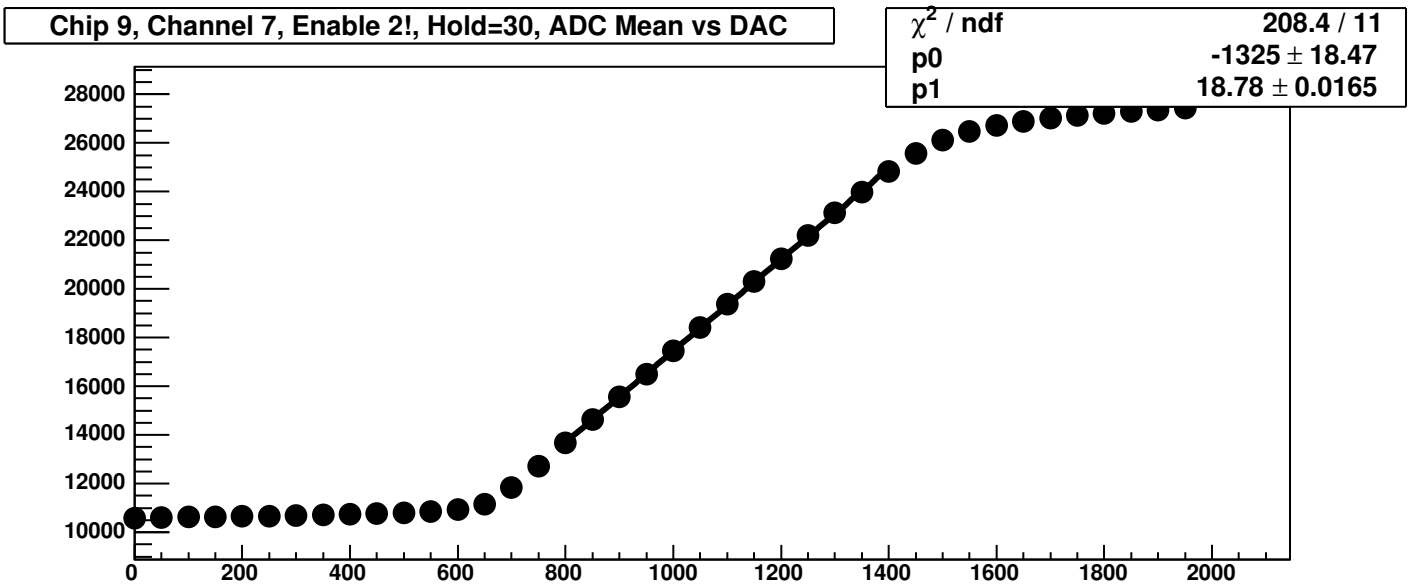


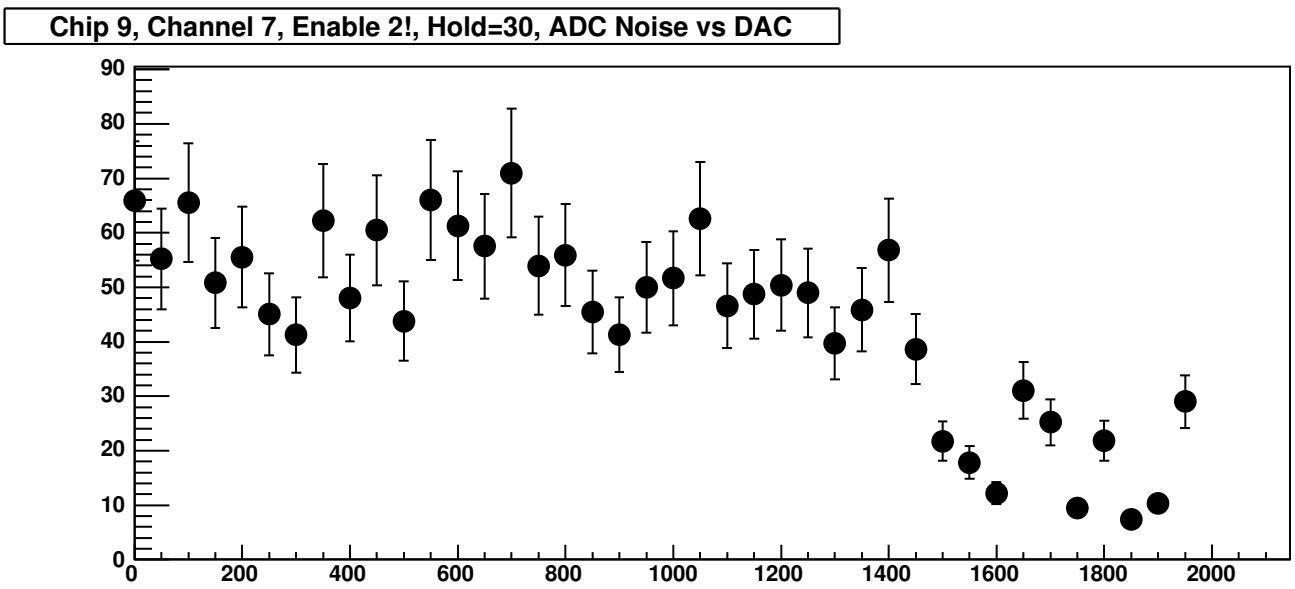
Chip 9, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC

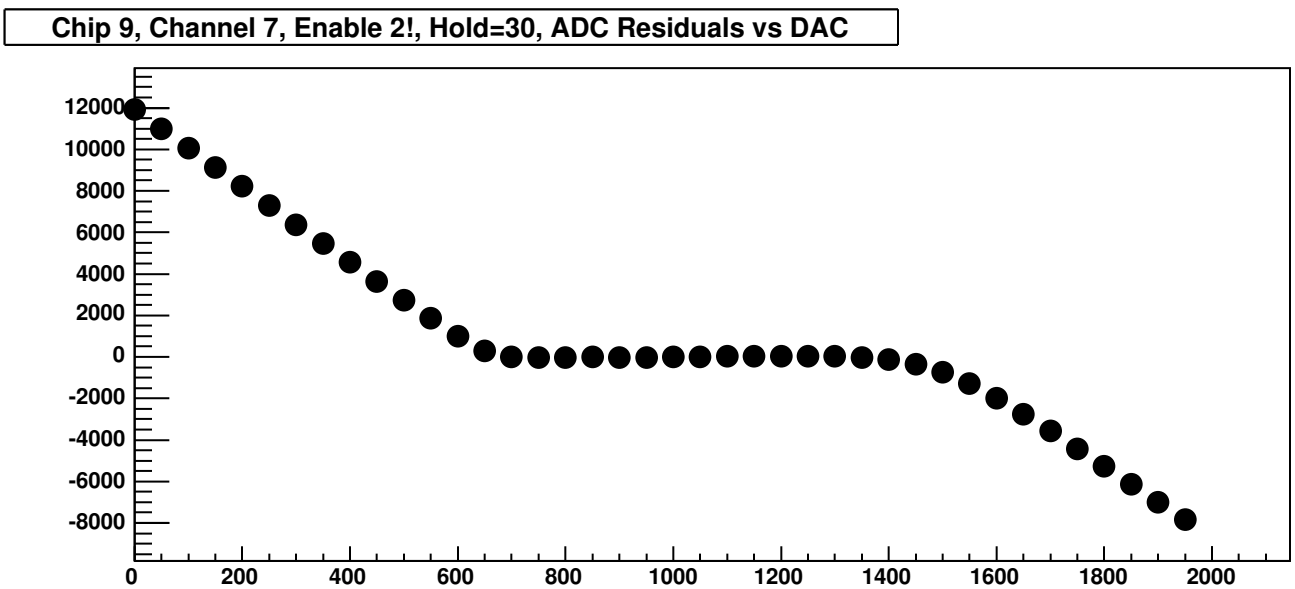


Chip 9, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC

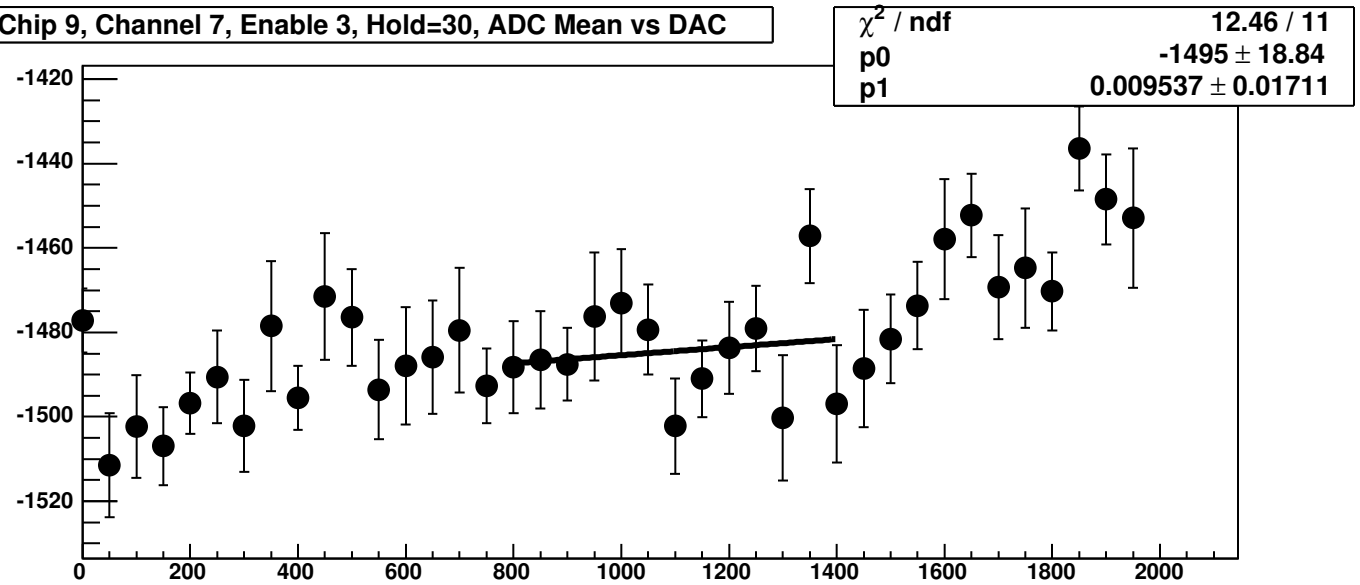



Chip 9, Channel 7, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 208.4 / 11
p0 -1325 ± 18.47
p1 18.78 ± 0.0165
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

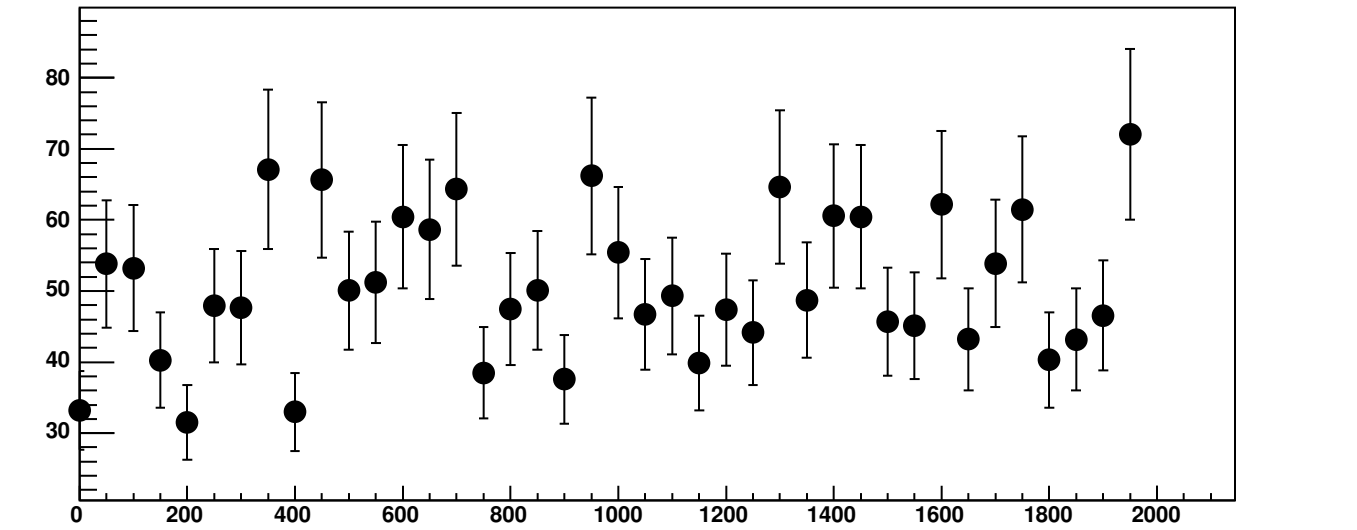

Chip 9, Channel 7, Enable 2!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 7, Enable 2!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

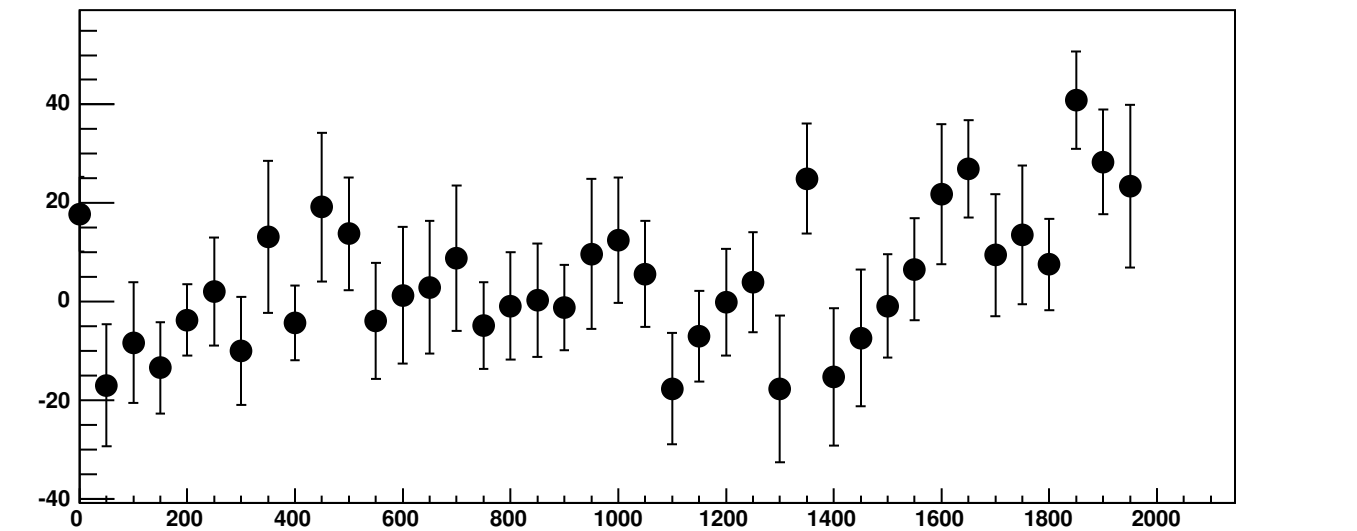
Chip 9, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC

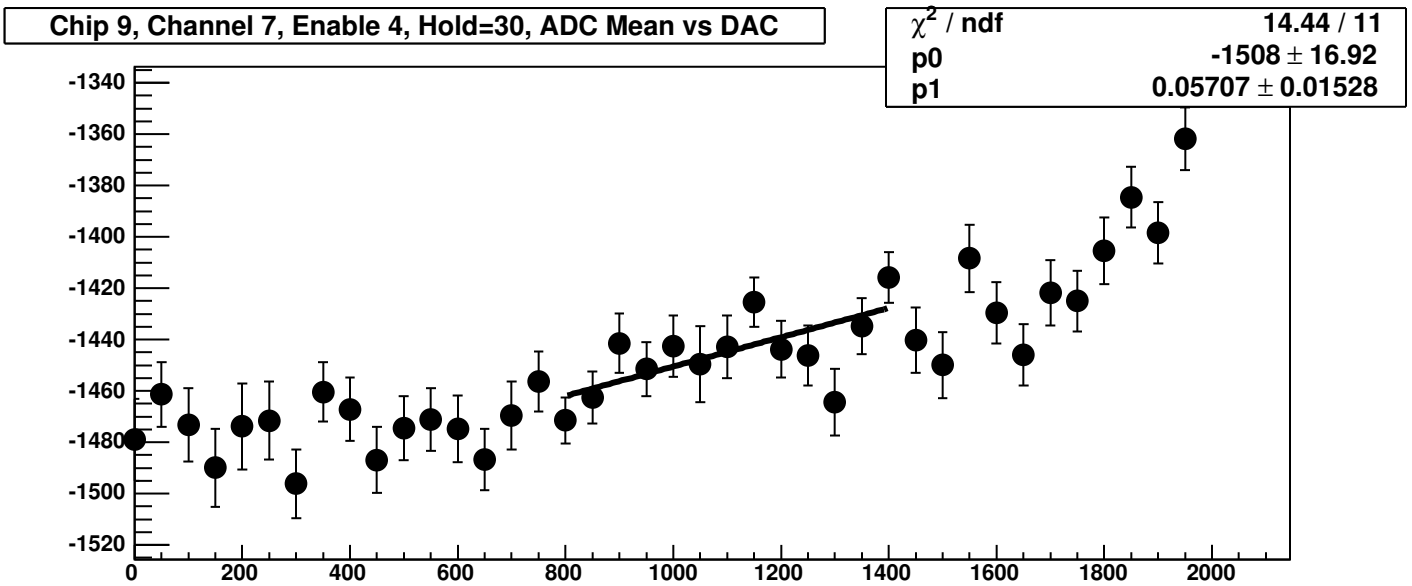


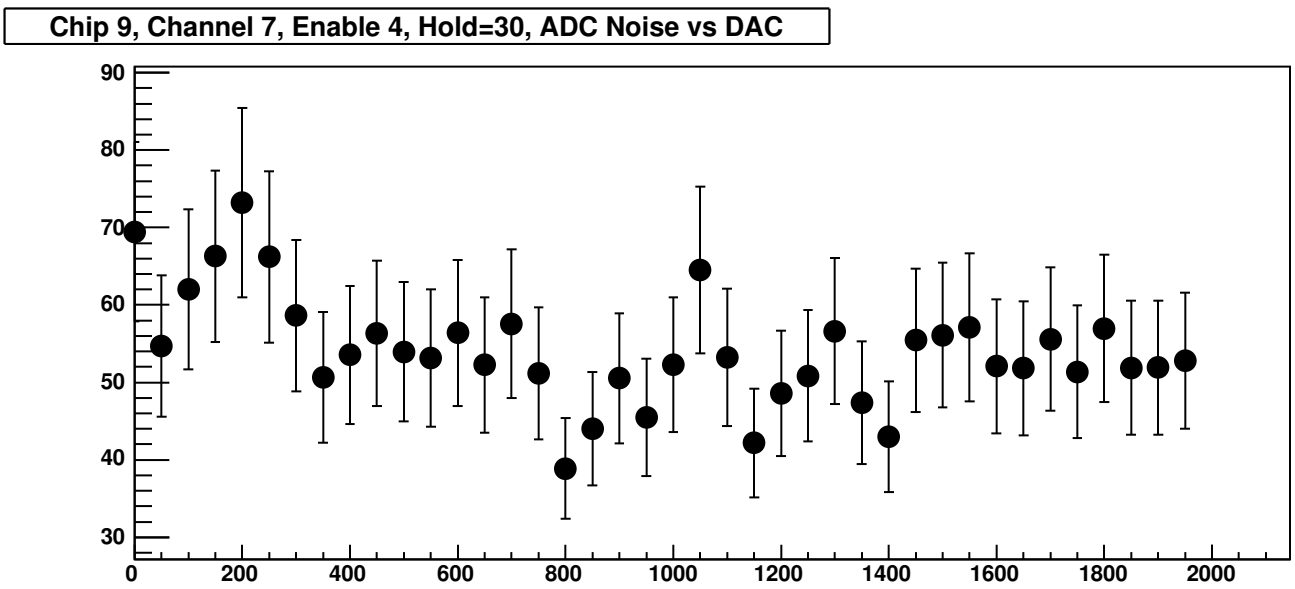
Chip 9, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC

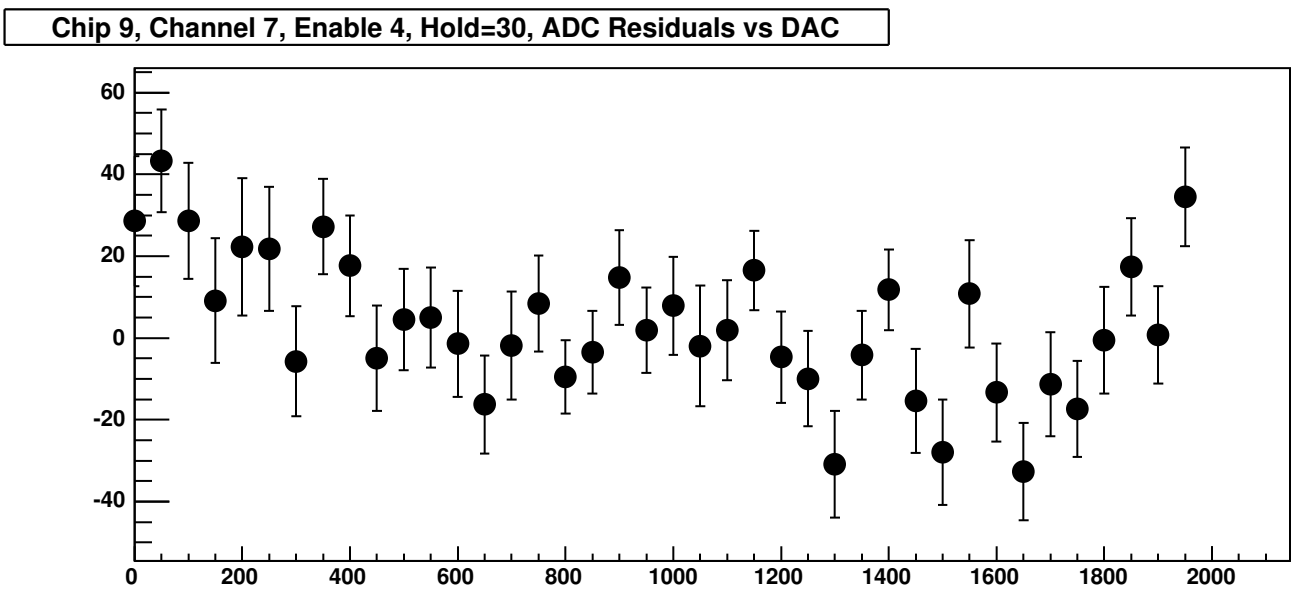


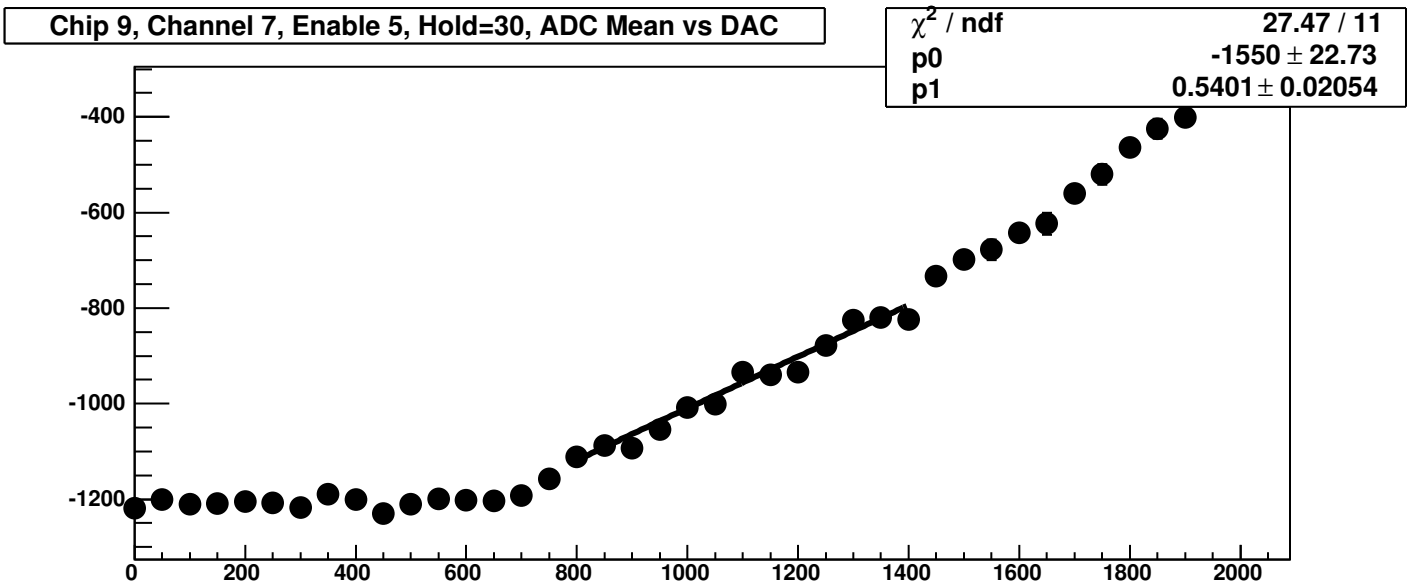
Chip 9, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC

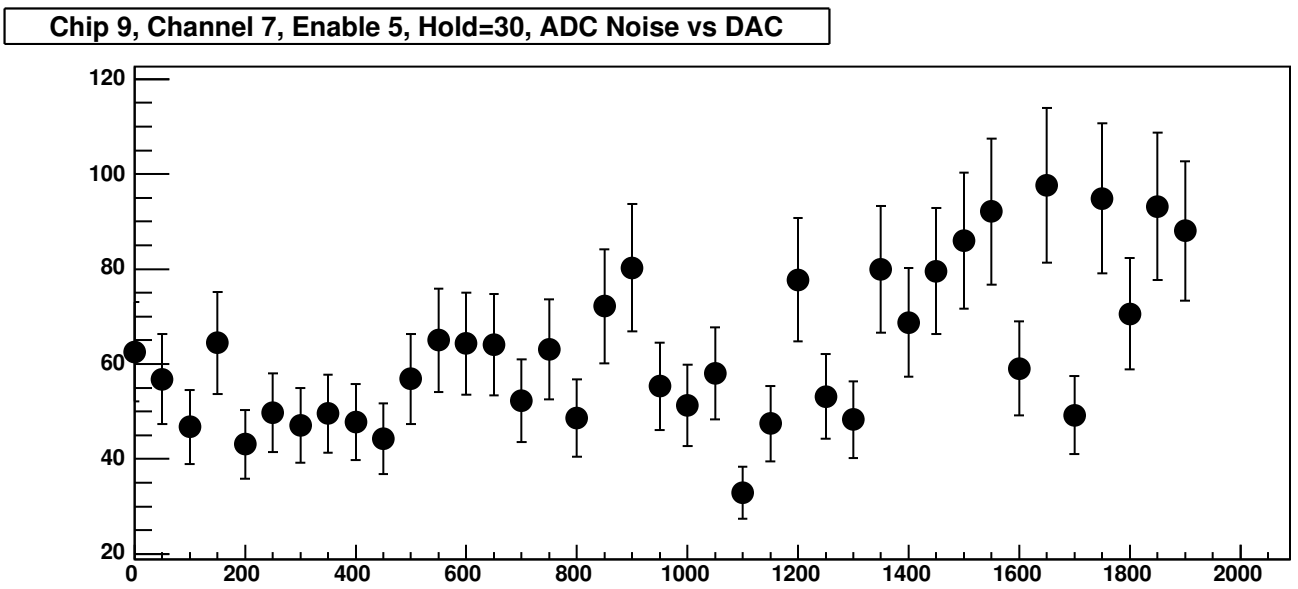


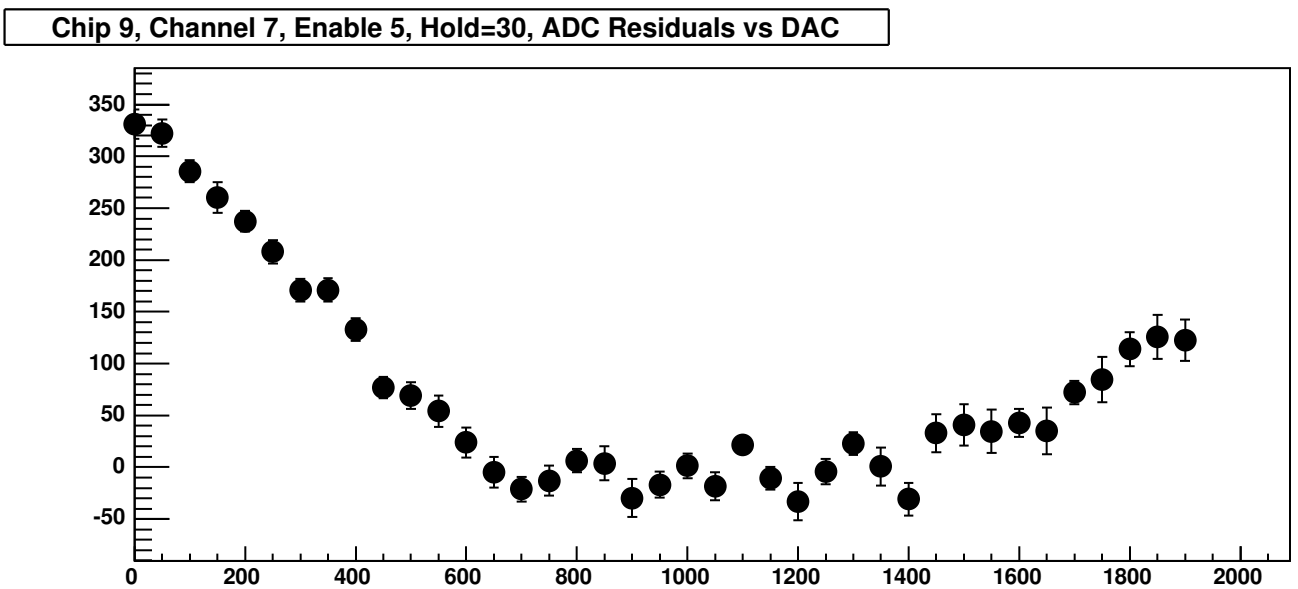

Chip 9, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 14.44 / 11
p0 -1508 ± 16.92
p1 0.05707 ± 0.01528
-1340
-1360
-1380
-1400
-1420
-1440
-1460
-1480
-1500
-1520
0
200
400
600
800
1000
1200
1400
1600
1800
2000

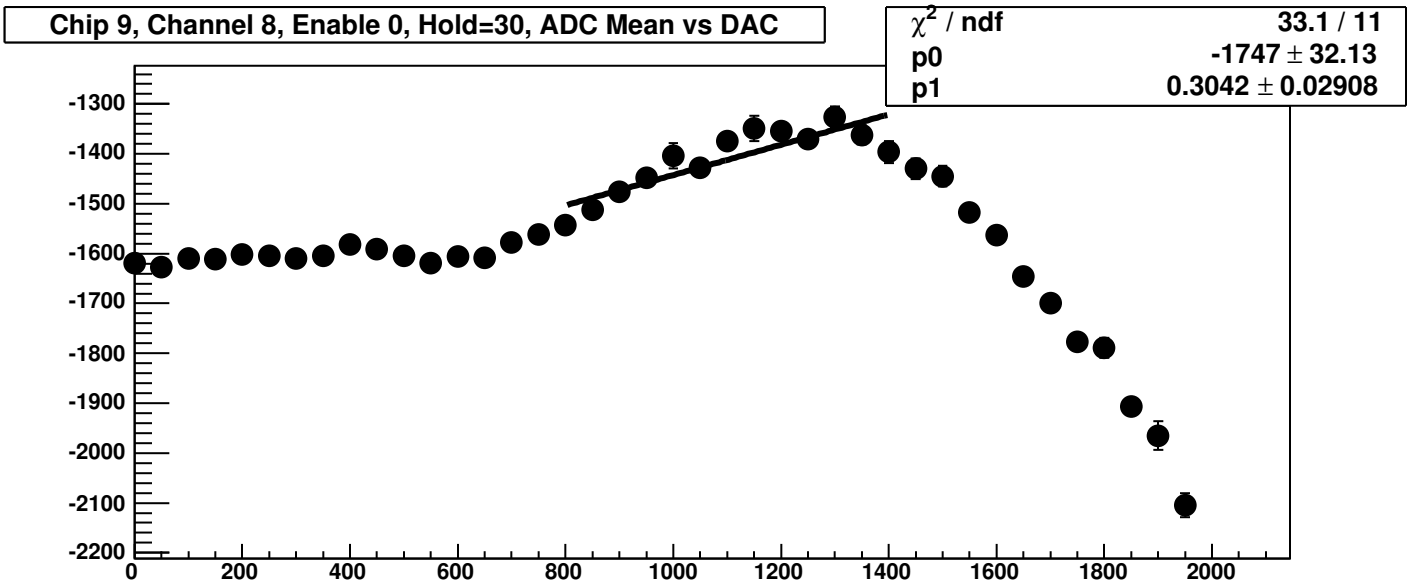

Chip 9, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

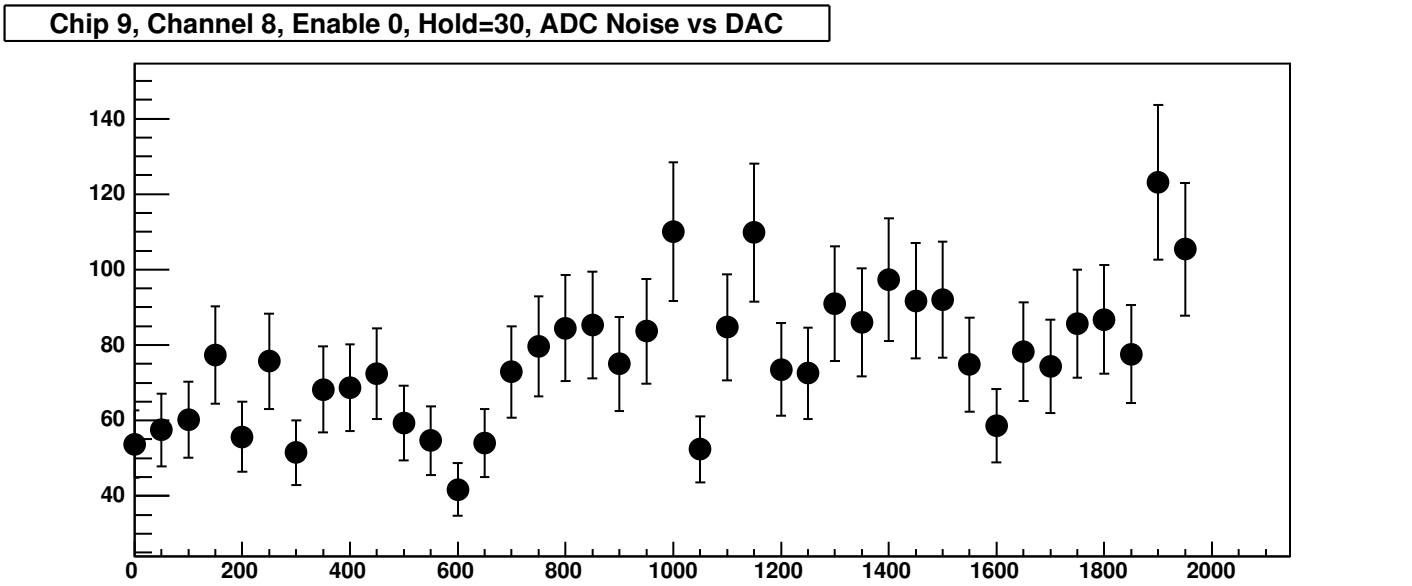

Chip 9, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

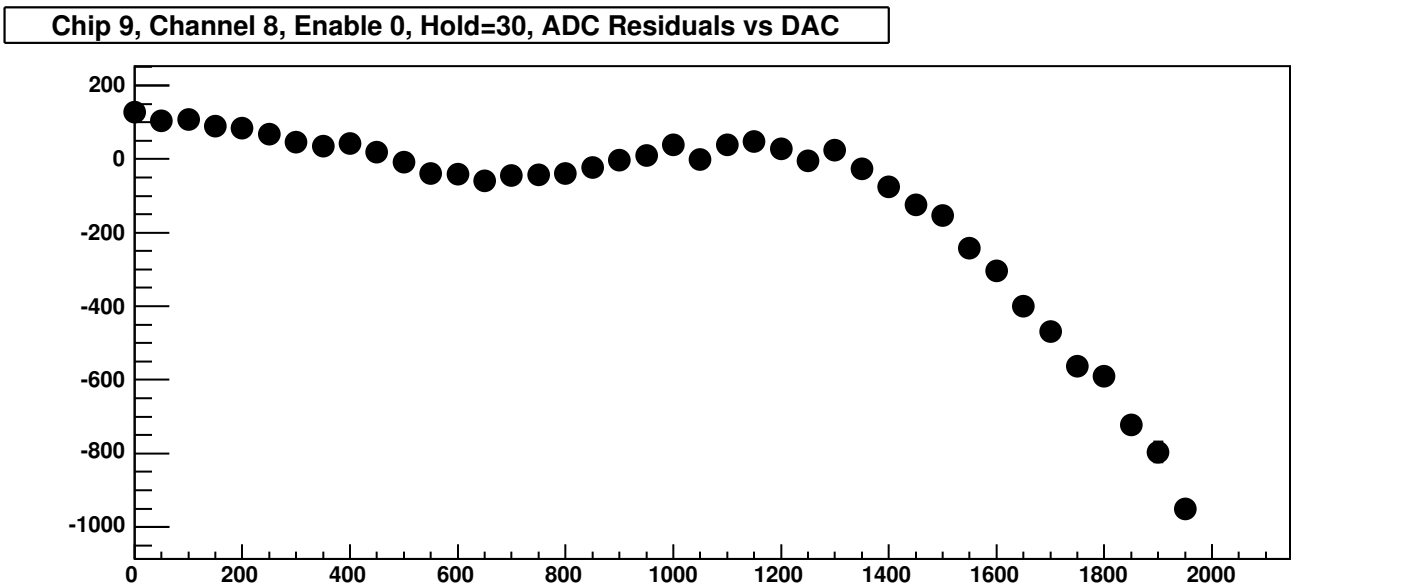

Chip 9, Channel 7, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 27.47 / 11
p0 -1550 ± 22.73
p1 0.5401 ± 0.02054
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

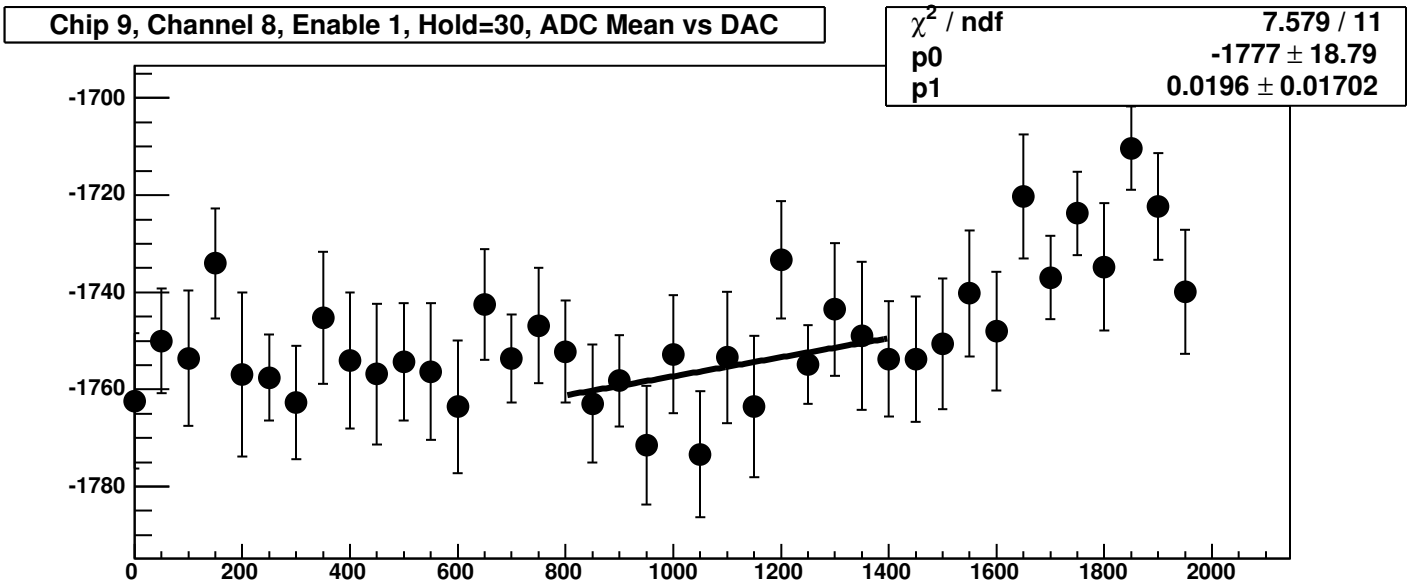

Chip 9, Channel 7, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

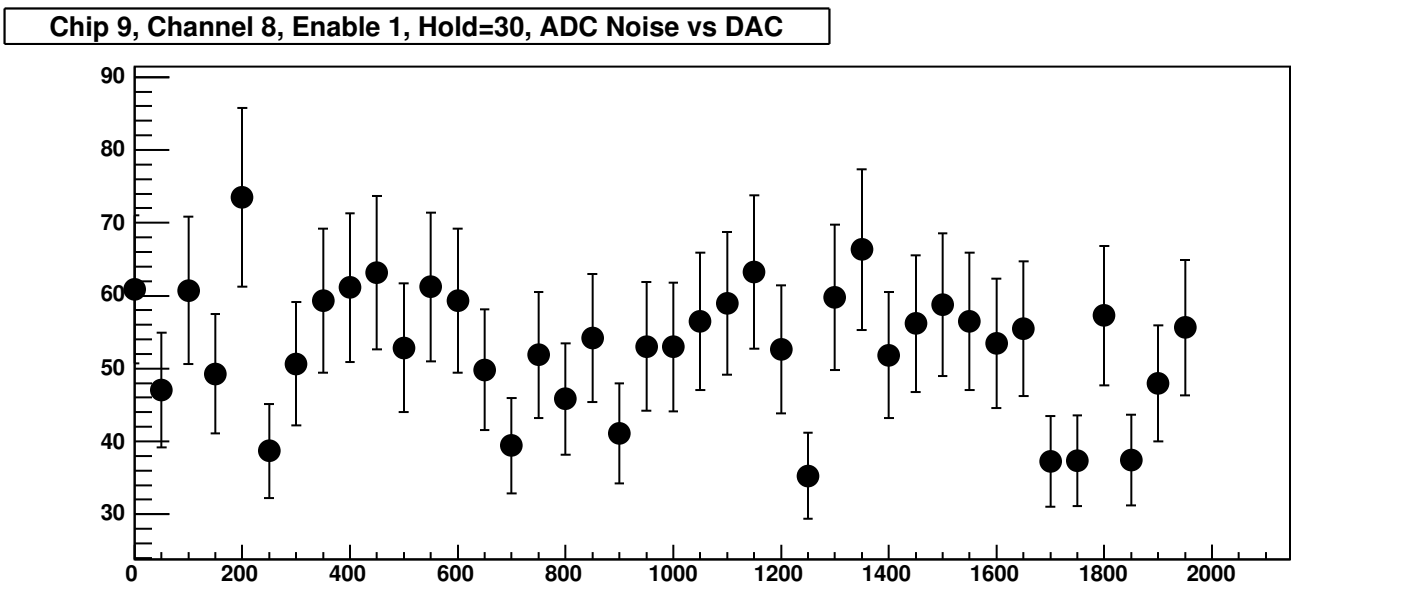

Chip 9, Channel 7, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

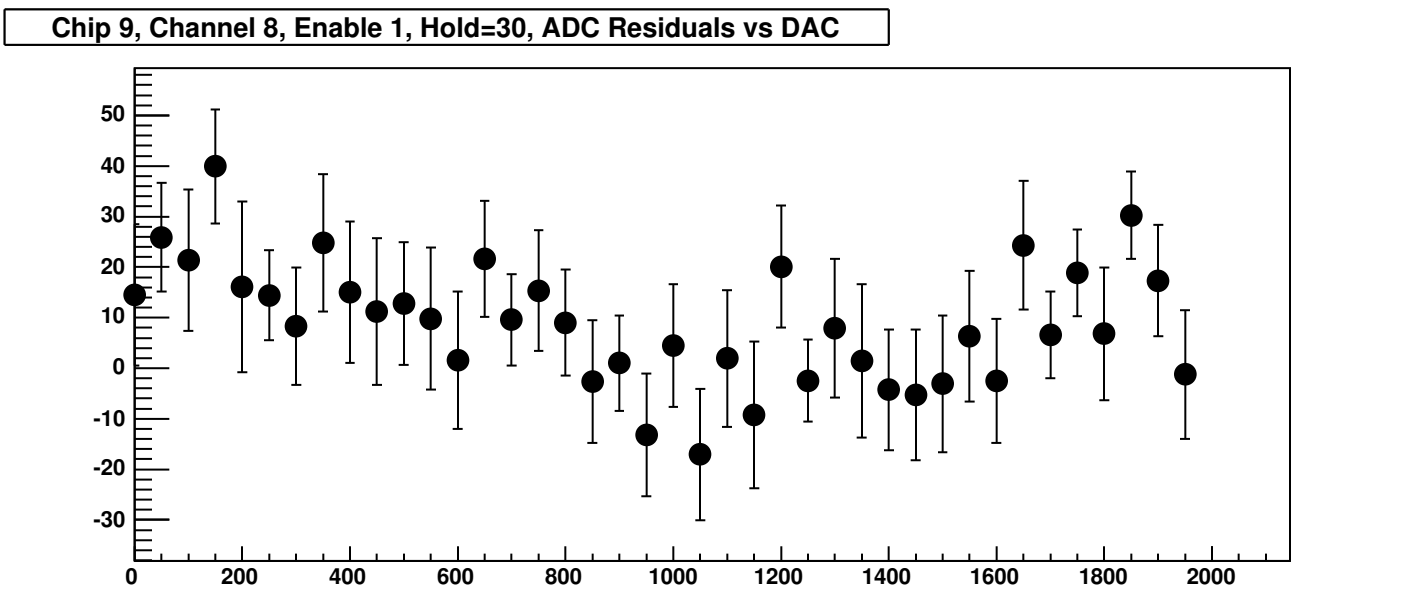

Chip 9, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 33.1 / 11
p0 -1747 ± 32.13
p1 0.3042 ± 0.02908
-1300
-1400
-1500
-1600
-1700
-1800
-1900
-2000
-2100
-2200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

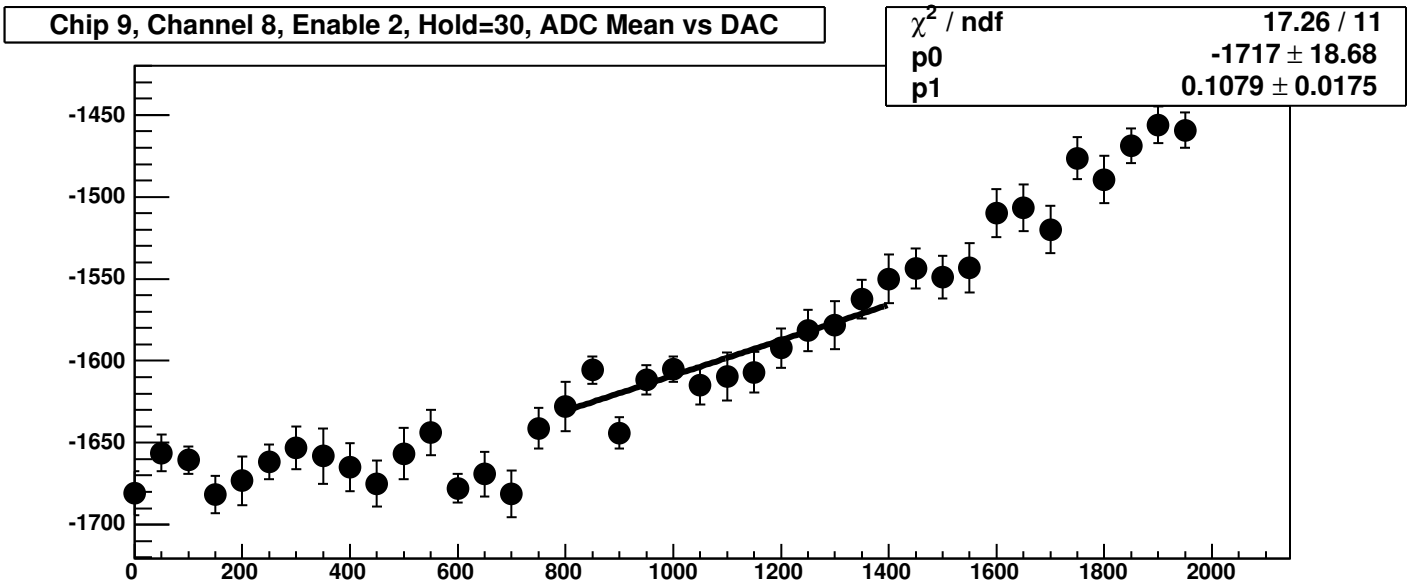

Chip 9, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

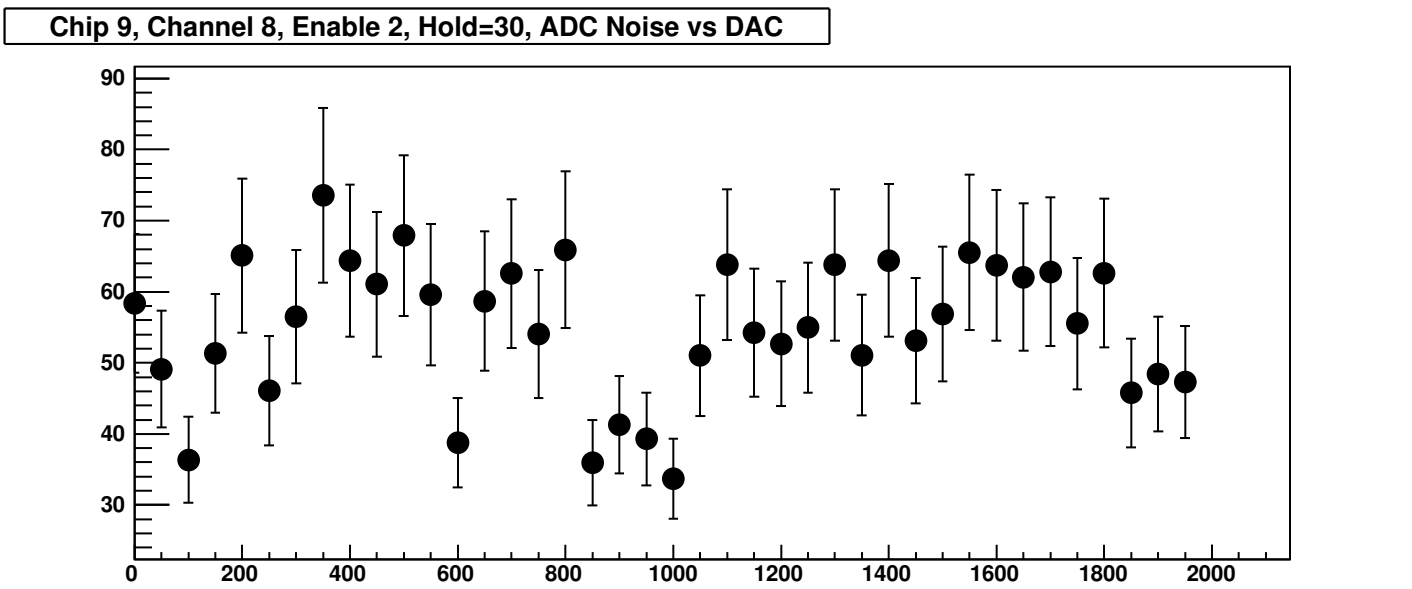

Chip 9, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

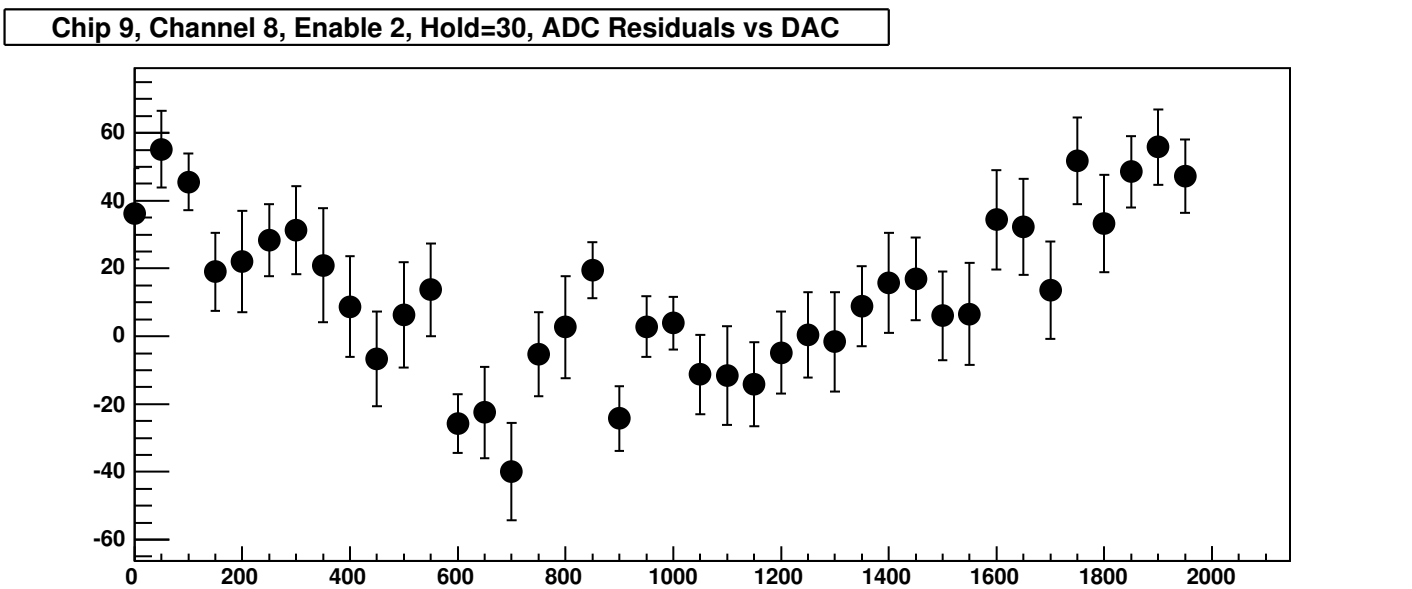

Chip 9, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7.579 / 11
p0 -1777 ± 18.79
p1 0.0196 ± 0.01702
-1700
-1720
-1740
-1760
-1780
0
200
400
600
800
1000
1200
1400
1600
1800
2000

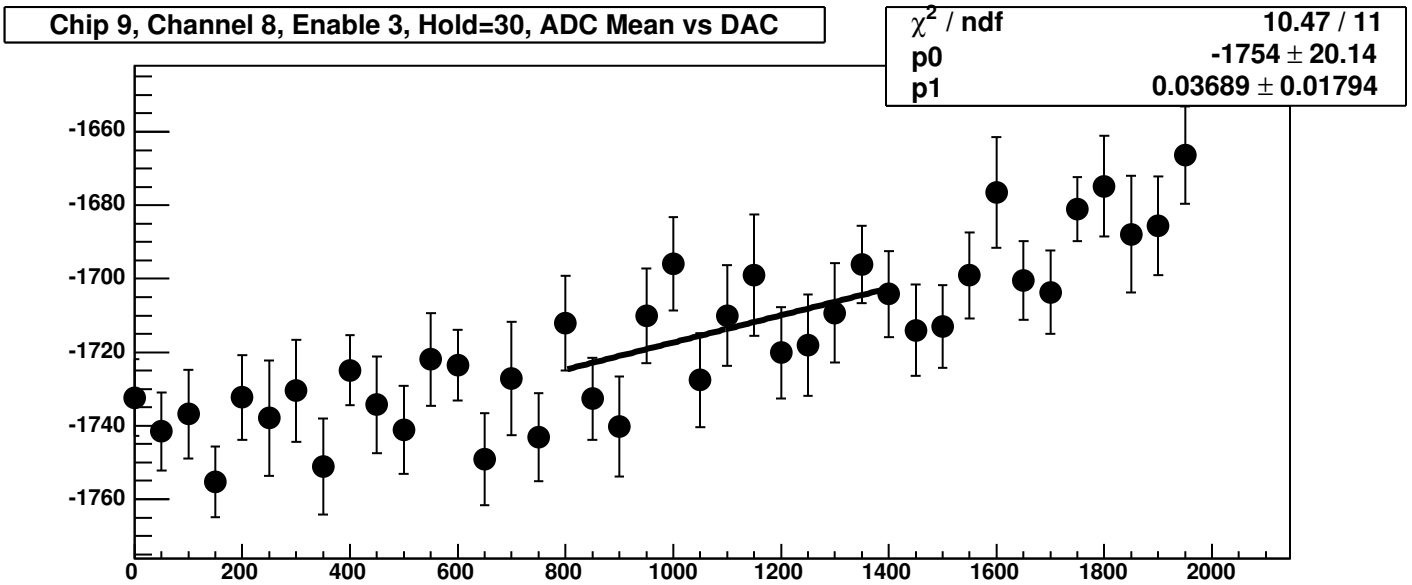

Chip 9, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

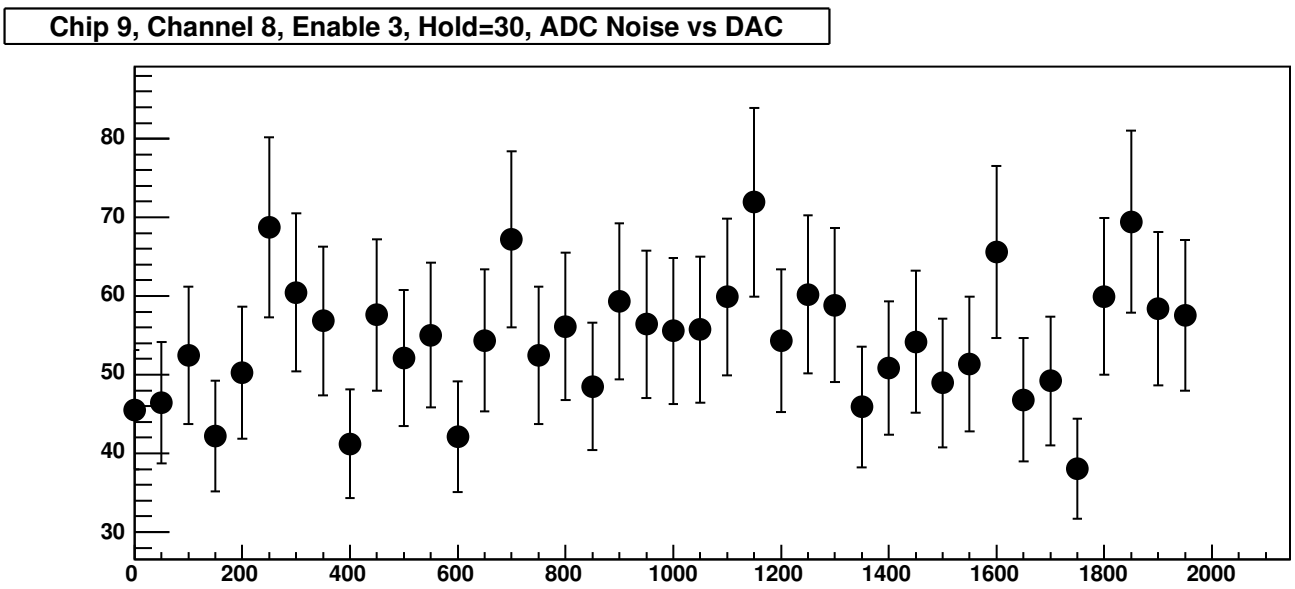

Chip 9, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
50
40
30
20
10
0
-10
-20
-30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

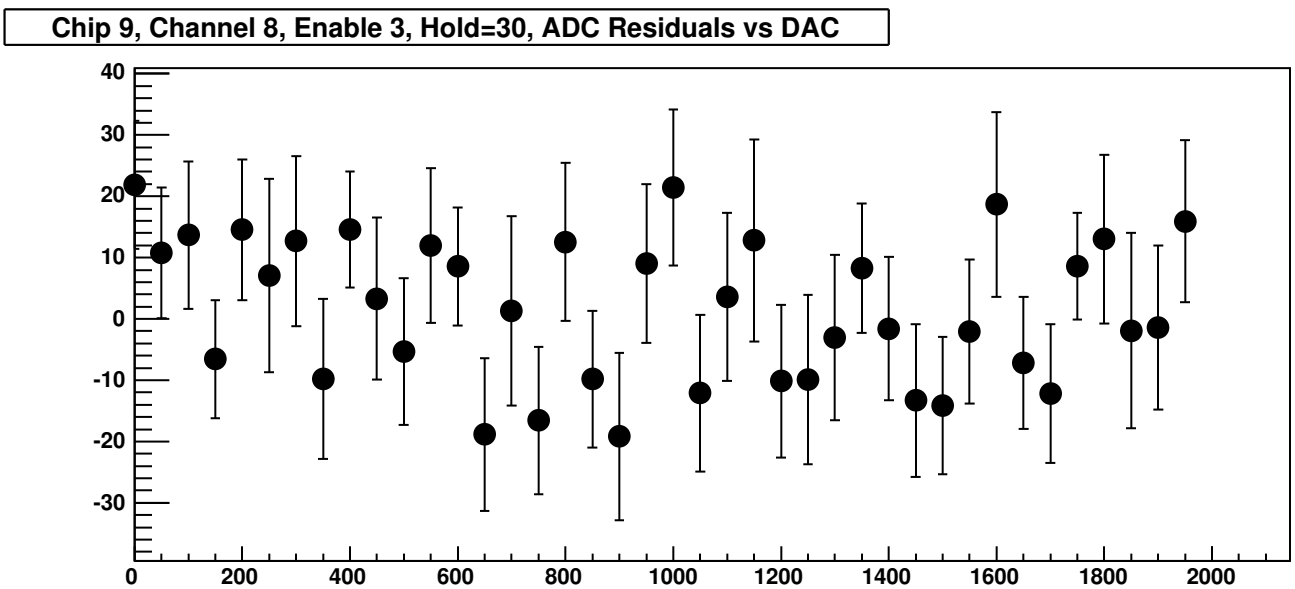

Chip 9, Channel 8, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 17.26 / 11
p0 -1717 ± 18.68
p1 0.1079 ± 0.0175
-1450
-1500
-1550
-1600
-1650
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 8, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

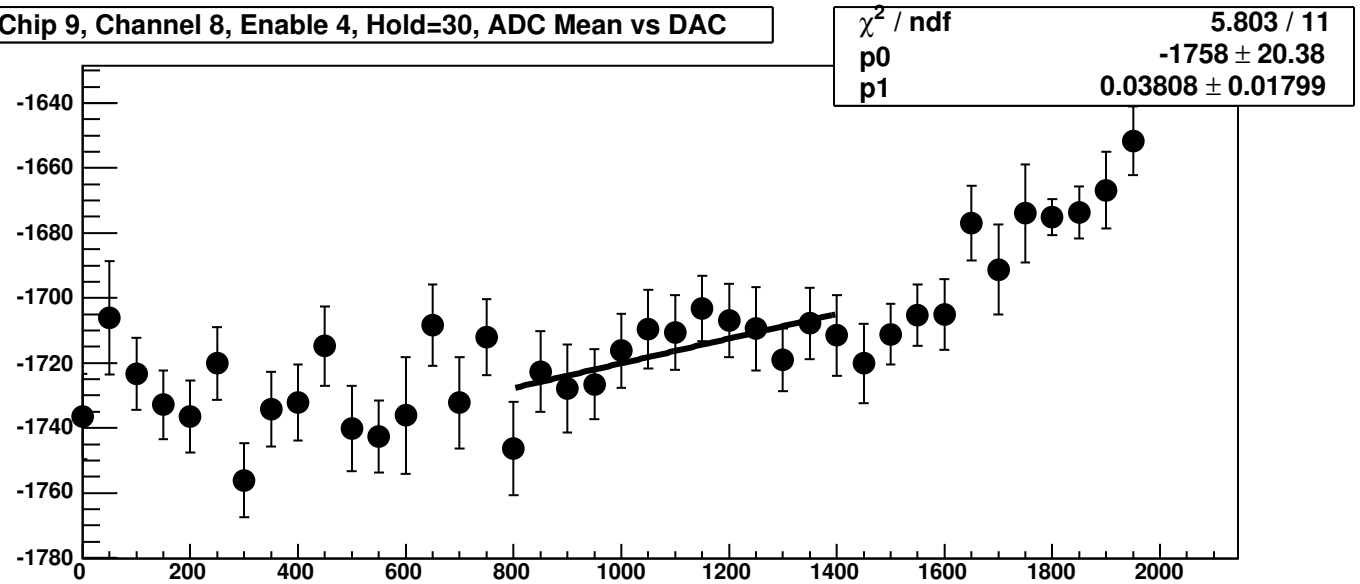

Chip 9, Channel 8, Enable 2, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 10.47 / 11
p0 -1754 ± 20.14
p1 0.03689 ± 0.01794
-1660
-1680
-1700
-1720
-1740
-1760
0
200
400
600
800
1000
1200
1400
1600
1800
2000

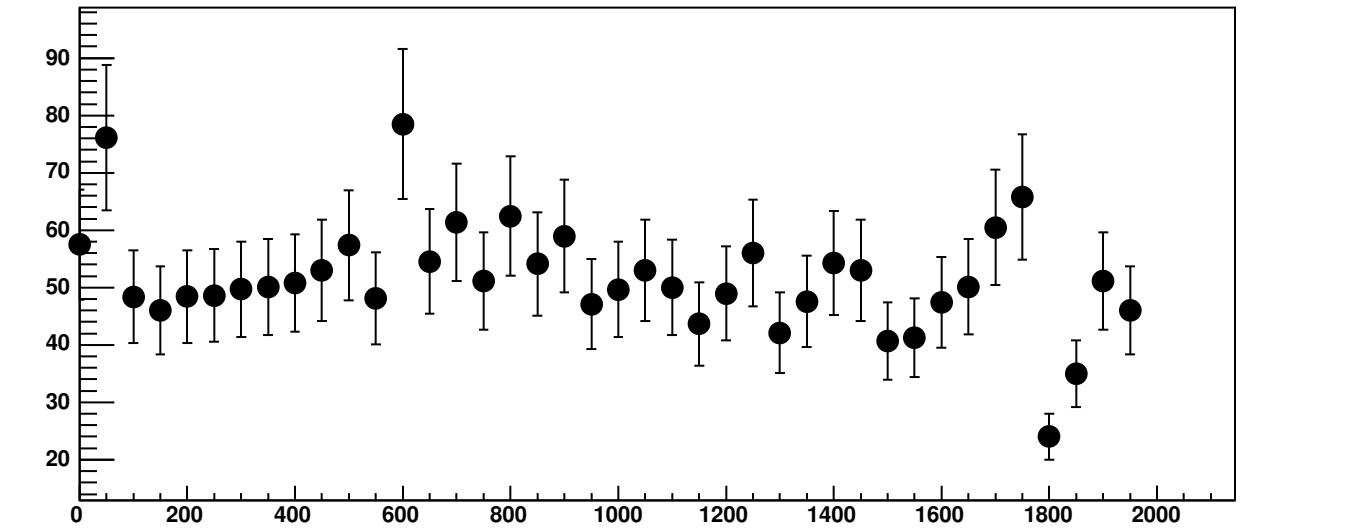

Chip 9, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
40
30
20
10
0
-10
-20
-30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

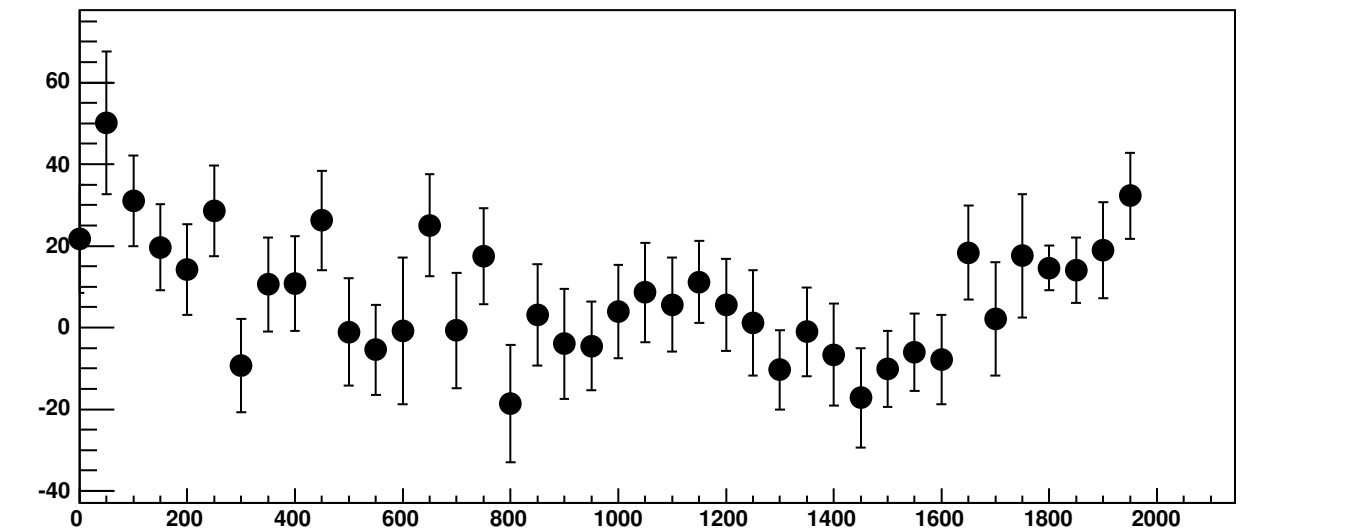
Chip 9, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC

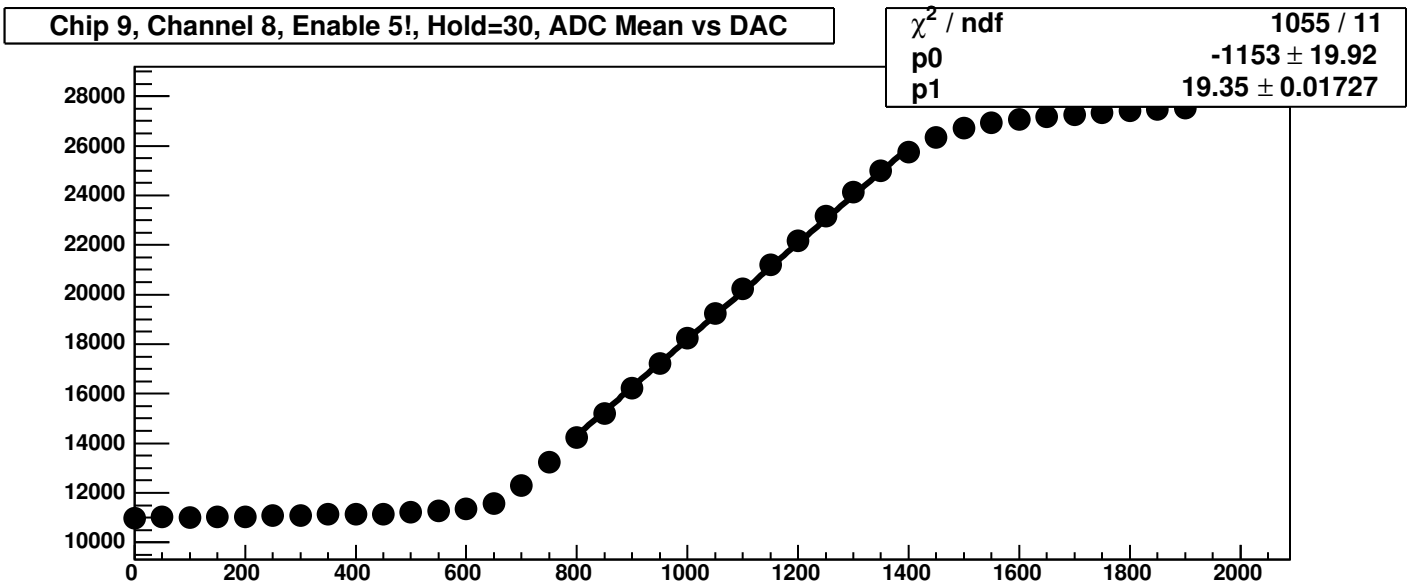


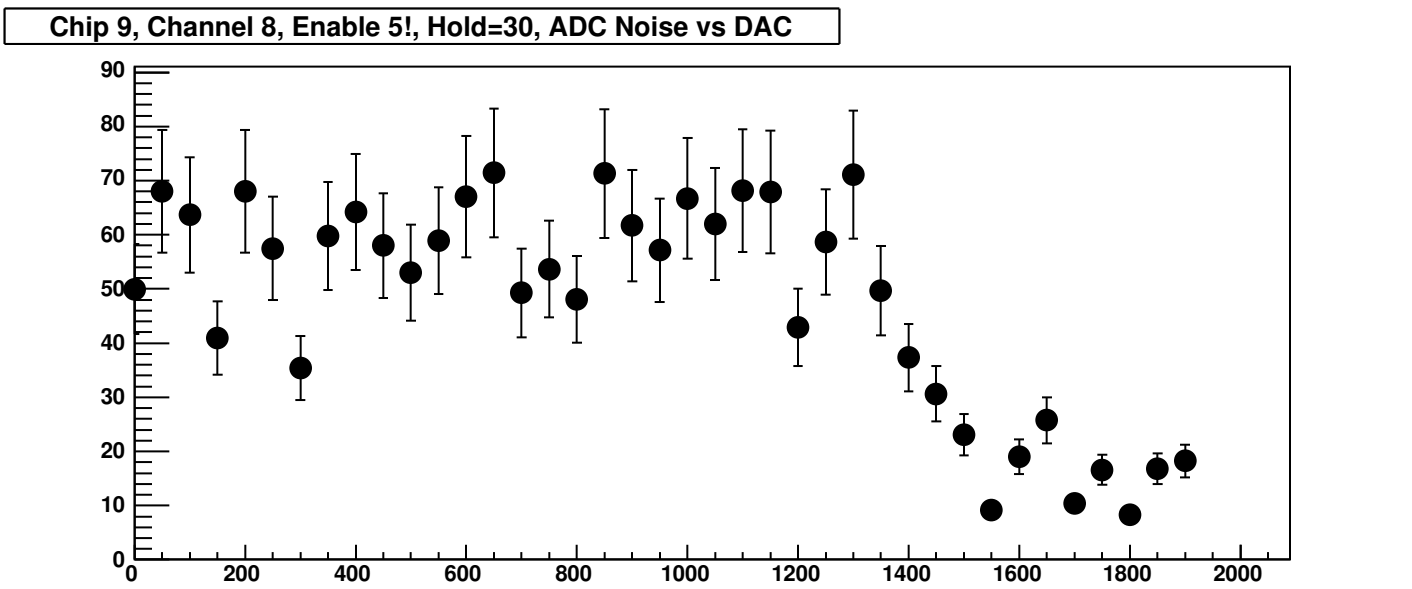
Chip 9, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC

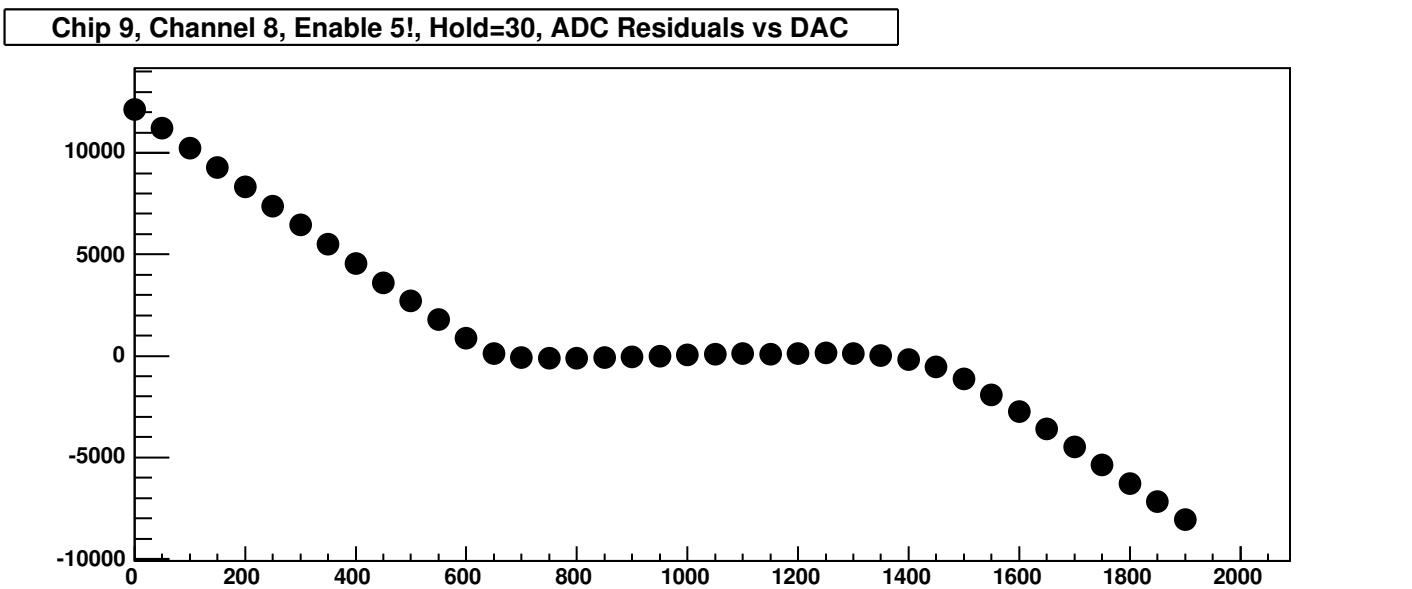


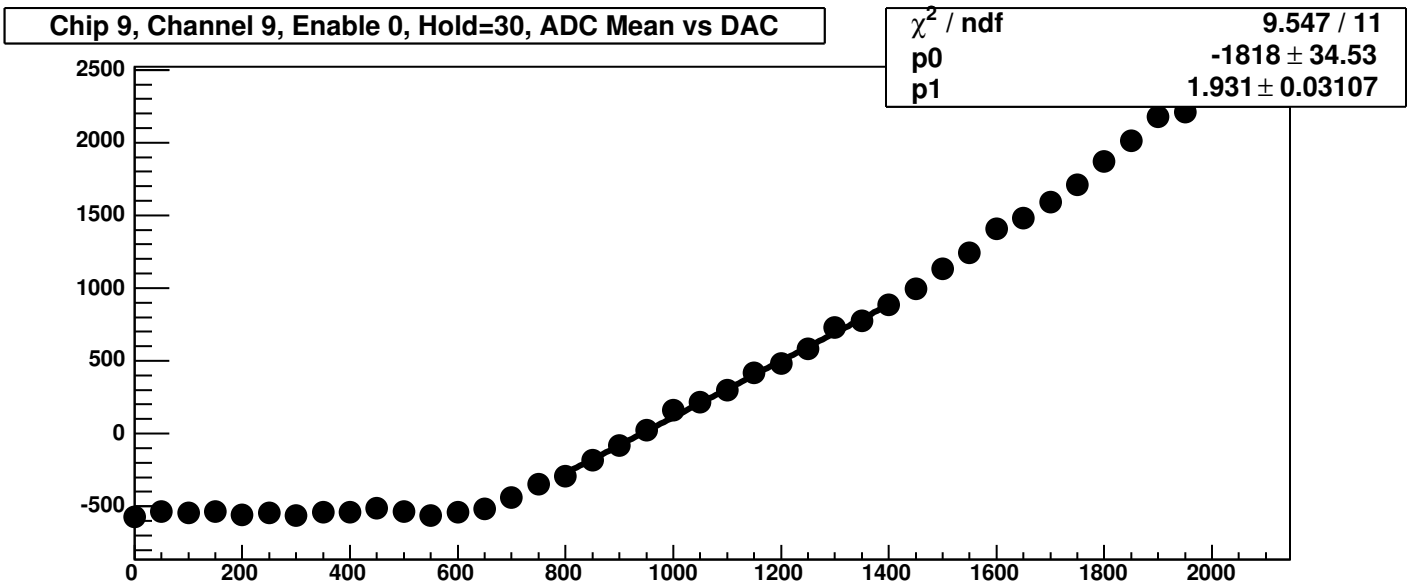
Chip 9, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC

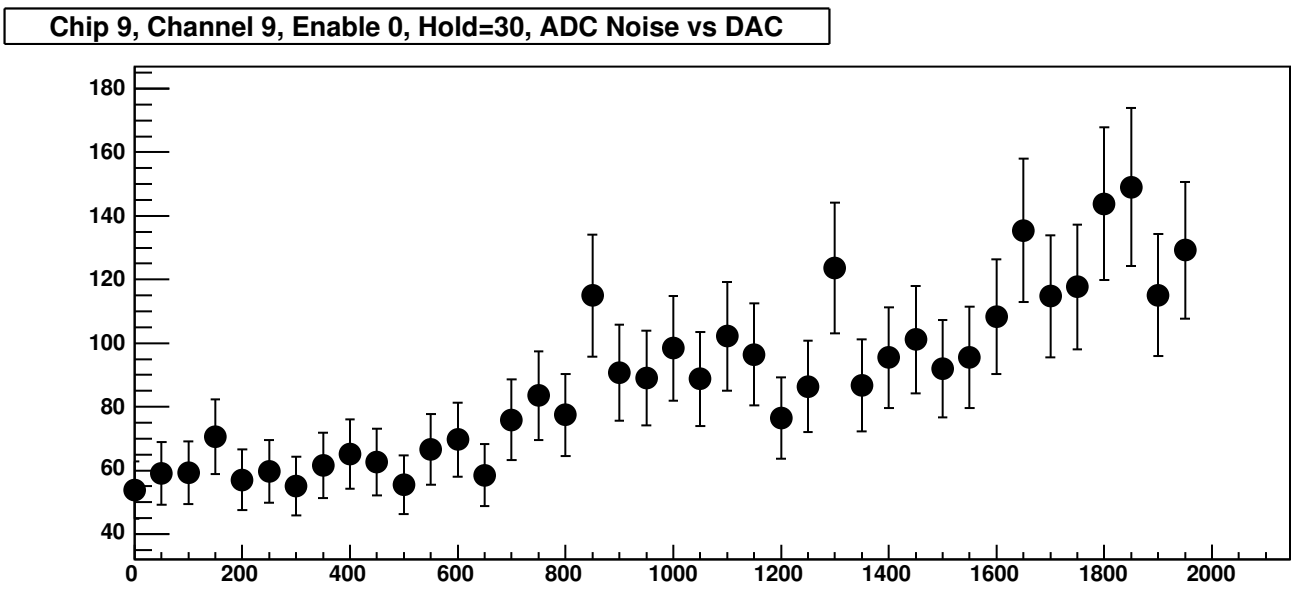


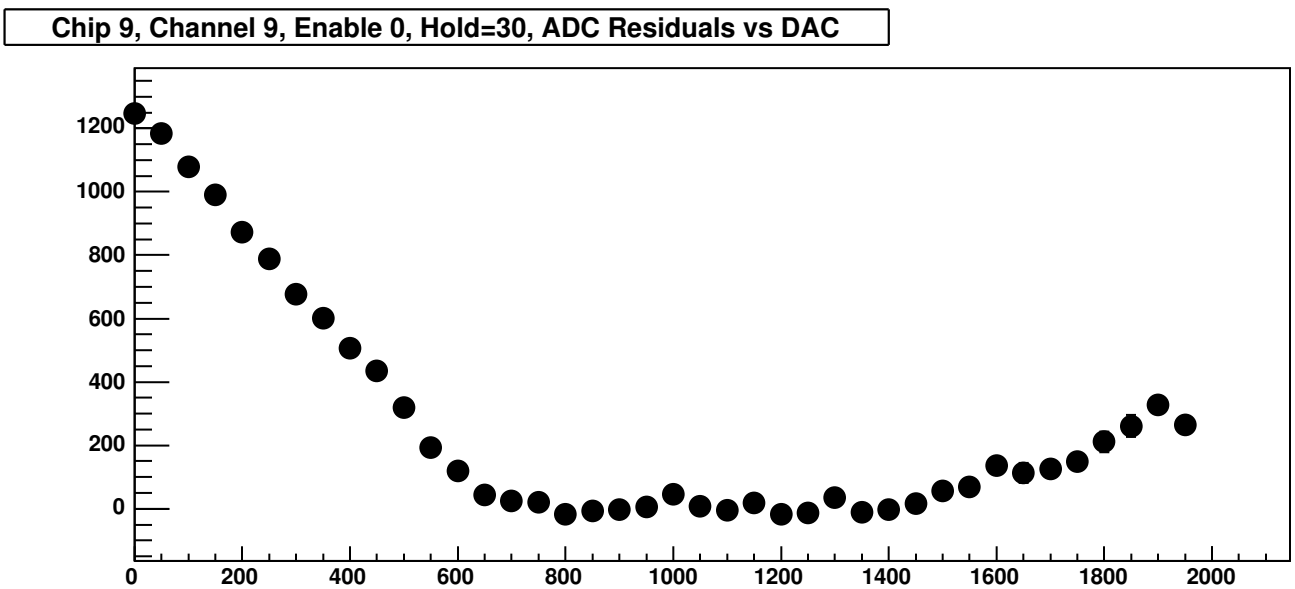

Chip 9, Channel 8, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 1055 / 11
p0 -1153 ± 19.92
p1 19.35 ± 0.01727
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

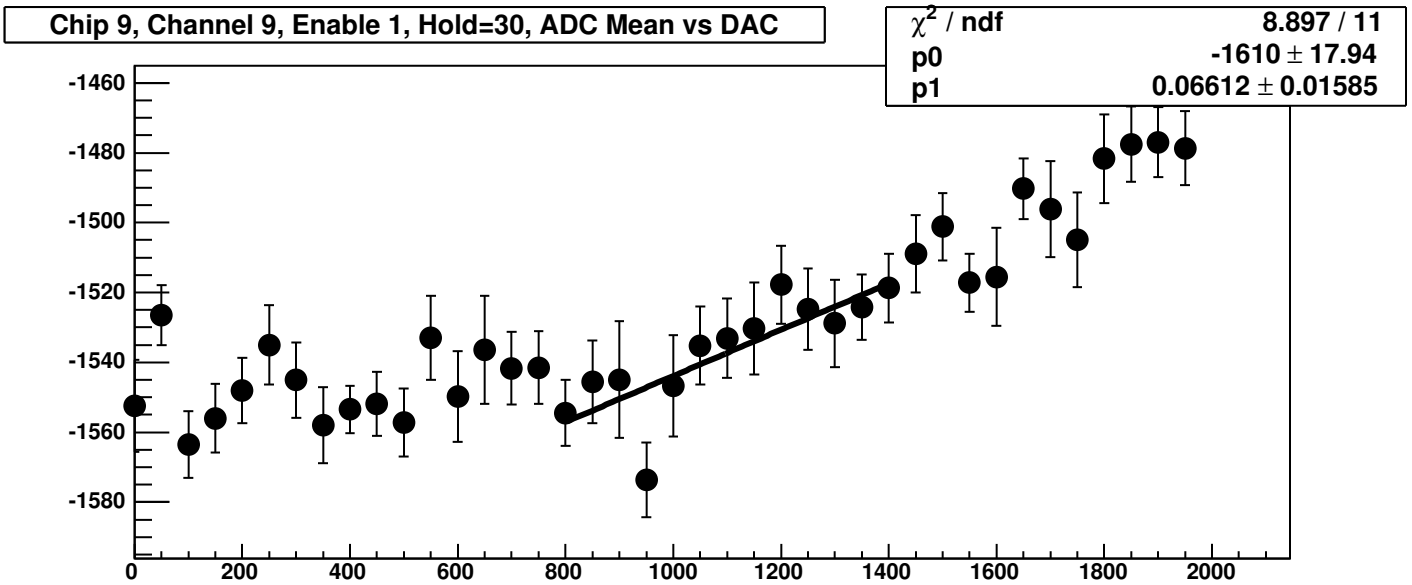

Chip 9, Channel 8, Enable 5!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

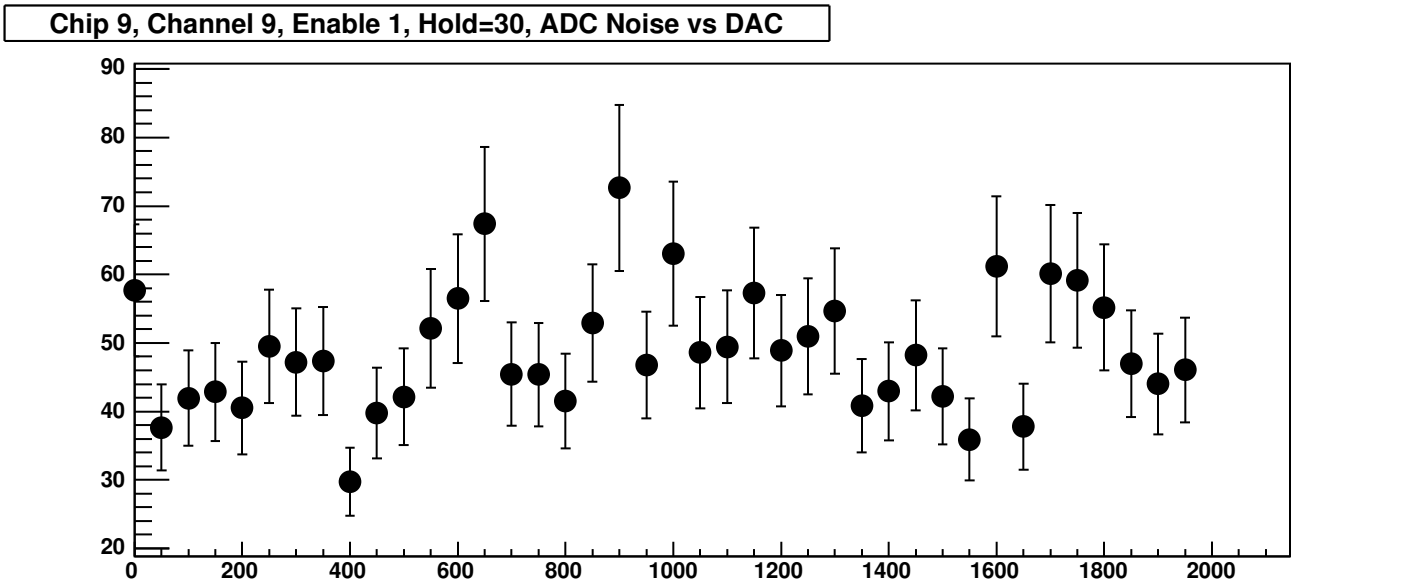

Chip 9, Channel 8, Enable 5!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

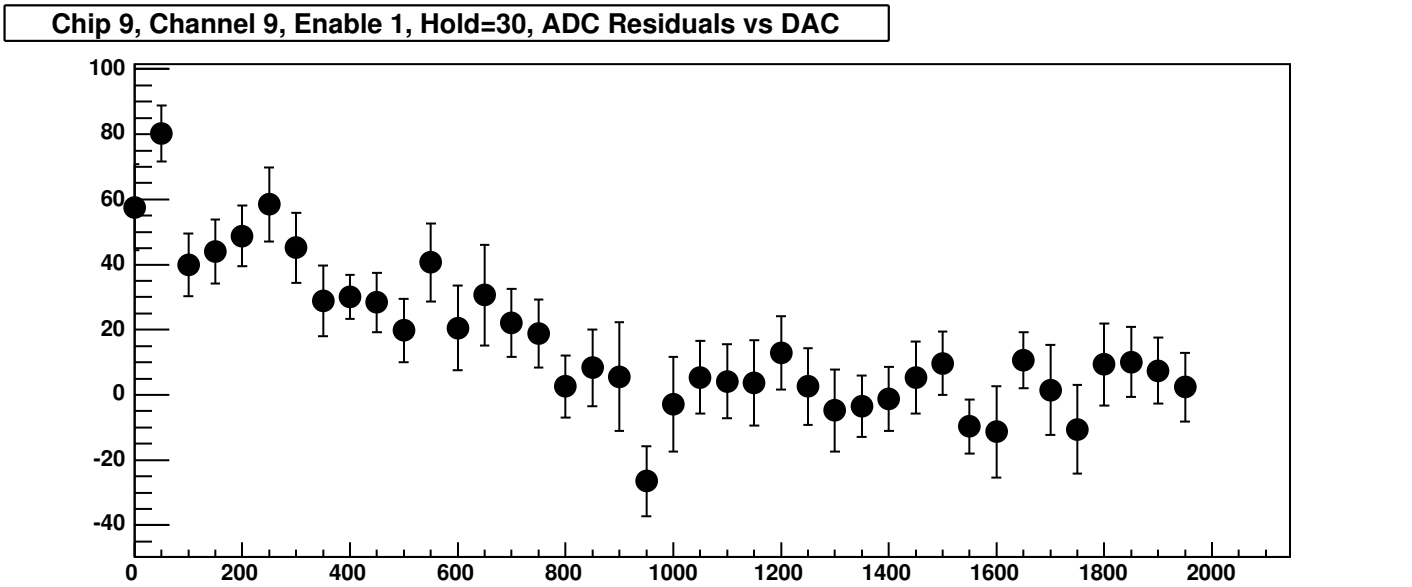

Chip 9, Channel 9, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 9.547 / 11
p0 -1818 ± 34.53
p1 1.931 ± 0.03107
2500
2000
1500
1000
500
0
-500
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 9, Enable 0, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

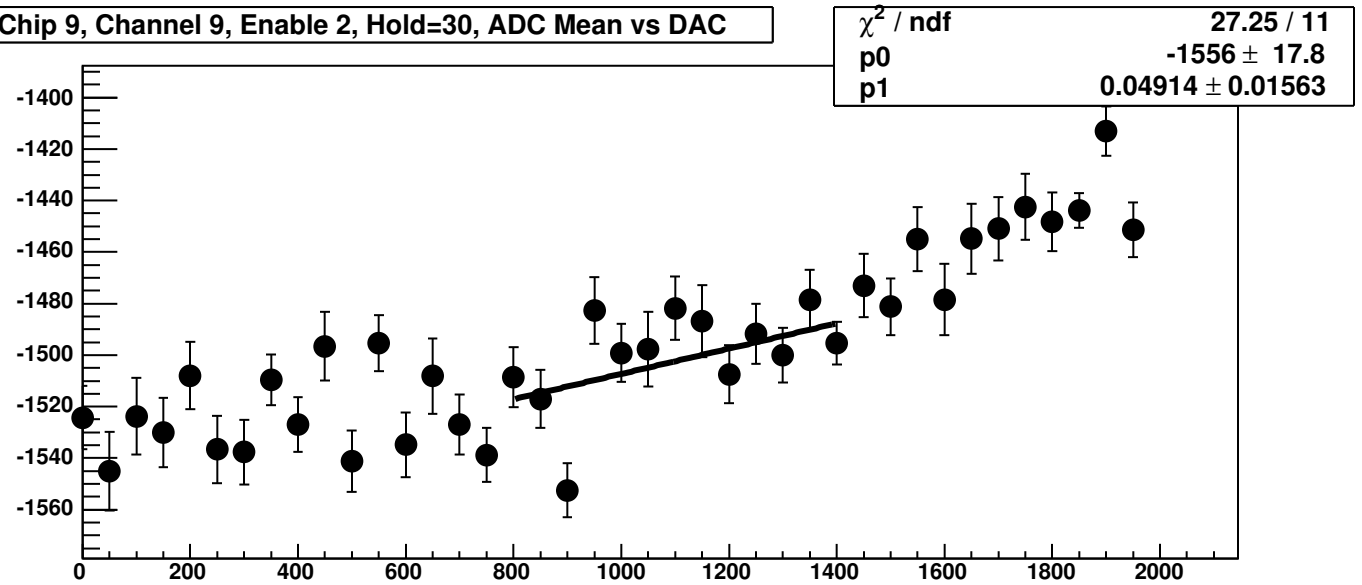

Chip 9, Channel 9, Enable 0, Hold=30, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 8.897 / 11
p0 -1610 ± 17.94
p1 0.06612 ± 0.01585
-1460
-1480
-1500
-1520
-1540
-1560
-1580
0
200
400
600
800
1000
1200
1400
1600
1800
2000

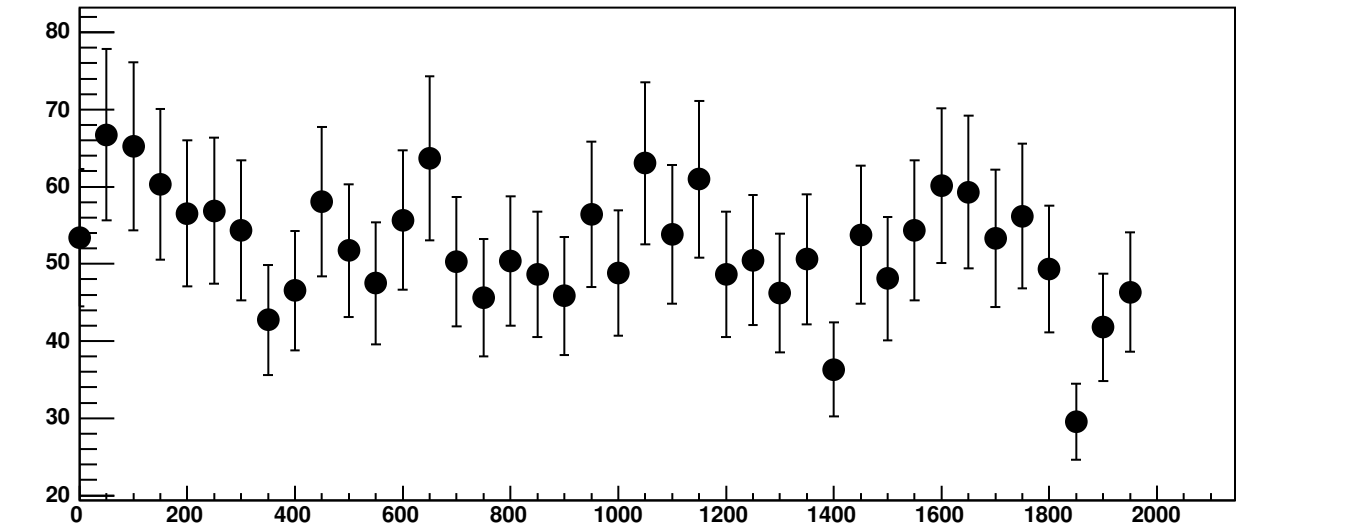

Chip 9, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

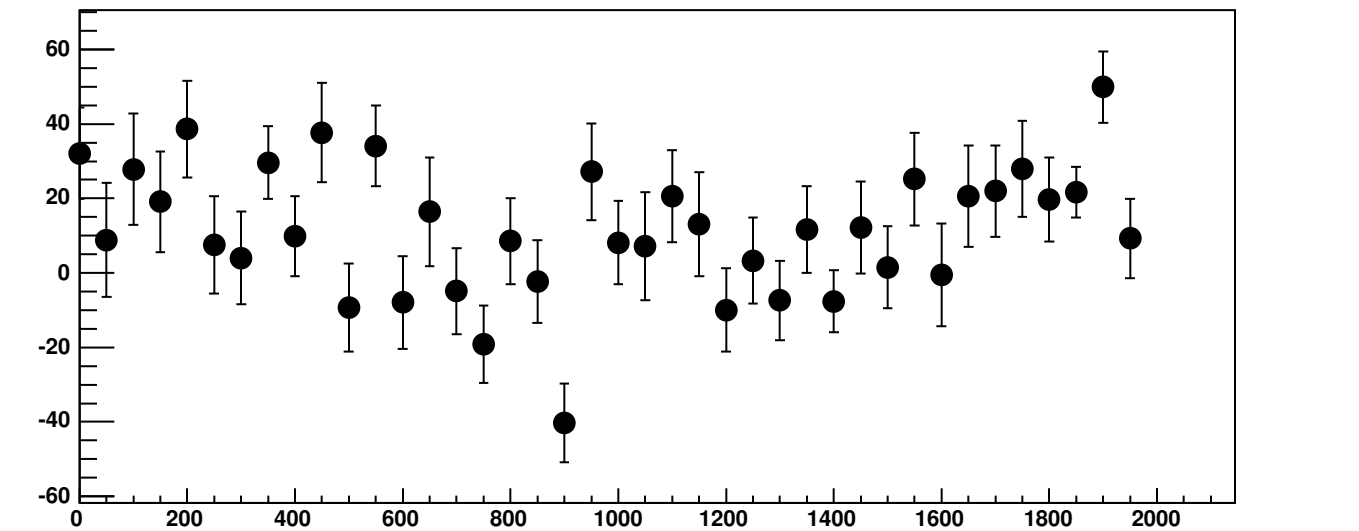
Chip 9, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC

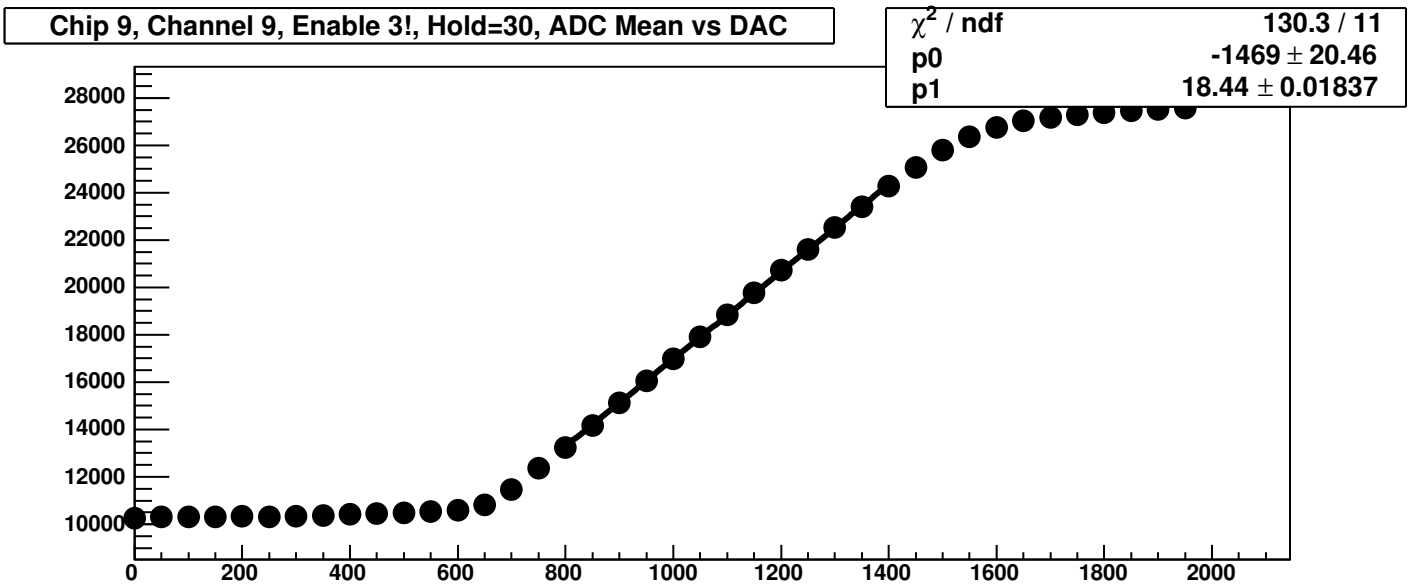


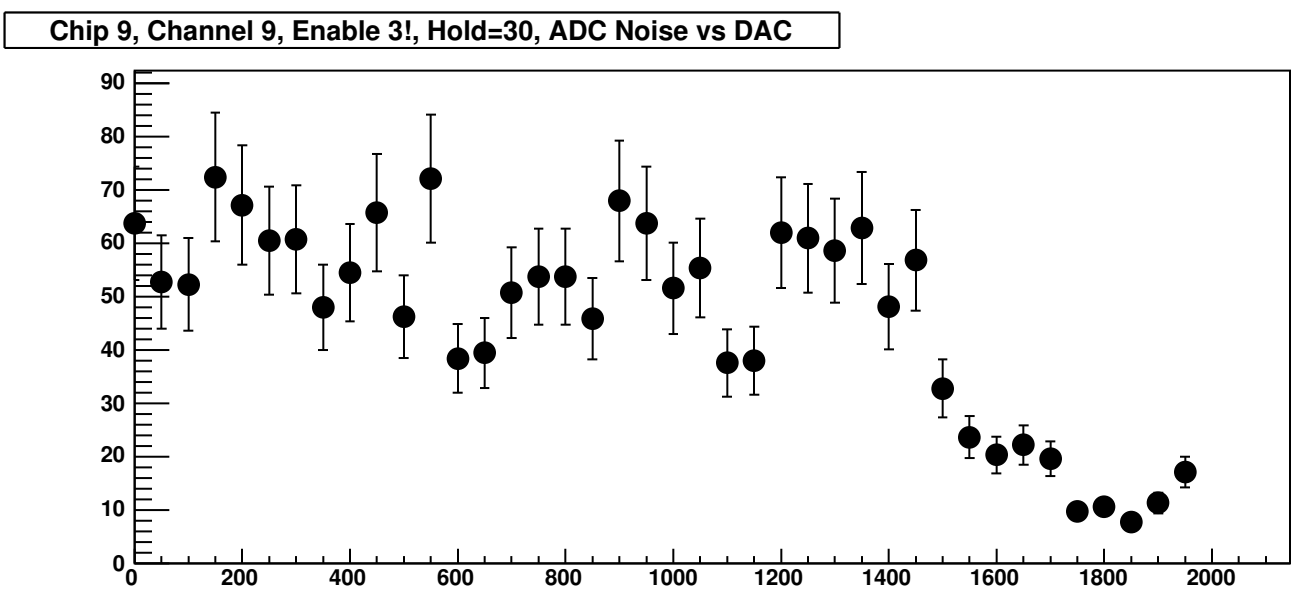
Chip 9, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC

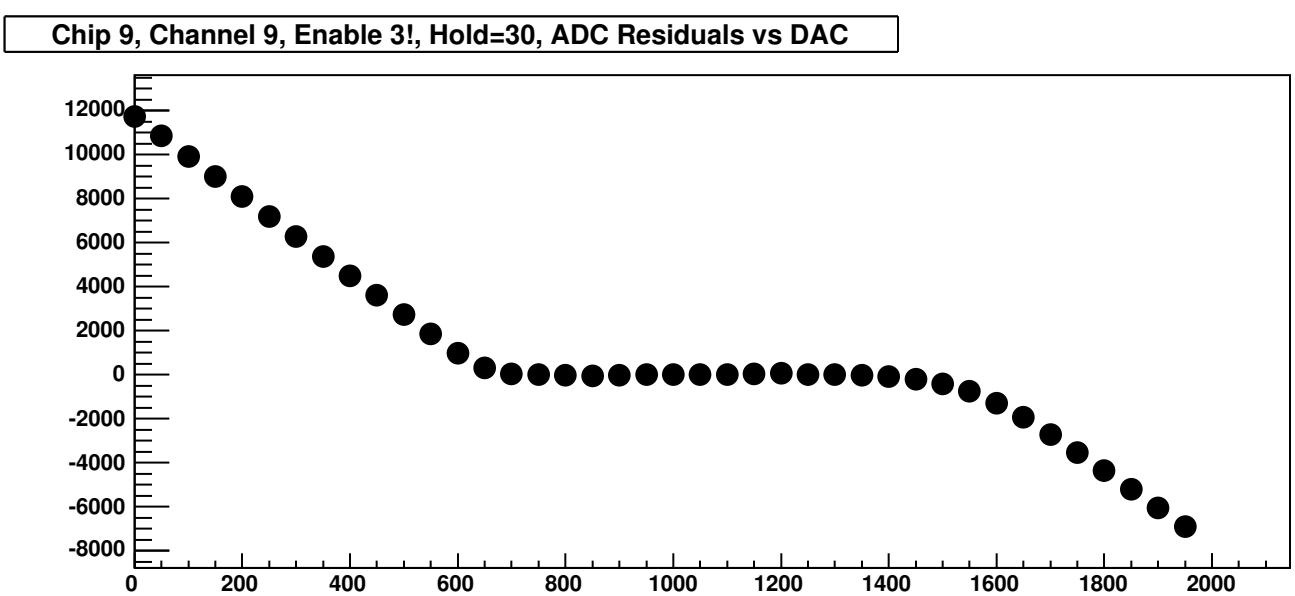


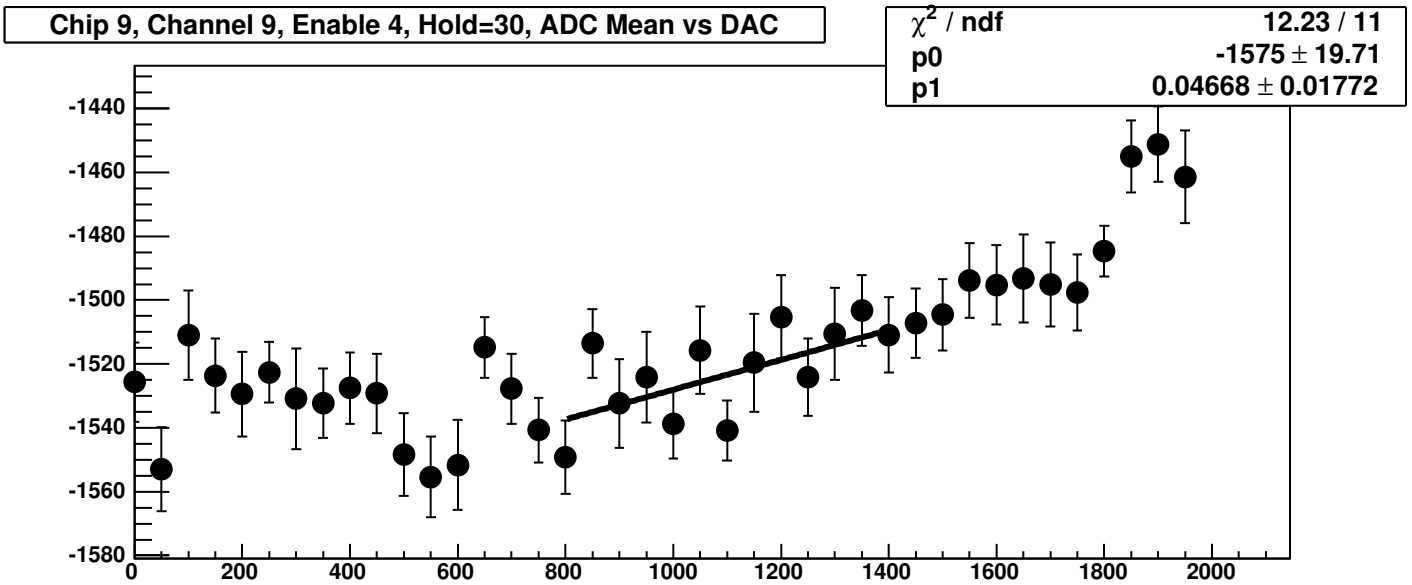
Chip 9, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC

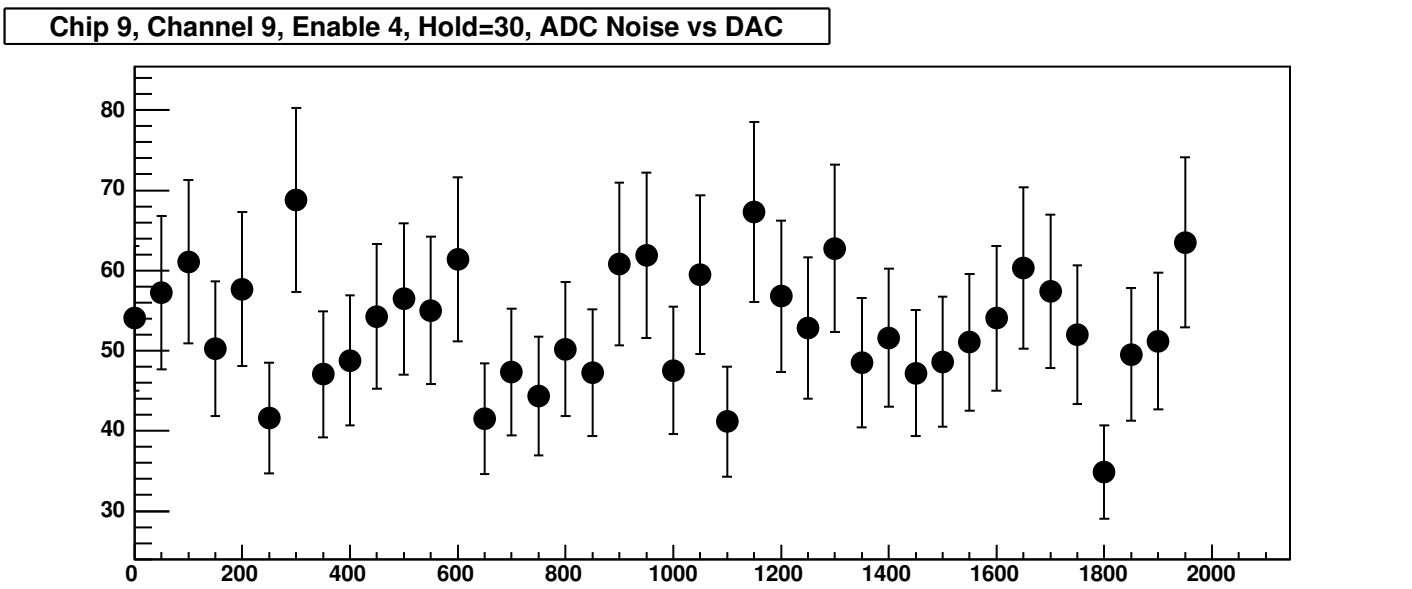


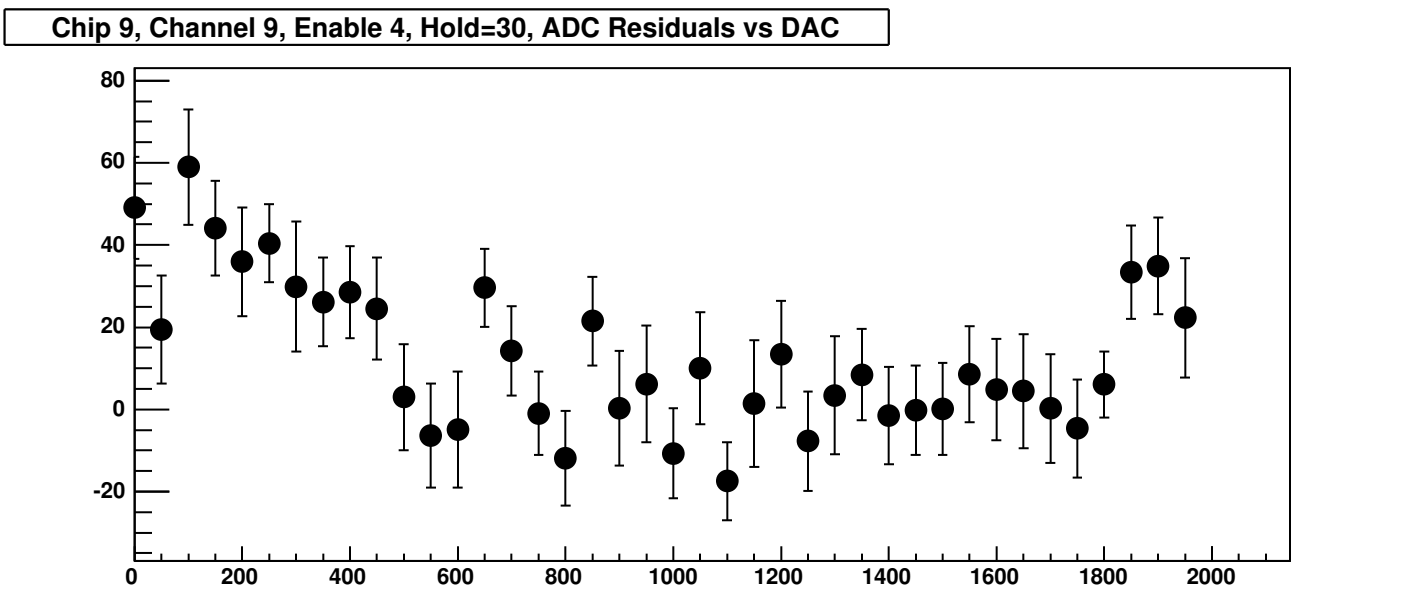

Chip 9, Channel 9, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 130.3 / 11
p0 -1469 ± 20.46
p1 18.44 ± 0.01837
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 9, Enable 3!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

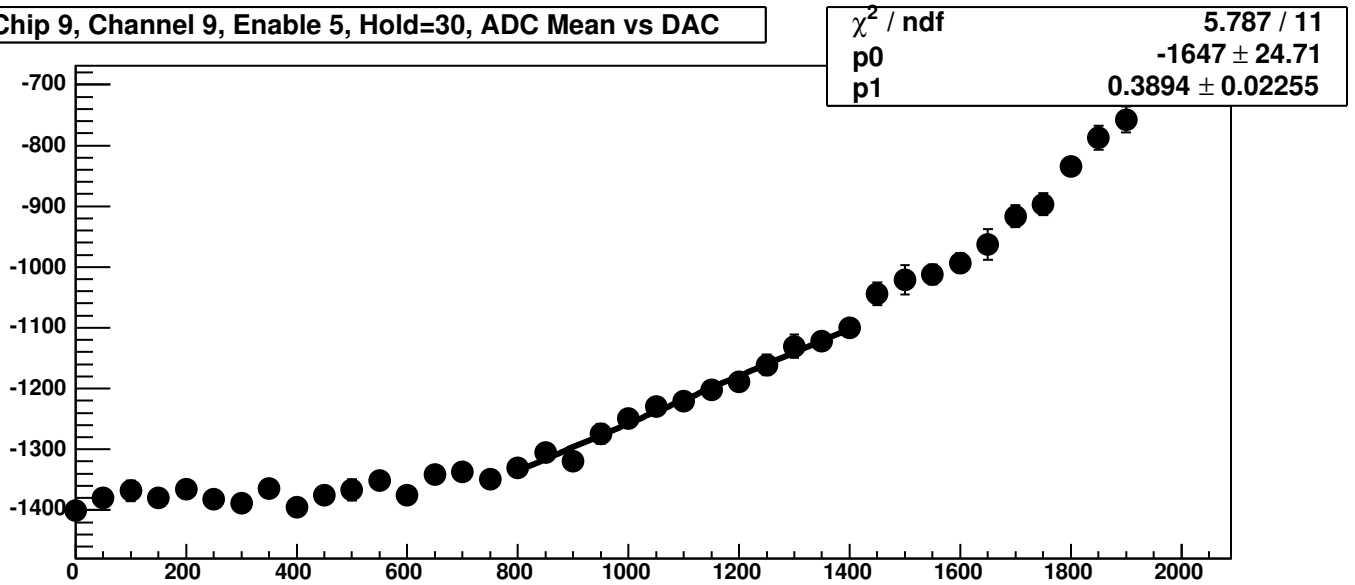

Chip 9, Channel 9, Enable 3!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 12.23 / 11
p0 -1575 ± 19.71
p1 0.04668 ± 0.01772
-1440
-1460
-1480
-1500
-1520
-1540
-1560
-1580
0
200
400
600
800
1000
1200
1400
1600
1800
2000

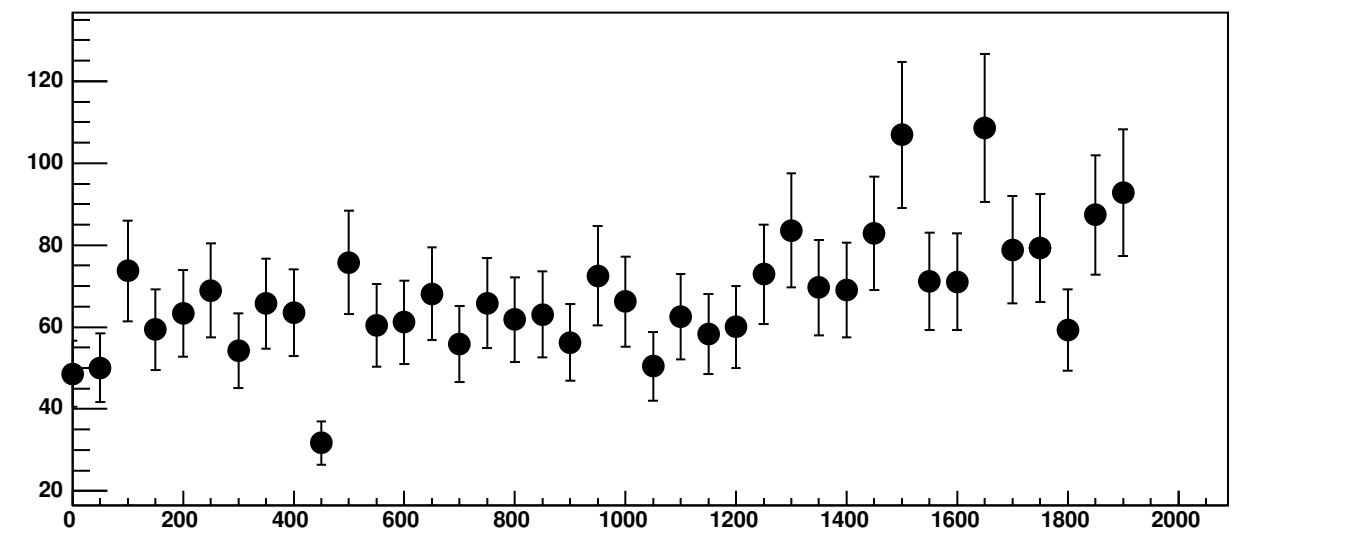

Chip 9, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

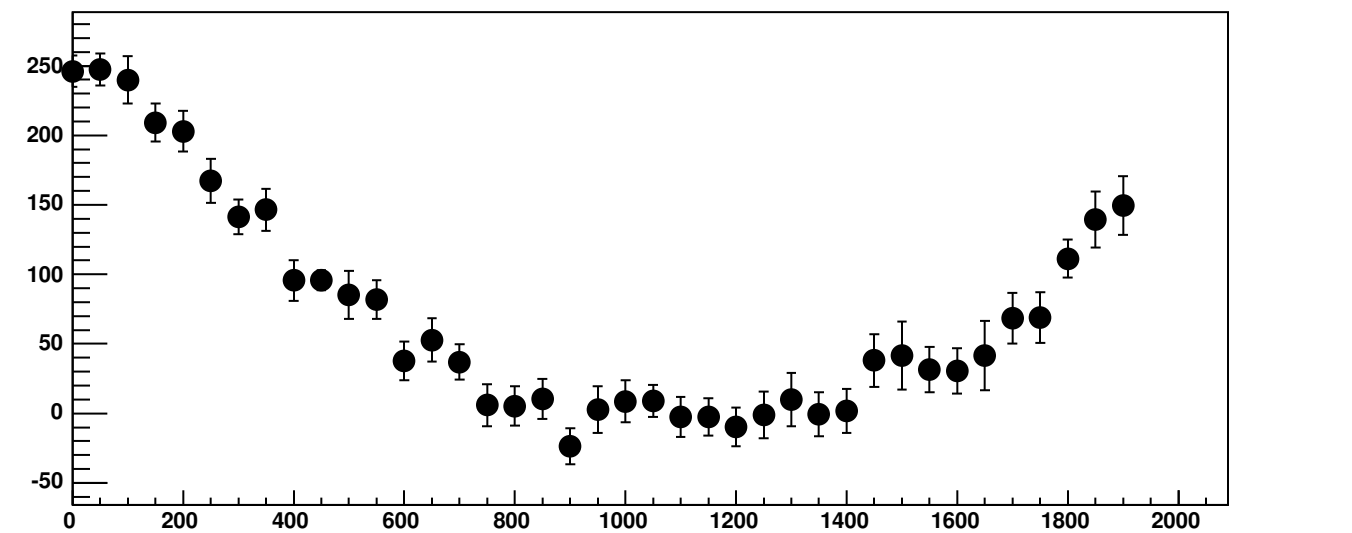
Chip 9, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC



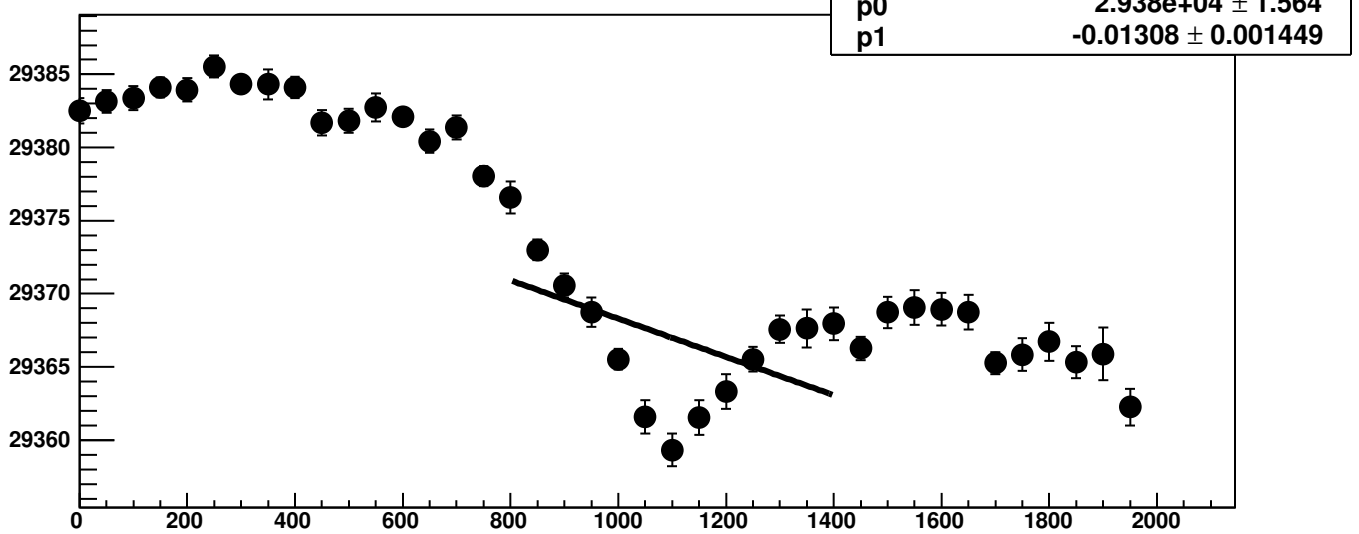
Chip 9, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC



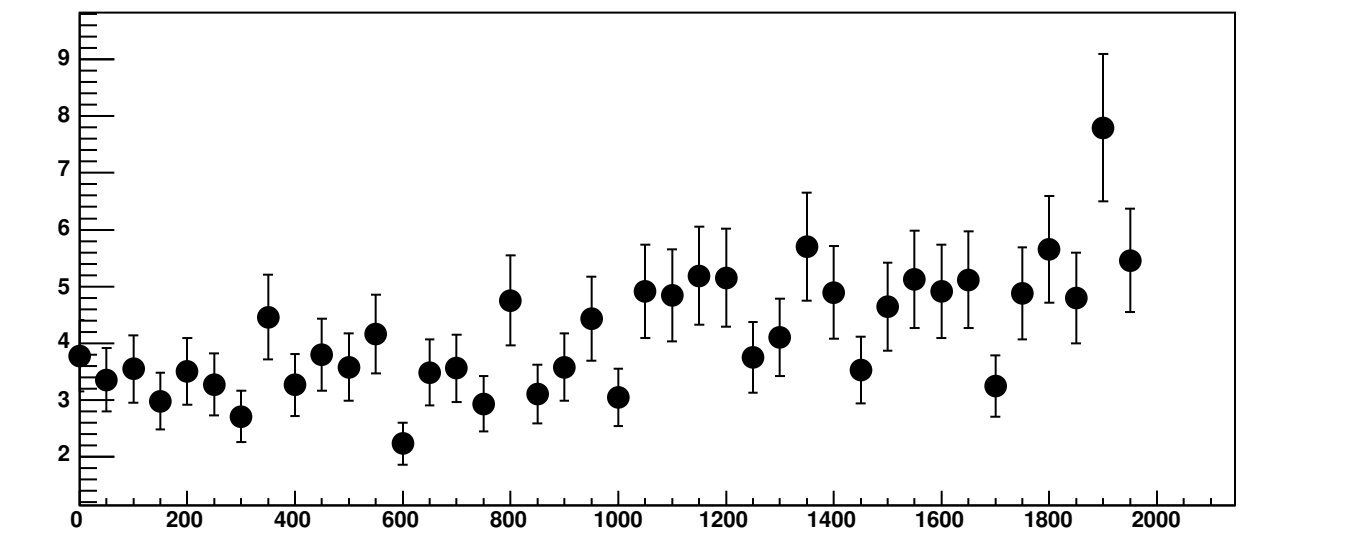
Chip 9, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC



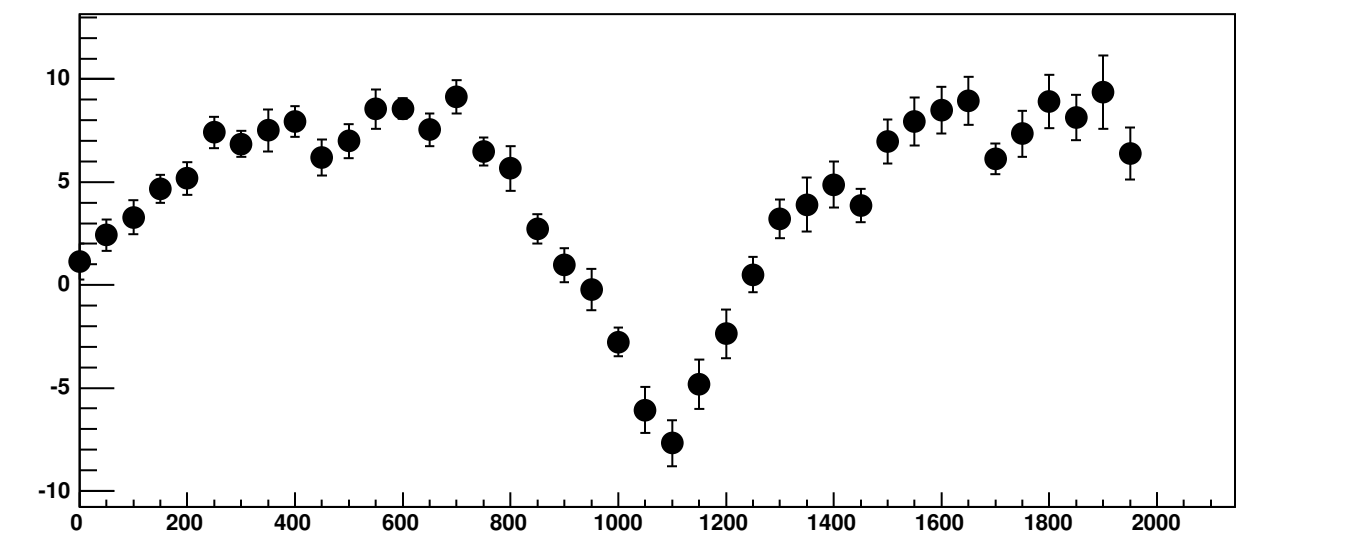
Chip 9, Channel 10, Enable 0!, Hold=30, ADC Mean vs DAC

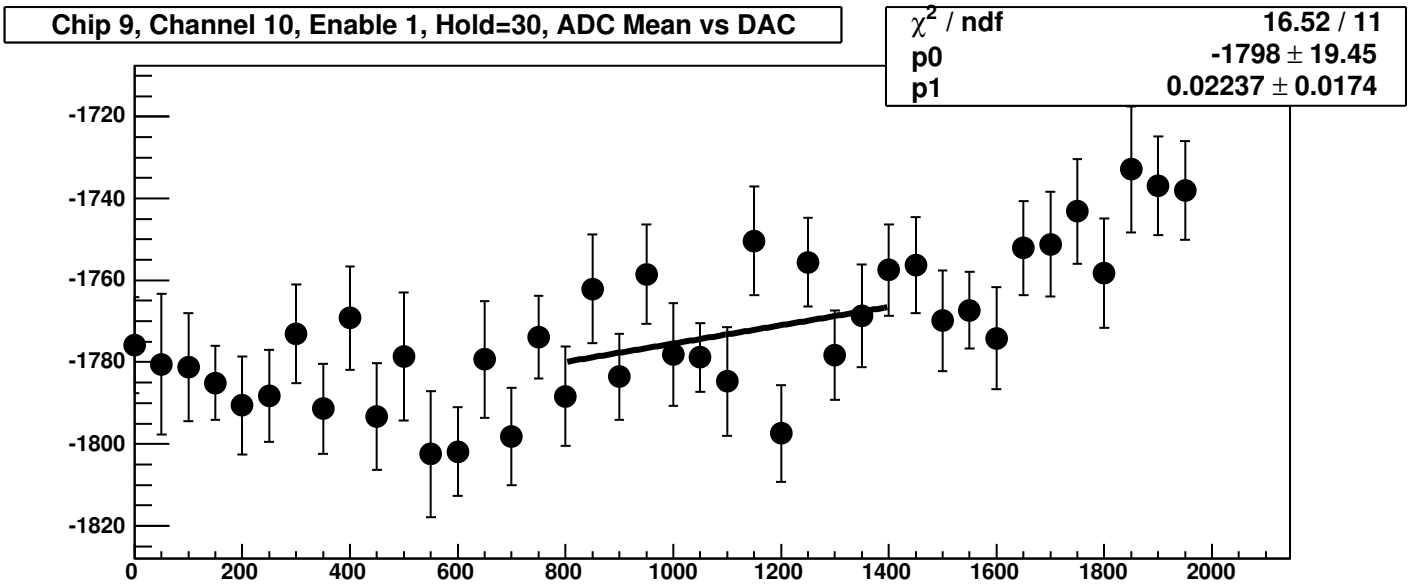


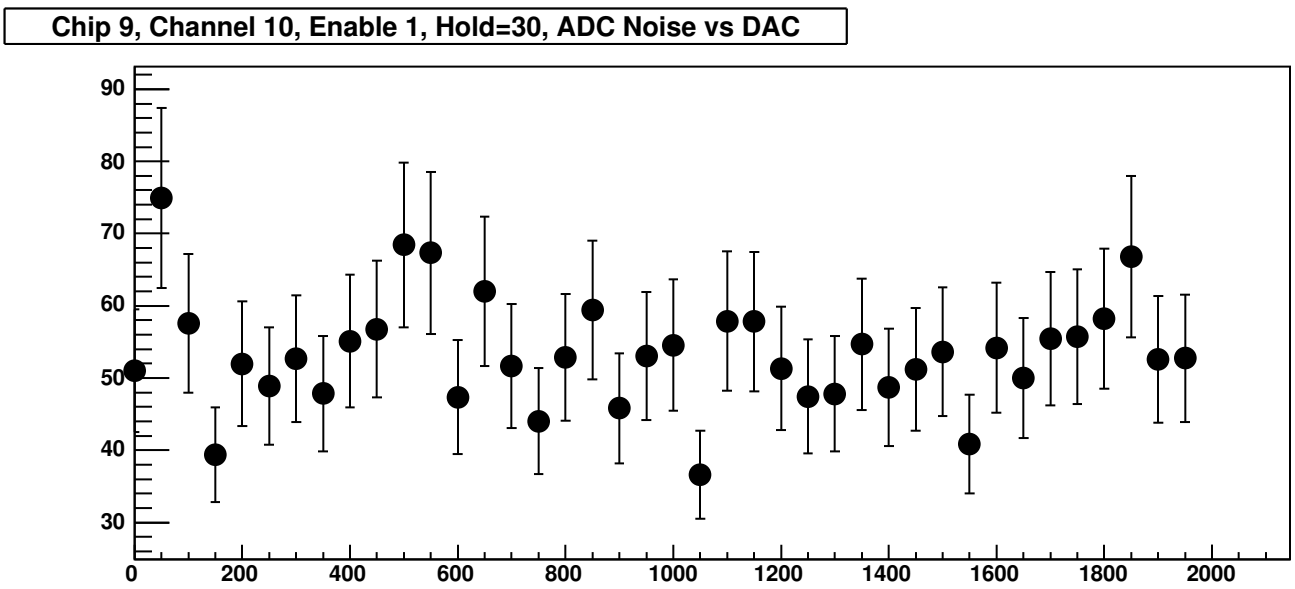
Chip 9, Channel 10, Enable 0!, Hold=30, ADC Noise vs DAC

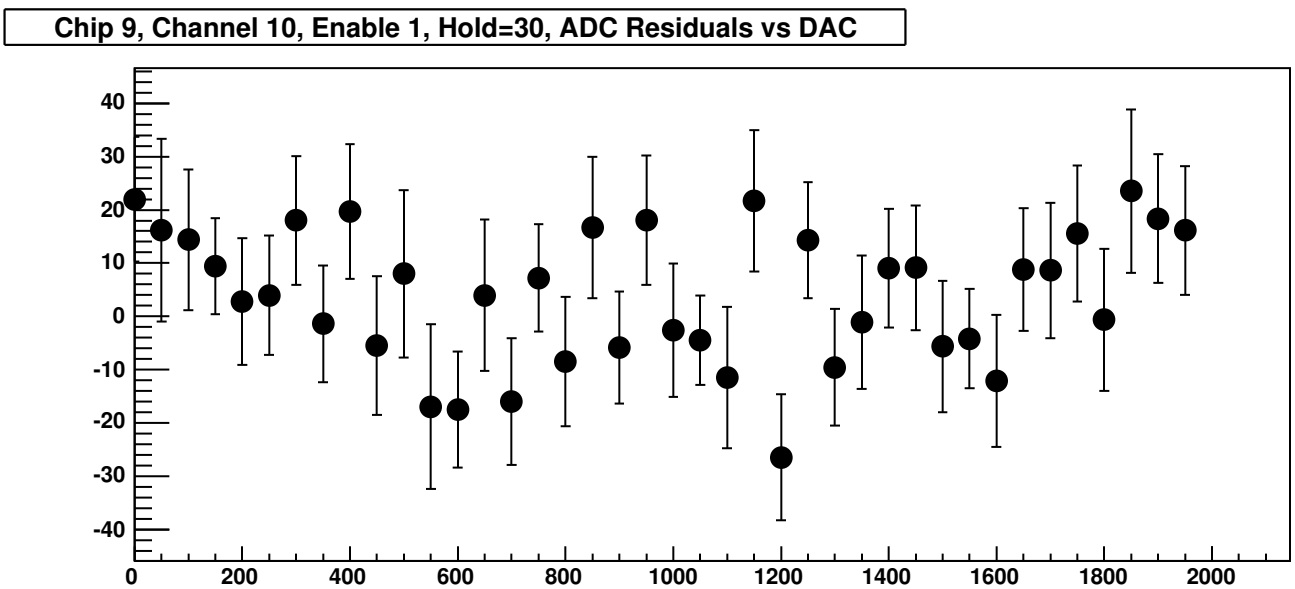


Chip 9, Channel 10, Enable 0!, Hold=30, ADC Residuals vs DAC

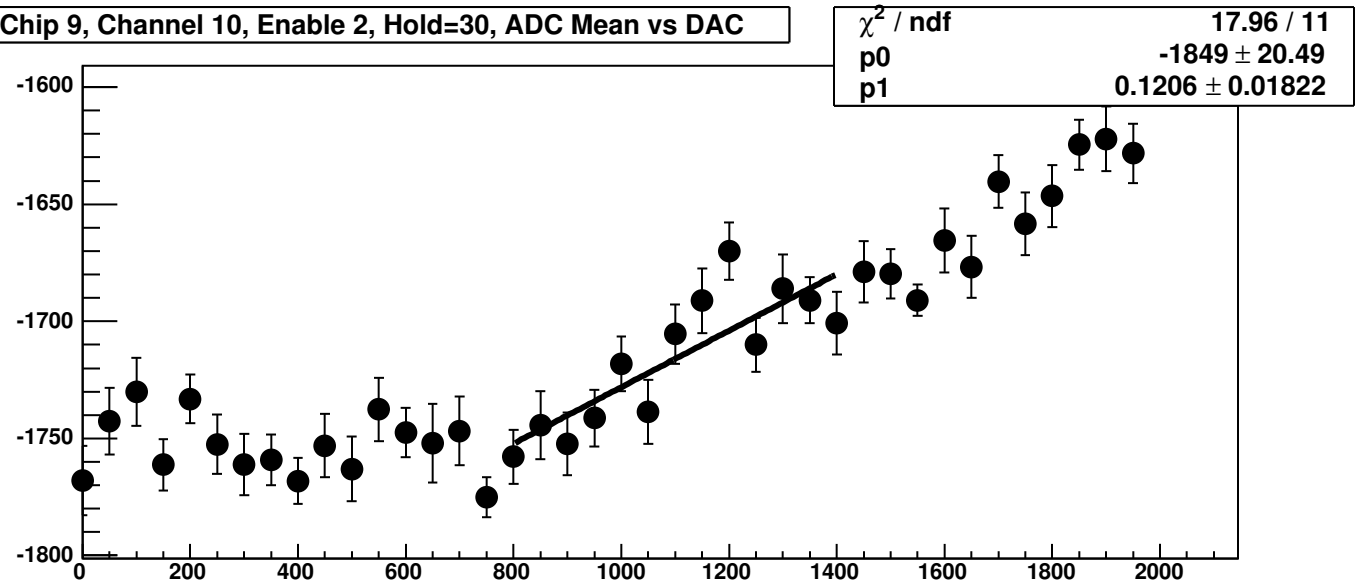



Chip 9, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 16.52 / 11
p0 -1798 ± 19.45
p1 0.02237 ± 0.0174
-1720
-1740
-1760
-1780
-1800
-1820
0
200
400
600
800
1000
1200
1400
1600
1800
2000

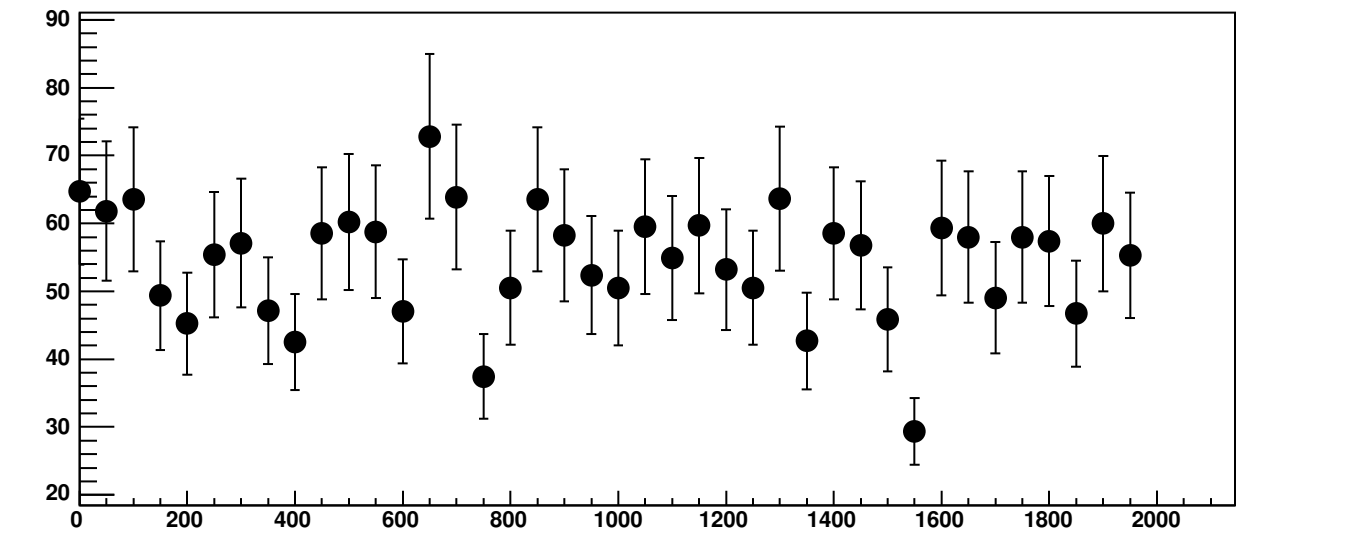

Chip 9, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
40
30
20
10
0
-10
-20
-30
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

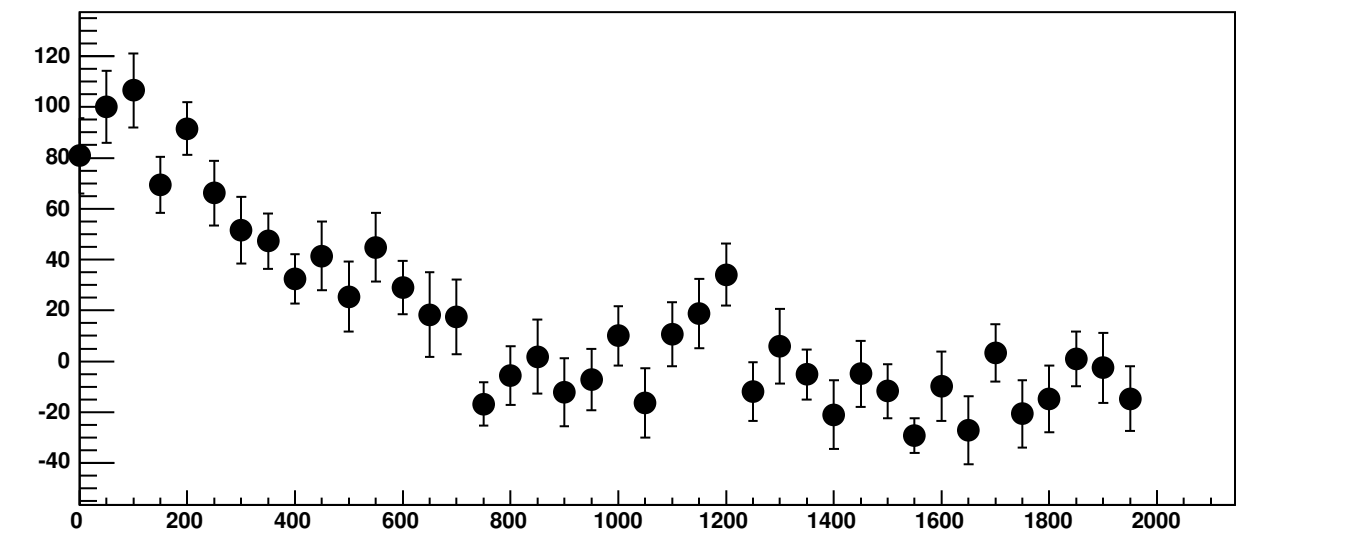
Chip 9, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC

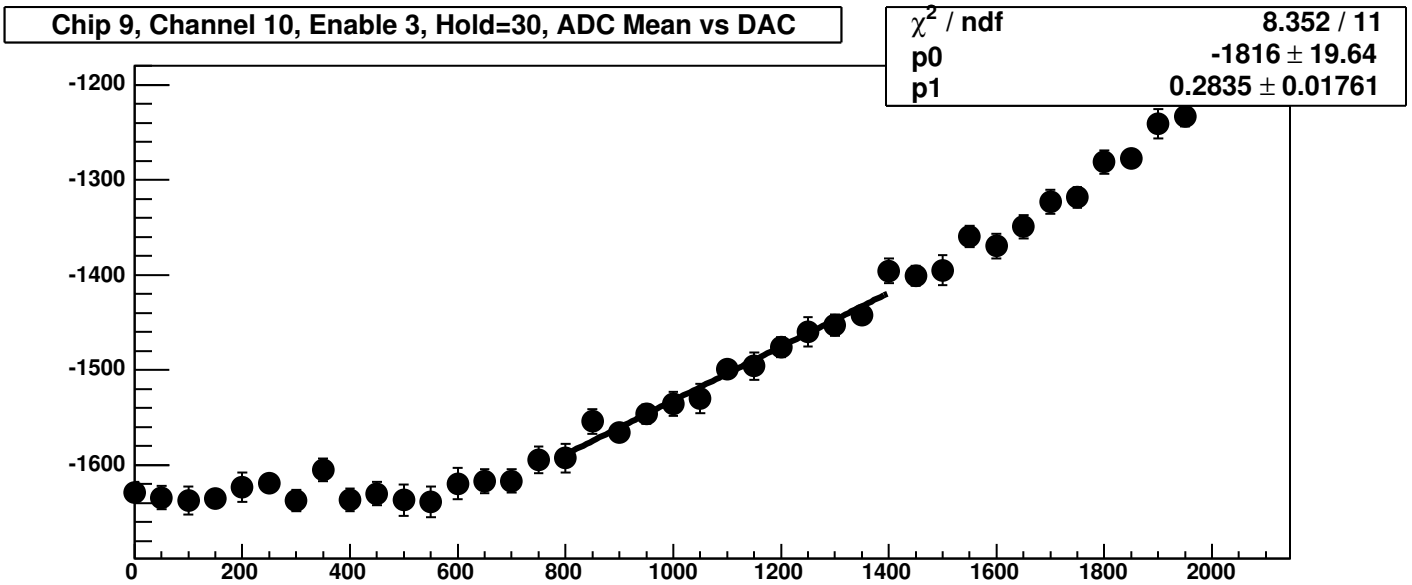


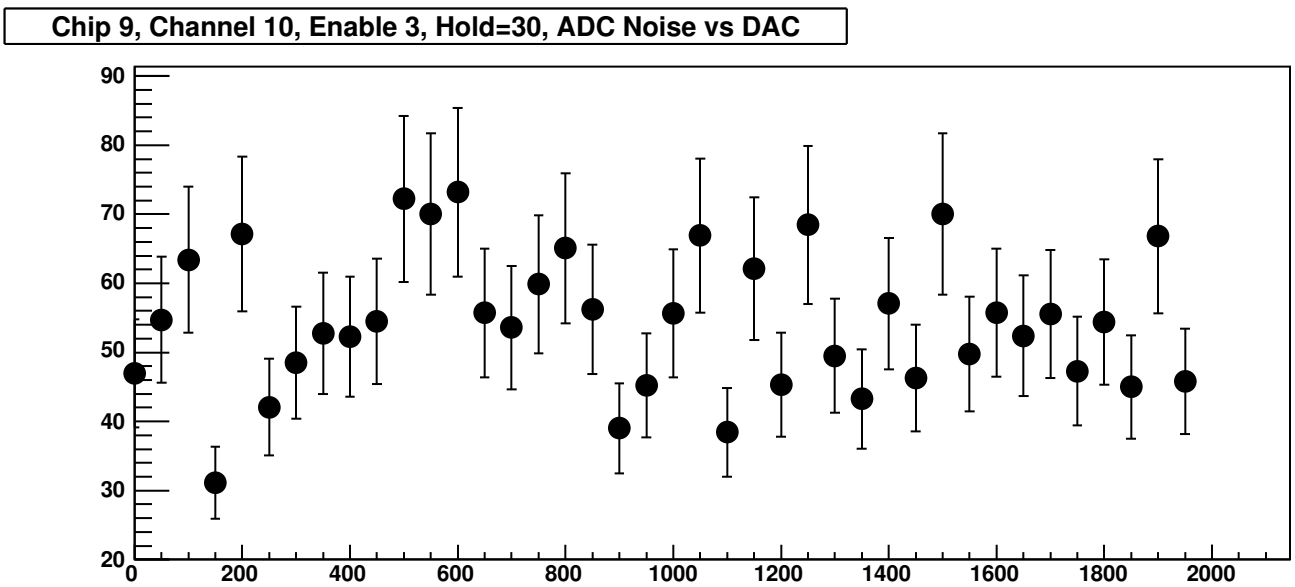
Chip 9, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC

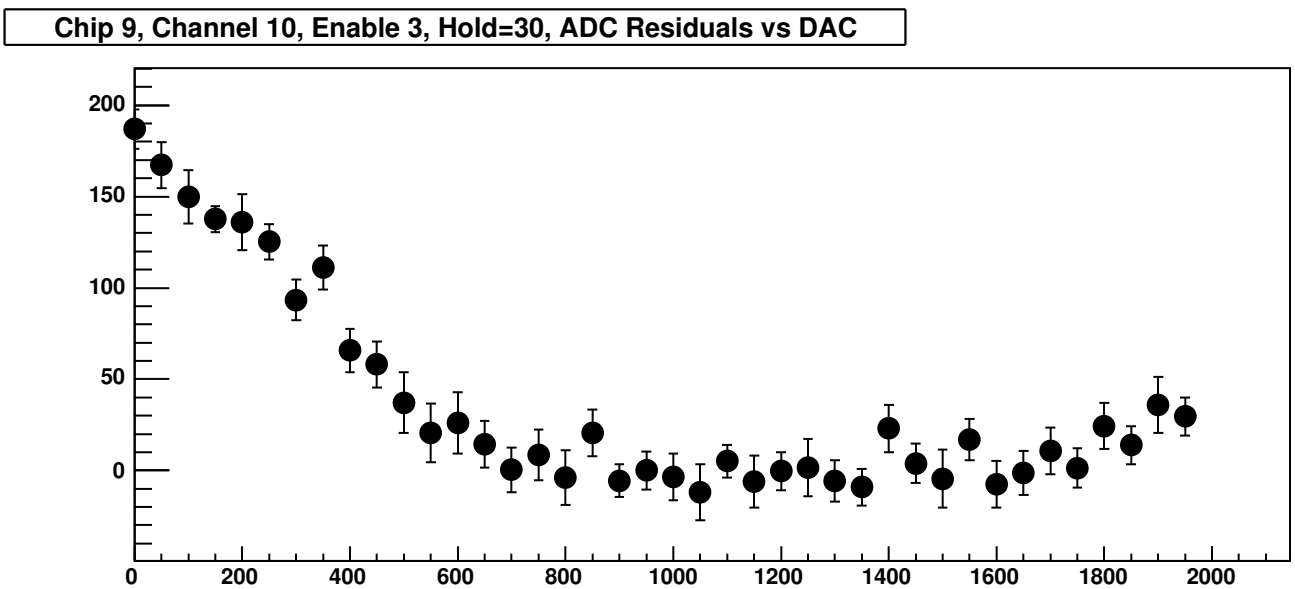


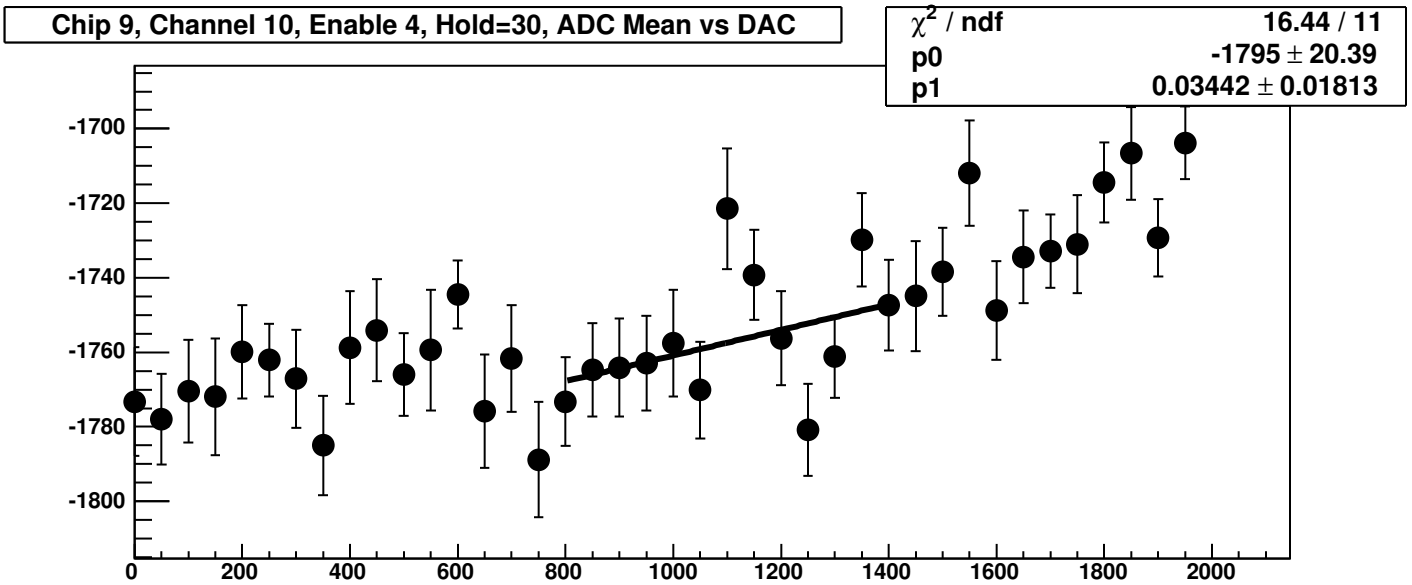
Chip 9, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC

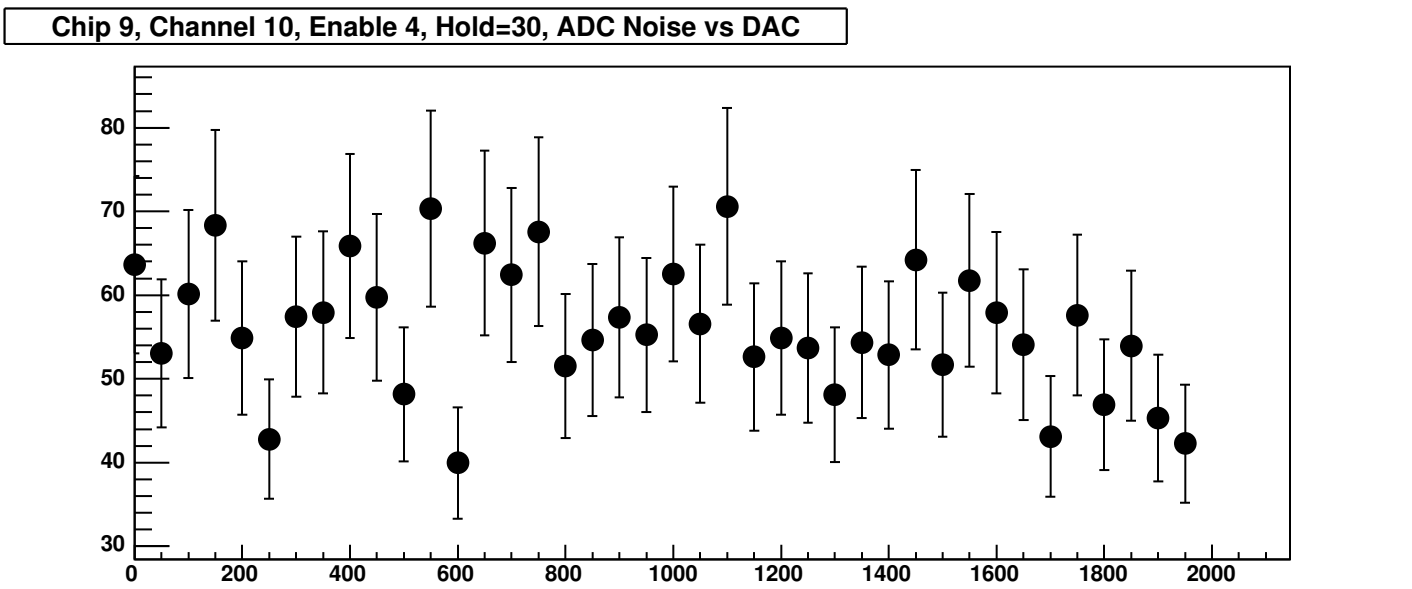


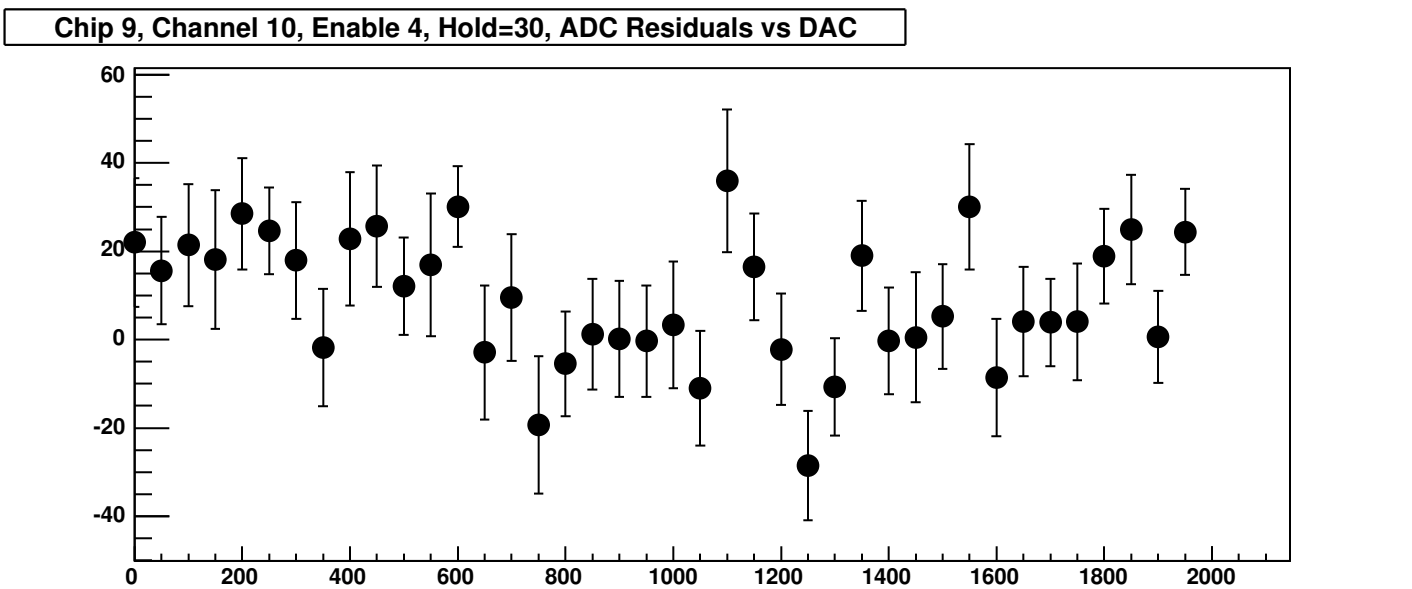

Chip 9, Channel 10, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 8.352 / 11
p0 -1816 ± 19.64
p1 0.2835 ± 0.01761
-1200
-1300
-1400
-1500
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

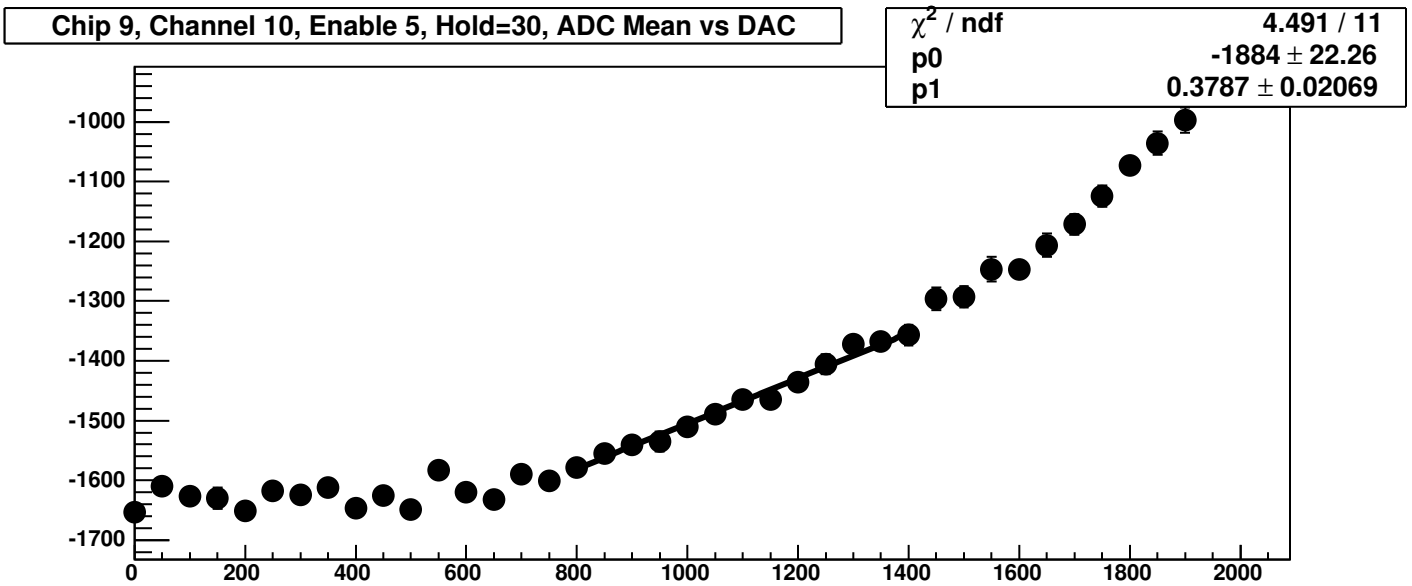

Chip 9, Channel 10, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

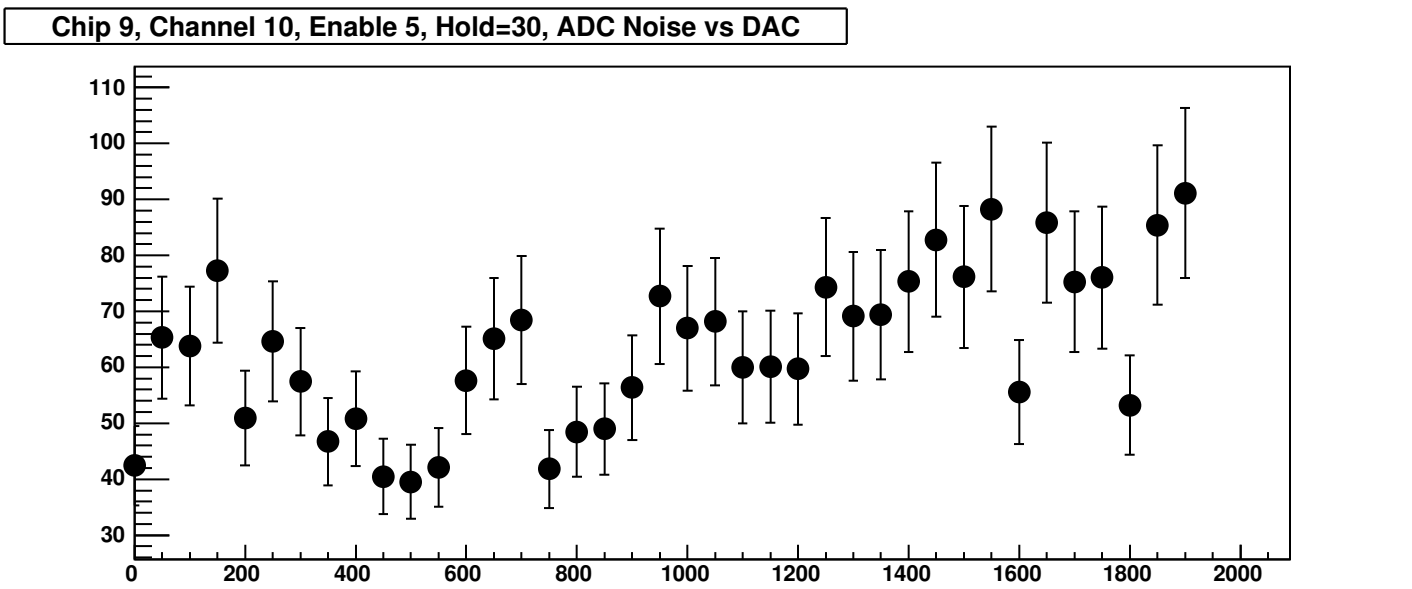

Chip 9, Channel 10, Enable 3, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

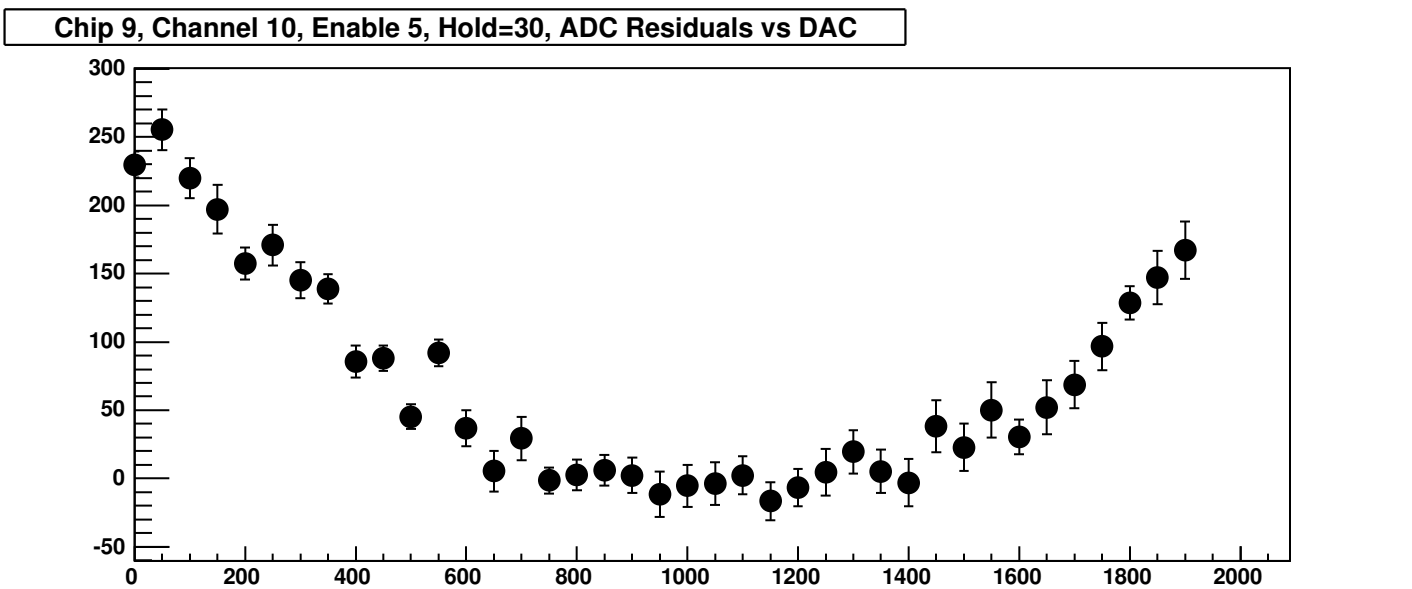

Chip 9, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 16.44 / 11
p0 -1795 ± 20.39
p1 0.03442 ± 0.01813
-1700
-1720
-1740
-1760
-1780
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

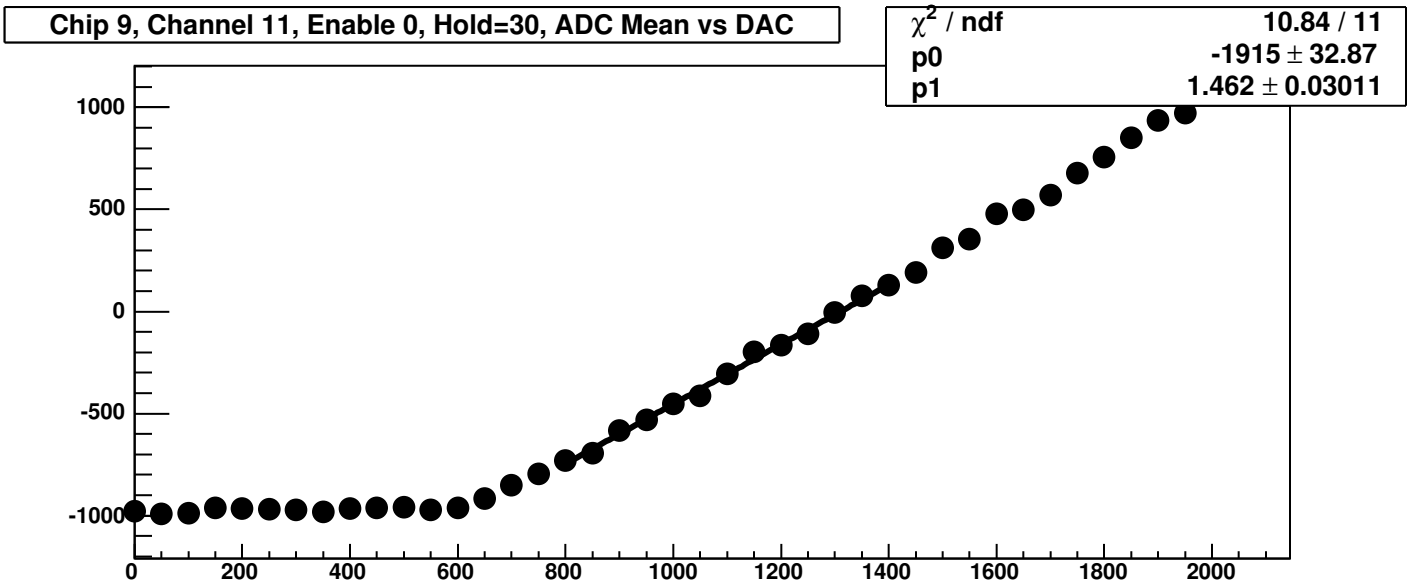

Chip 9, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

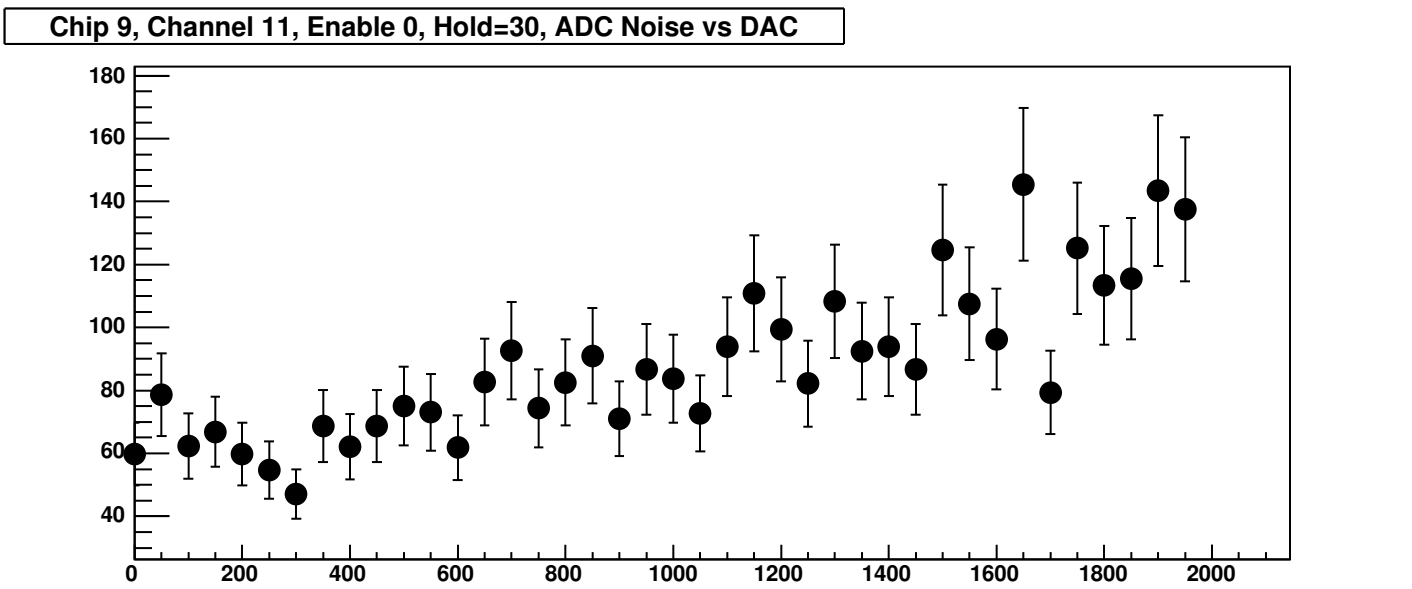

Chip 9, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

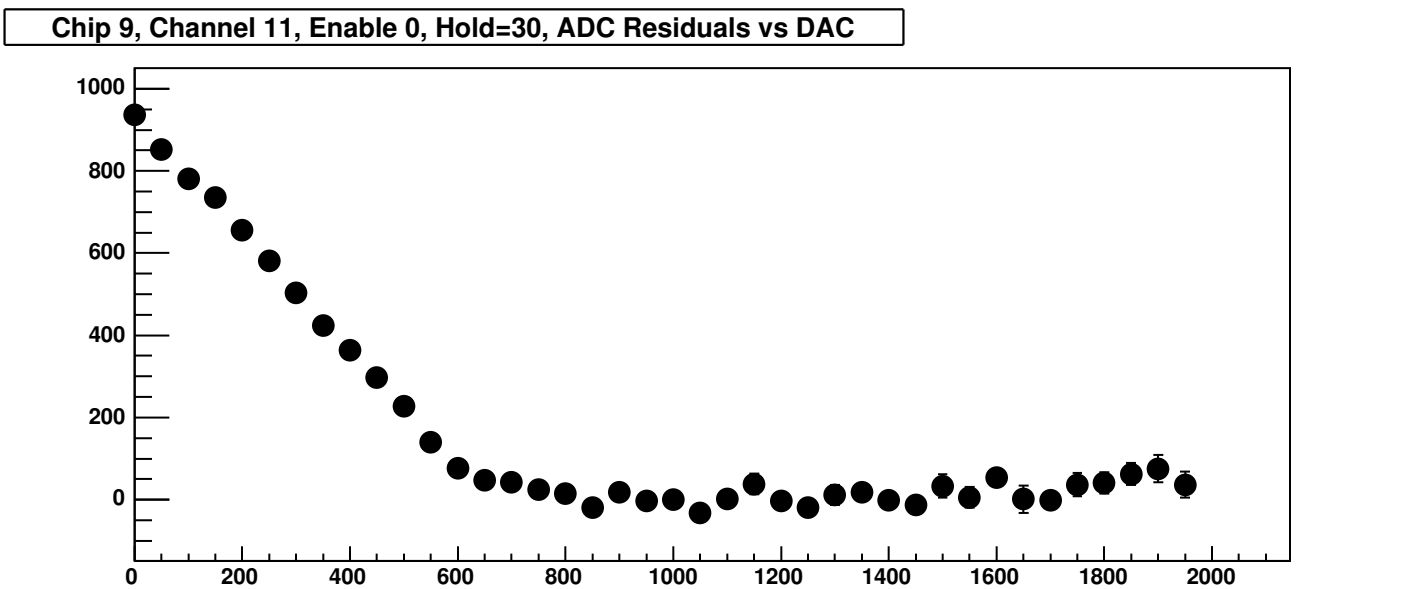

Chip 9, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 4.491 / 11
p0 -1884 ± 22.26
p1 0.3787 ± 0.02069
-1000
-1100
-1200
-1300
-1400
-1500
-1600
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

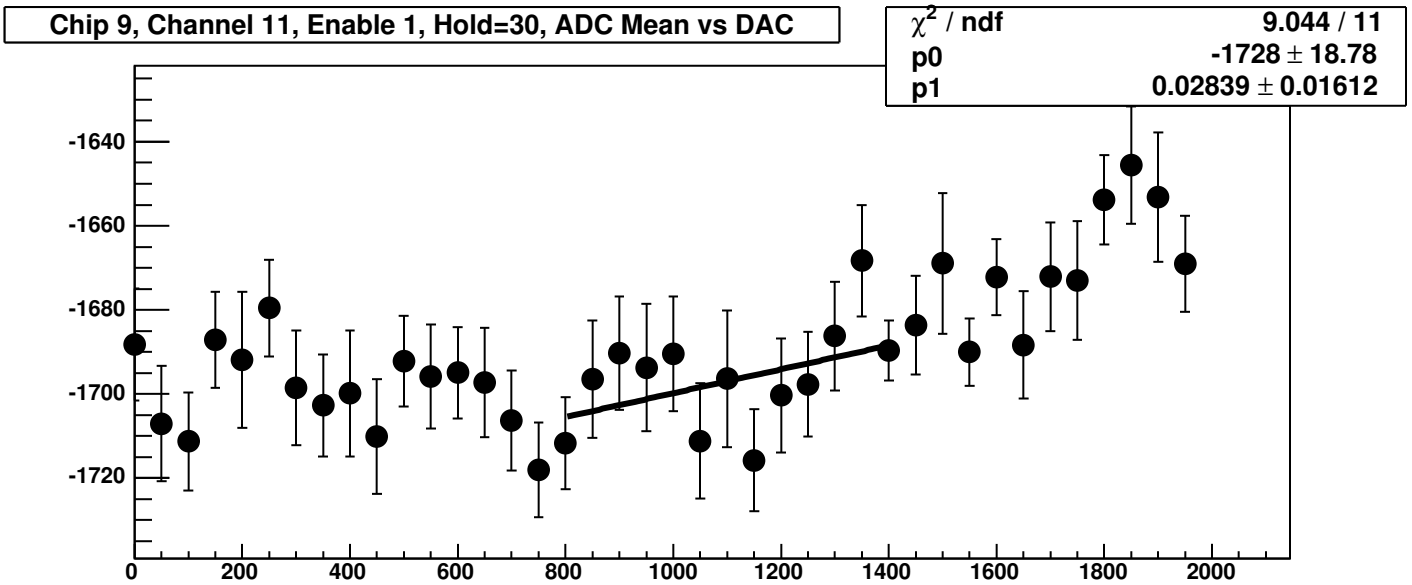

Chip 9, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

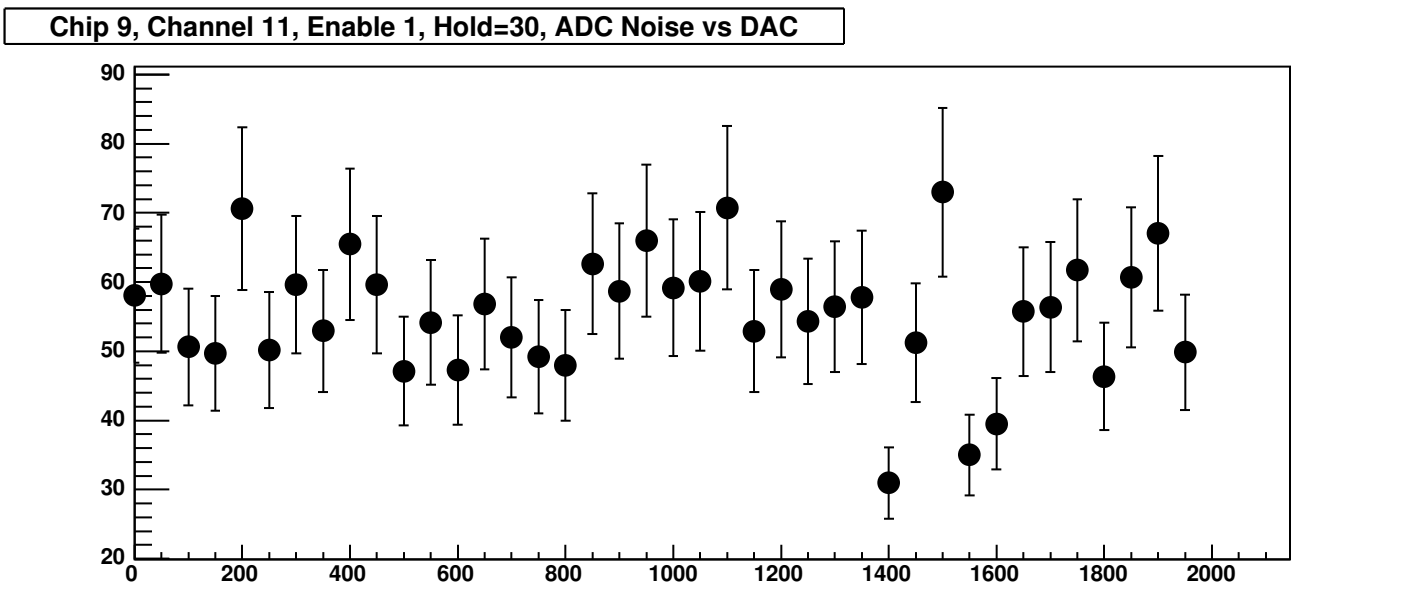

Chip 9, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

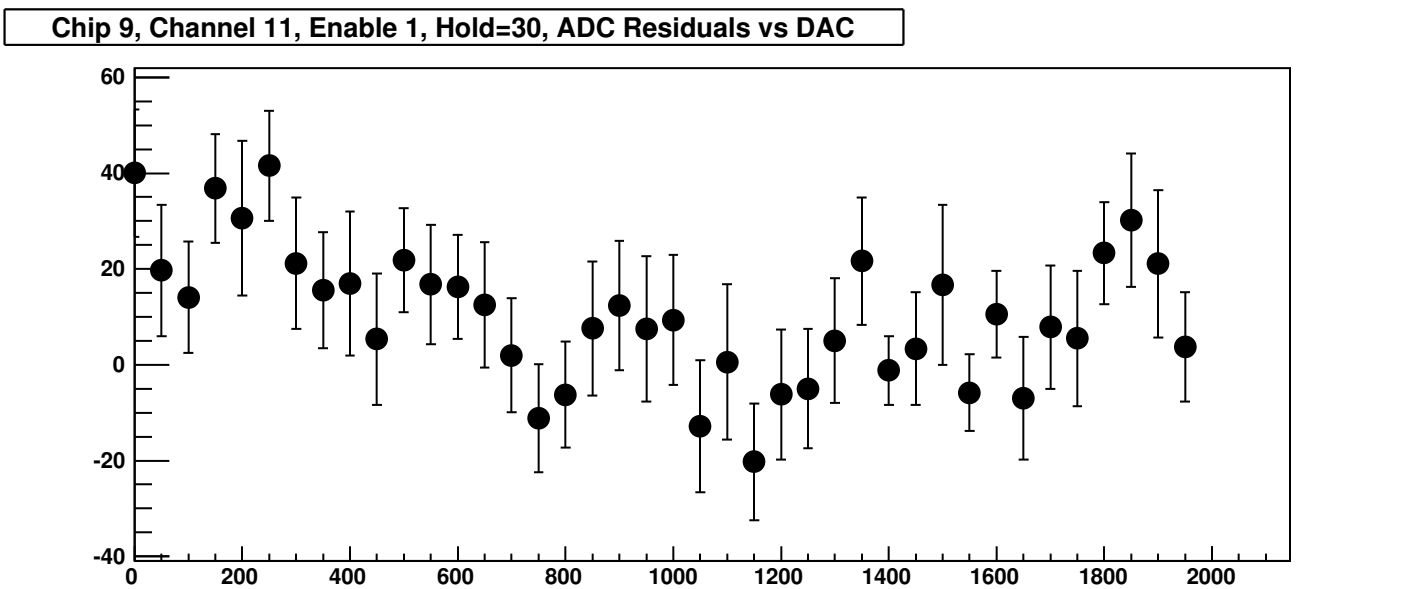

Chip 9, Channel 11, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 10.84 / 11
p0 -1915 ± 32.87
p1 1.462 ± 0.03011
1000
500
0
-500
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 11, Enable 0, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

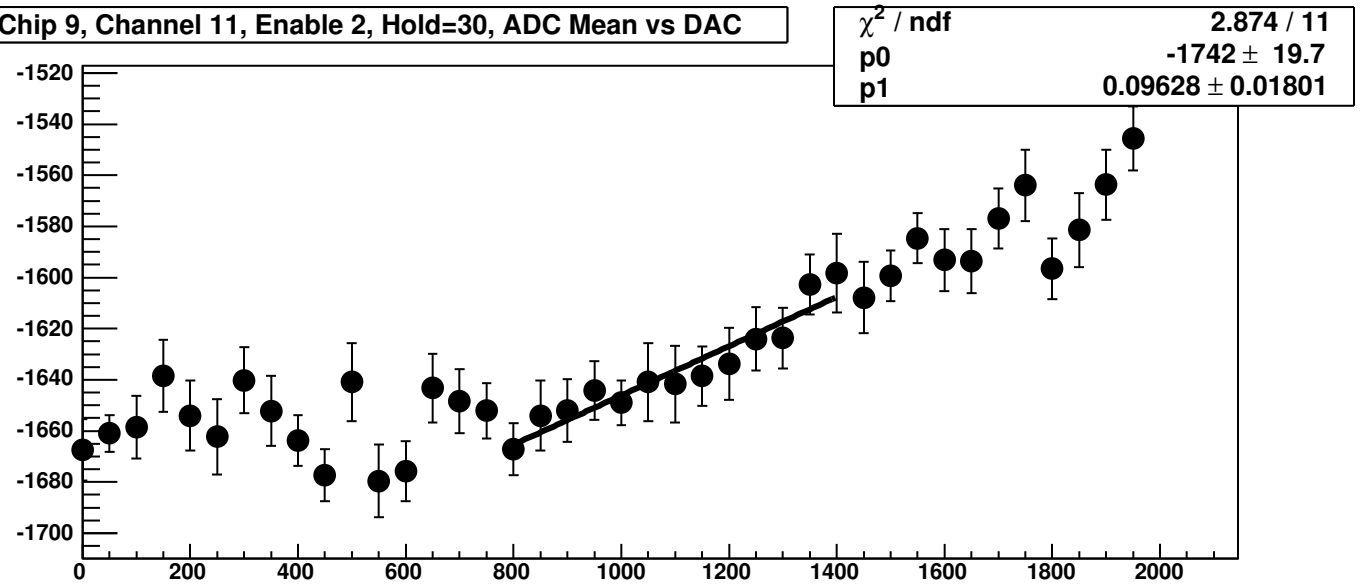

Chip 9, Channel 11, Enable 0, Hold=30, ADC Residuals vs DAC
1000
800
600
400
200
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 9.044 / 11
p0 -1728 ± 18.78
p1 0.02839 ± 0.01612
-1640
-1660
-1680
-1700
-1720
0
200
400
600
800
1000
1200
1400
1600
1800
2000

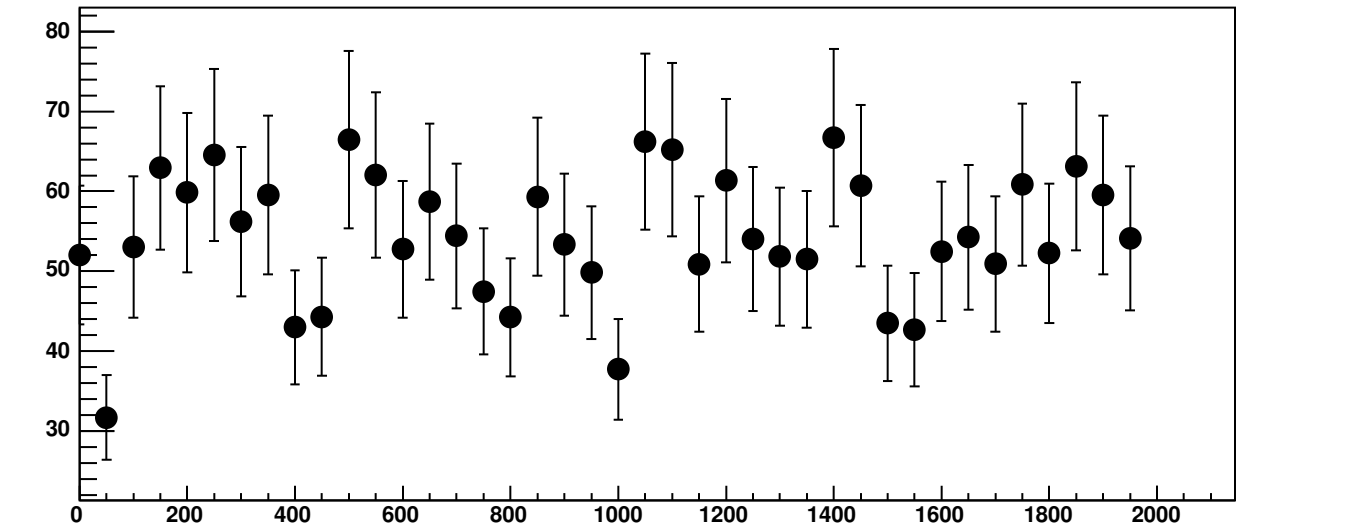

Chip 9, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

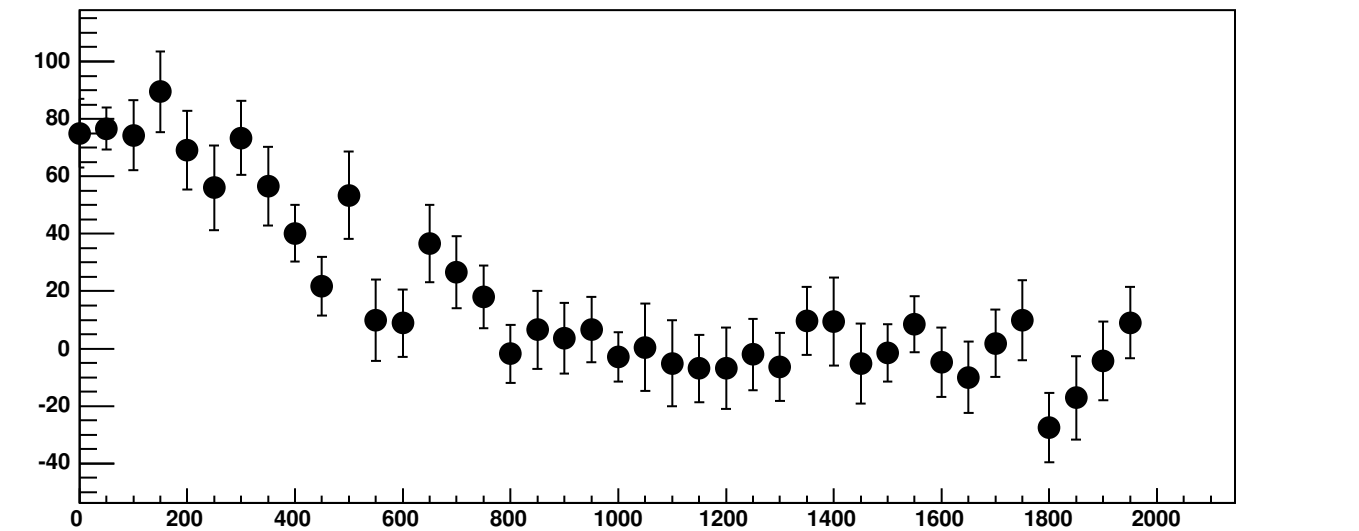
Chip 9, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC

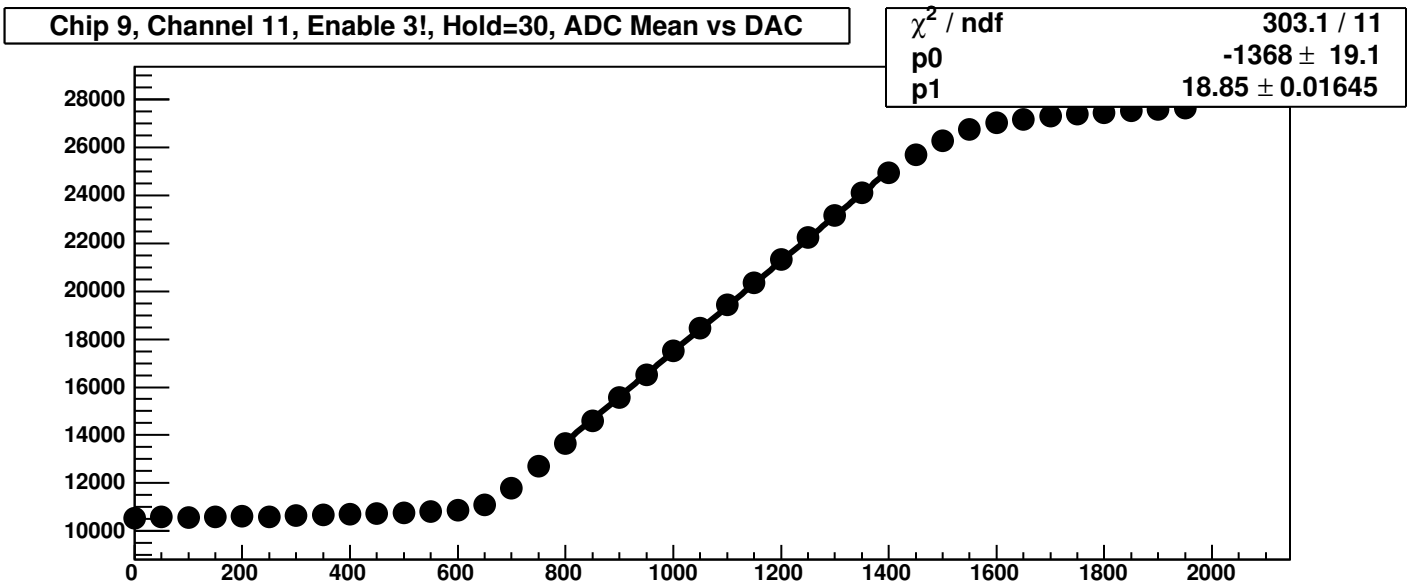


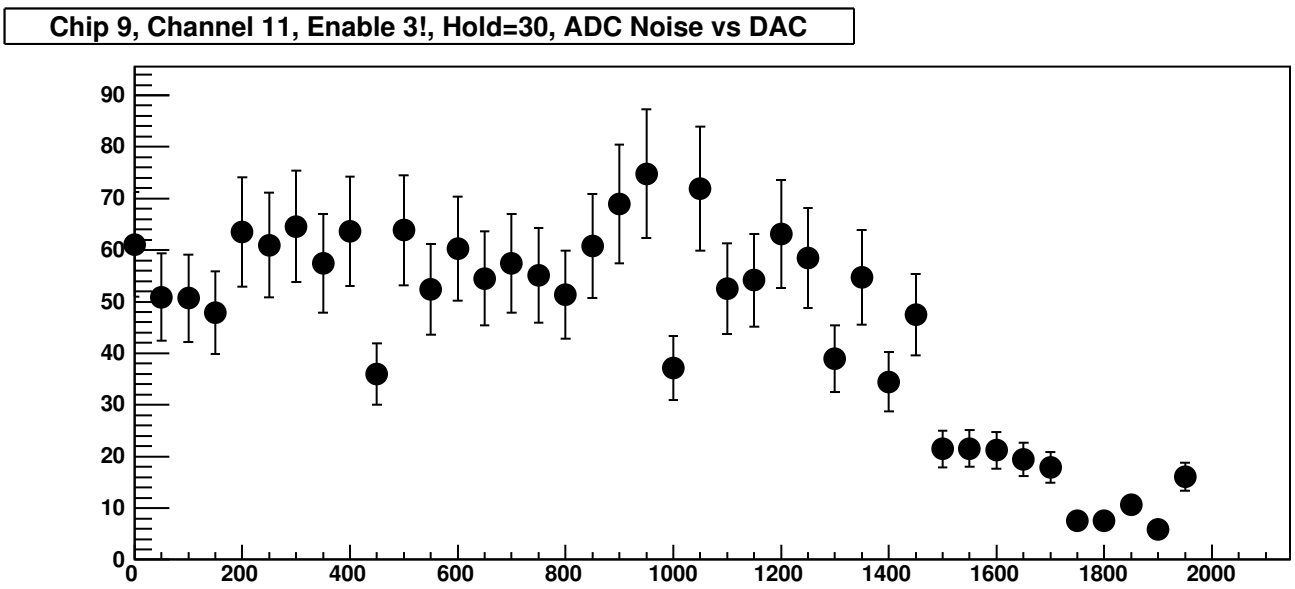
Chip 9, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC

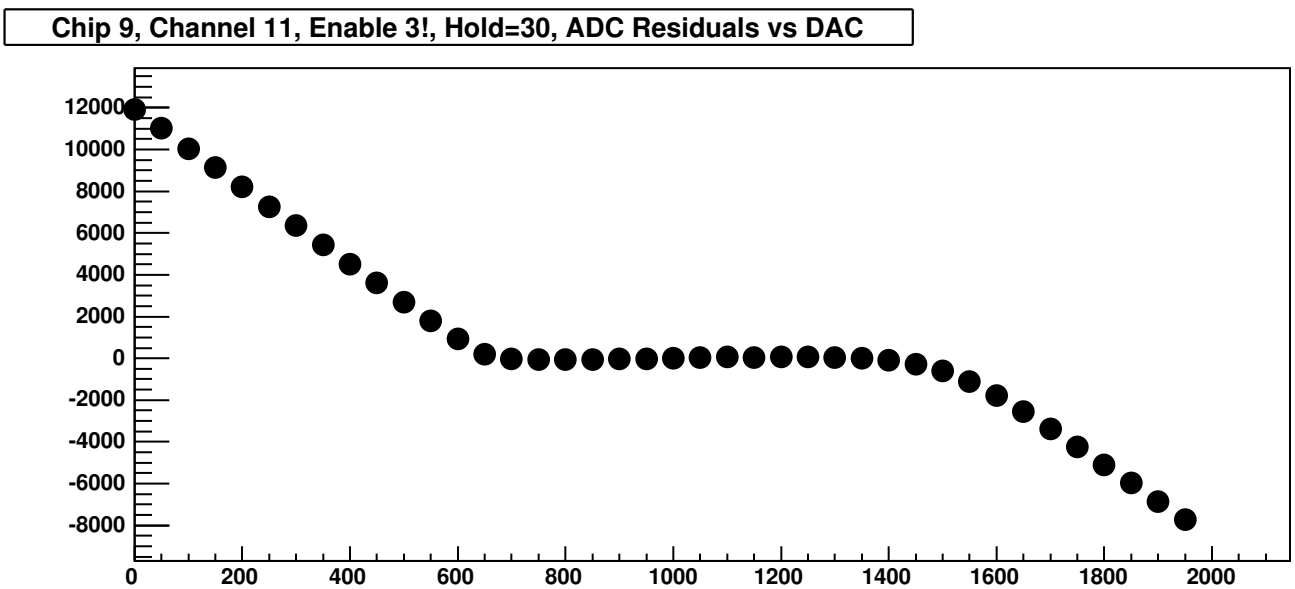


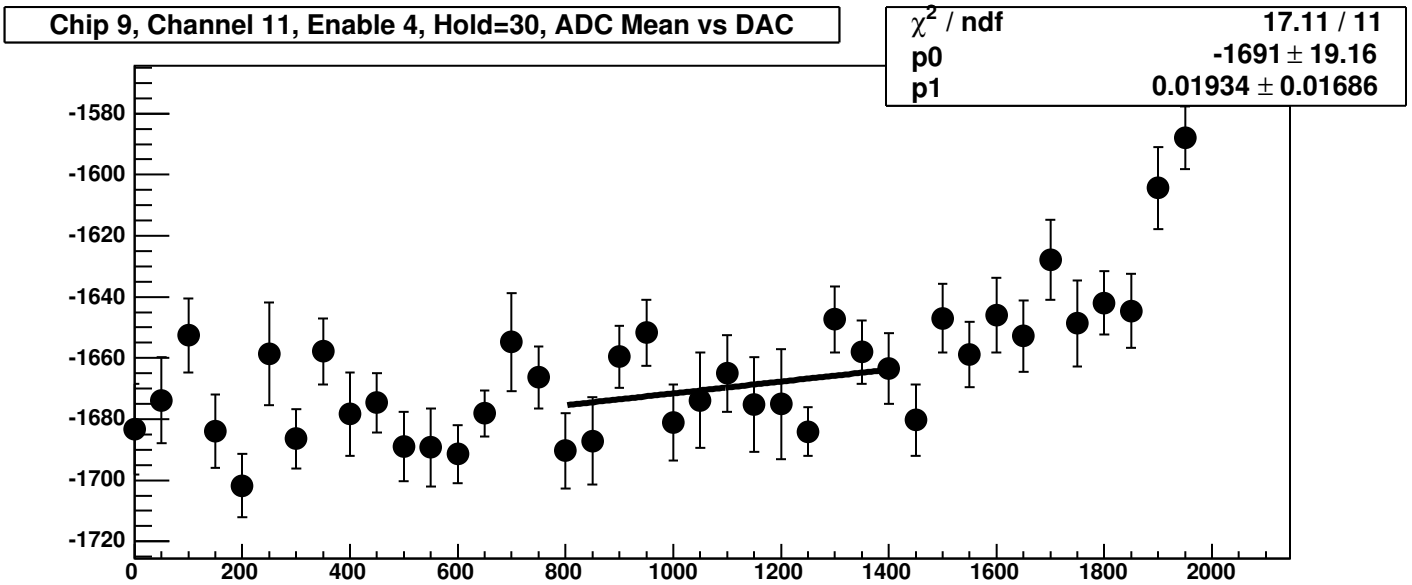
Chip 9, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC

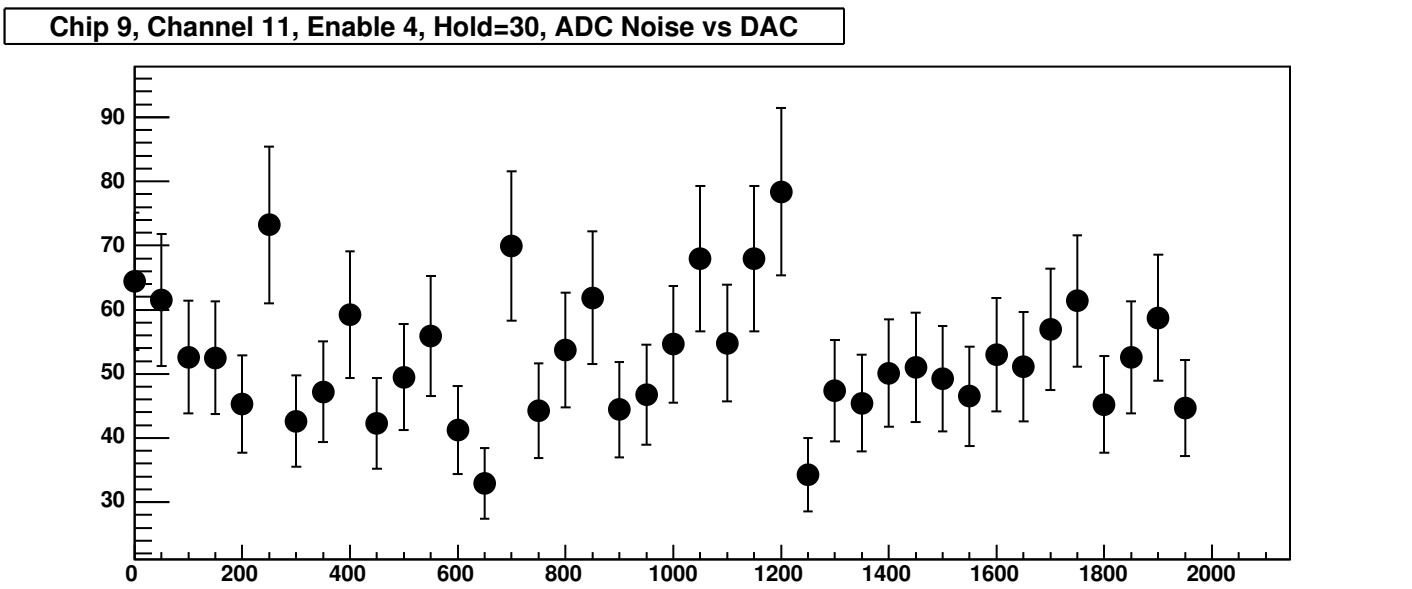


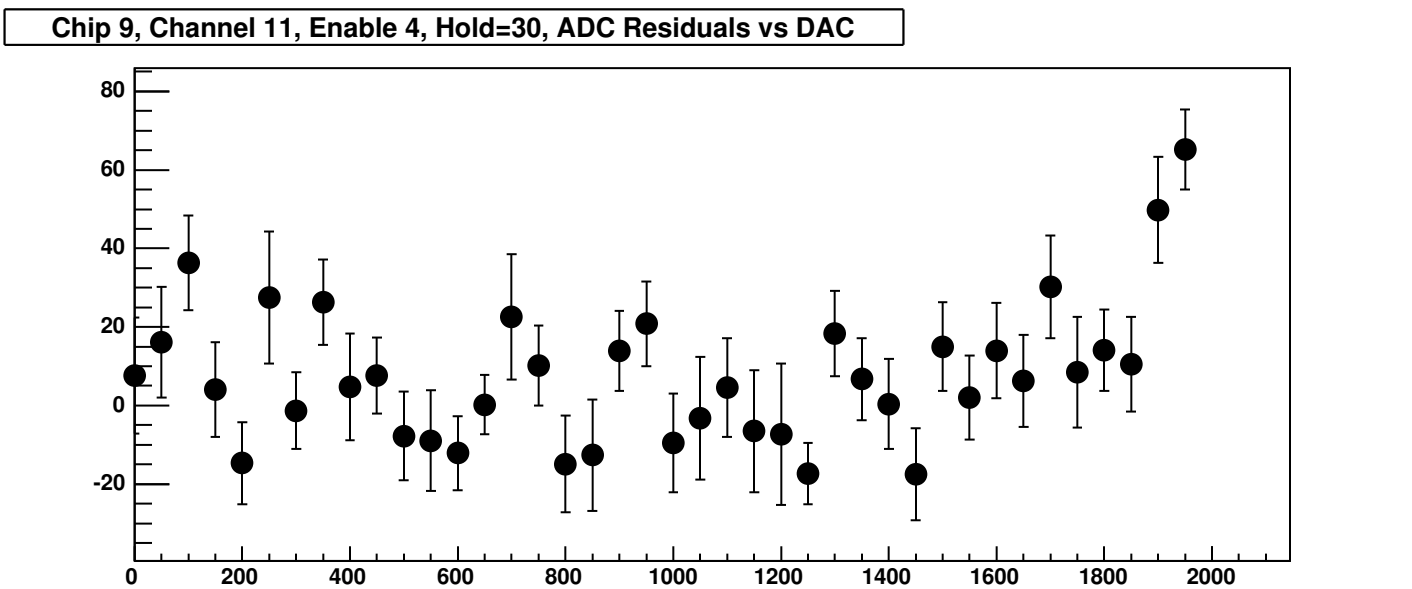

Chip 9, Channel 11, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 303.1 / 11
p0 -1368 ± 19.1
p1 18.85 ± 0.01645
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

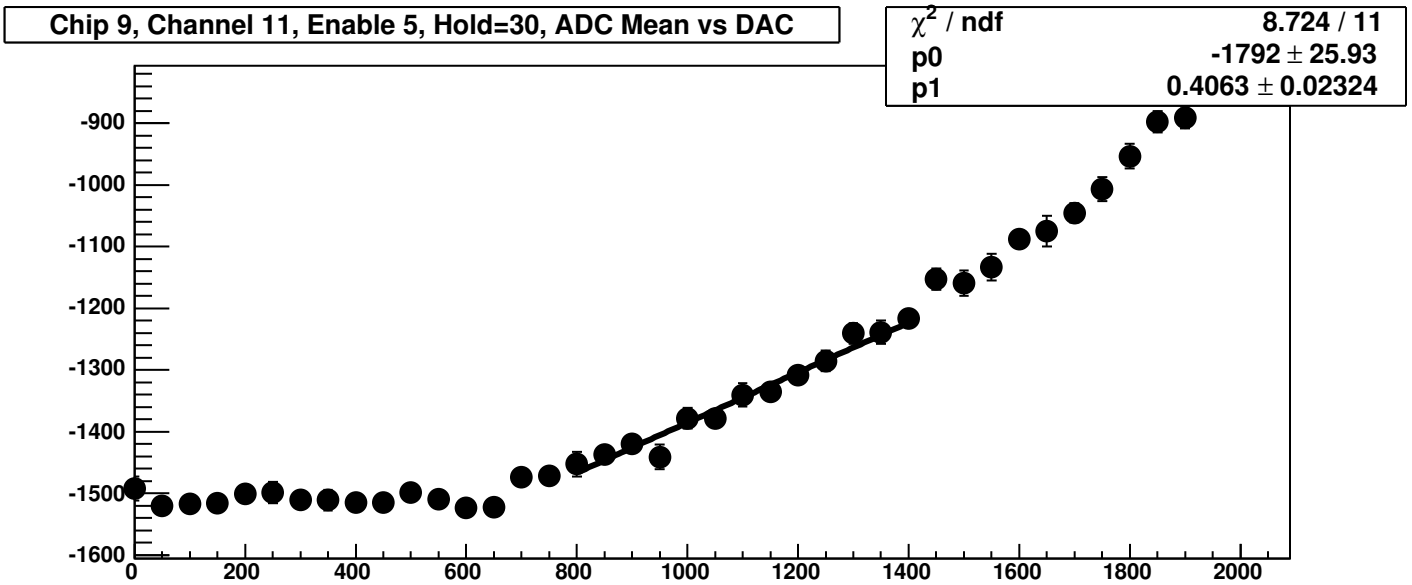

Chip 9, Channel 11, Enable 3!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

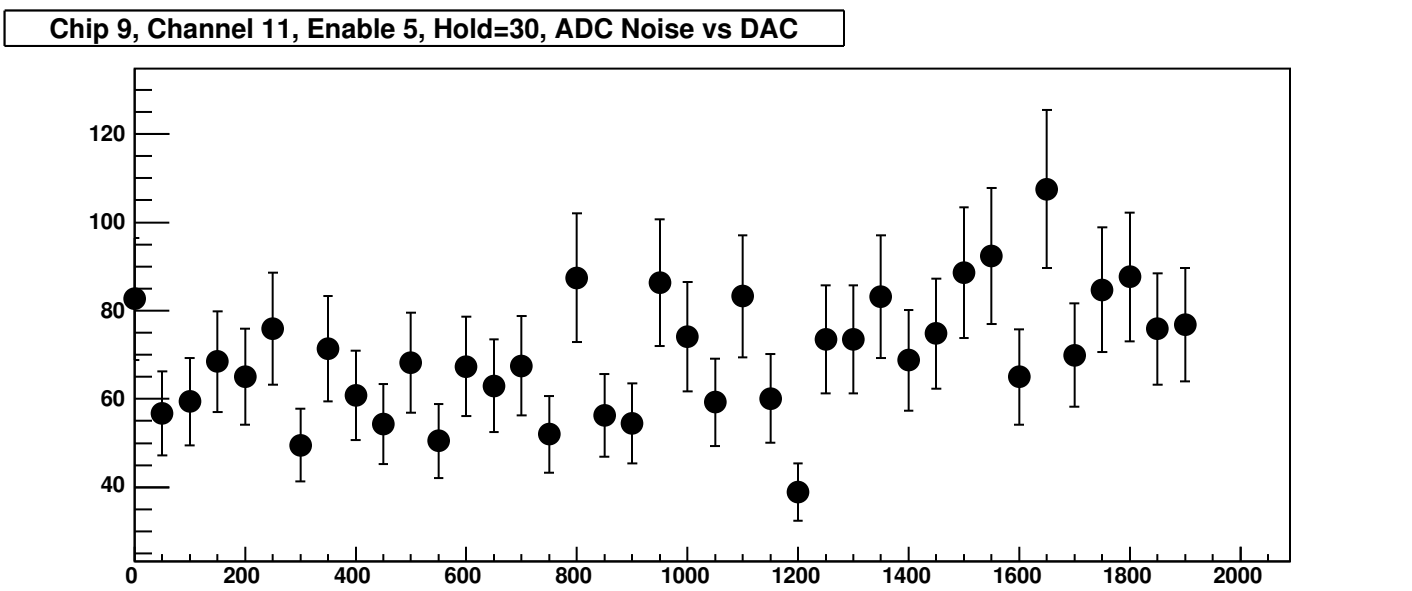

Chip 9, Channel 11, Enable 3!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

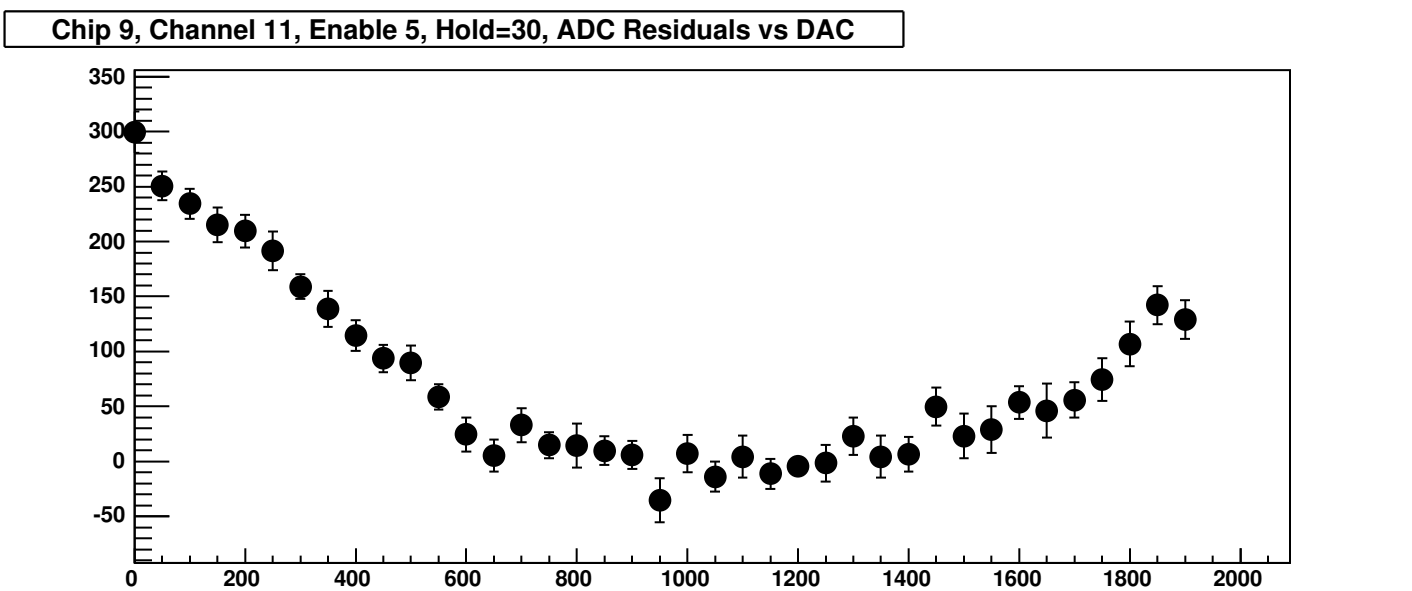

Chip 9, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 17.11 / 11
p0 -1691 ± 19.16
p1 0.01934 ± 0.01686
-1580
-1600
-1620
-1640
-1660
-1680
-1700
-1720
0
200
400
600
800
1000
1200
1400
1600
1800
2000

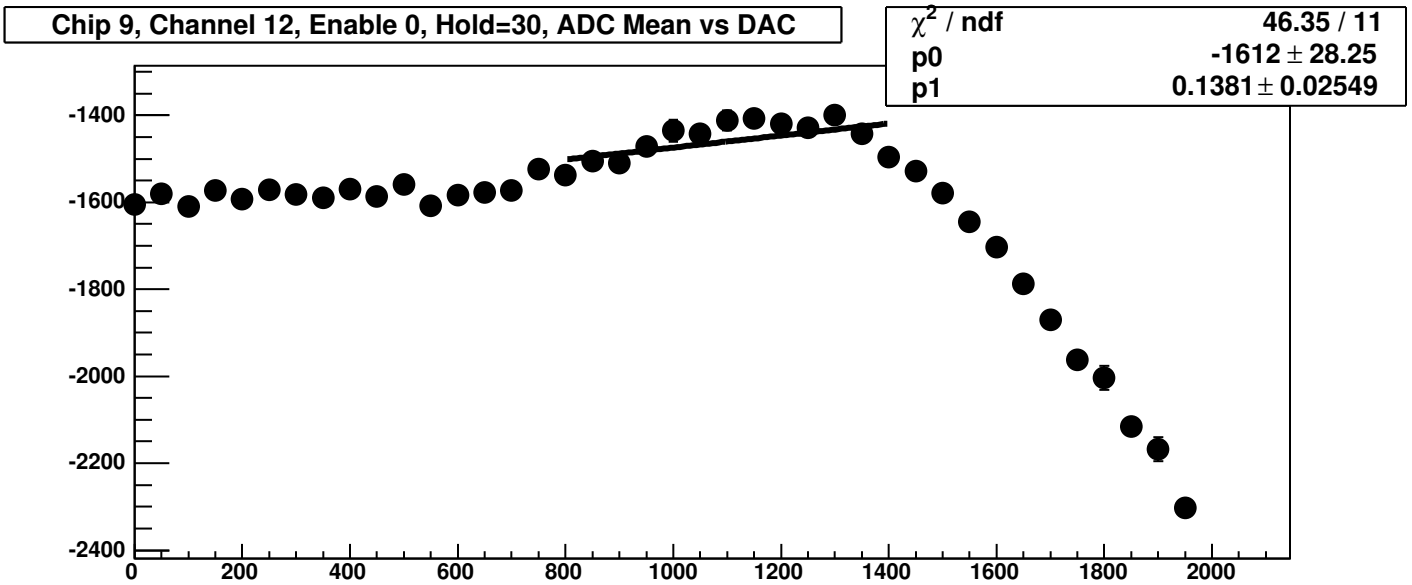

Chip 9, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

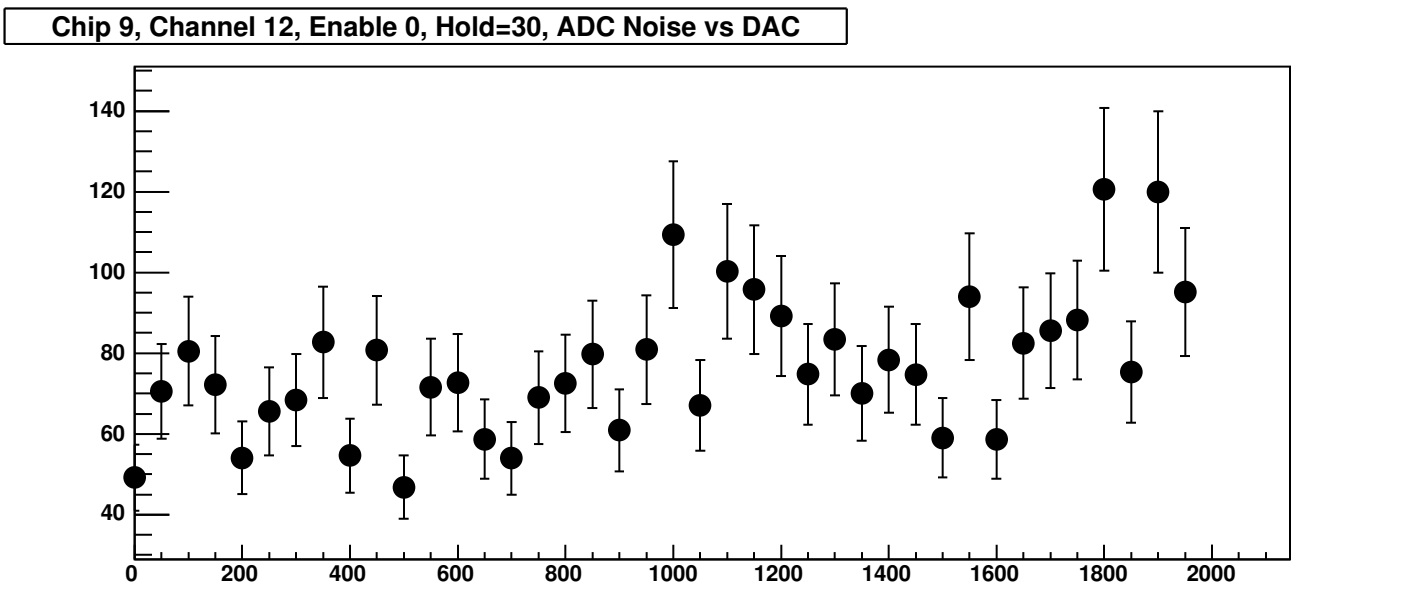

Chip 9, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

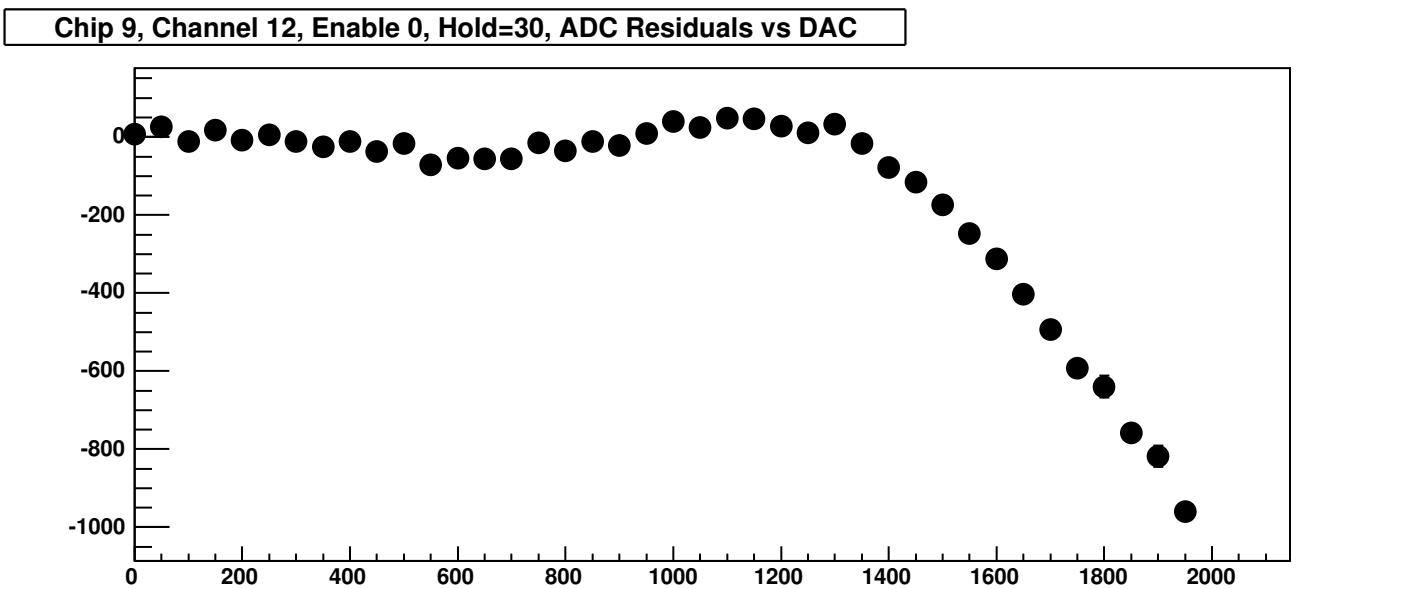

Chip 9, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8.724 / 11
p0 -1792 ± 25.93
p1 0.4063 ± 0.02324
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

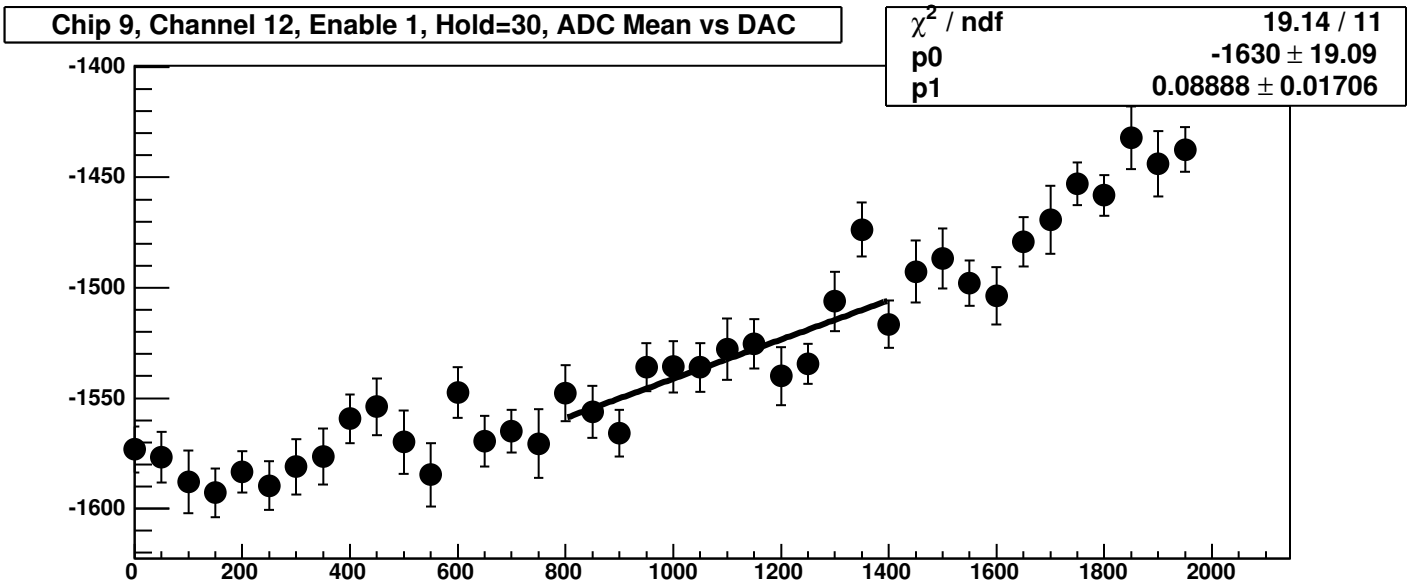

Chip 9, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

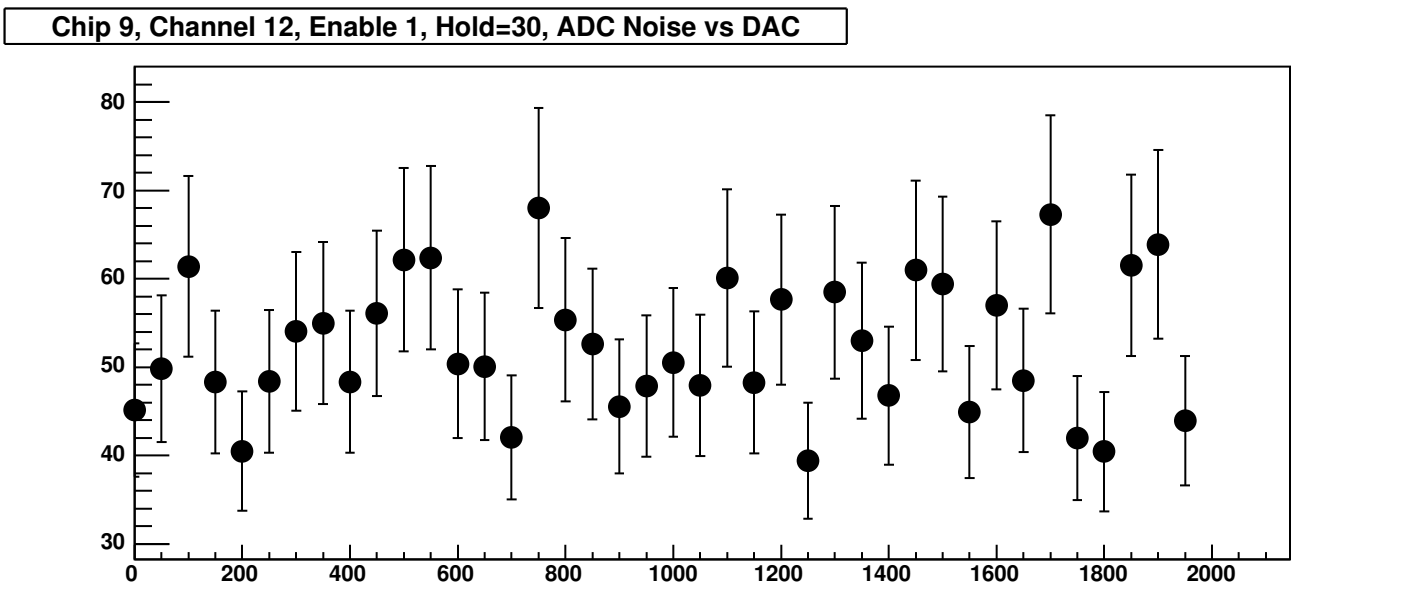

Chip 9, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

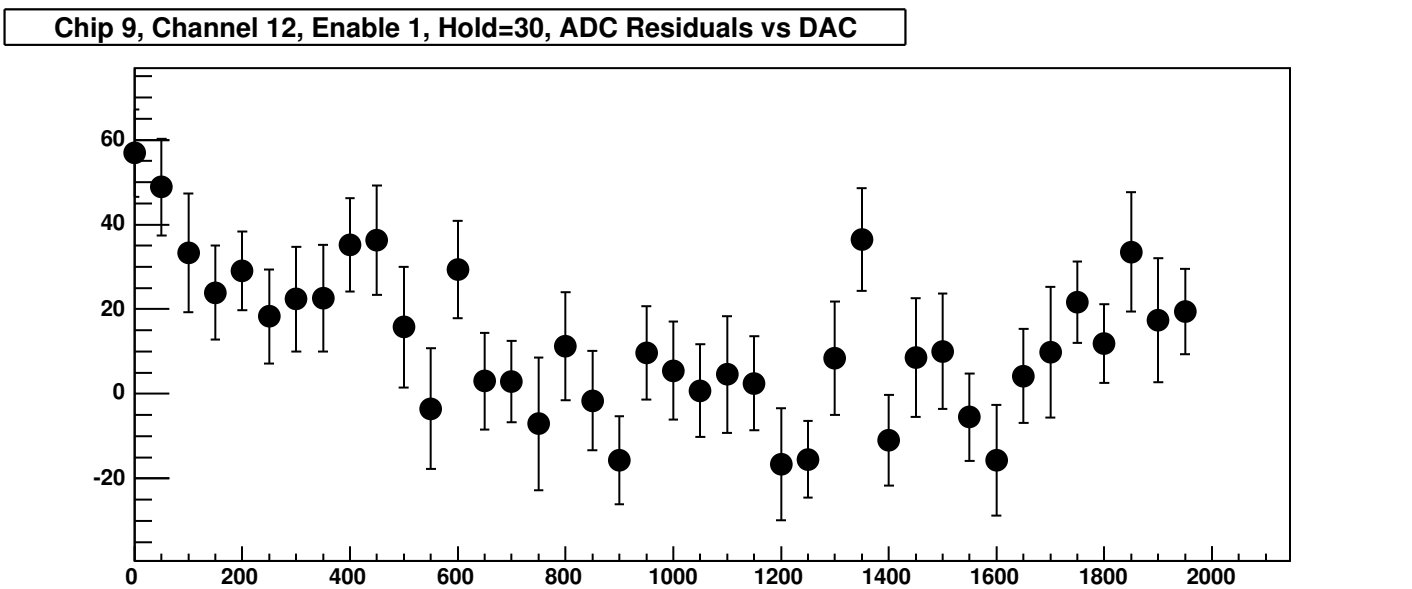

Chip 9, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC
χ^2 / ndf 46.35 / 11
p0 -1612 ± 28.25
p1 0.1381 ± 0.02549
-1400
-1600
-1800
-2000
-2200
-2400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

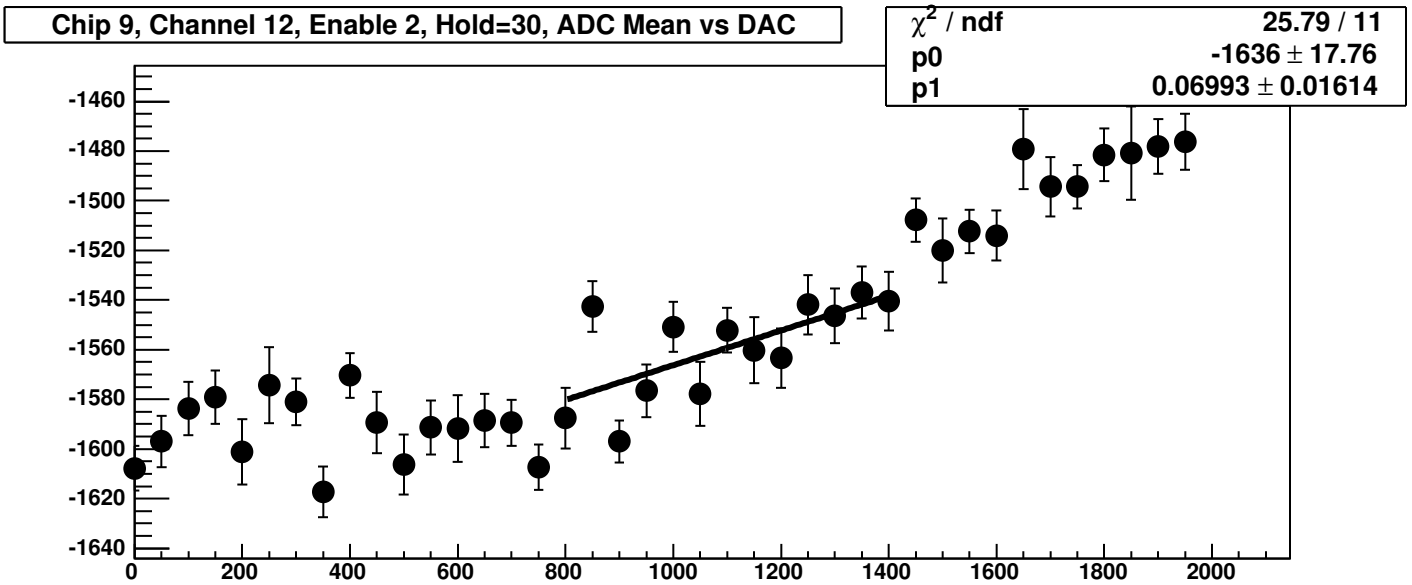

Chip 9, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

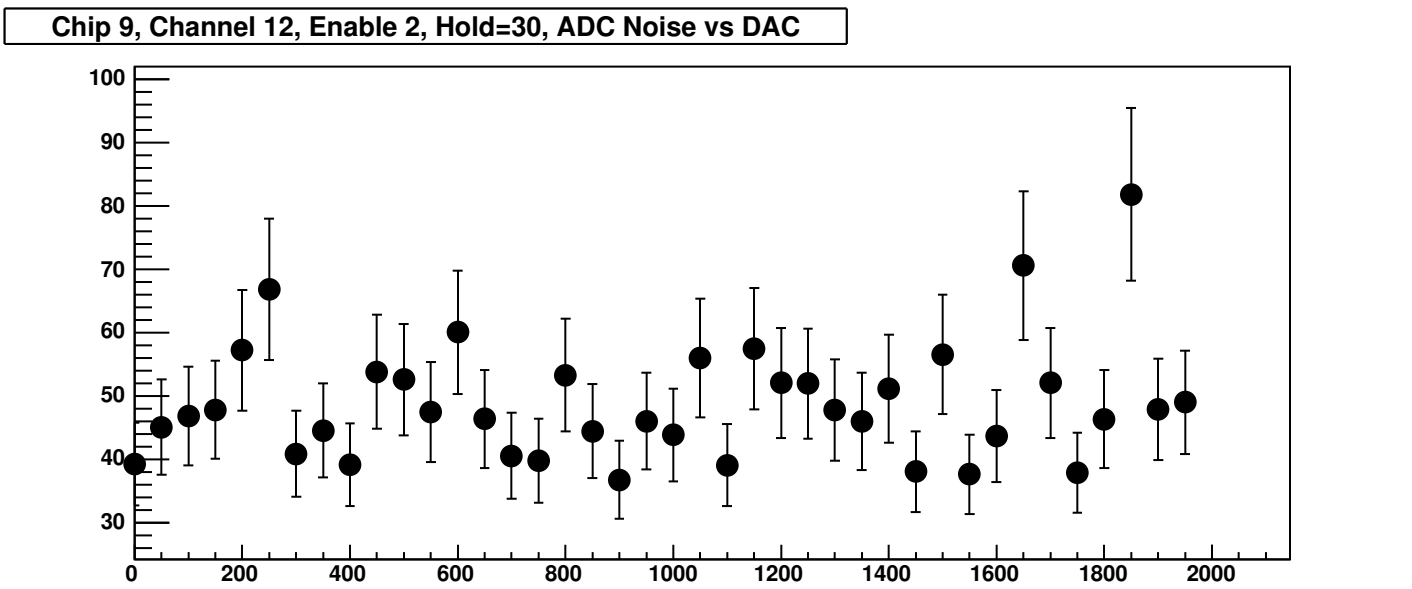

Chip 9, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

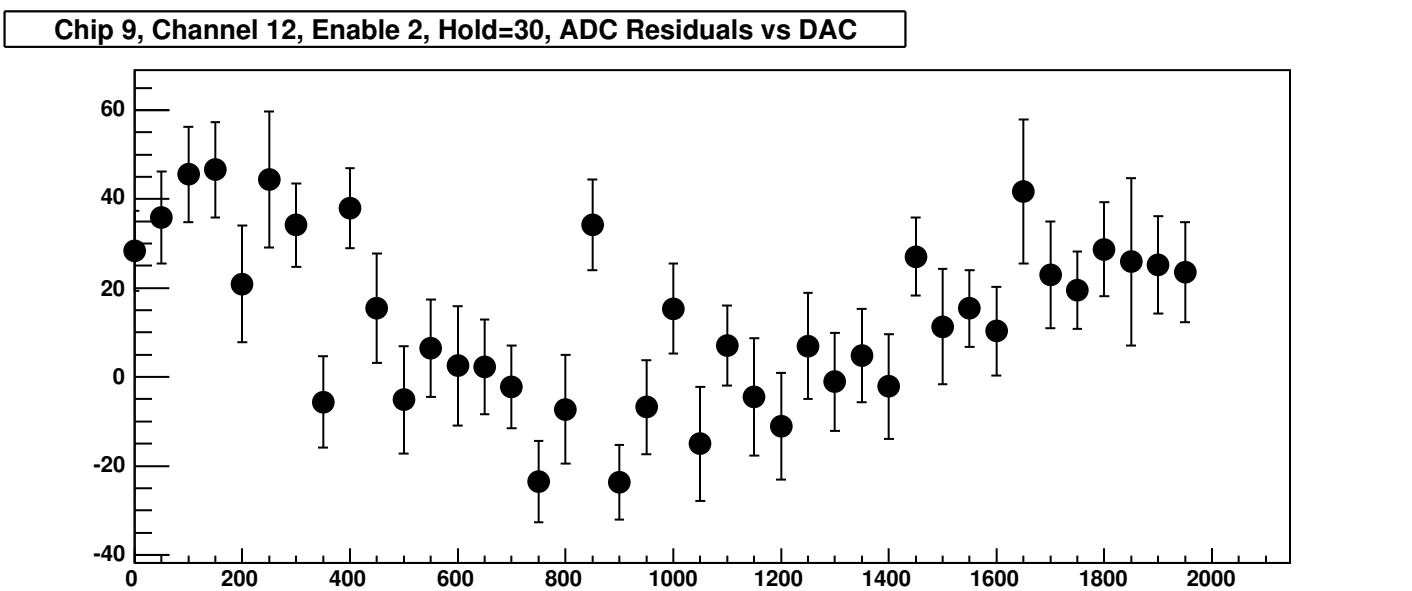

Chip 9, Channel 12, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 19.14 / 11
p0 -1630 ± 19.09
p1 0.08888 ± 0.01706
-1400
-1450
-1500
-1550
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

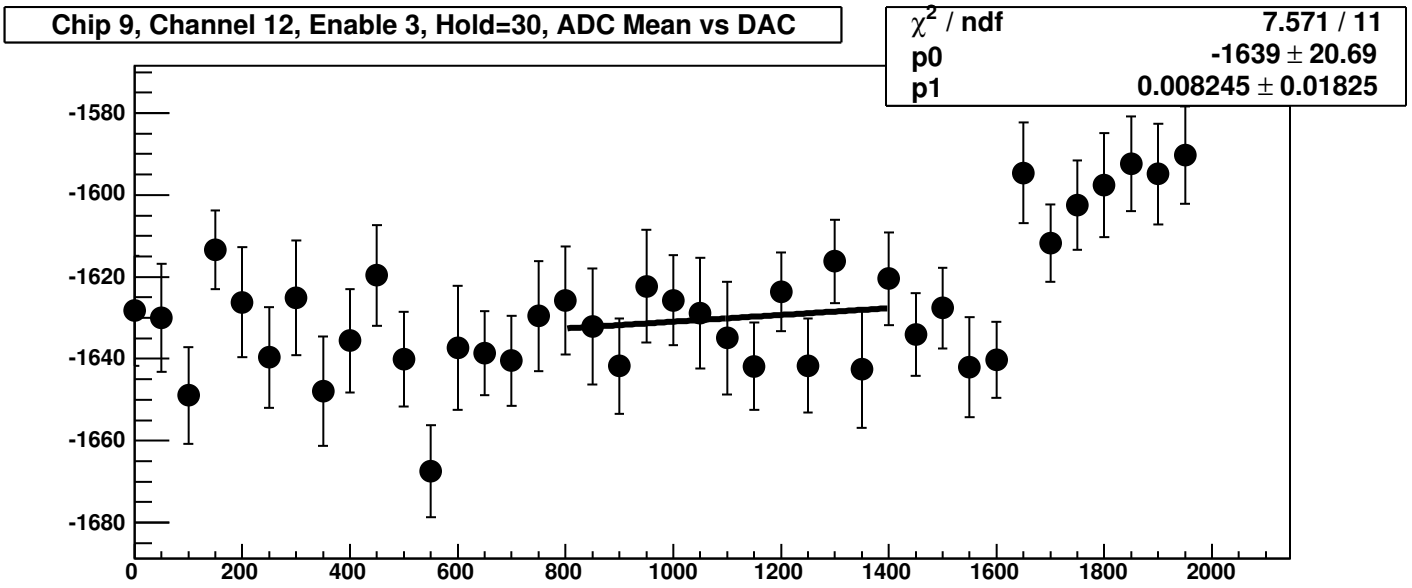

Chip 9, Channel 12, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

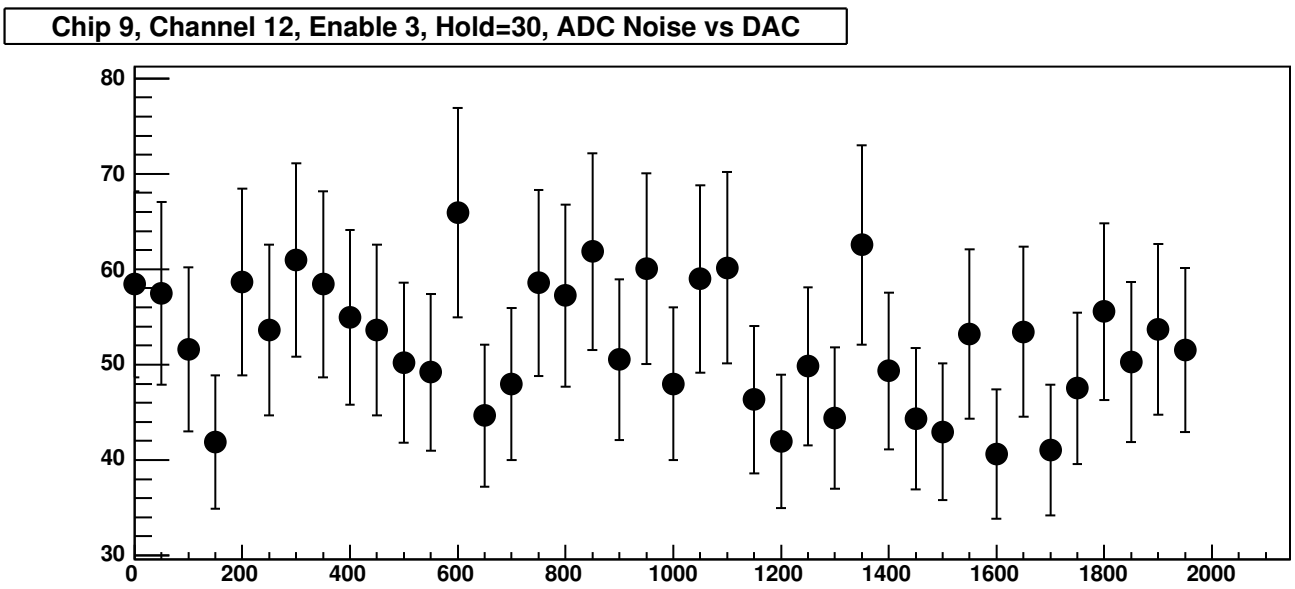

Chip 9, Channel 12, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

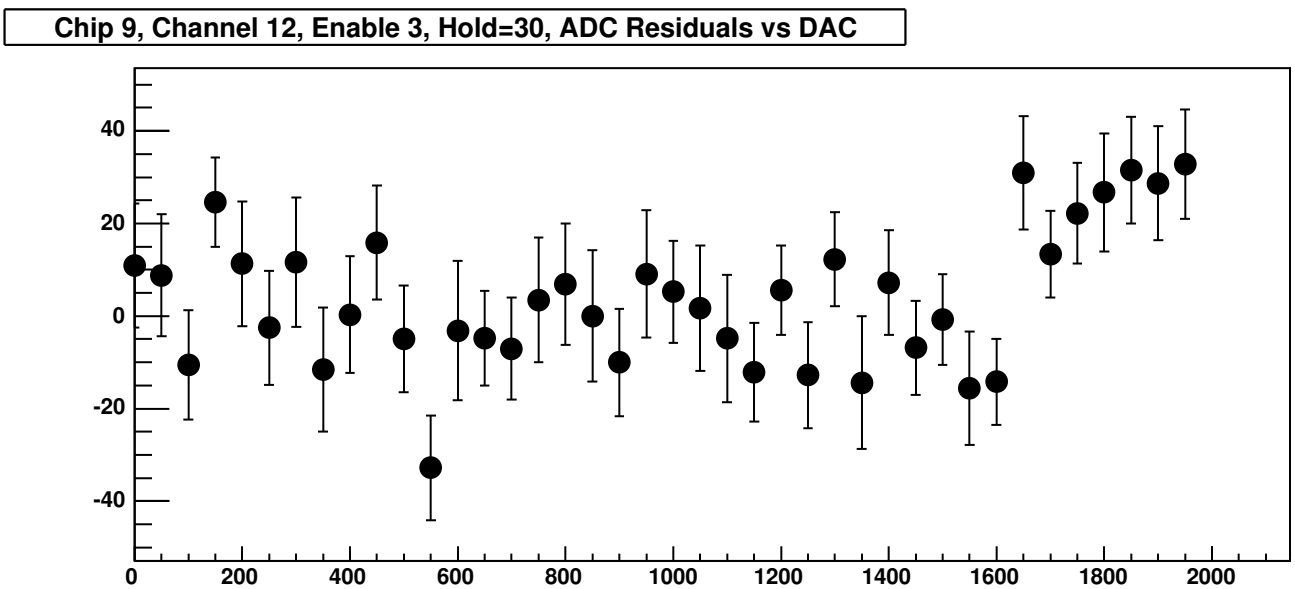

Chip 9, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 25.79 / 11
p0 -1636 ± 17.76
p1 0.06993 ± 0.01614
-1460
-1480
-1500
-1520
-1540
-1560
-1580
-1600
-1620
-1640
0
200
400
600
800
1000
1200
1400
1600
1800
2000

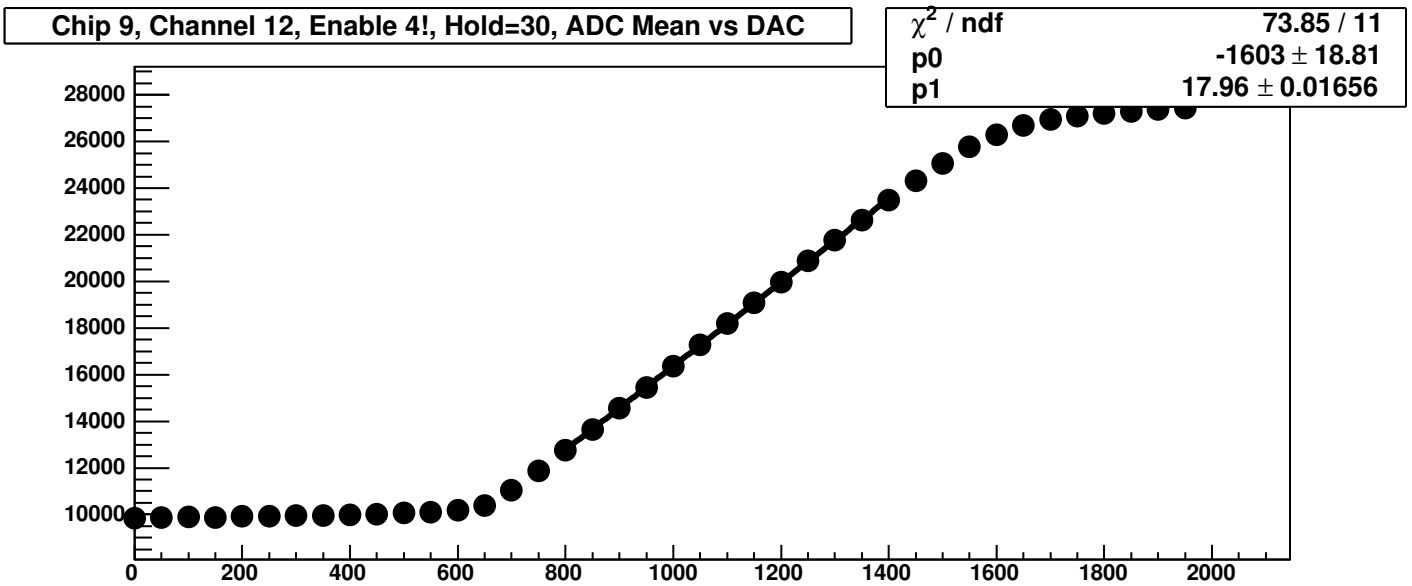

Chip 9, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

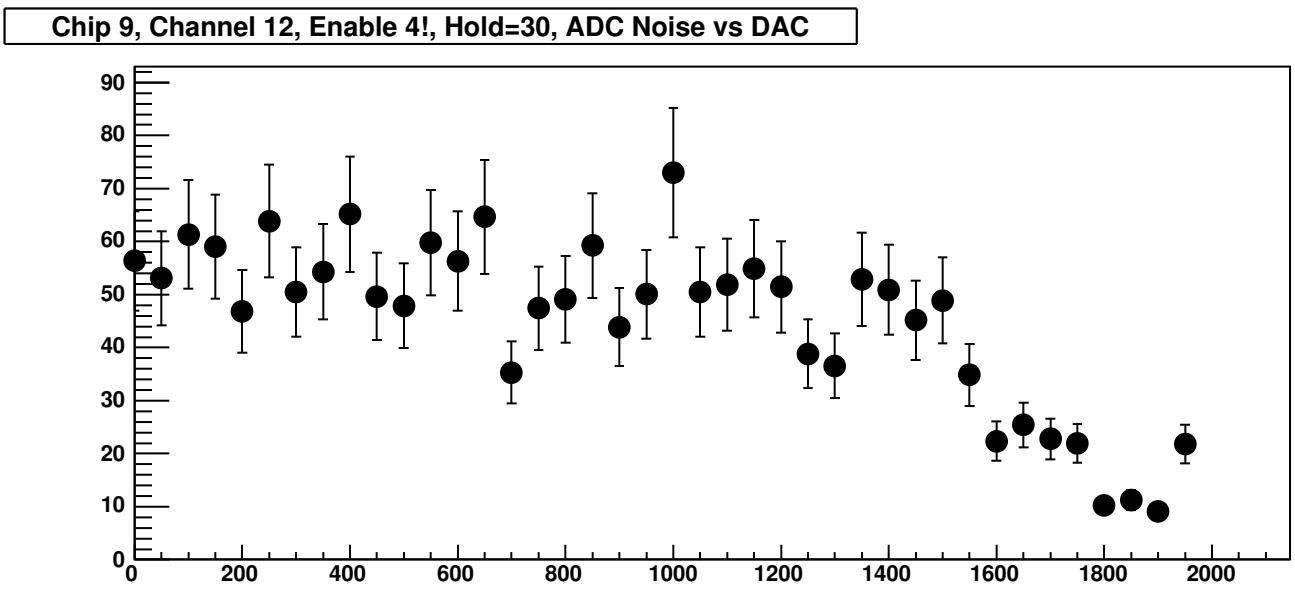

Chip 9, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

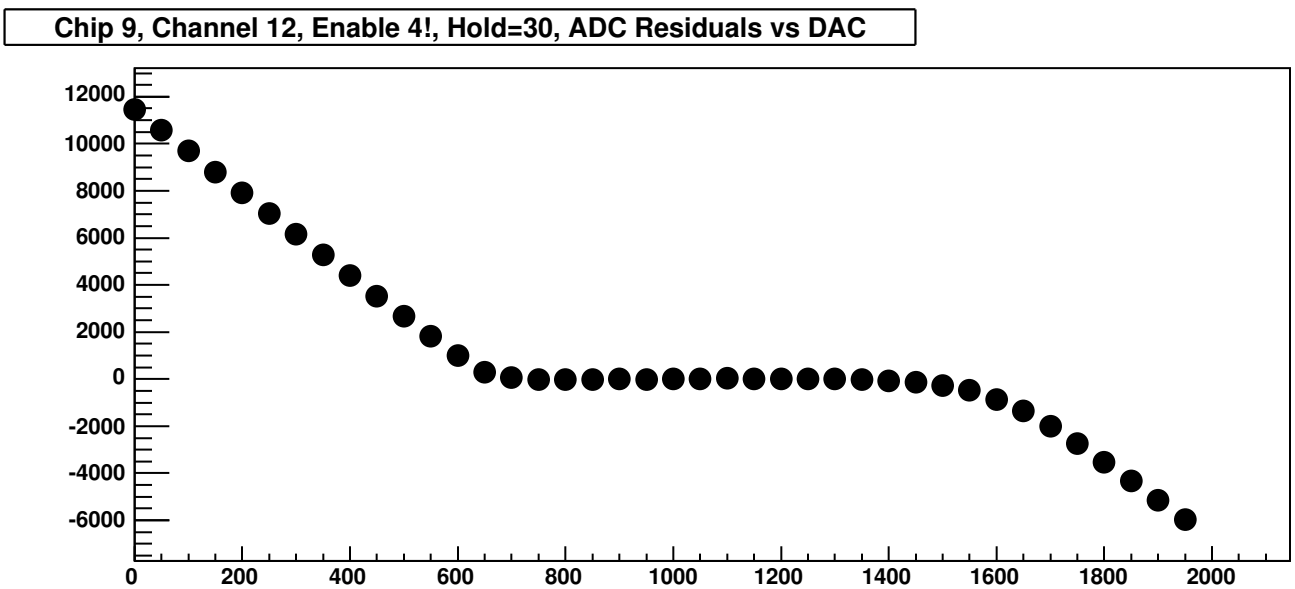

Chip 9, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7.571 / 11
p0 -1639 ± 20.69
p1 0.008245 ± 0.01825
-1580
-1600
-1620
-1640
-1660
-1680
0
200
400
600
800
1000
1200
1400
1600
1800
2000

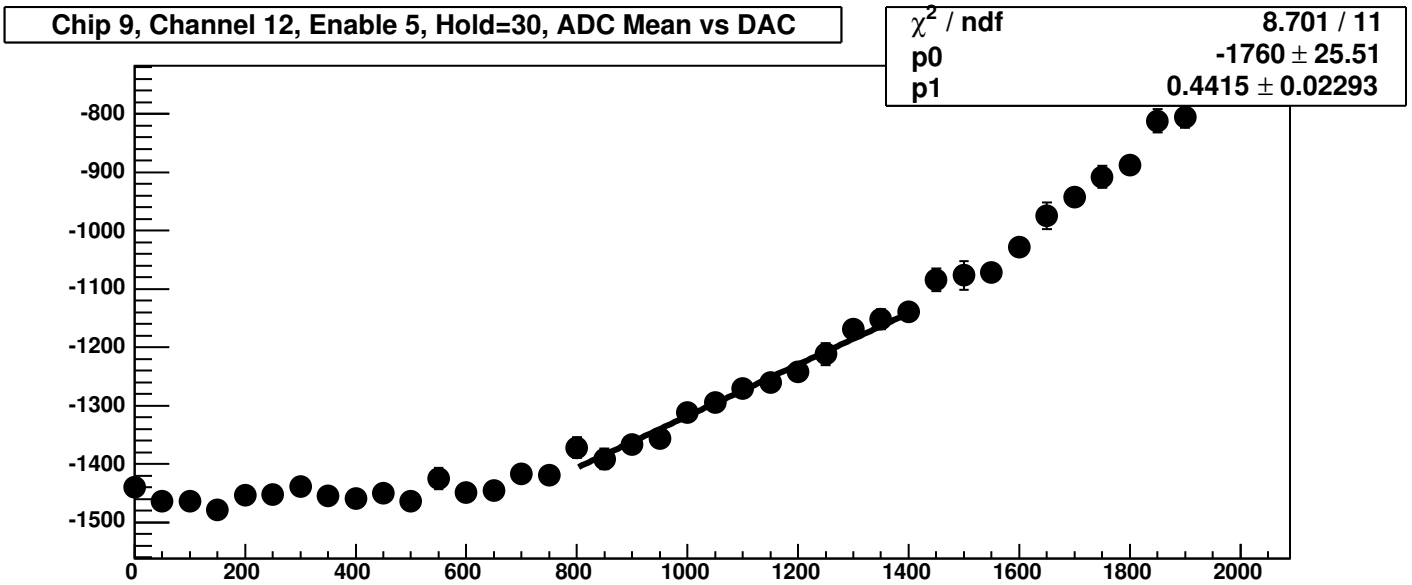

Chip 9, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

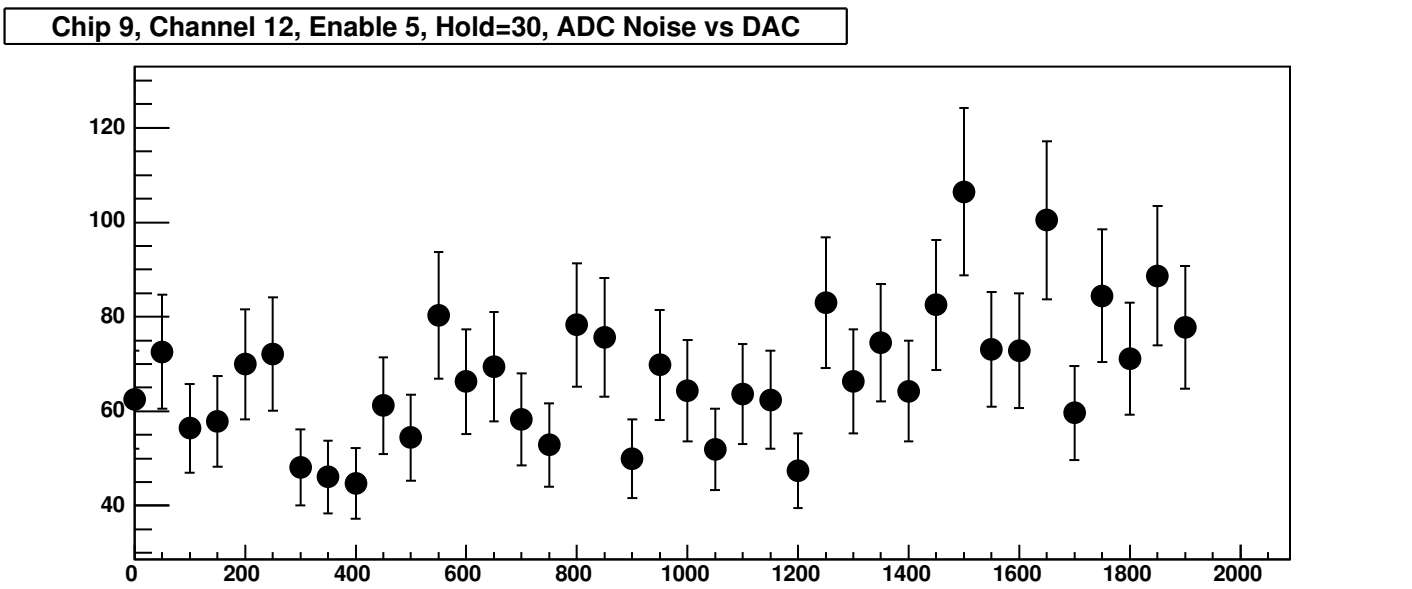

Chip 9, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

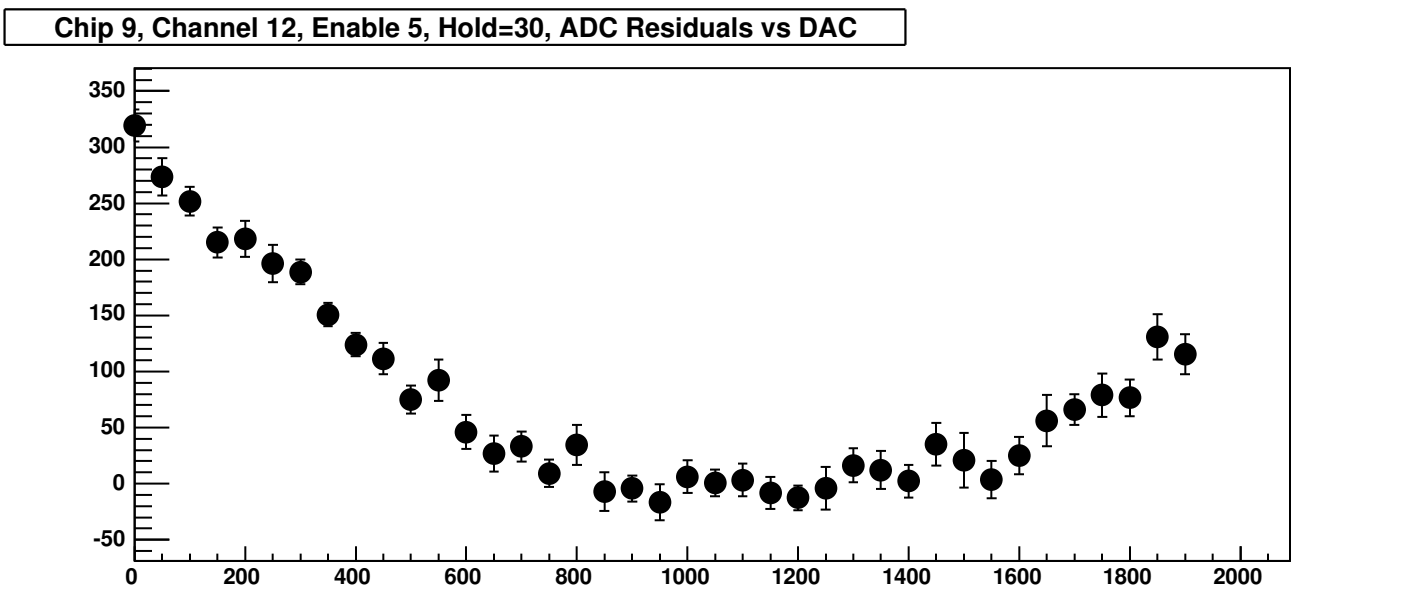

Chip 9, Channel 12, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 73.85 / 11
p0 -1603 ± 18.81
p1 17.96 ± 0.01656
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

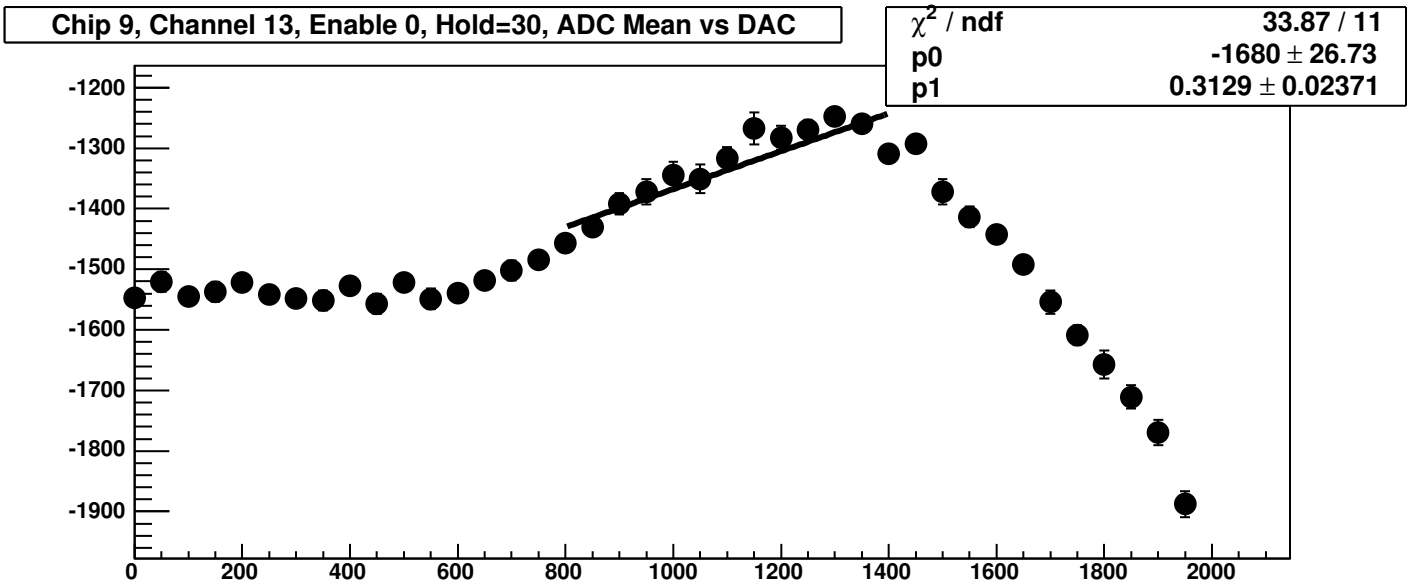

Chip 9, Channel 12, Enable 4!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

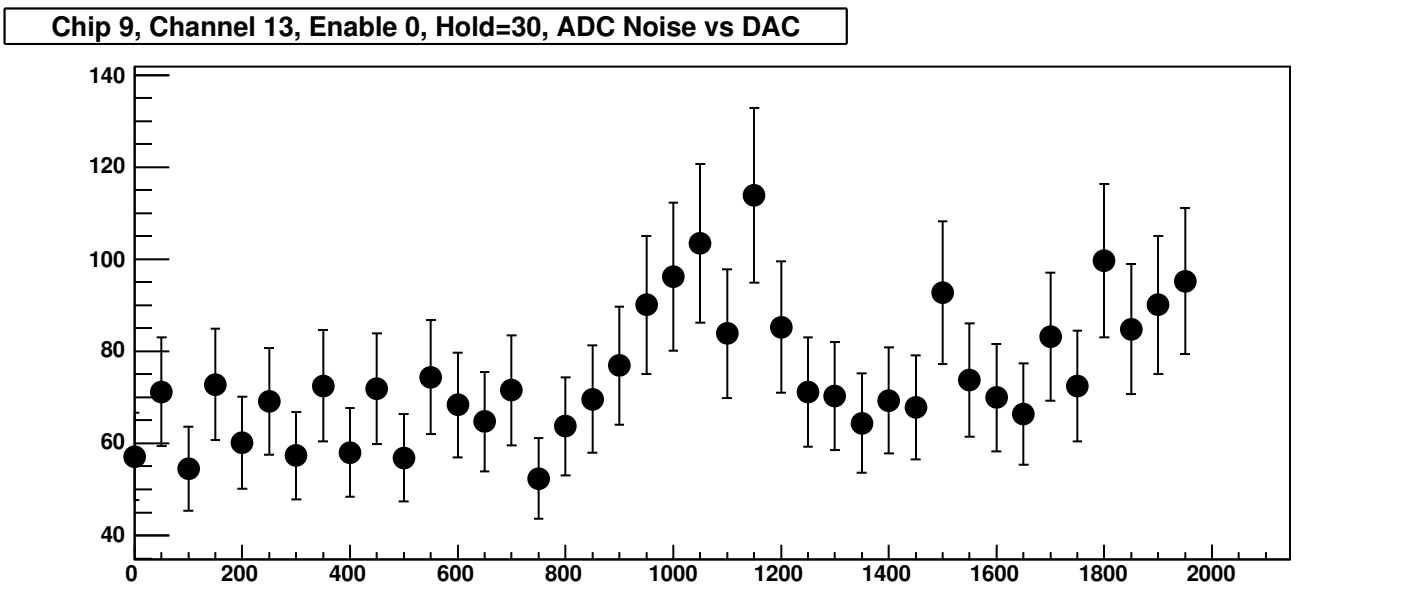

Chip 9, Channel 12, Enable 4!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

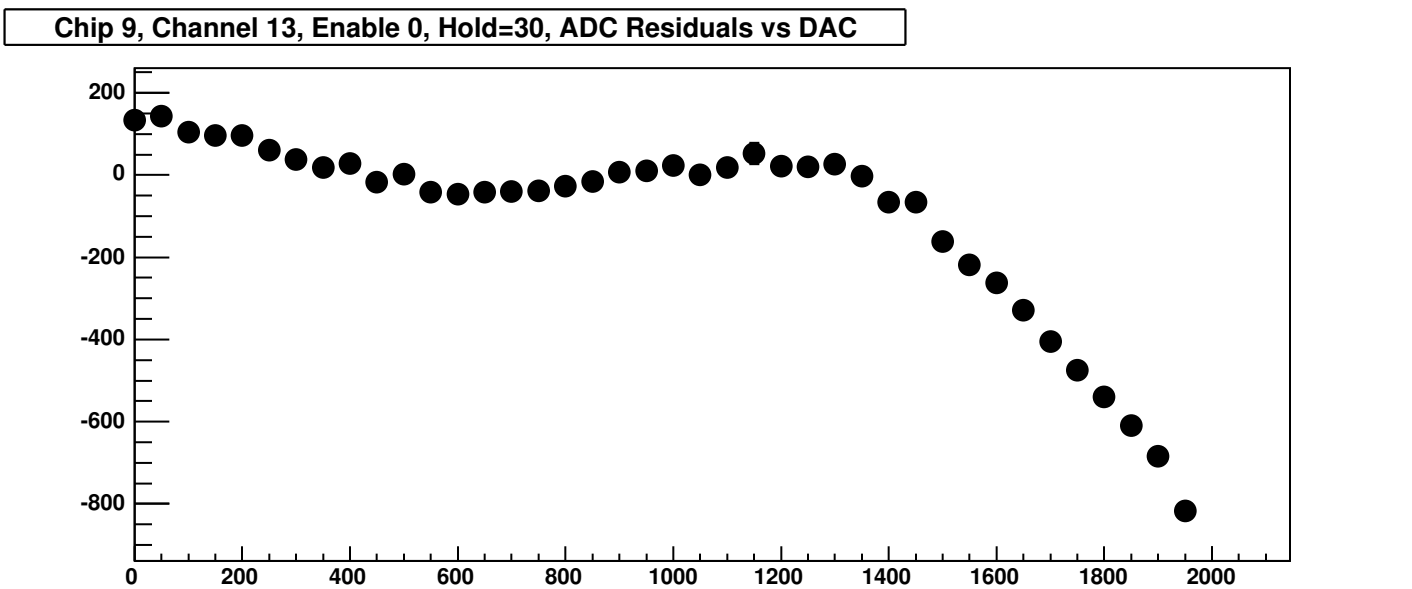

Chip 9, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8.701 / 11
p0 -1760 ± 25.51
p1 0.4415 ± 0.02293
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

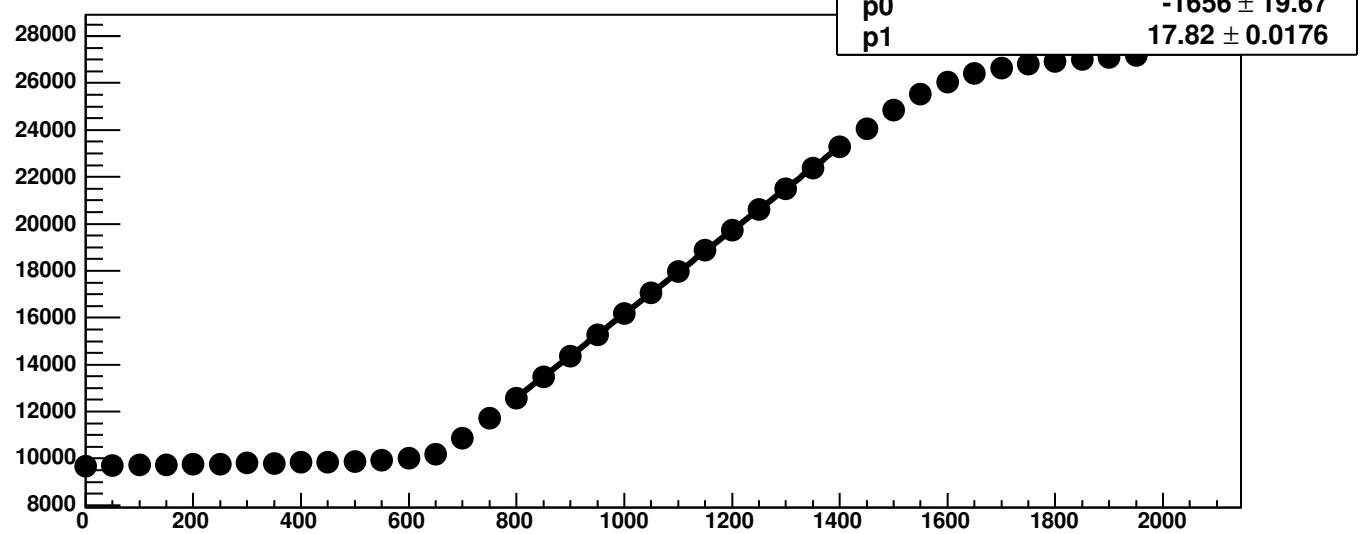

Chip 9, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 33.87 / 11
p0 -1680 ± 26.73
p1 0.3129 ± 0.02371
-1200
-1300
-1400
-1500
-1600
-1700
-1800
-1900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

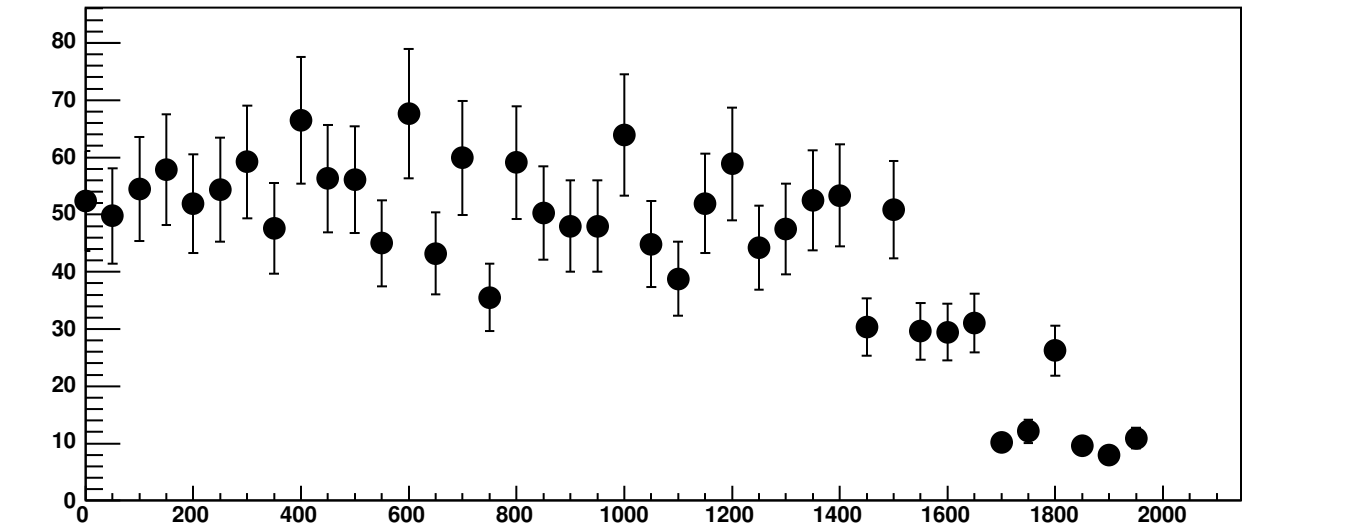

Chip 9, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

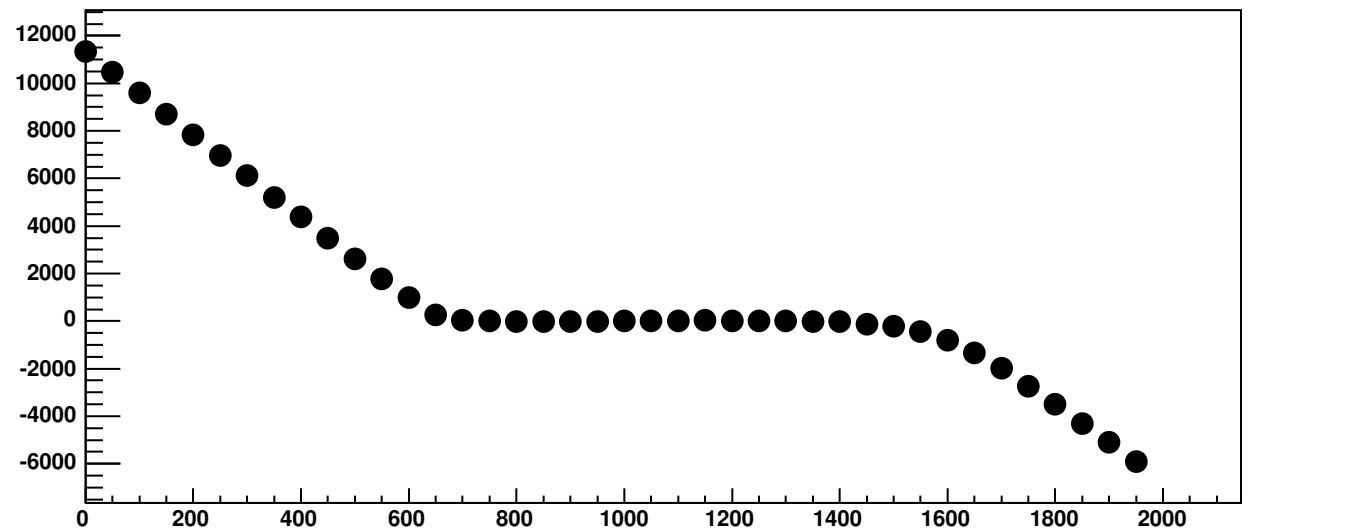
Chip 9, Channel 13, Enable 1!, Hold=30, ADC Mean vs DAC

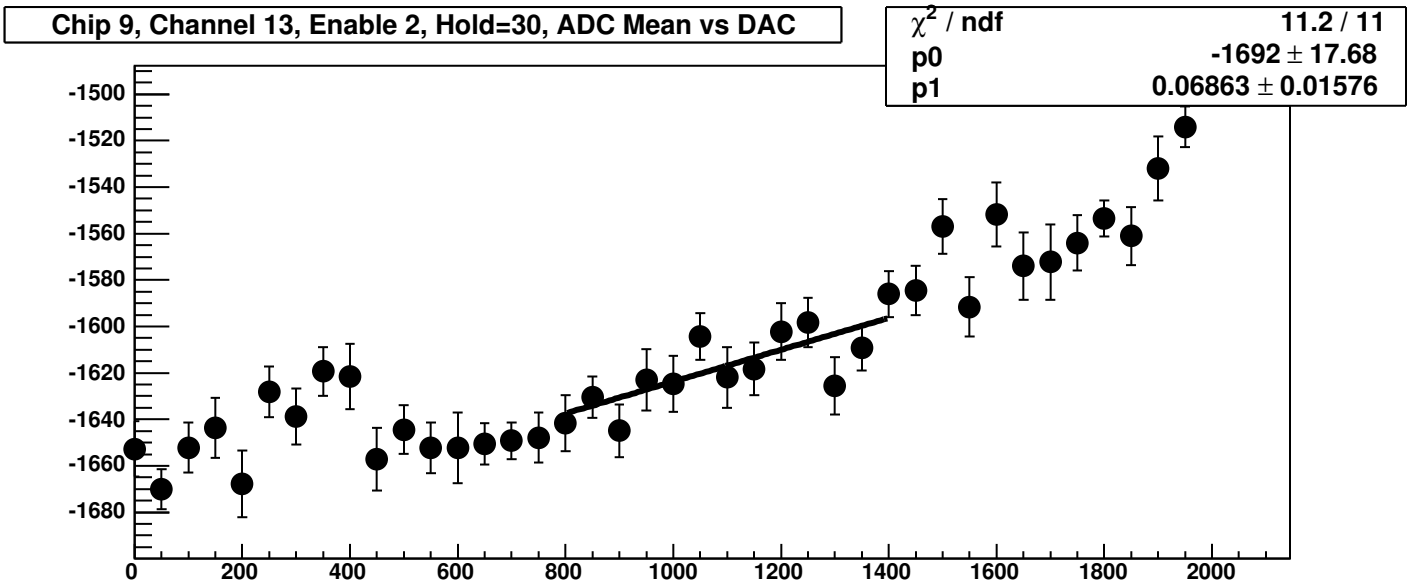


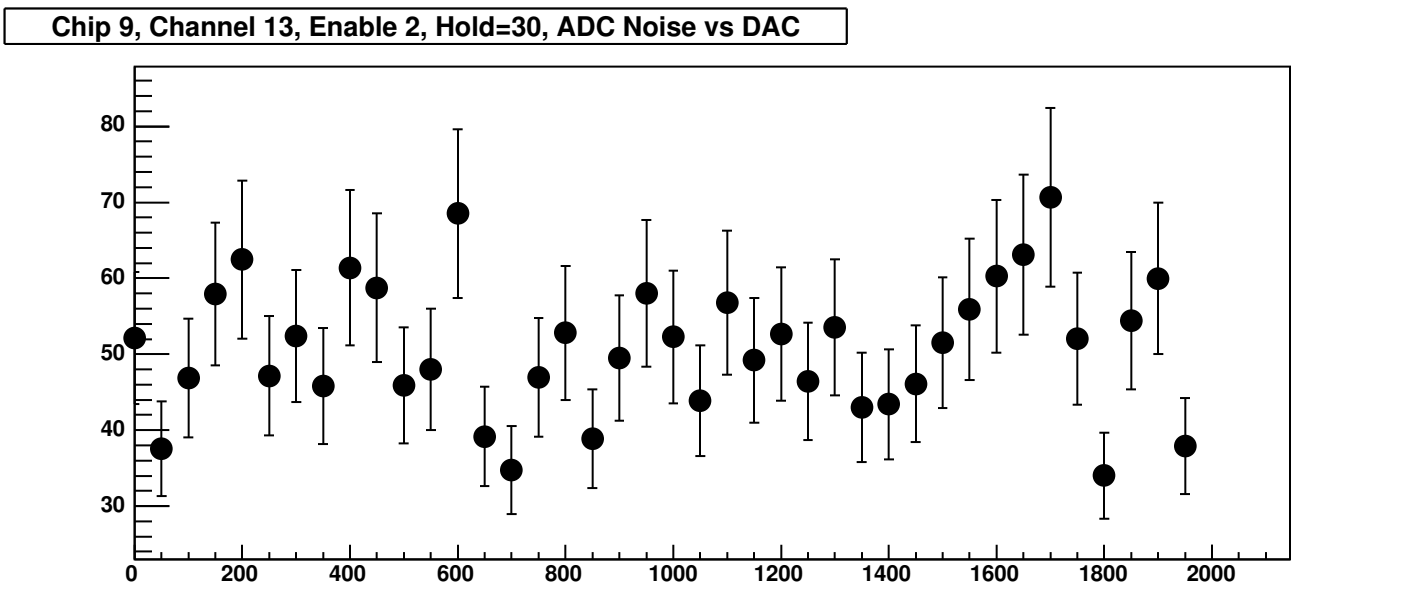
Chip 9, Channel 13, Enable 1!, Hold=30, ADC Noise vs DAC

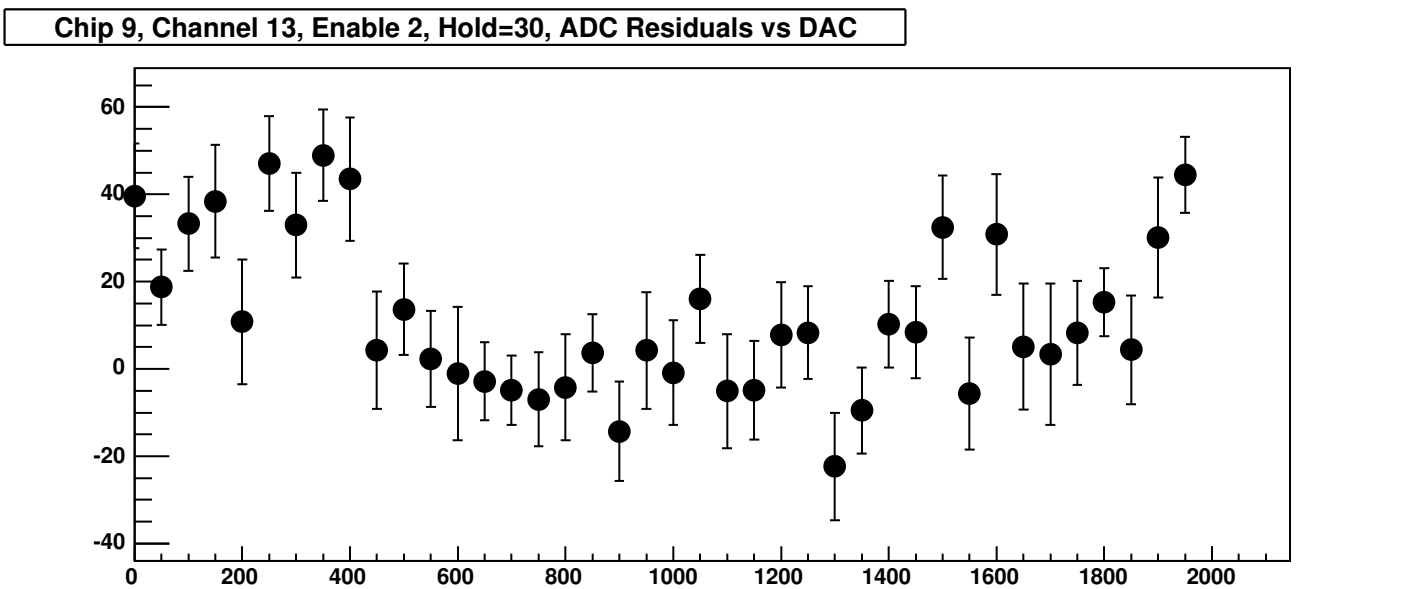


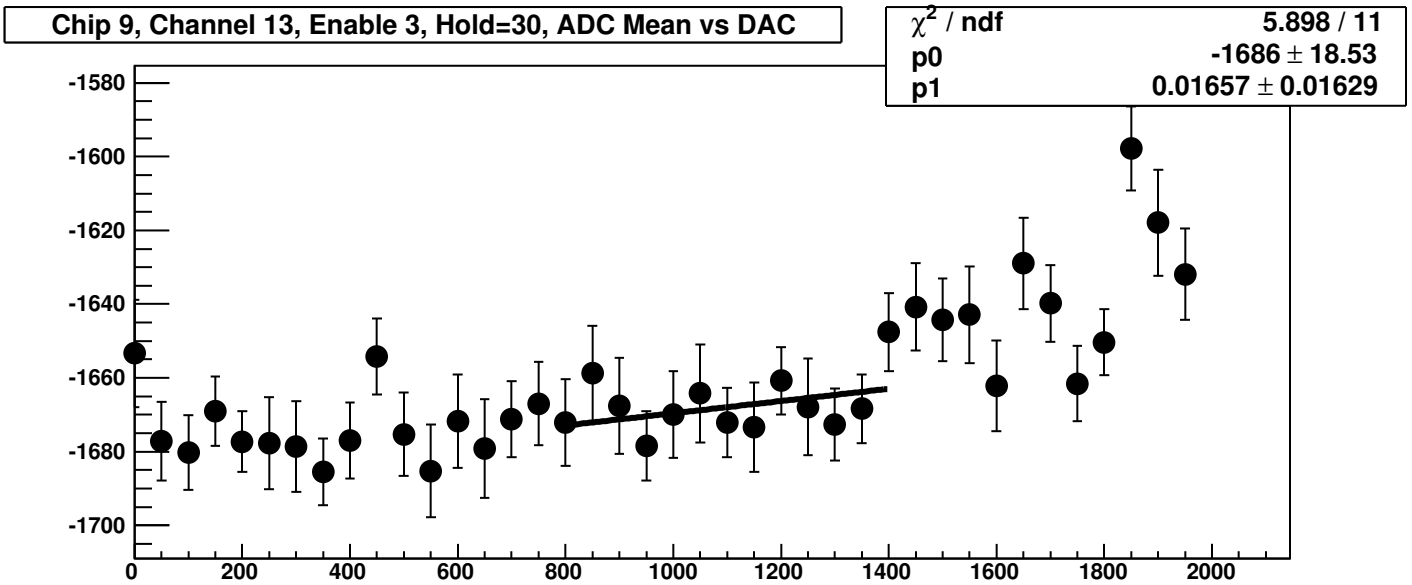
Chip 9, Channel 13, Enable 1!, Hold=30, ADC Residuals vs DAC

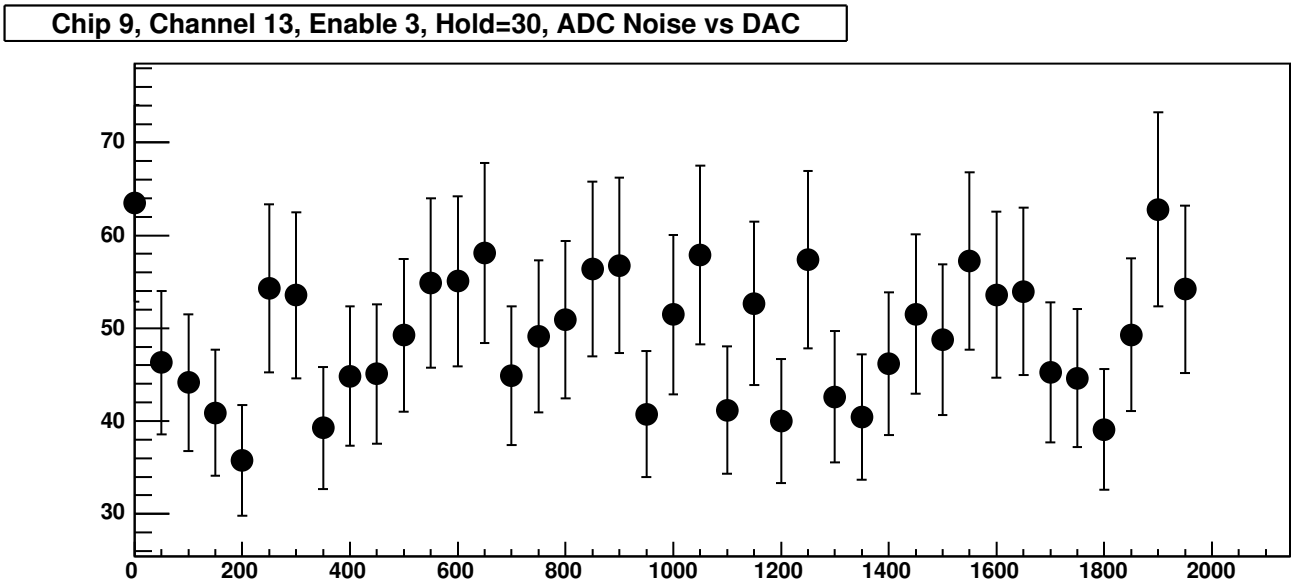


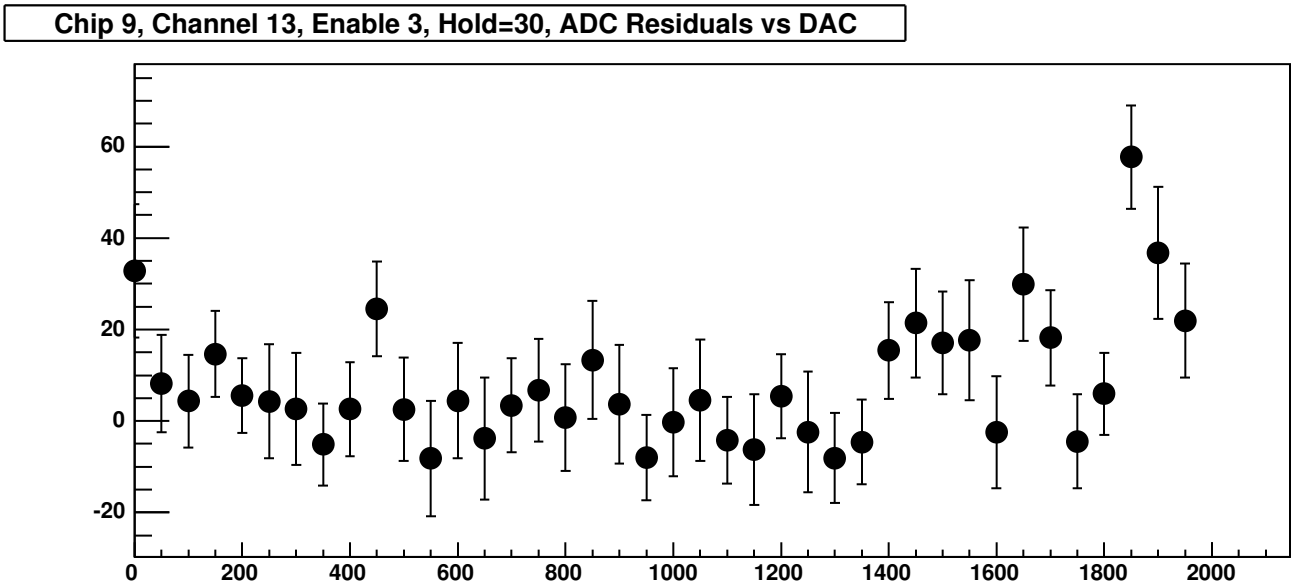

Chip 9, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 11.2 / 11
p0 -1692 ± 17.68
p1 0.06863 ± 0.01576
-1500
-1520
-1540
-1560
-1580
-1600
-1620
-1640
-1660
-1680
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

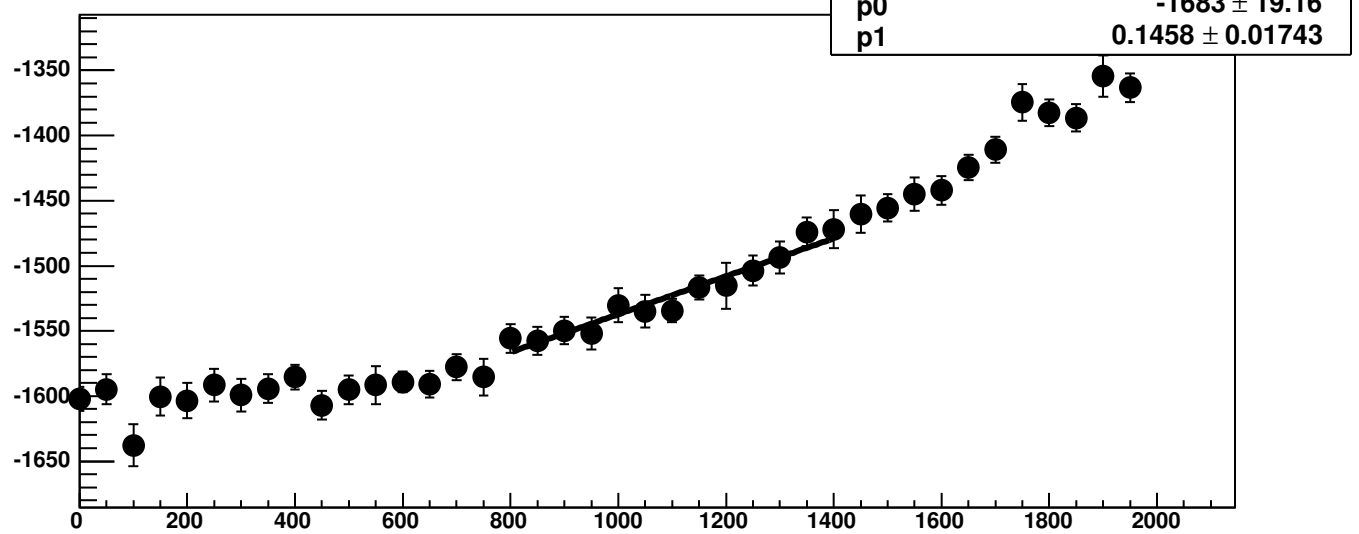

Chip 9, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 5.898 / 11
p0 -1686 ± 18.53
p1 0.01657 ± 0.01629
-1580
-1600
-1620
-1640
-1660
-1680
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

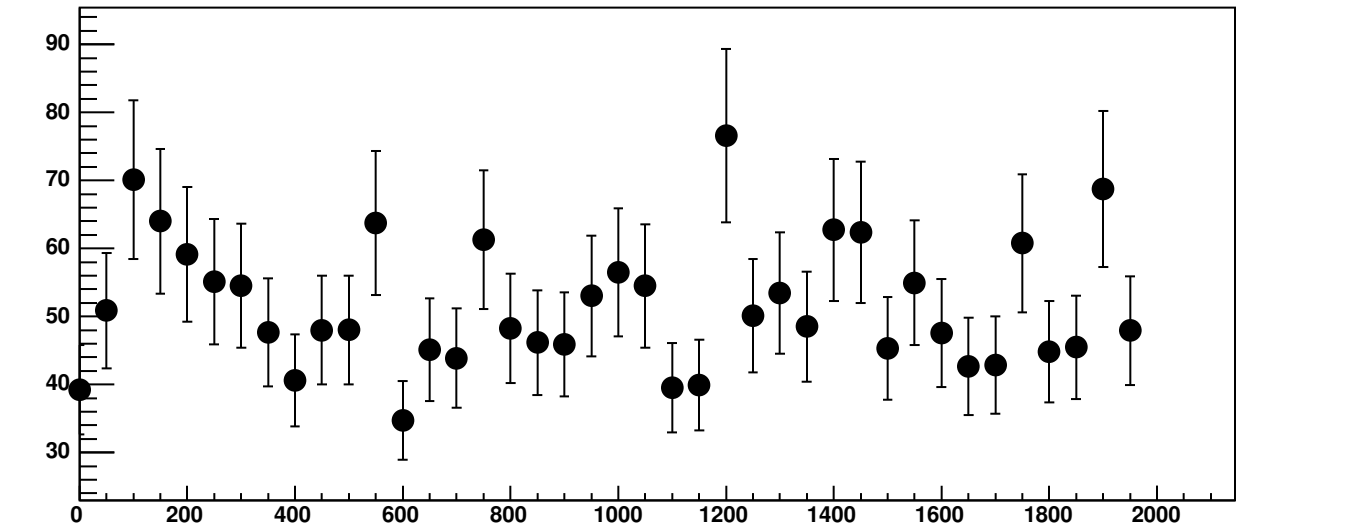

Chip 9, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

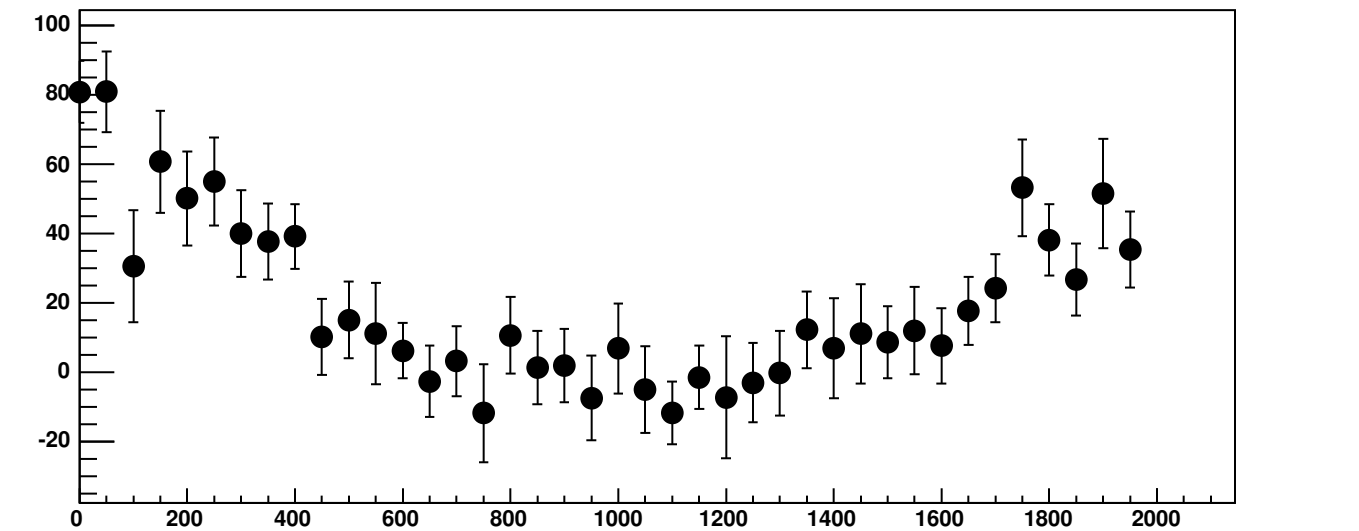
Chip 9, Channel 13, Enable 4, Hold=30, ADC Mean vs DAC

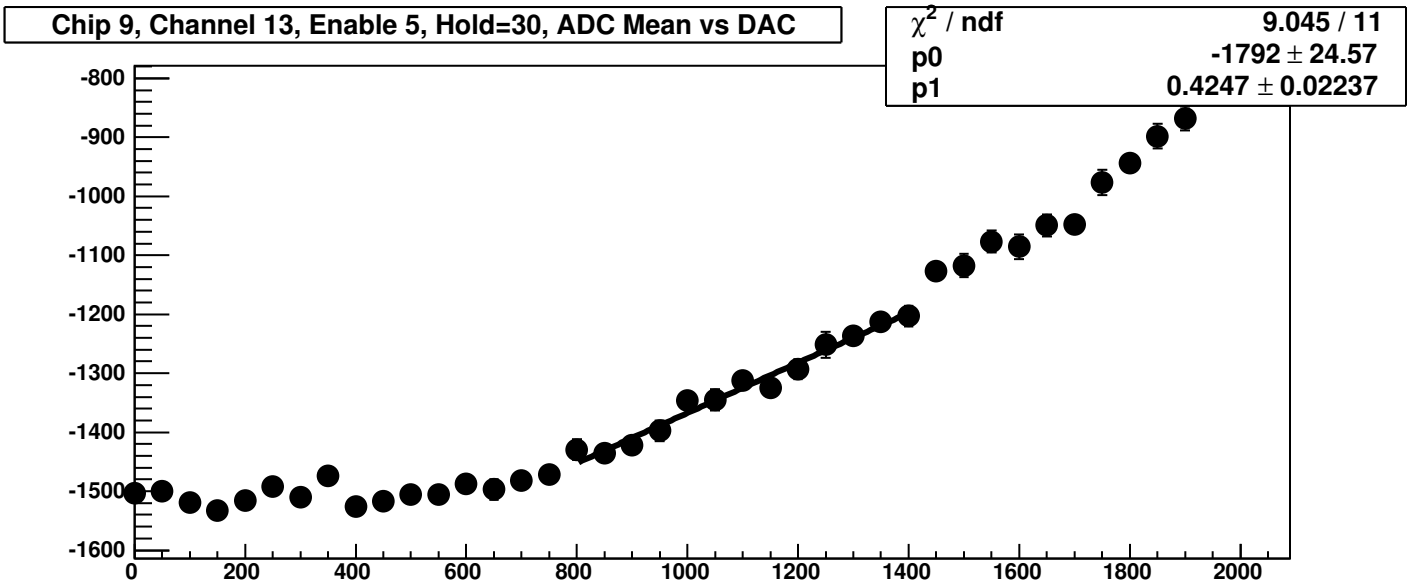


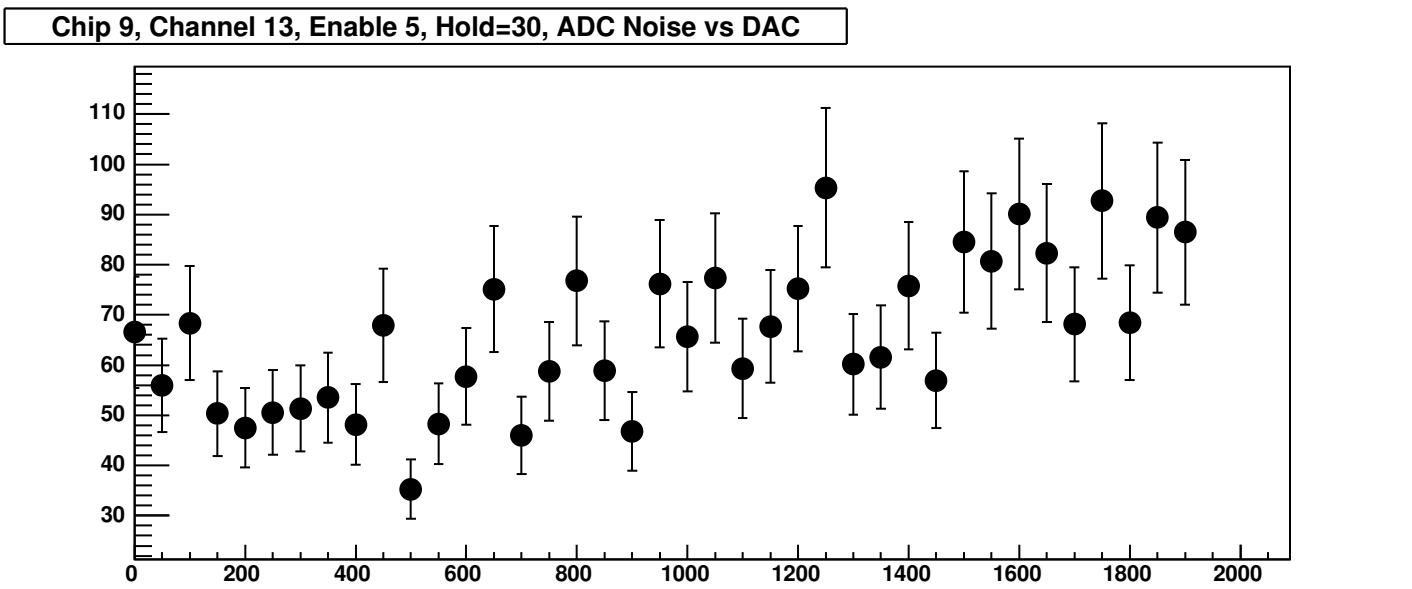
Chip 9, Channel 13, Enable 4, Hold=30, ADC Noise vs DAC

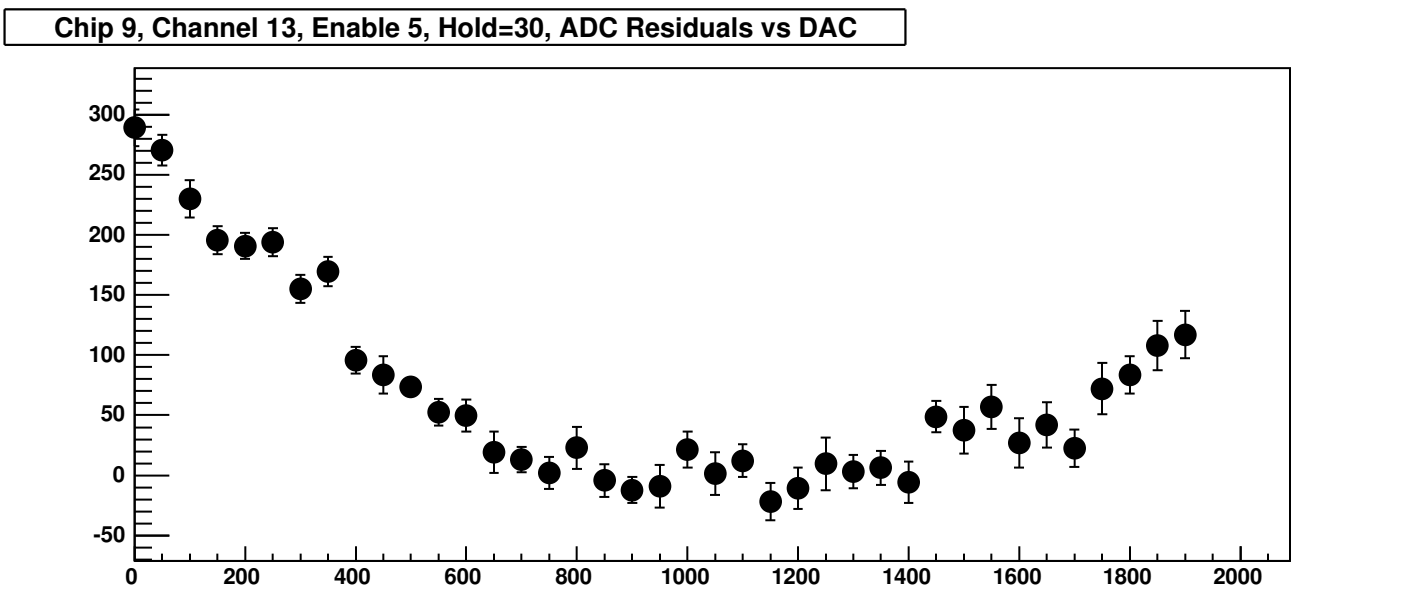


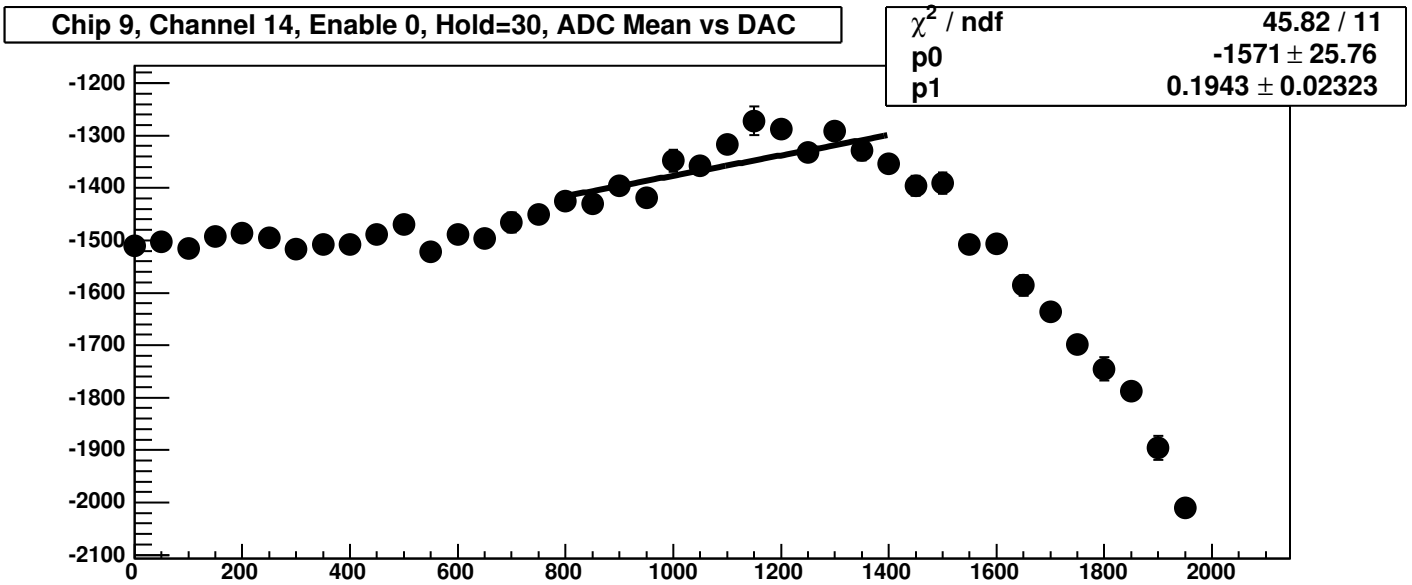
Chip 9, Channel 13, Enable 4, Hold=30, ADC Residuals vs DAC

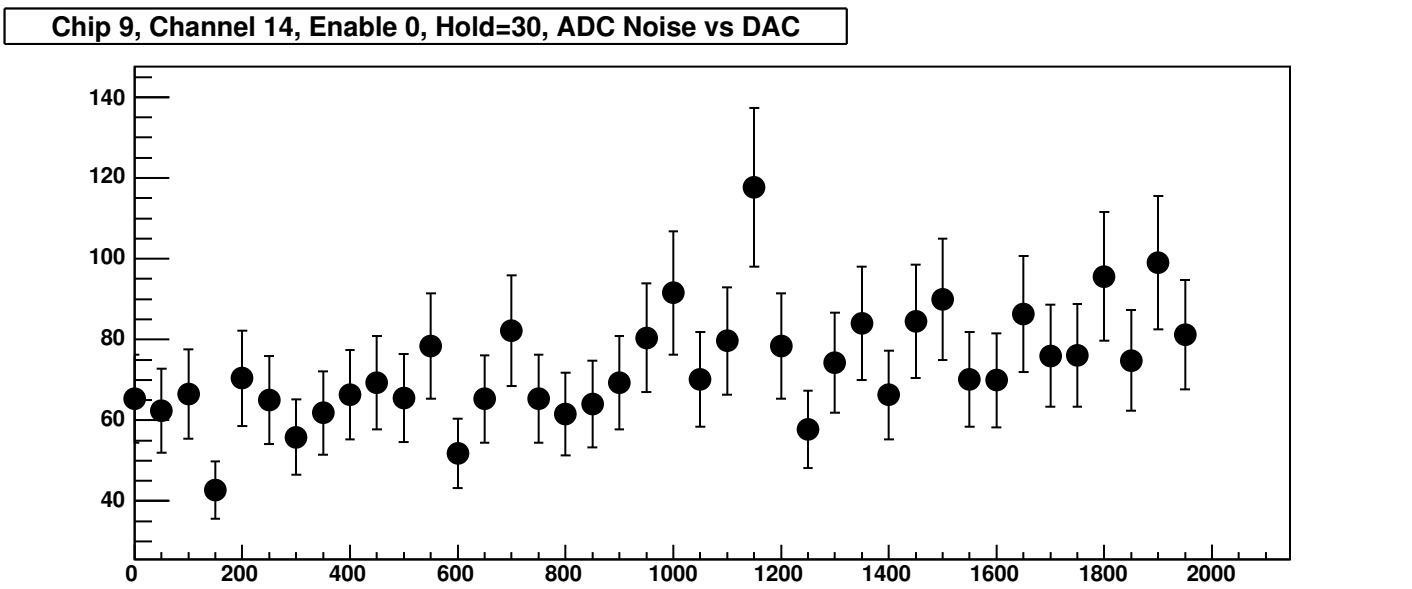


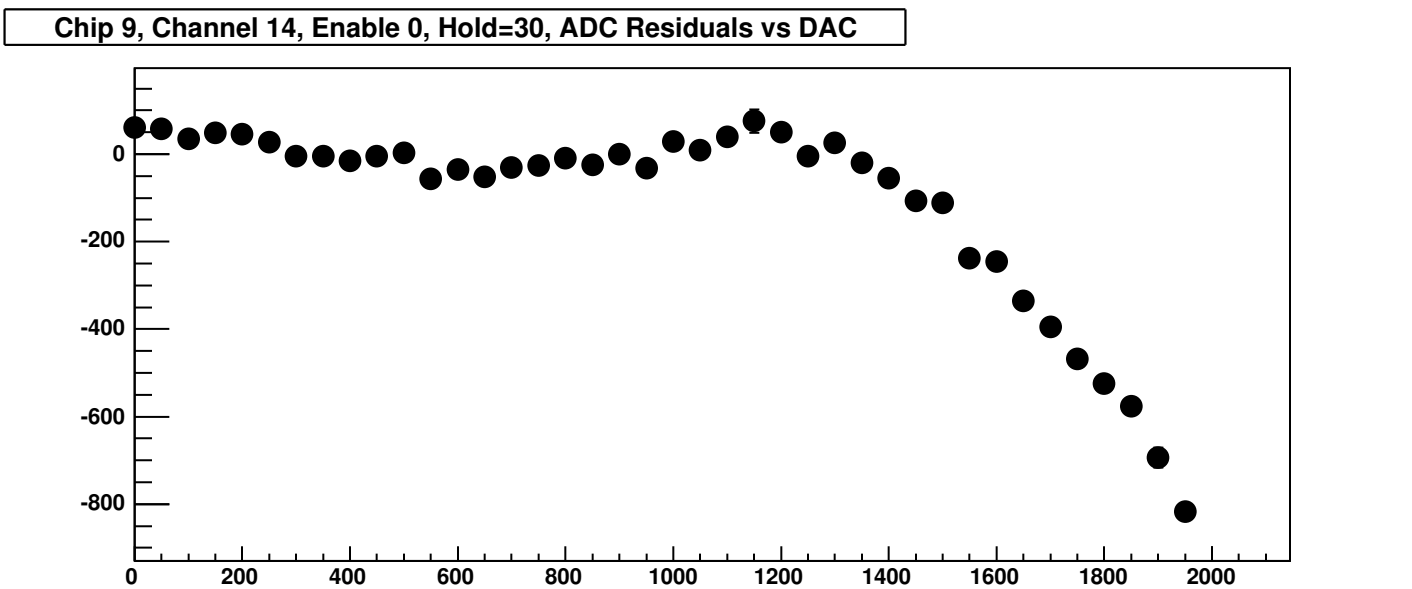

Chip 9, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9.045 / 11
p0 -1792 ± 24.57
p1 0.4247 ± 0.02237
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

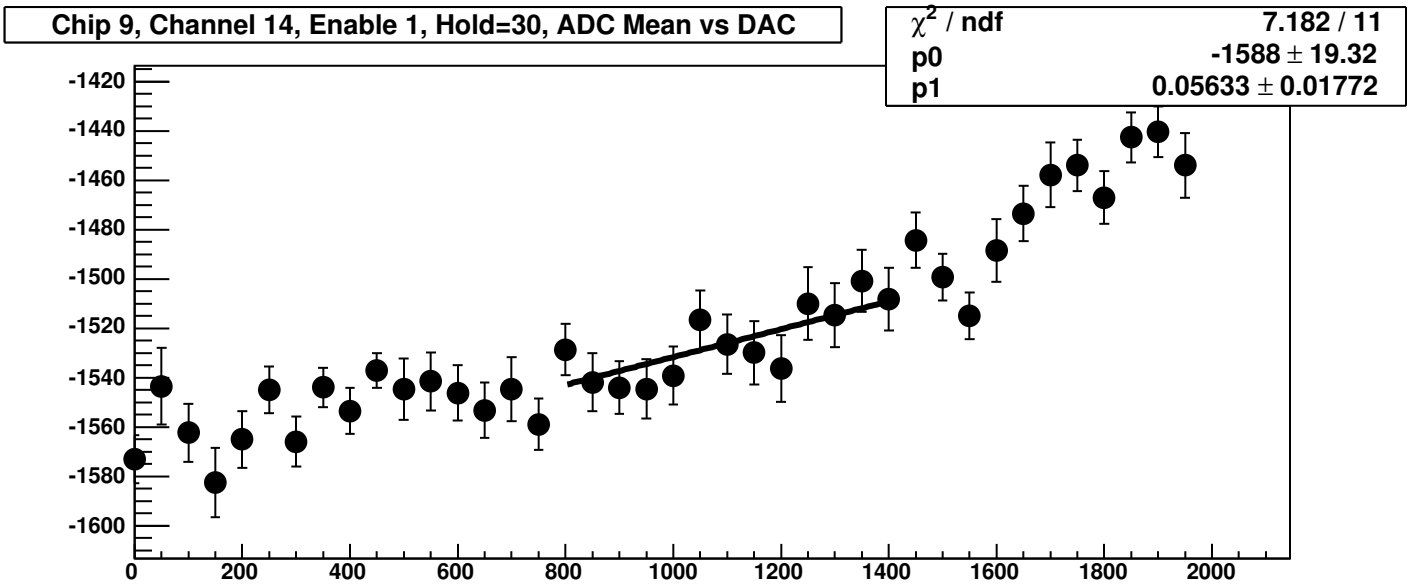

Chip 9, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

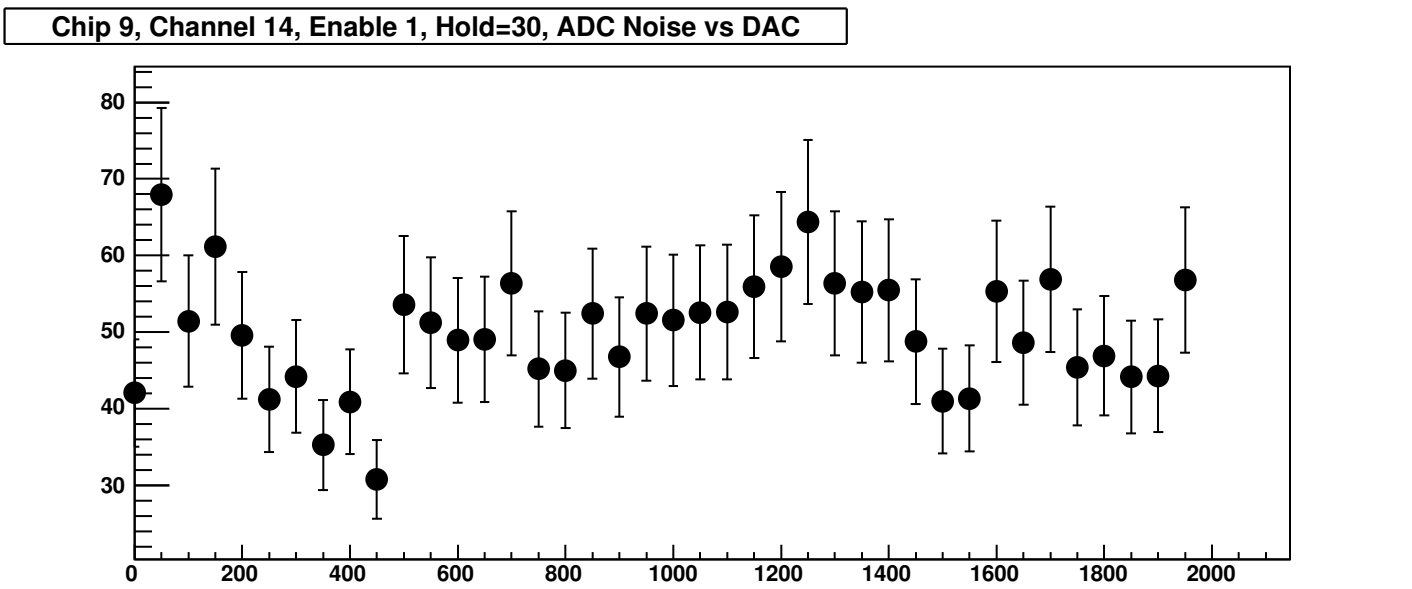

Chip 9, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

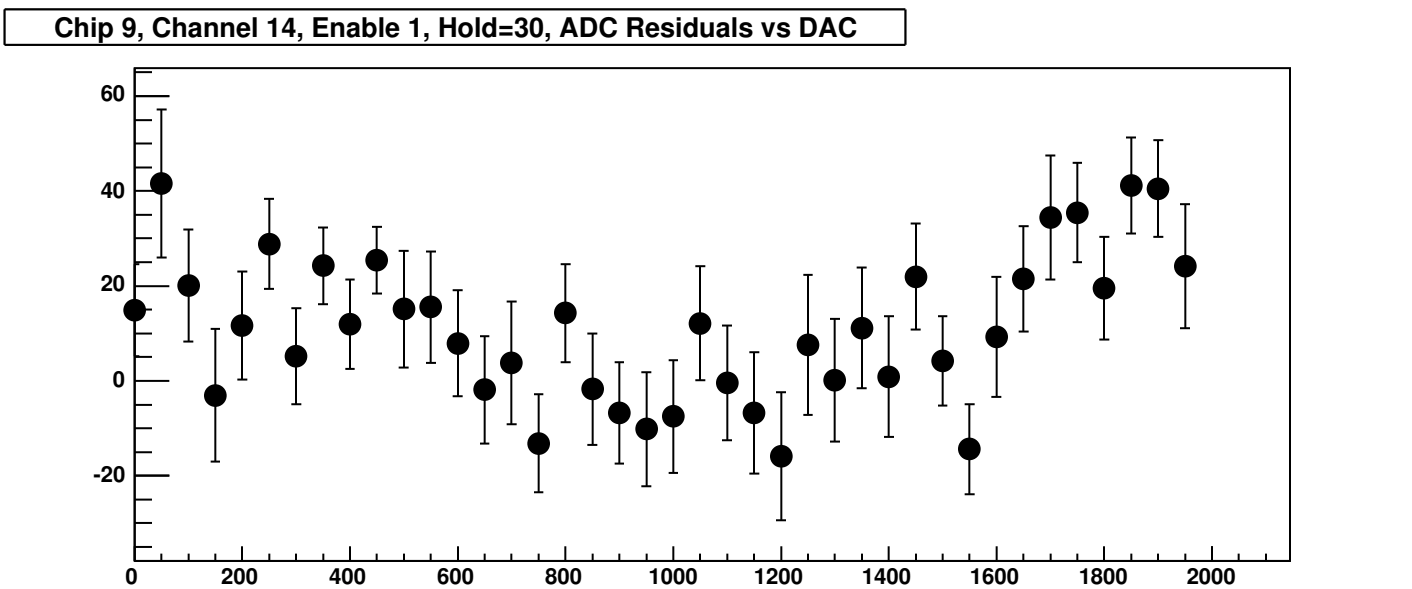

Chip 9, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 45.82 / 11
p0 -1571 ± 25.76
p1 0.1943 ± 0.02323
-1200
-1300
-1400
-1500
-1600
-1700
-1800
-1900
-2000
-2100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

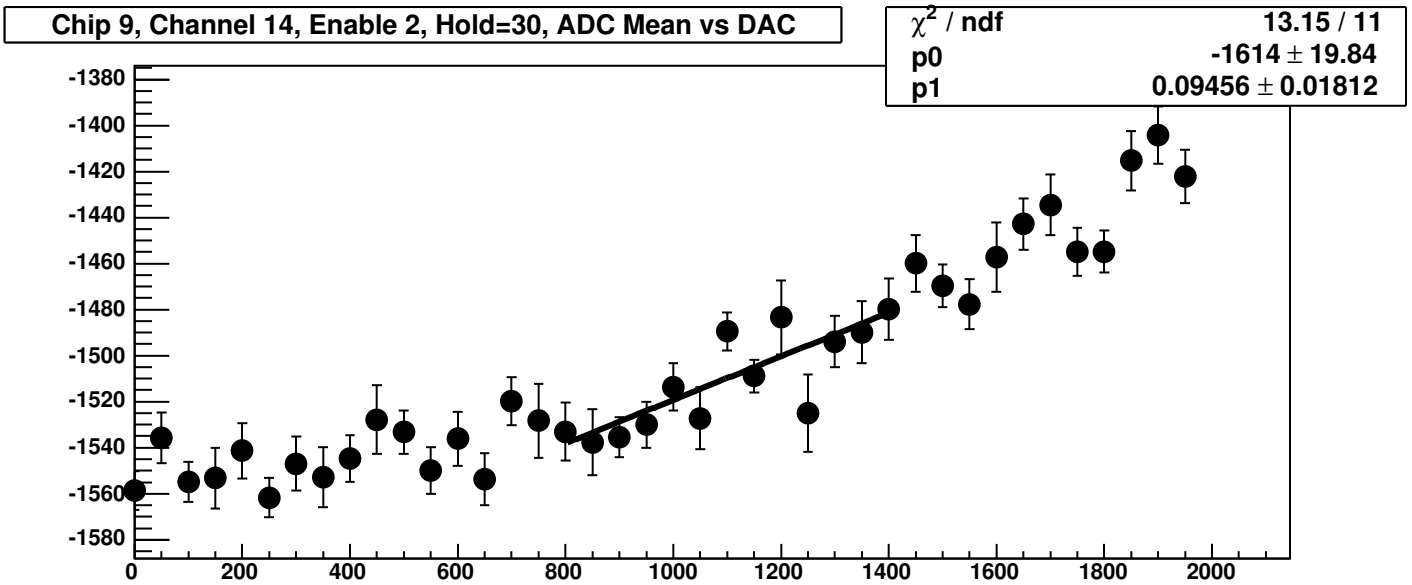

Chip 9, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

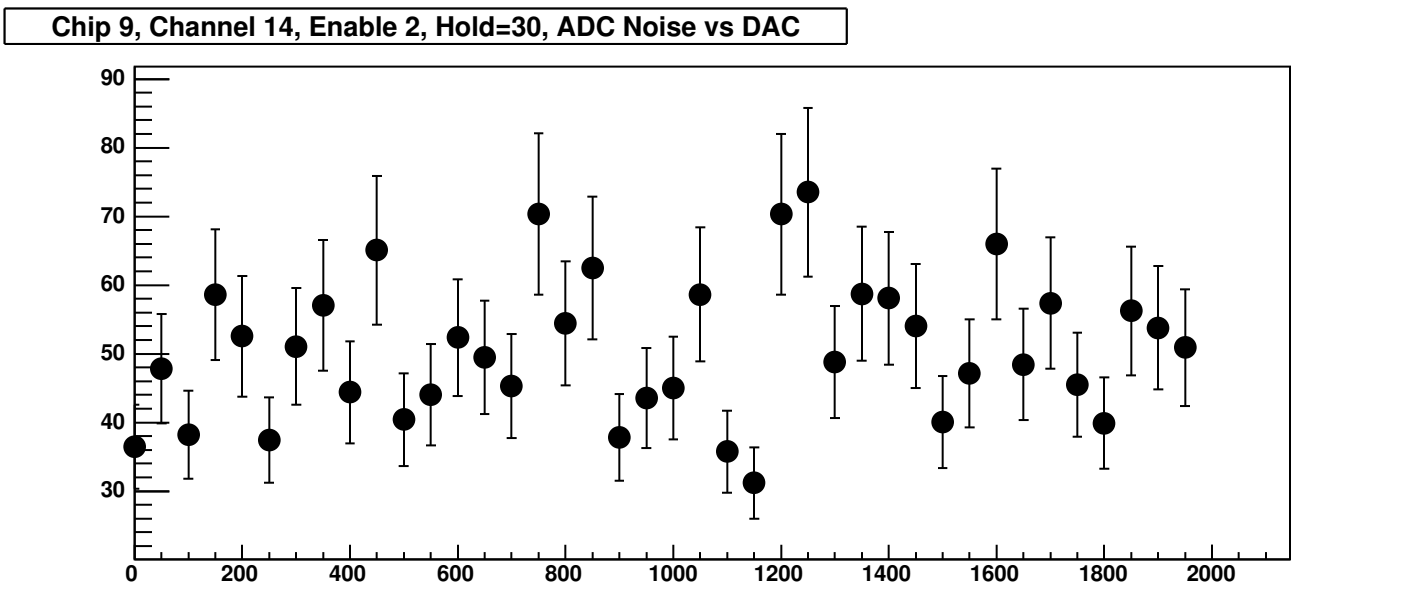

Chip 9, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

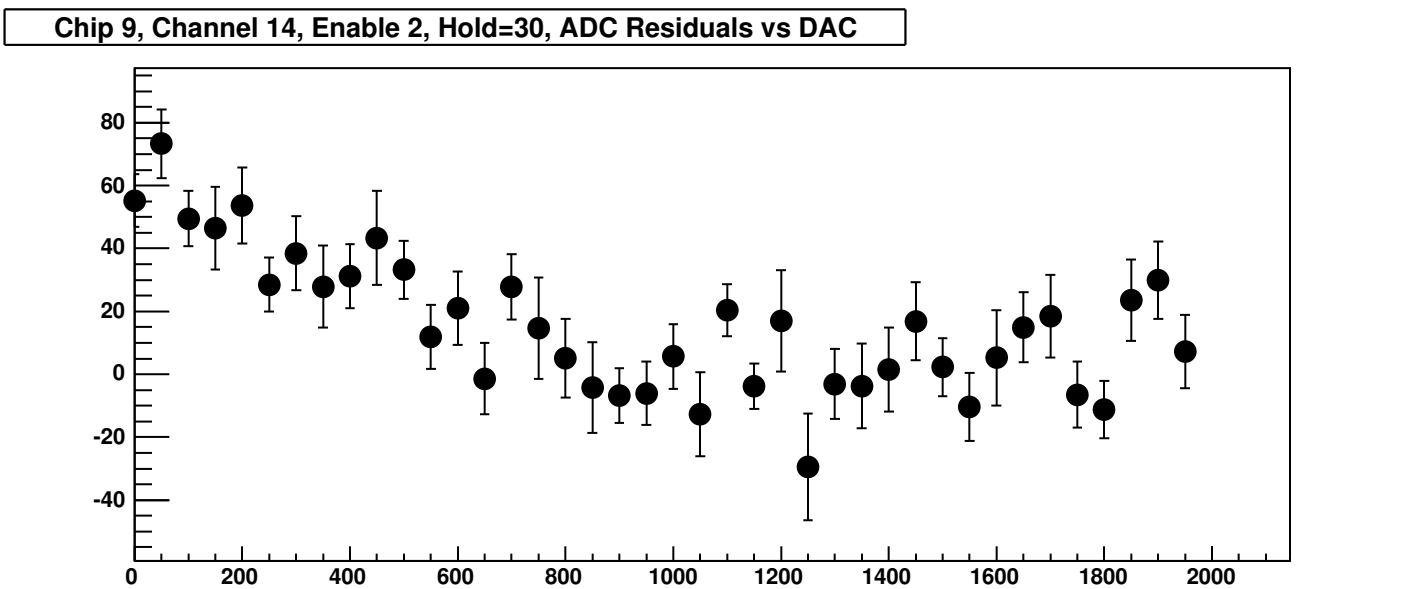

Chip 9, Channel 14, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7.182 / 11
p0 -1588 ± 19.32
p1 0.05633 ± 0.01772
-1420
-1440
-1460
-1480
-1500
-1520
-1540
-1560
-1580
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

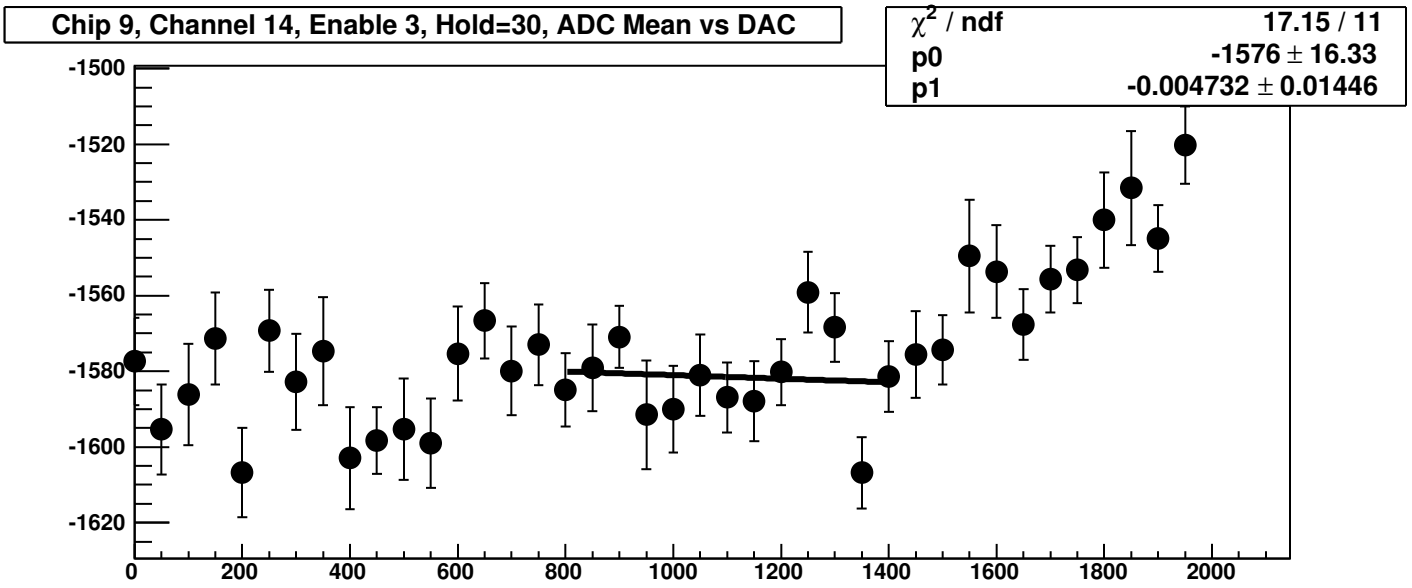

Chip 9, Channel 14, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

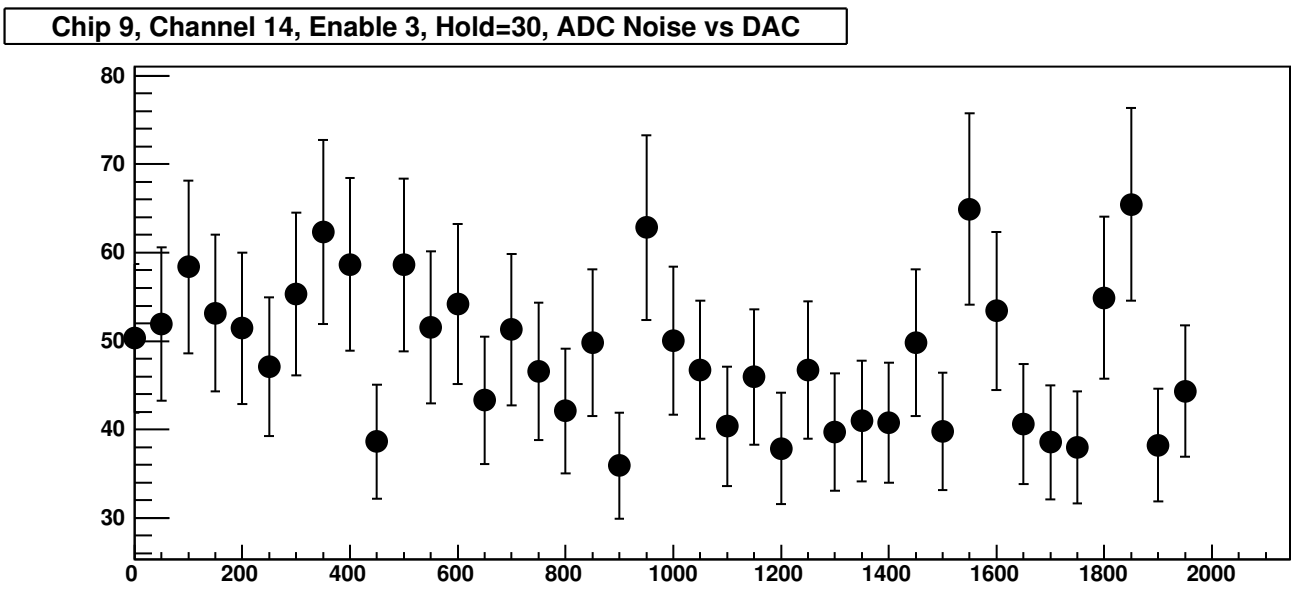

Chip 9, Channel 14, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

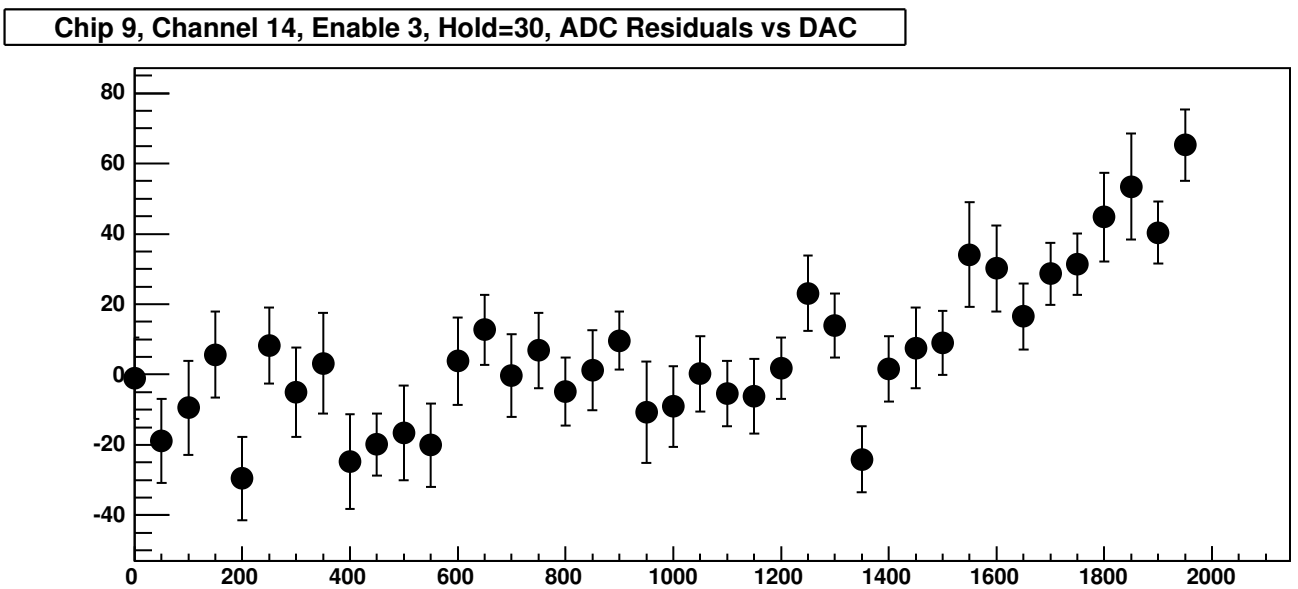

Chip 9, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 13.15 / 11
p0 -1614 ± 19.84
p1 0.09456 ± 0.01812
-1380
-1400
-1420
-1440
-1460
-1480
-1500
-1520
-1540
-1560
-1580
0
200
400
600
800
1000
1200
1400
1600
1800
2000

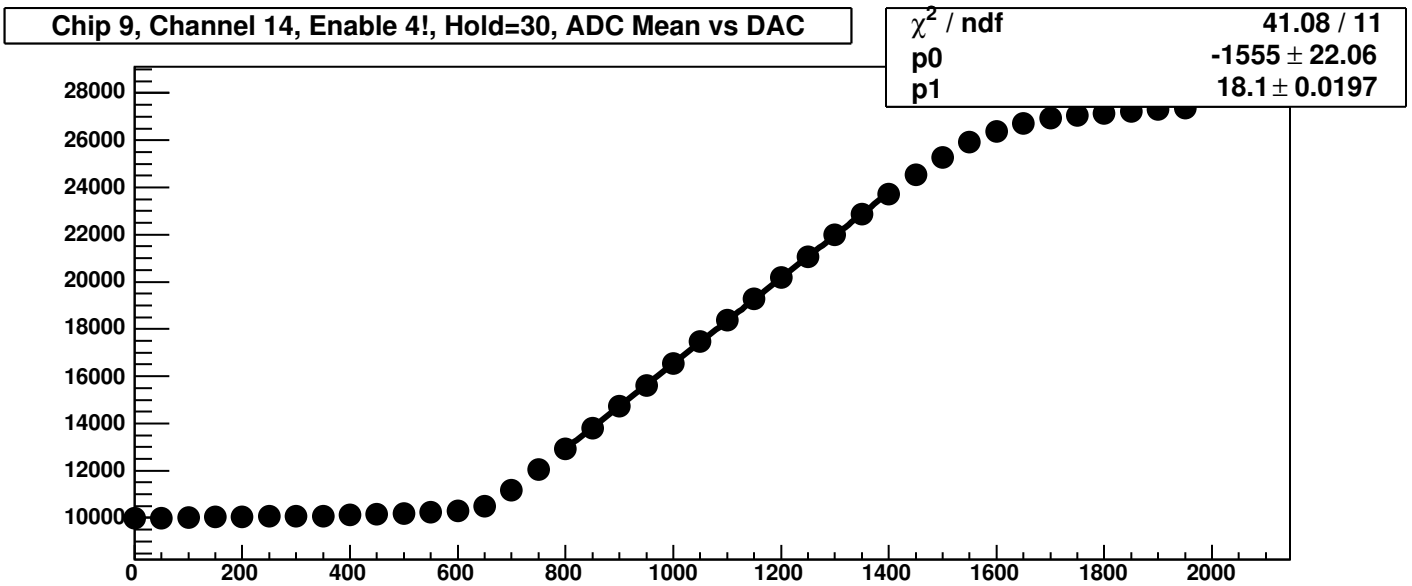

Chip 9, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

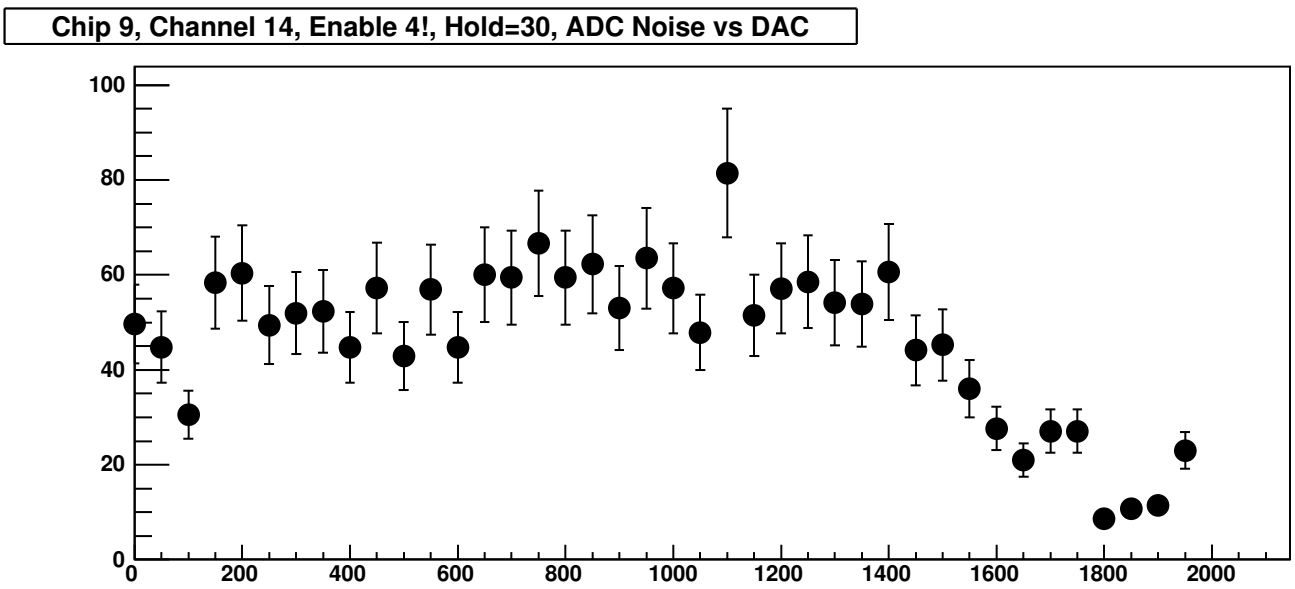

Chip 9, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

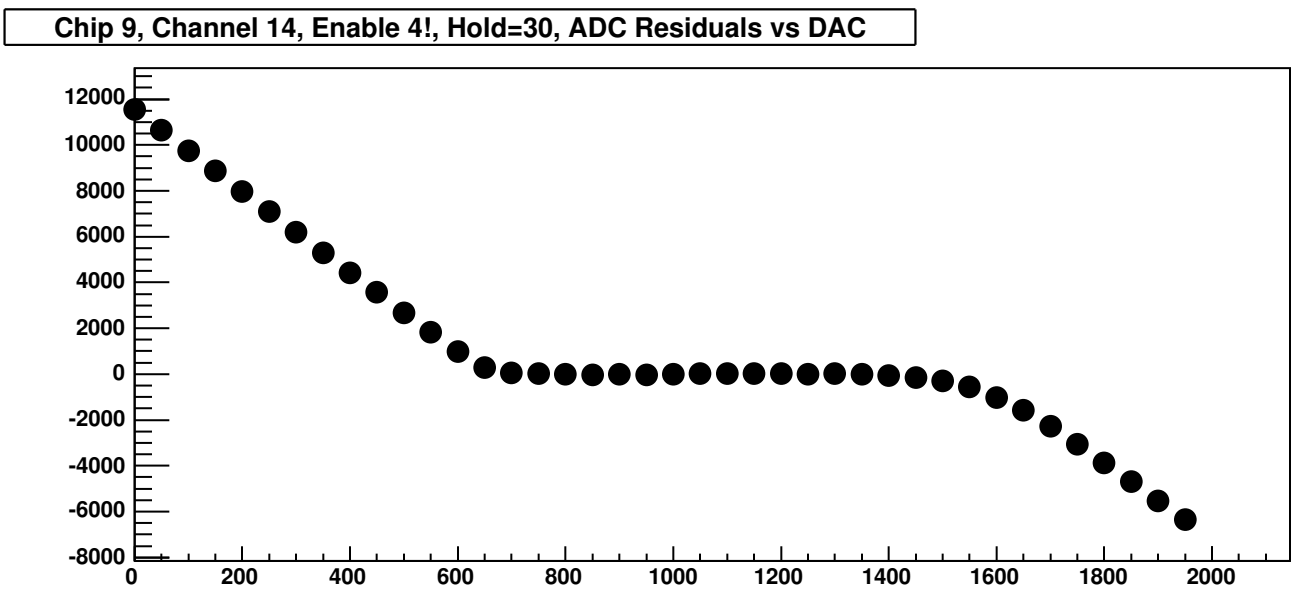

Chip 9, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 17.15 / 11
p0 -1576 ± 16.33
p1 -0.004732 ± 0.01446
-1500
-1520
-1540
-1560
-1580
-1600
-1620
0
200
400
600
800
1000
1200
1400
1600
1800
2000

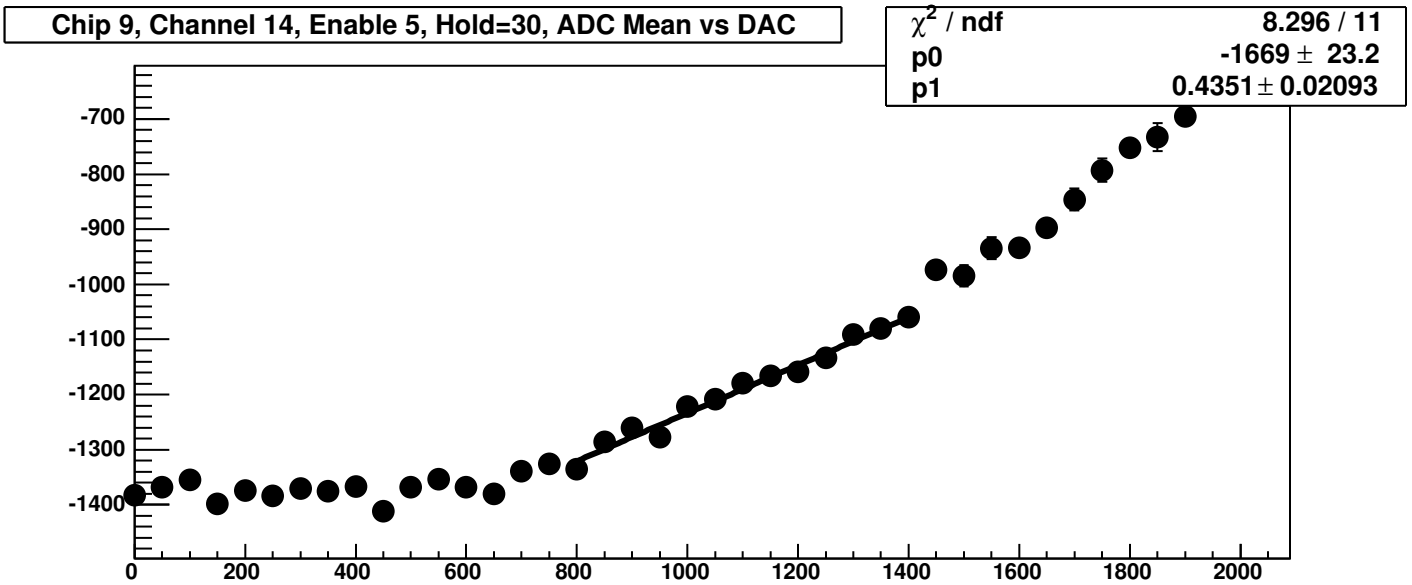

Chip 9, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

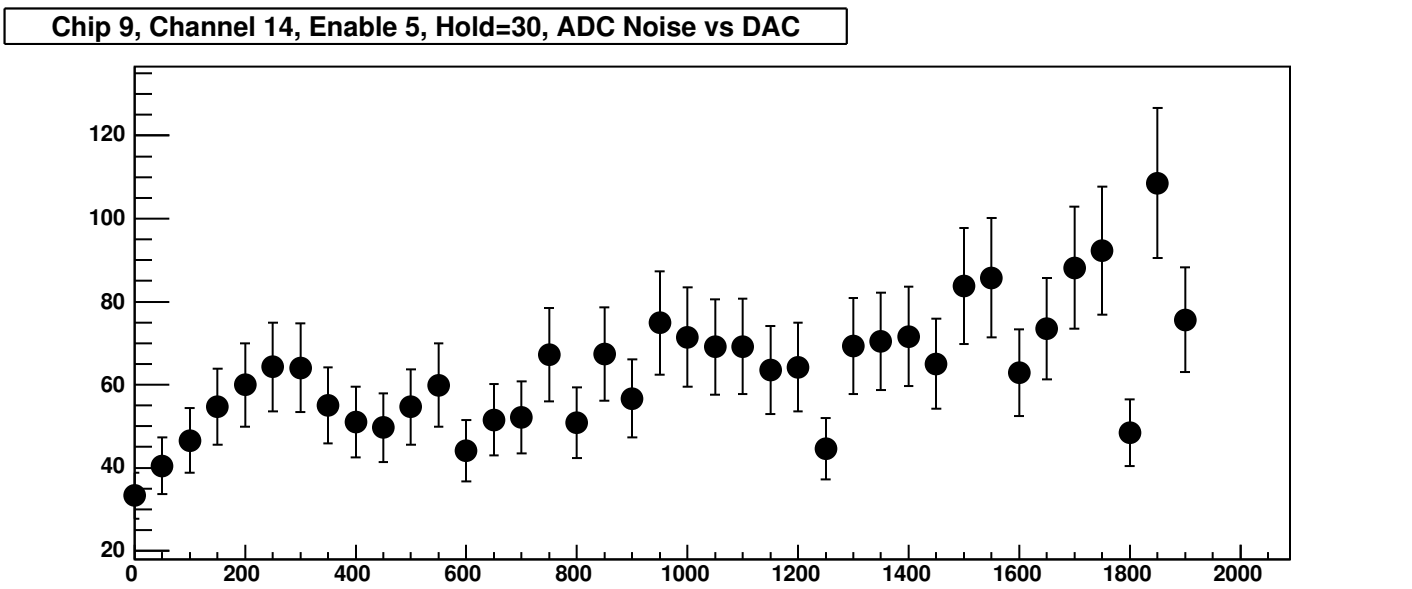

Chip 9, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

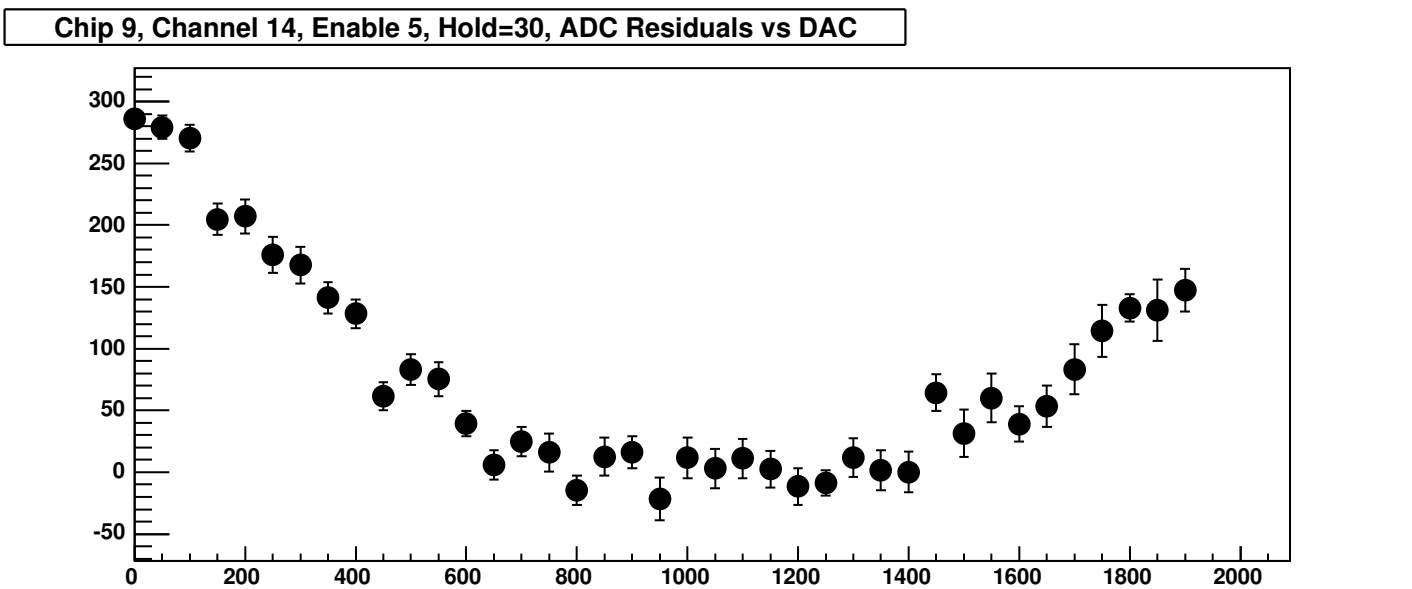

Chip 9, Channel 14, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 41.08 / 11
p0 -1555 ± 22.06
p1 18.1 ± 0.0197
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

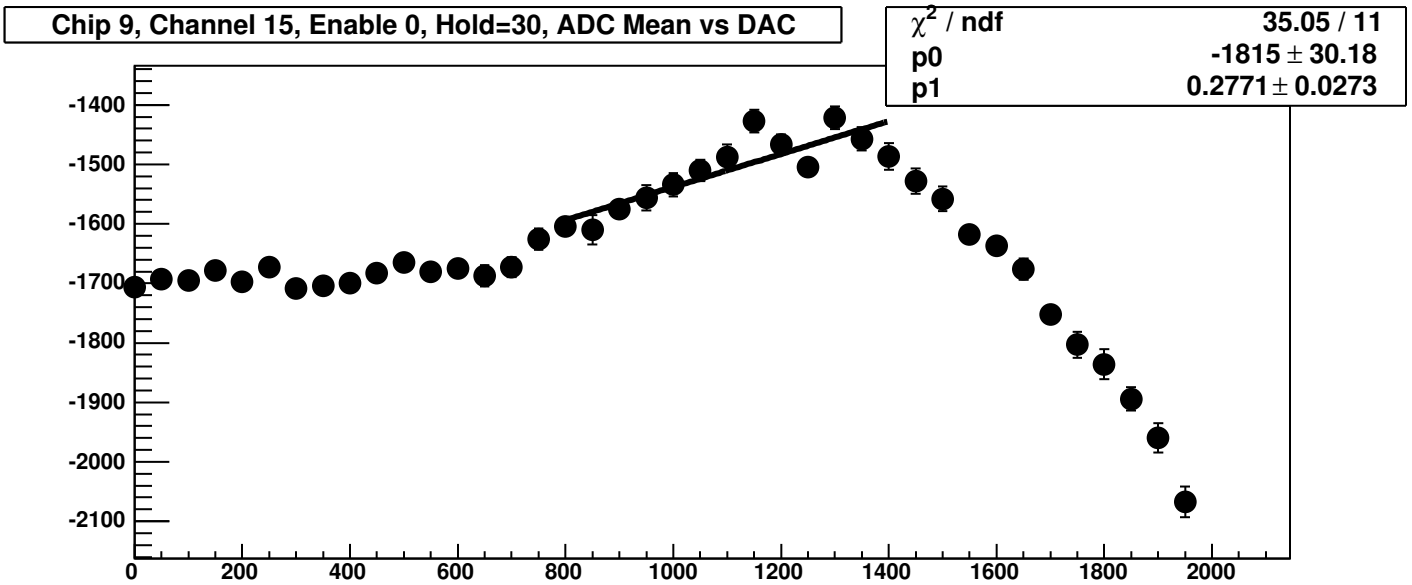

Chip 9, Channel 14, Enable 4!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

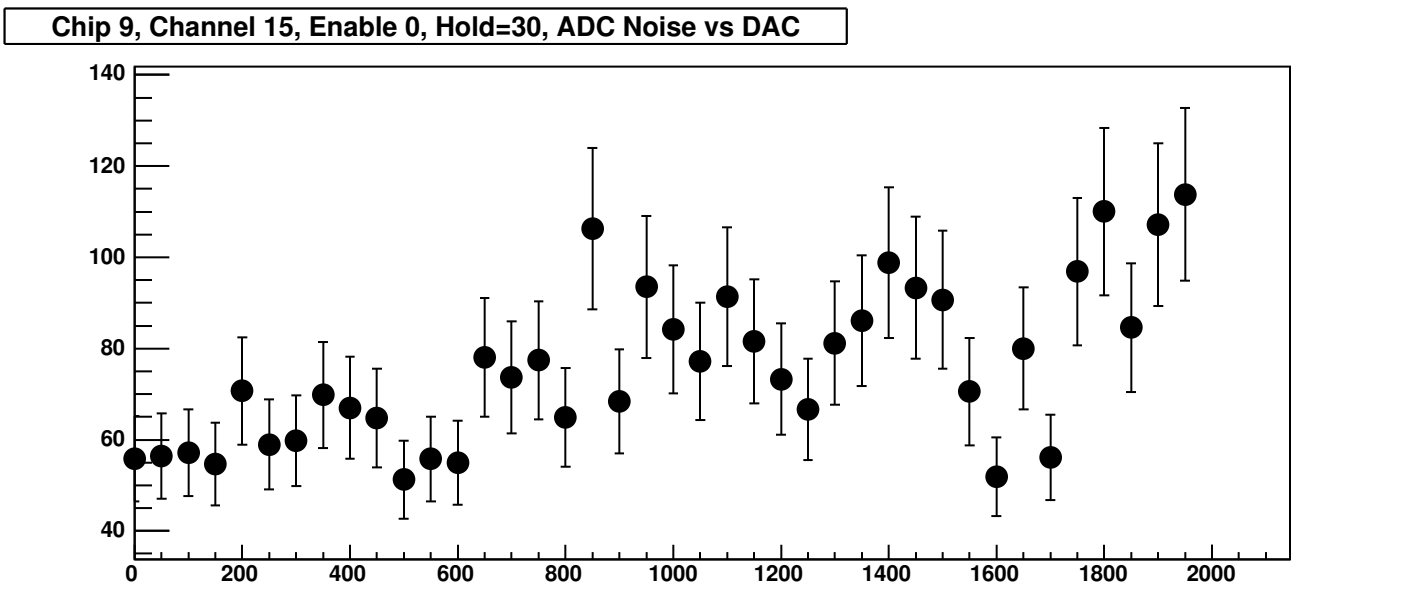

Chip 9, Channel 14, Enable 4!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

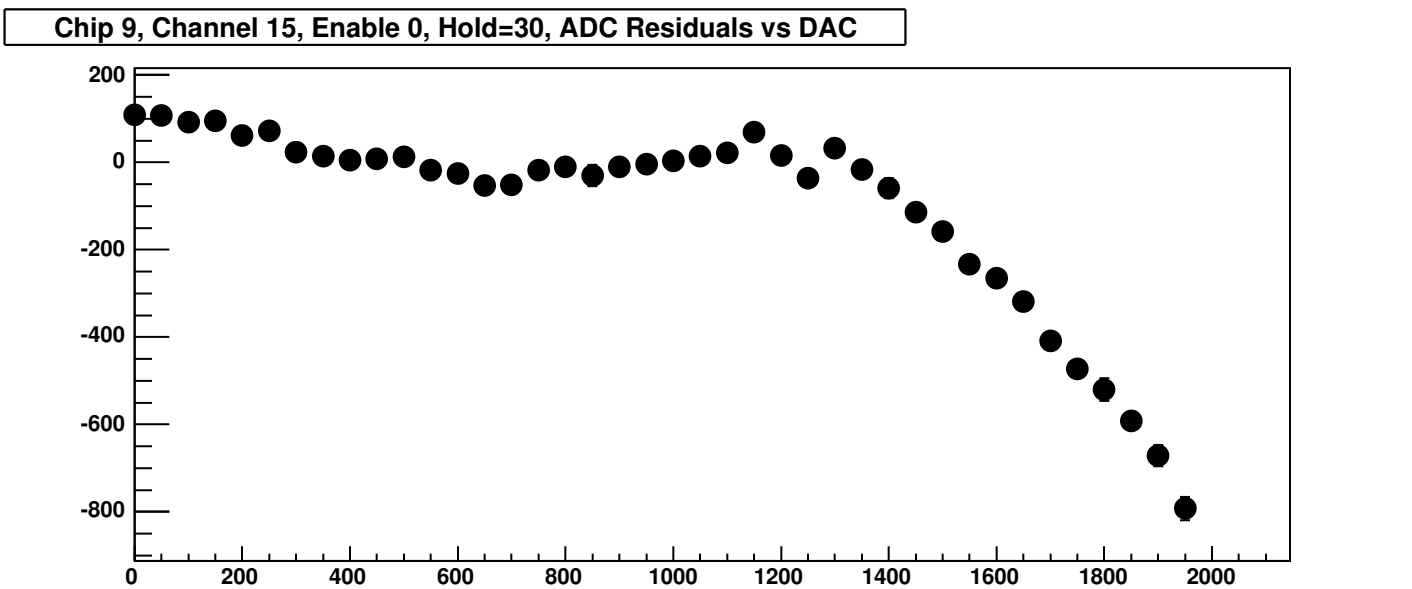

Chip 9, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8.296 / 11
p0 -1669 ± 23.2
p1 0.4351 ± 0.02093
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

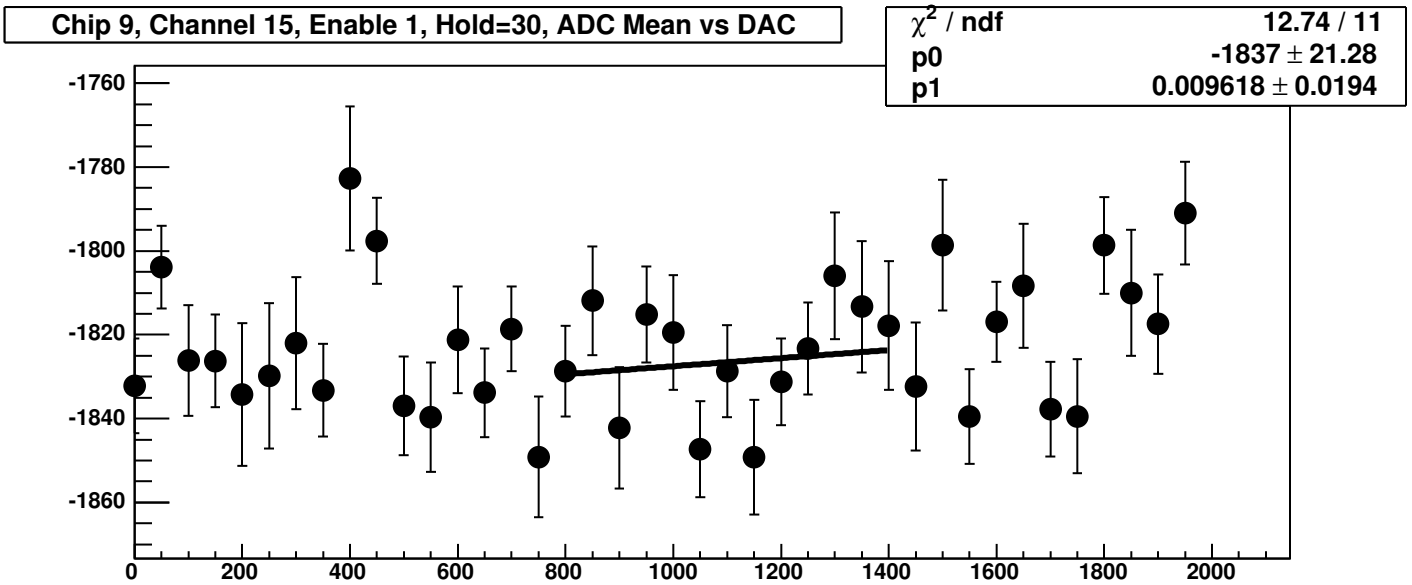

Chip 9, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

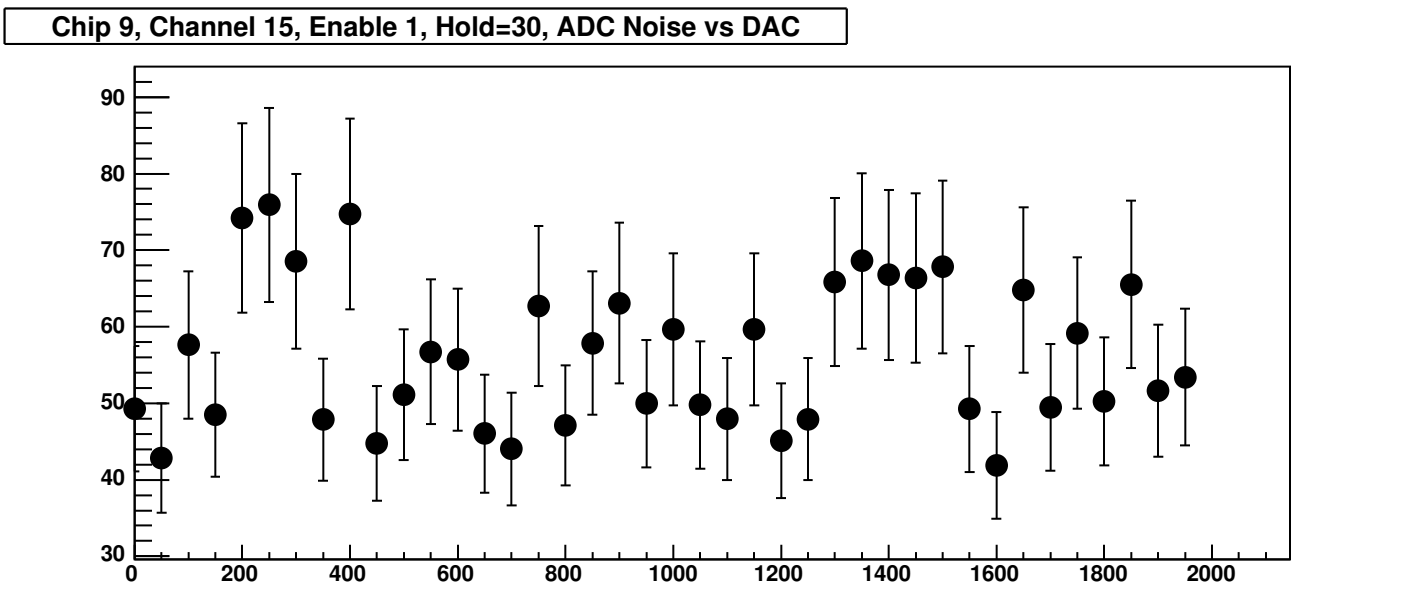

Chip 9, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

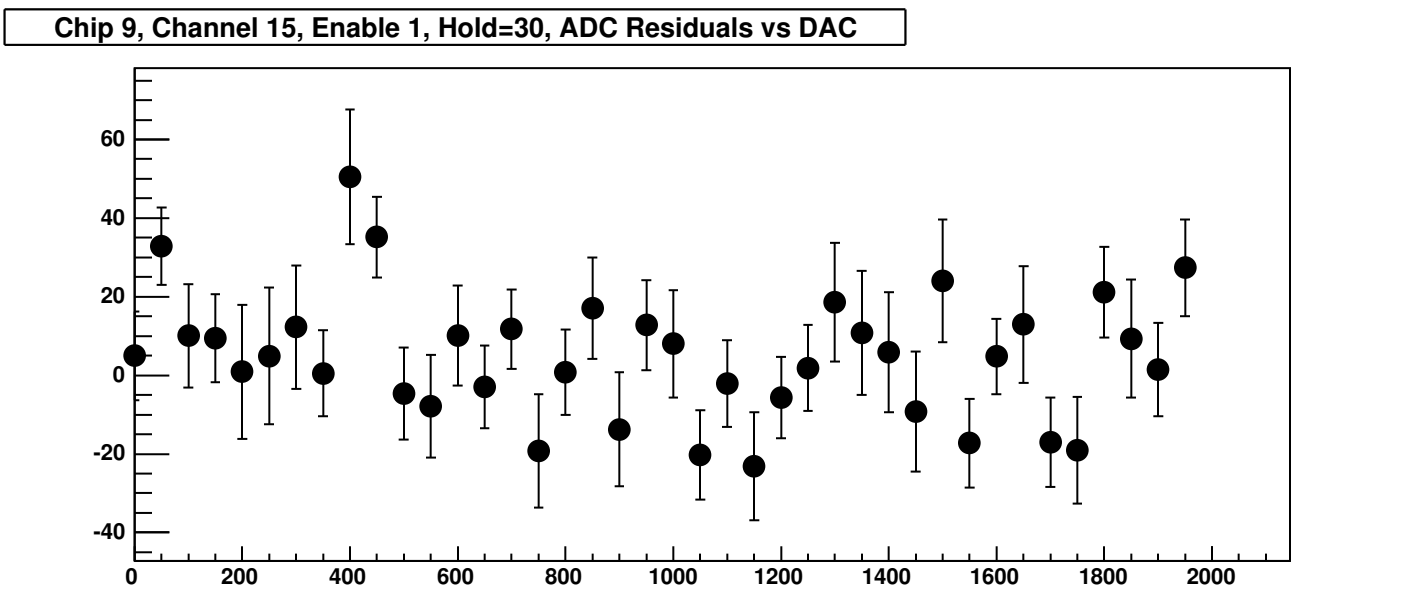

Chip 9, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 35.05 / 11
p0 -1815 ± 30.18
p1 0.2771 ± 0.0273
-1400
-1500
-1600
-1700
-1800
-1900
-2000
-2100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

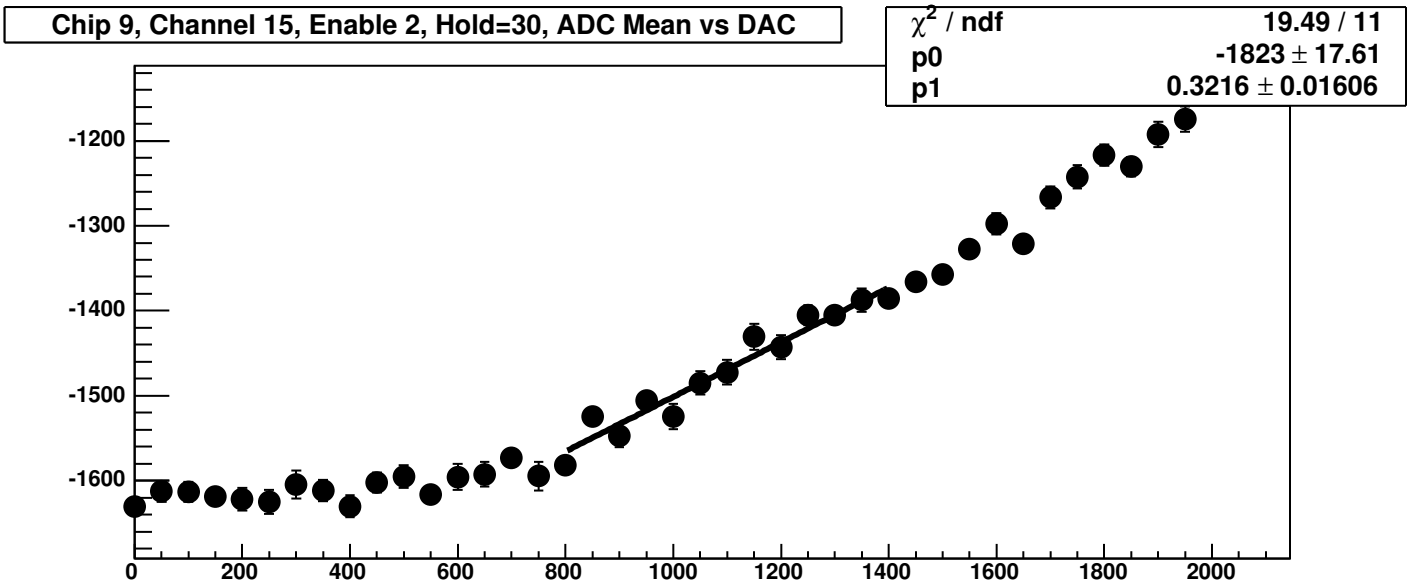

Chip 9, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

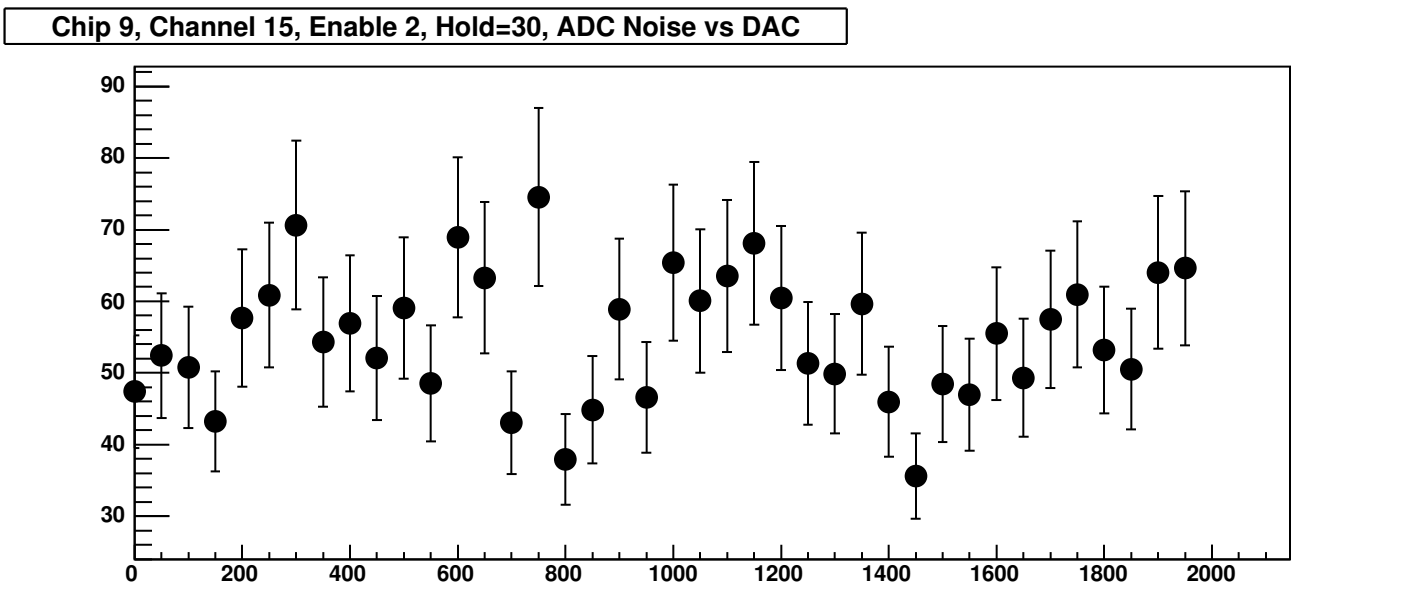

Chip 9, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

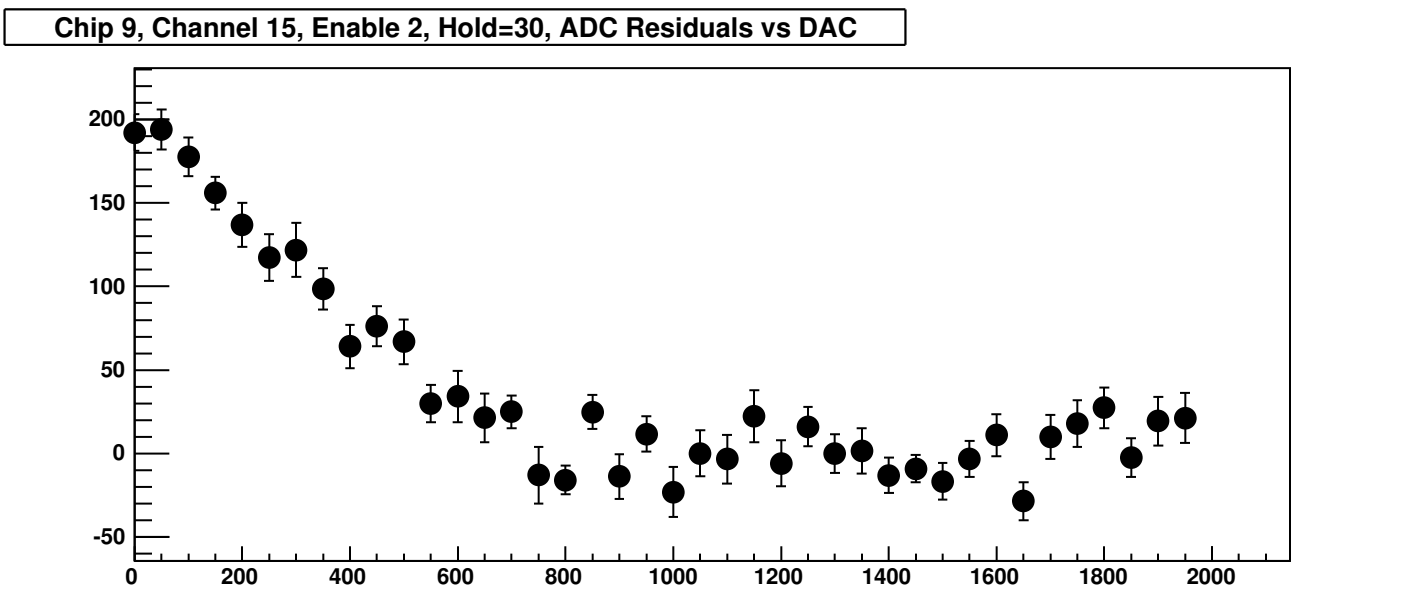

Chip 9, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 12.74 / 11
p0 -1837 ± 21.28
p1 0.009618 ± 0.0194
-1760
-1780
-1800
-1820
-1840
-1860
0
200
400
600
800
1000
1200
1400
1600
1800
2000

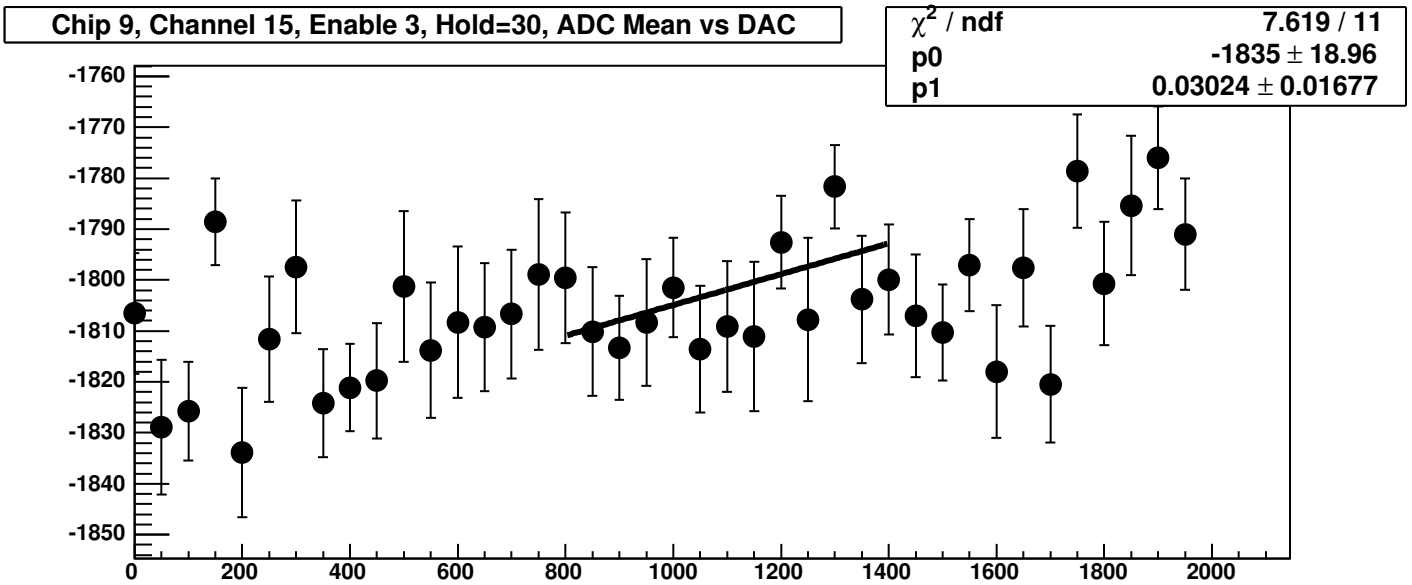

Chip 9, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

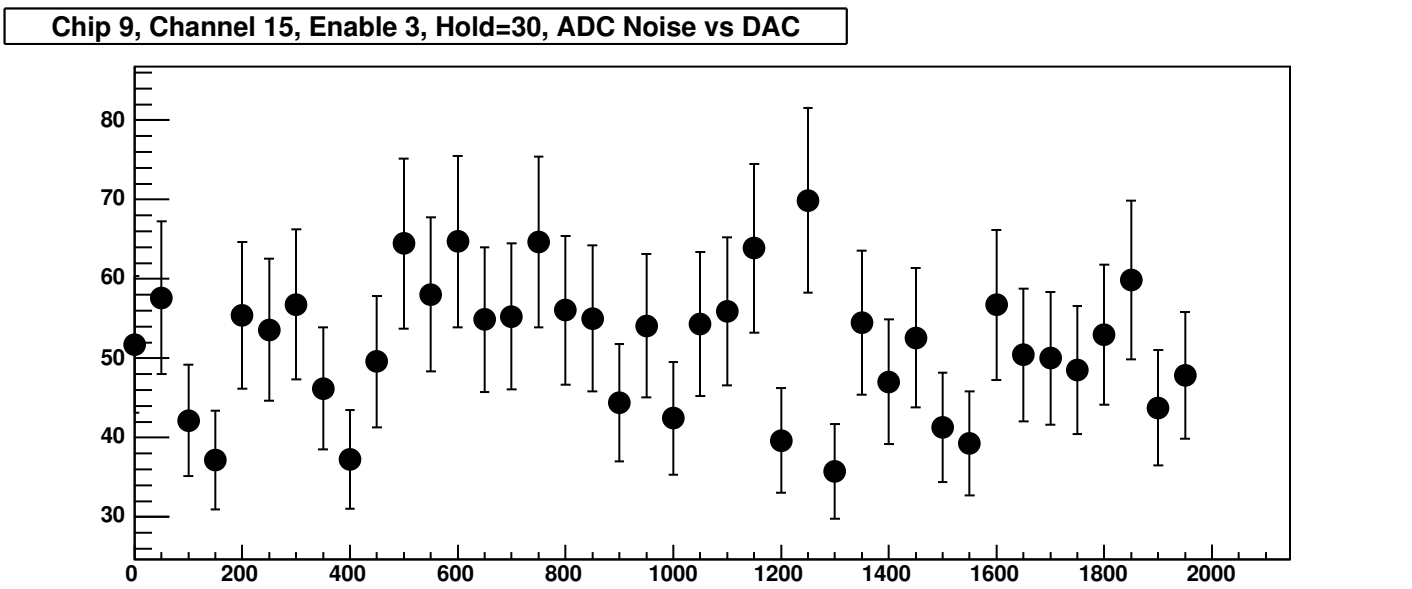

Chip 9, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

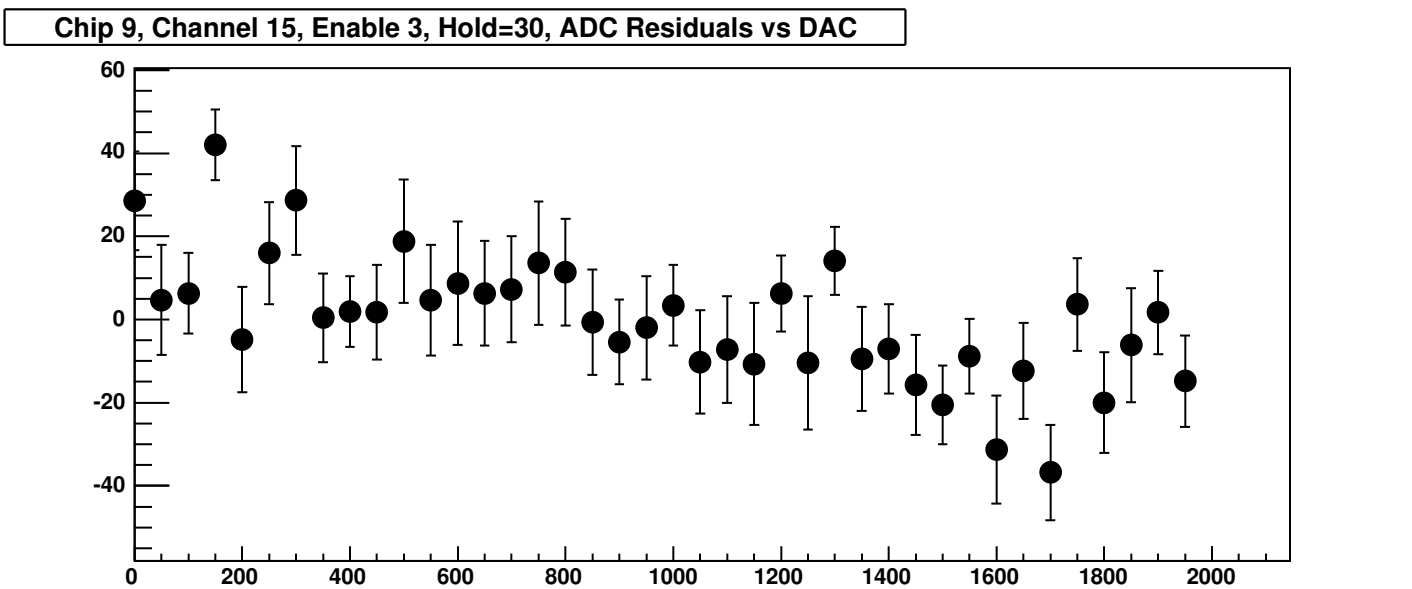

Chip 9, Channel 15, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 19.49 / 11
p0 -1823 ± 17.61
p1 0.3216 ± 0.01606
-1200
-1300
-1400
-1500
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

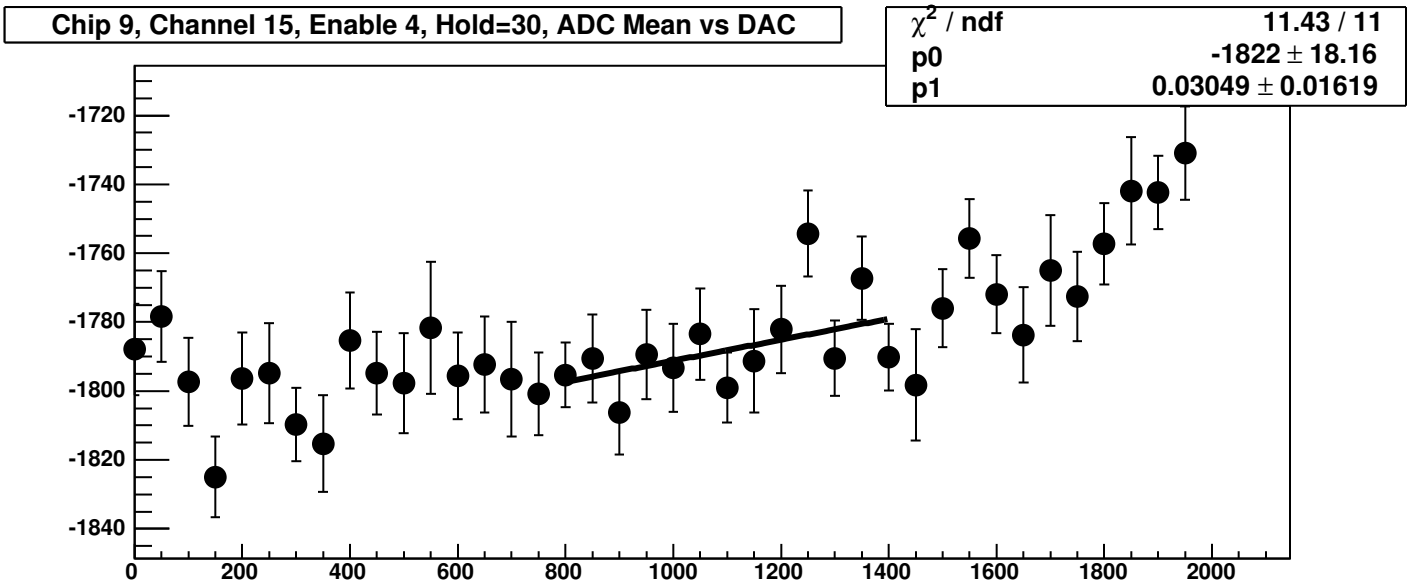

Chip 9, Channel 15, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

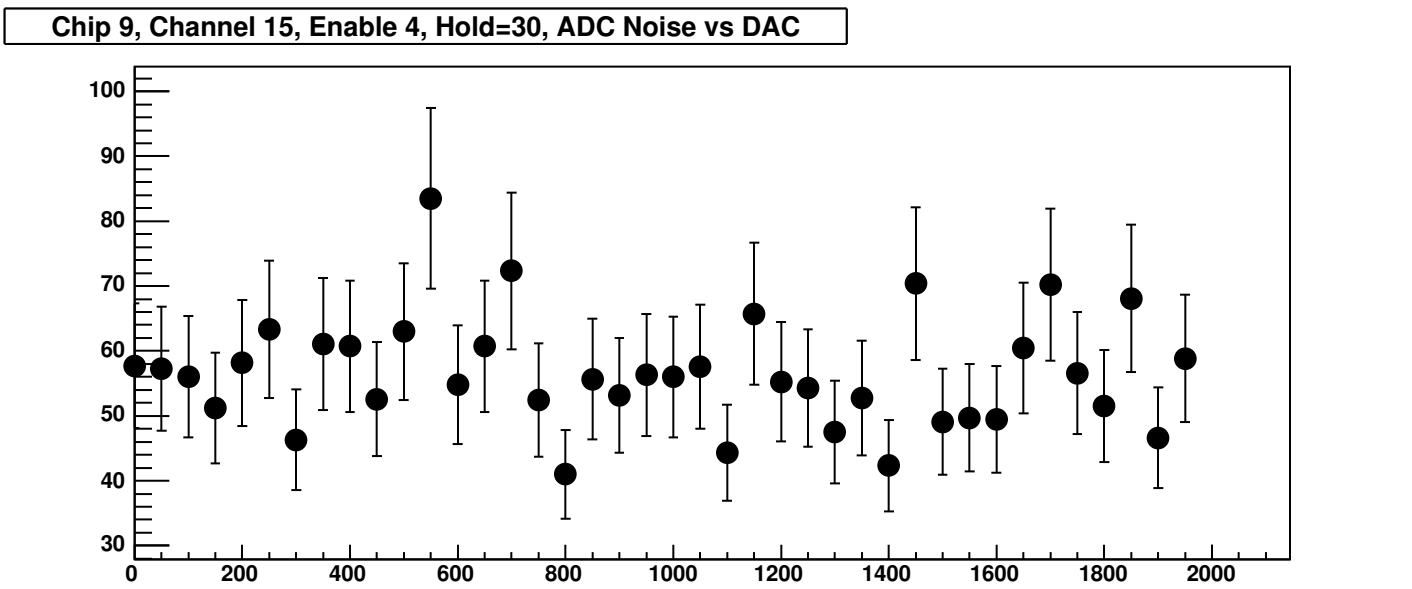

Chip 9, Channel 15, Enable 2, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

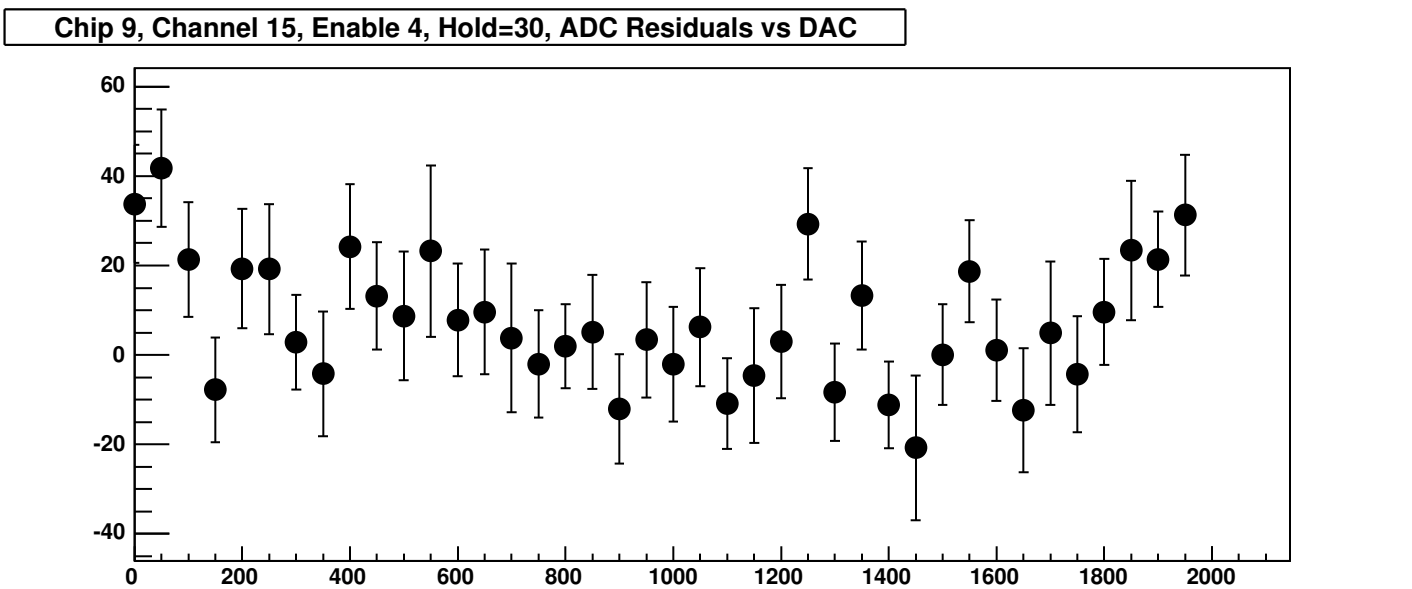

Chip 9, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7.619 / 11
p0 -1835 ± 18.96
p1 0.03024 ± 0.01677
-1760
-1770
-1780
-1790
-1800
-1810
-1820
-1830
-1840
-1850
0
200
400
600
800
1000
1200
1400
1600
1800
2000

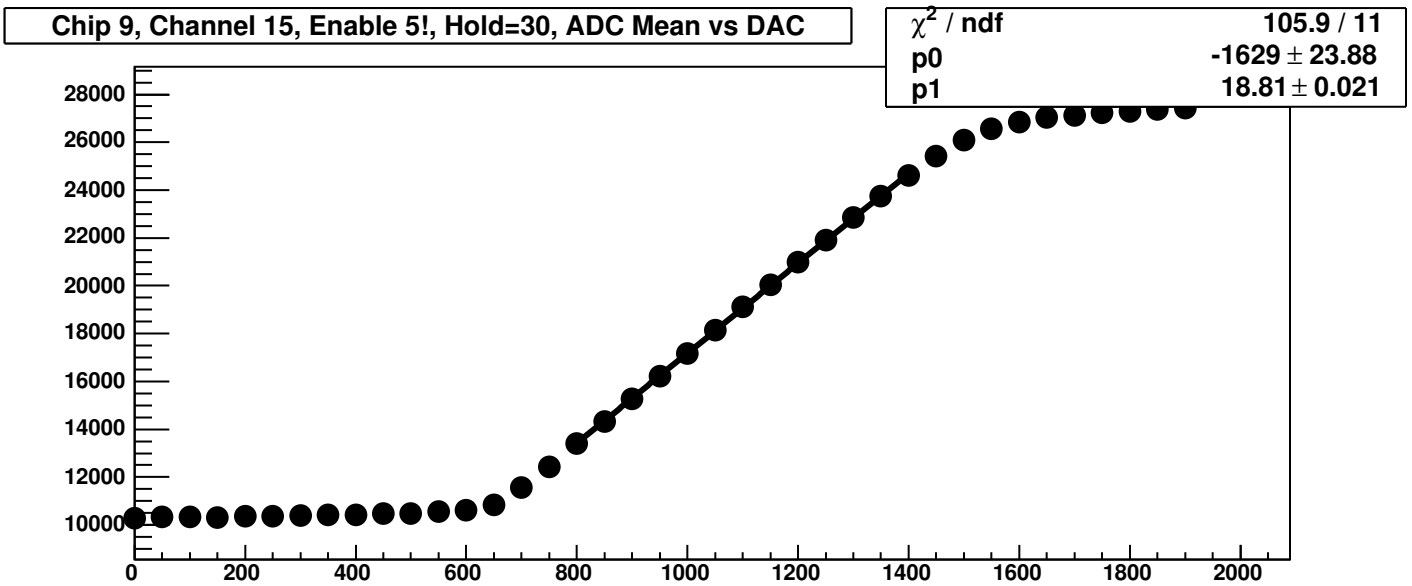

Chip 9, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

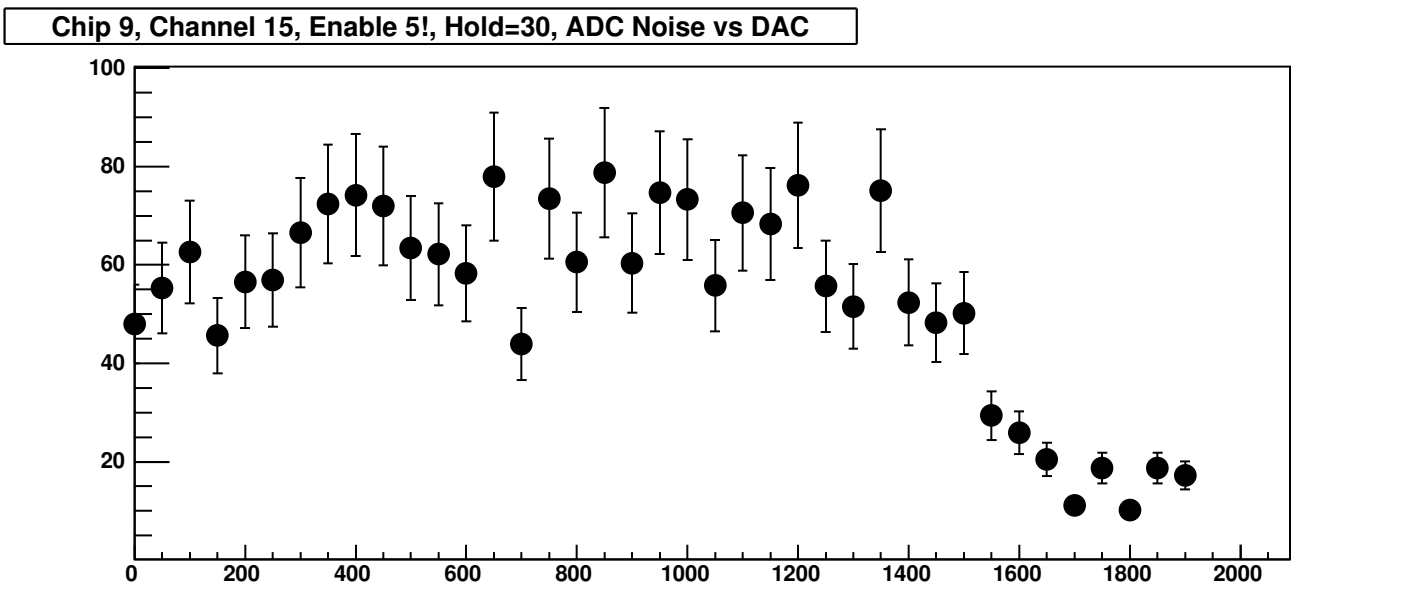

Chip 9, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

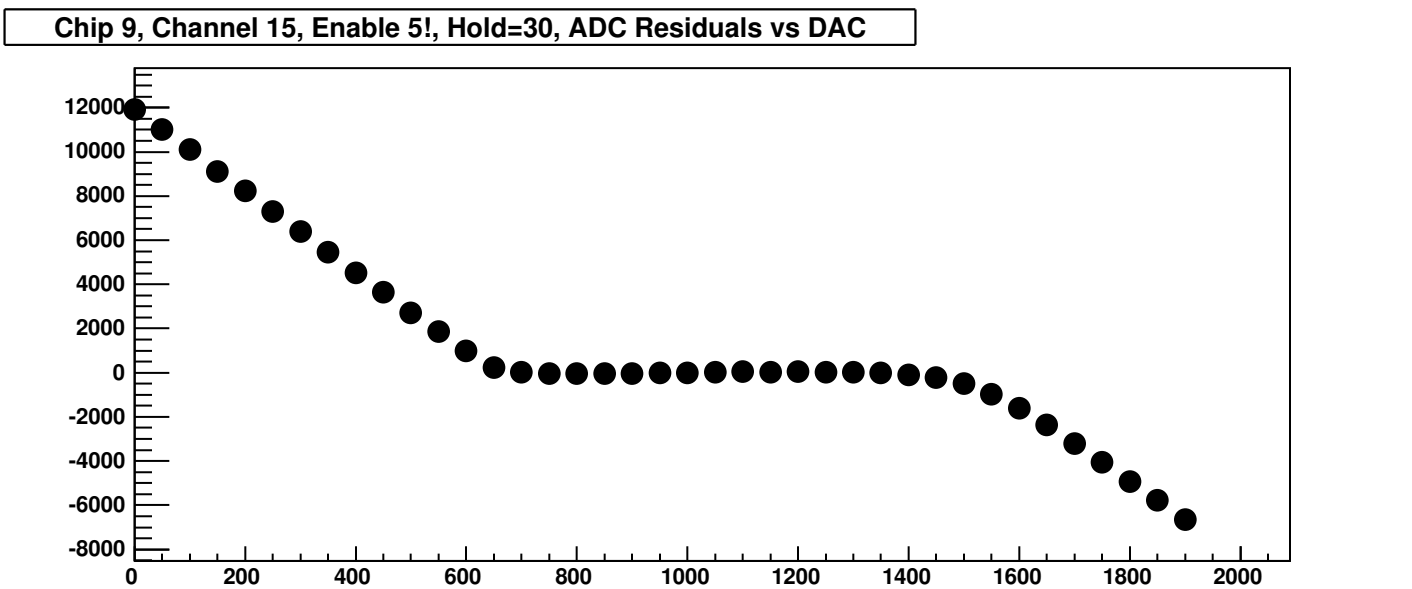

Chip 9, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 11.43 / 11
p0 -1822 ± 18.16
p1 0.03049 ± 0.01619
-1720
-1740
-1760
-1780
-1800
-1820
-1840
0
200
400
600
800
1000
1200
1400
1600
1800
2000

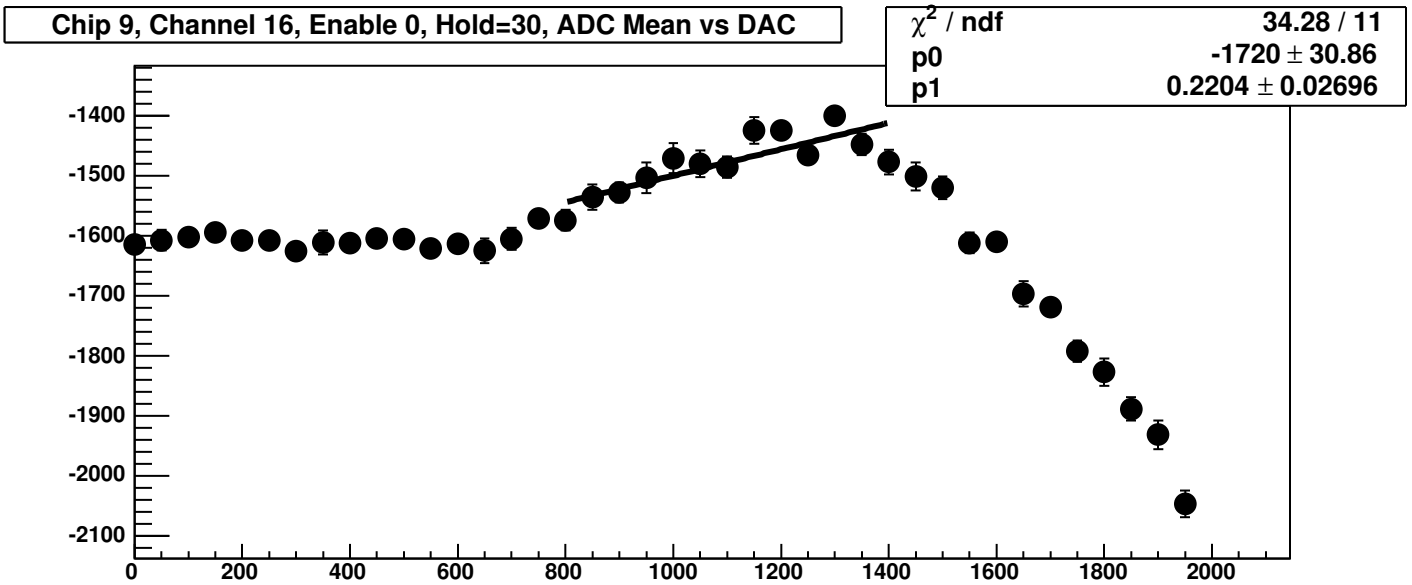

Chip 9, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

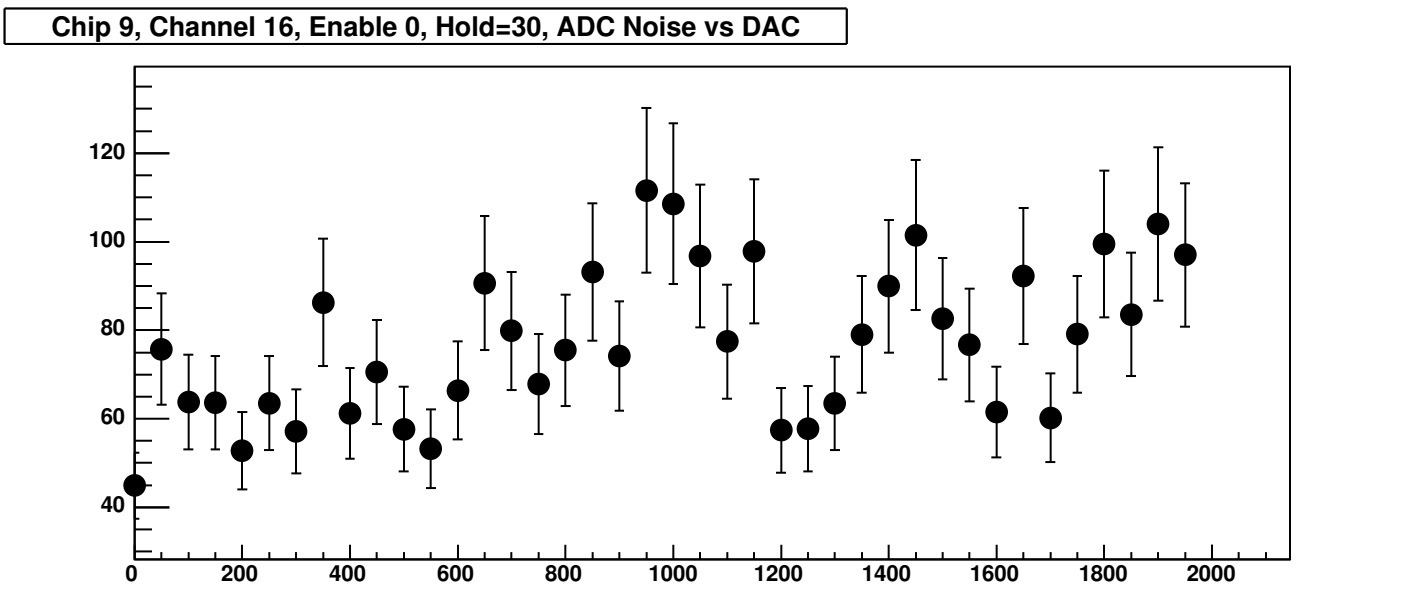

Chip 9, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

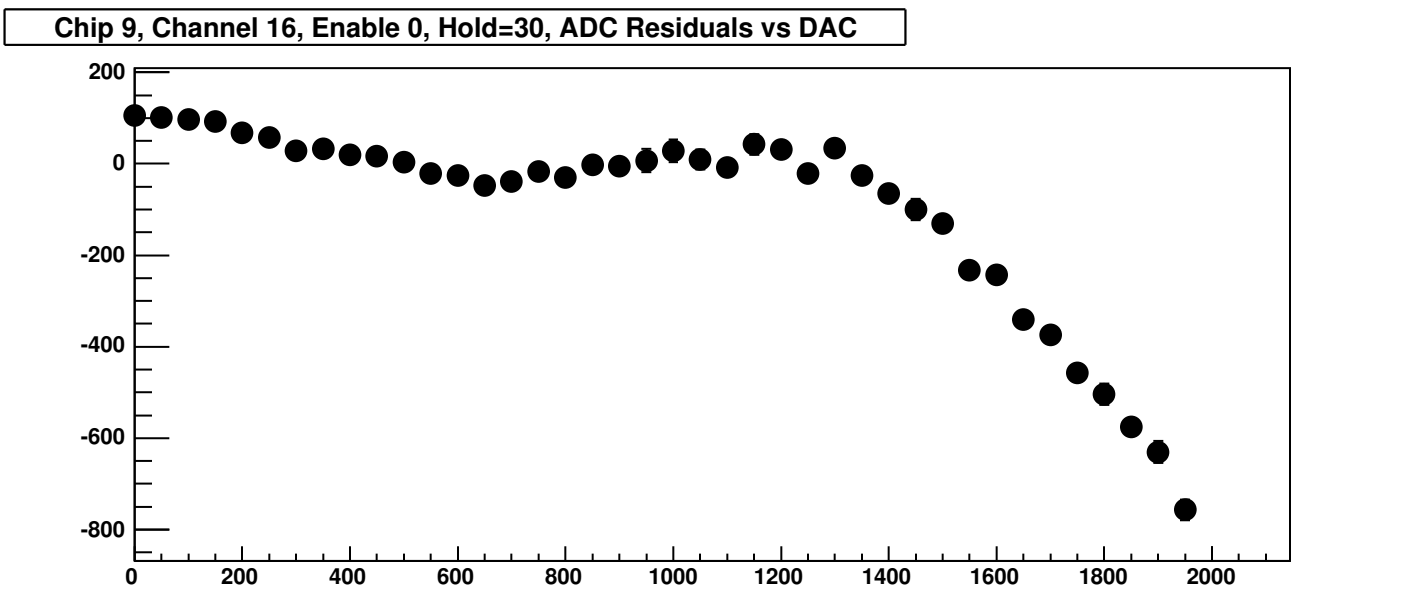

Chip 9, Channel 15, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 105.9 / 11
p0 -1629 ± 23.88
p1 18.81 ± 0.021
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

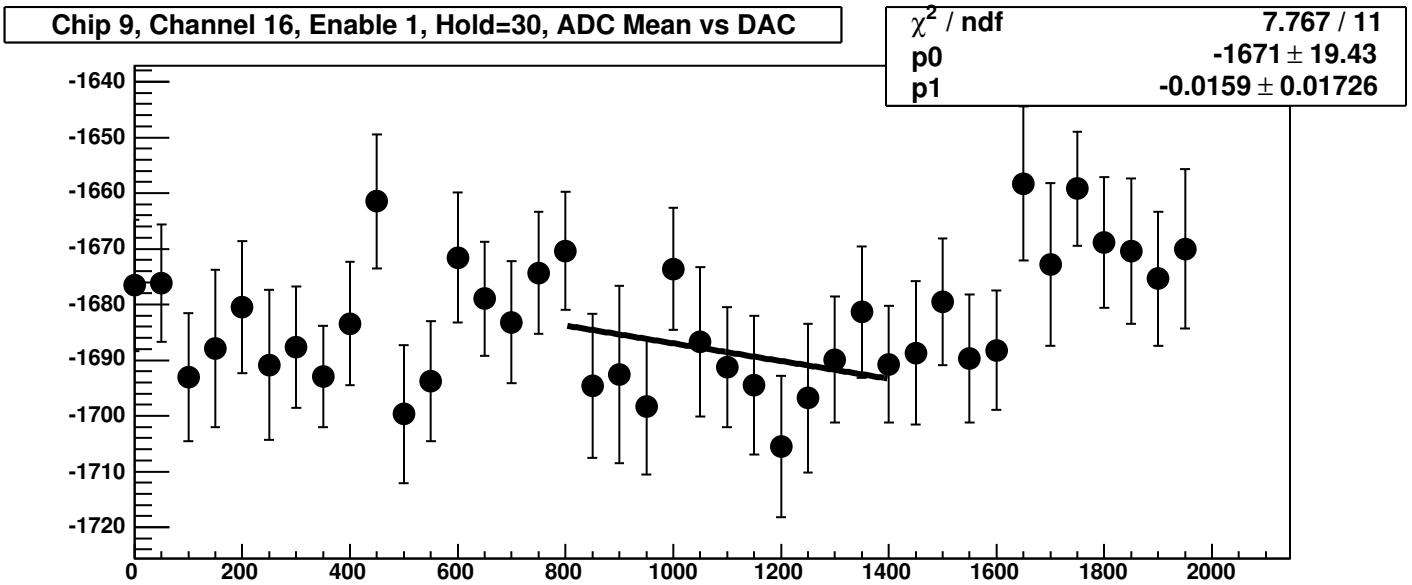

Chip 9, Channel 15, Enable 5!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

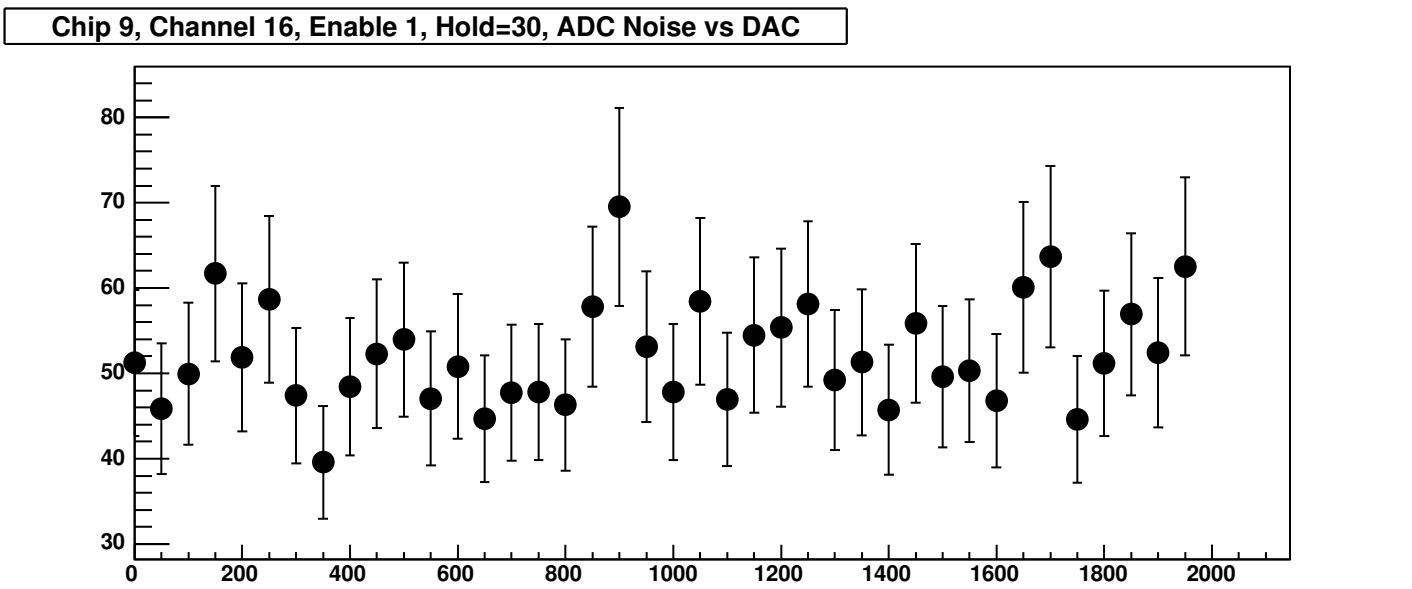

Chip 9, Channel 15, Enable 5!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

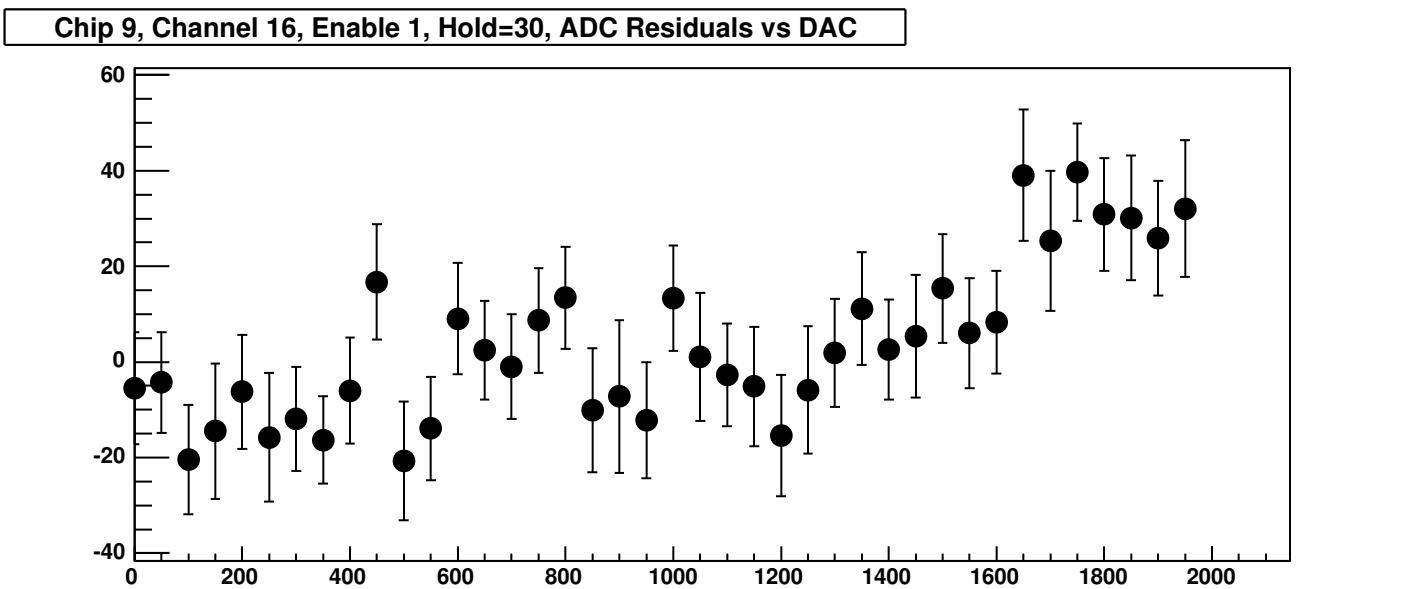

Chip 9, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 34.28 / 11
p0 -1720 ± 30.86
p1 0.2204 ± 0.02696
-1400
-1500
-1600
-1700
-1800
-1900
-2000
-2100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

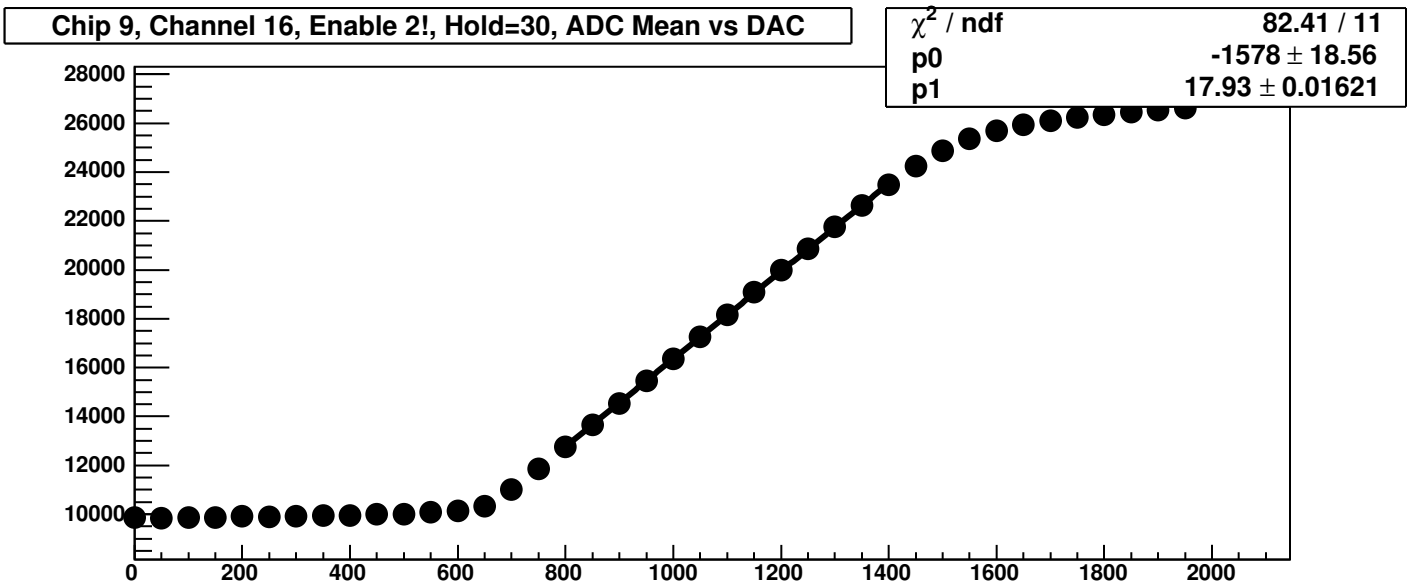

Chip 9, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

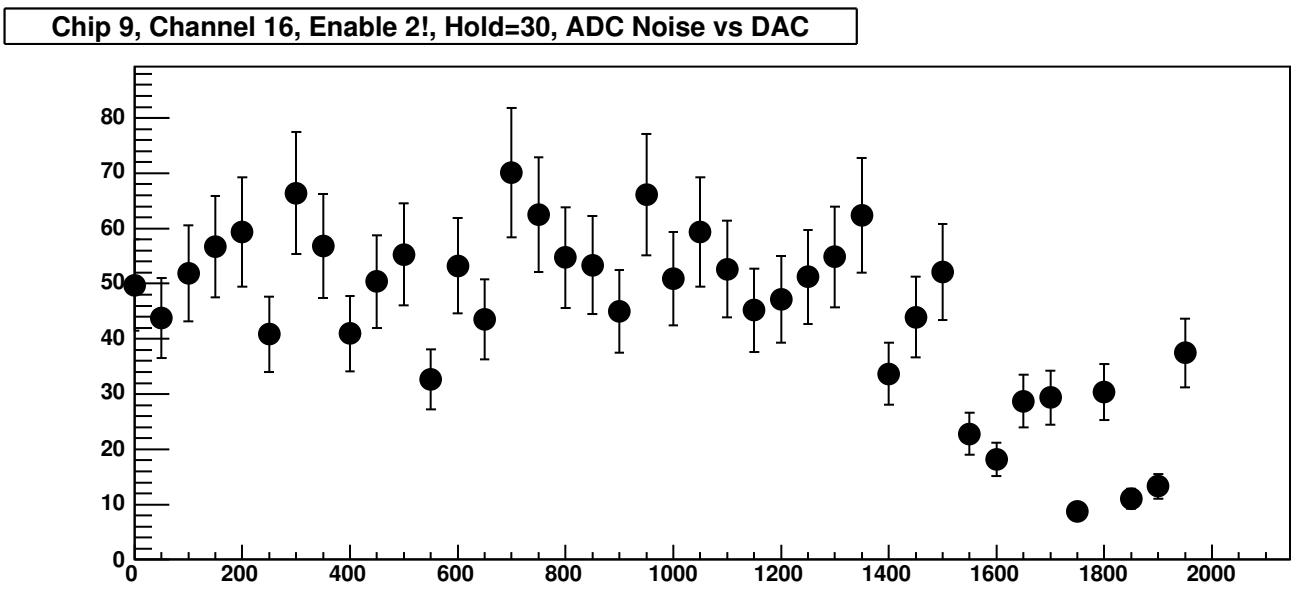

Chip 9, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

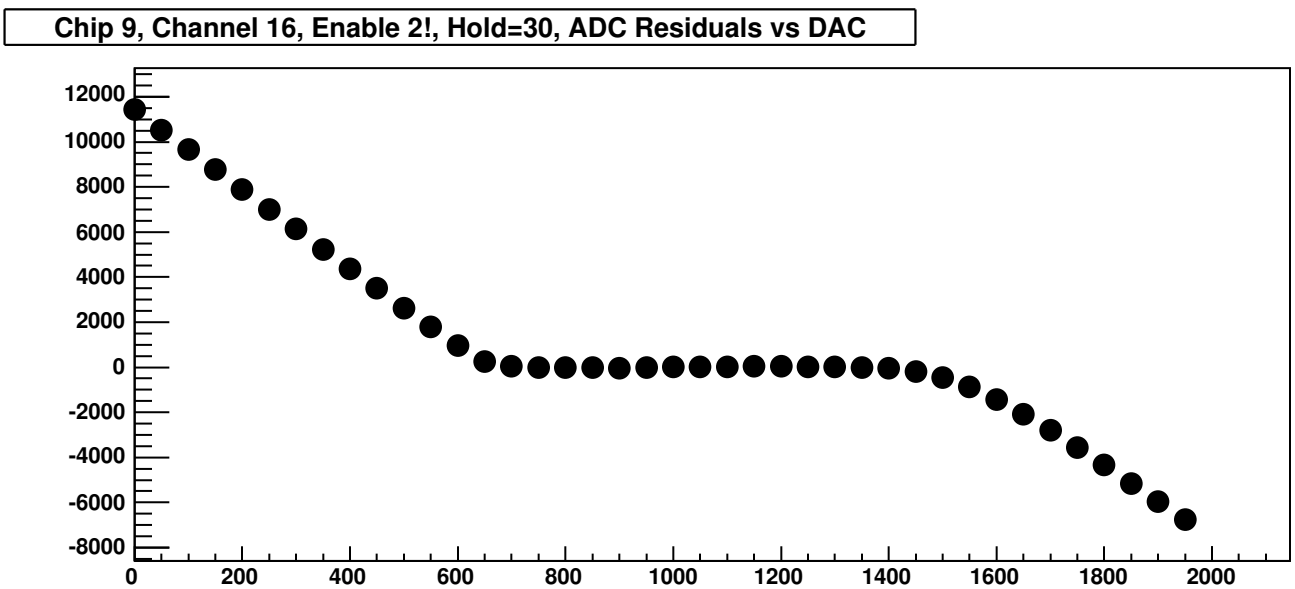

Chip 9, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7.767 / 11
p0 -1671 ± 19.43
p1 -0.0159 ± 0.01726
-1640
-1650
-1660
-1670
-1680
-1690
-1700
-1710
-1720
0
200
400
600
800
1000
1200
1400
1600
1800
2000

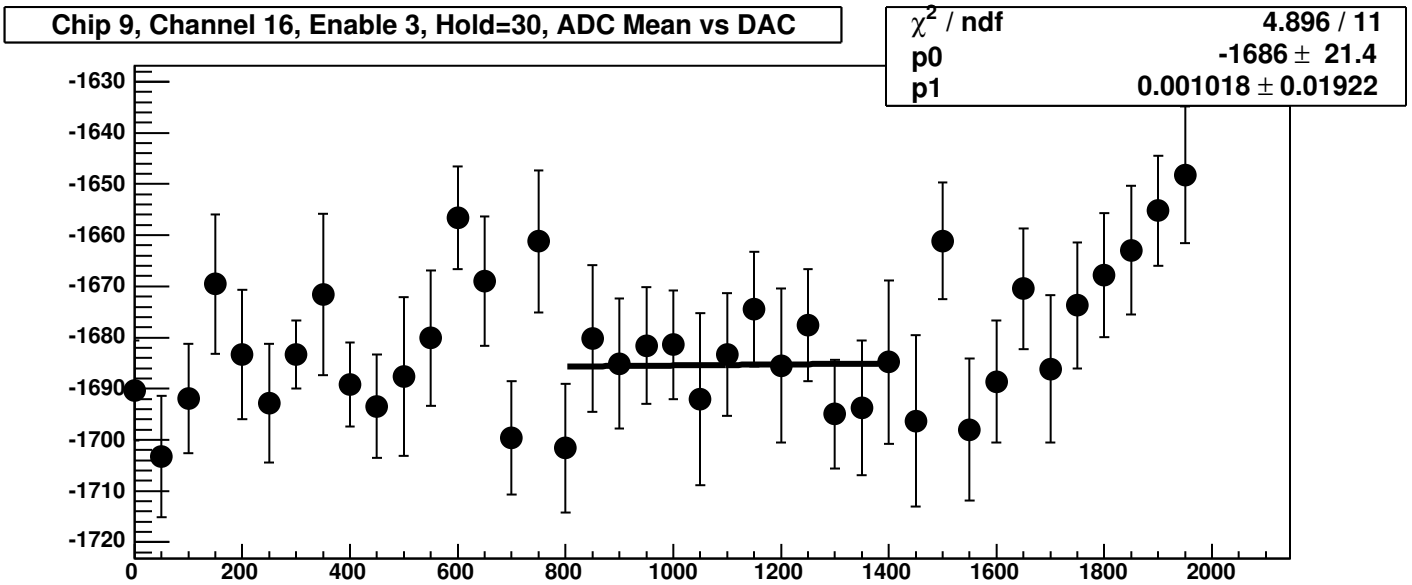

Chip 9, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

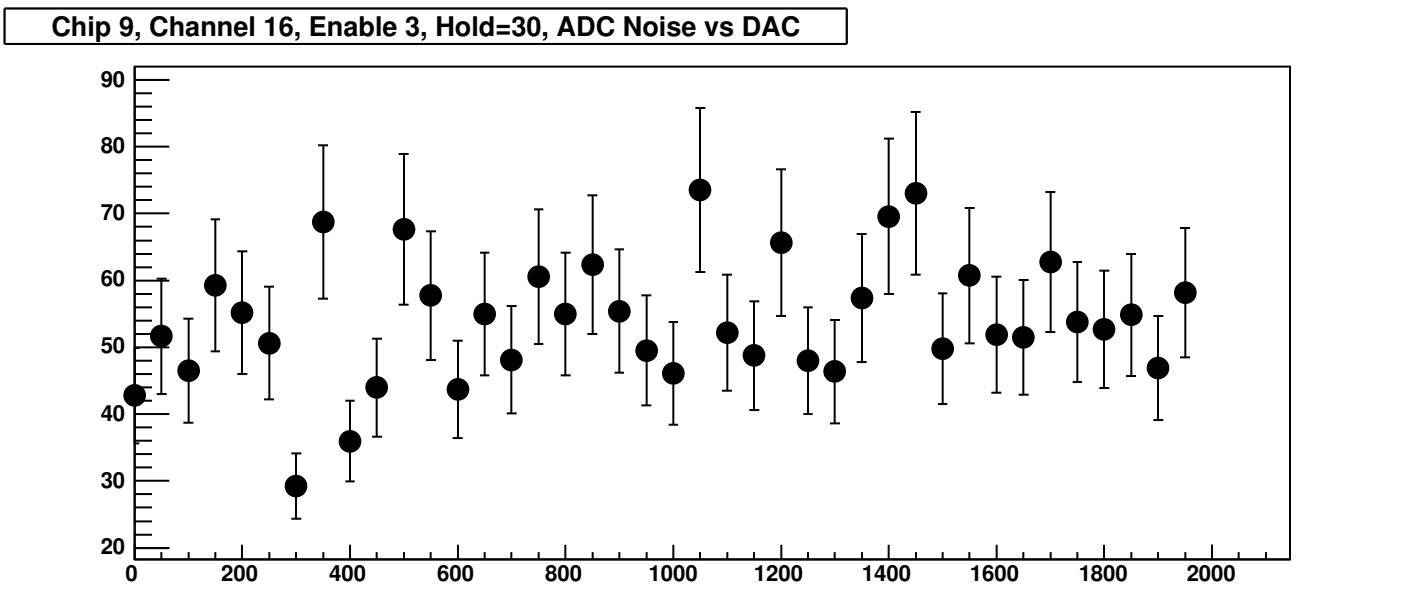

Chip 9, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

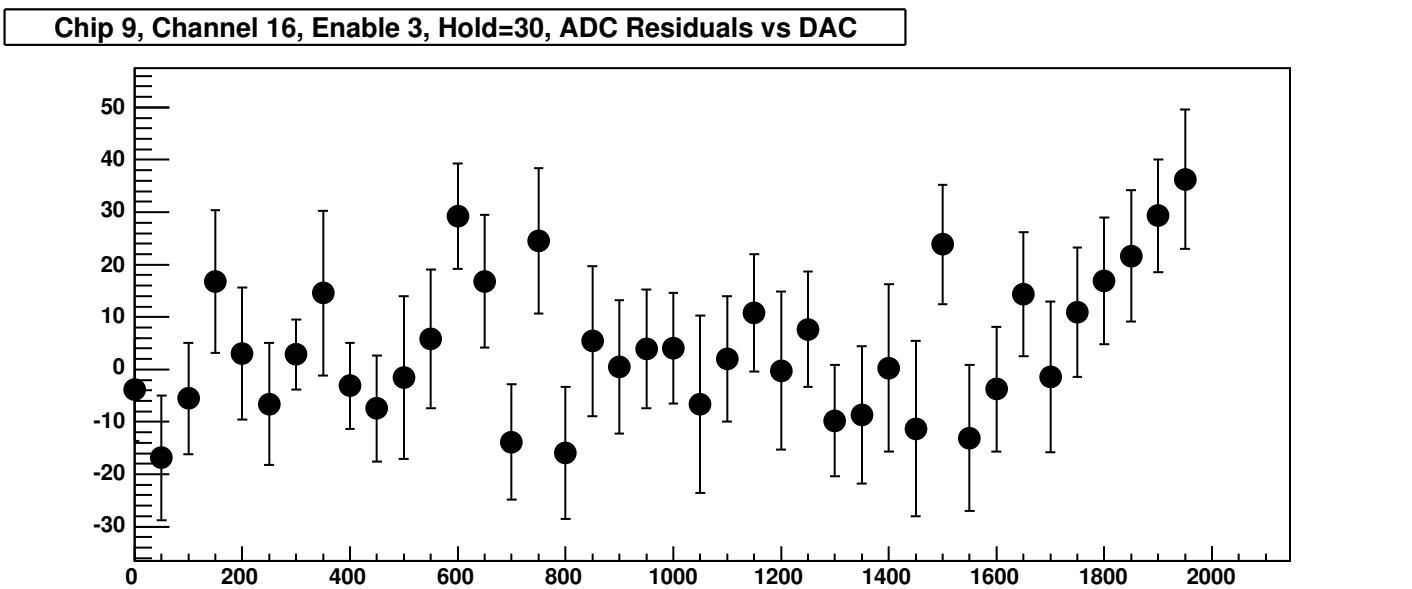

Chip 9, Channel 16, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 82.41 / 11
p0 -1578 ± 18.56
p1 17.93 ± 0.01621
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

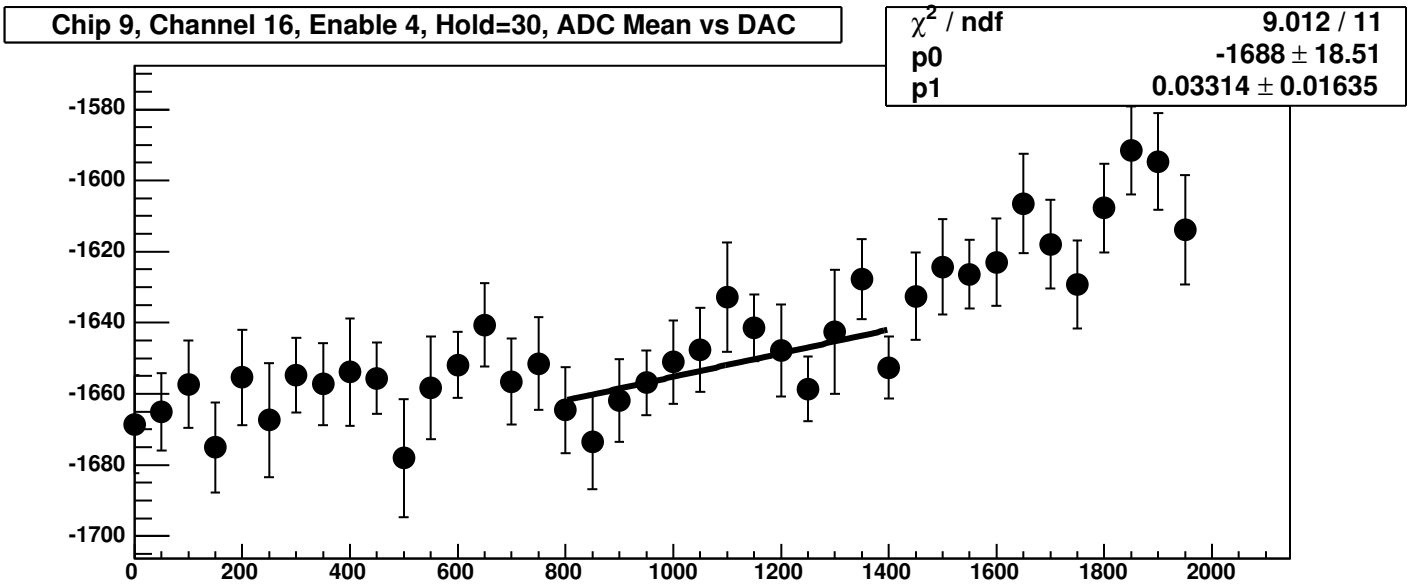

Chip 9, Channel 16, Enable 2!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

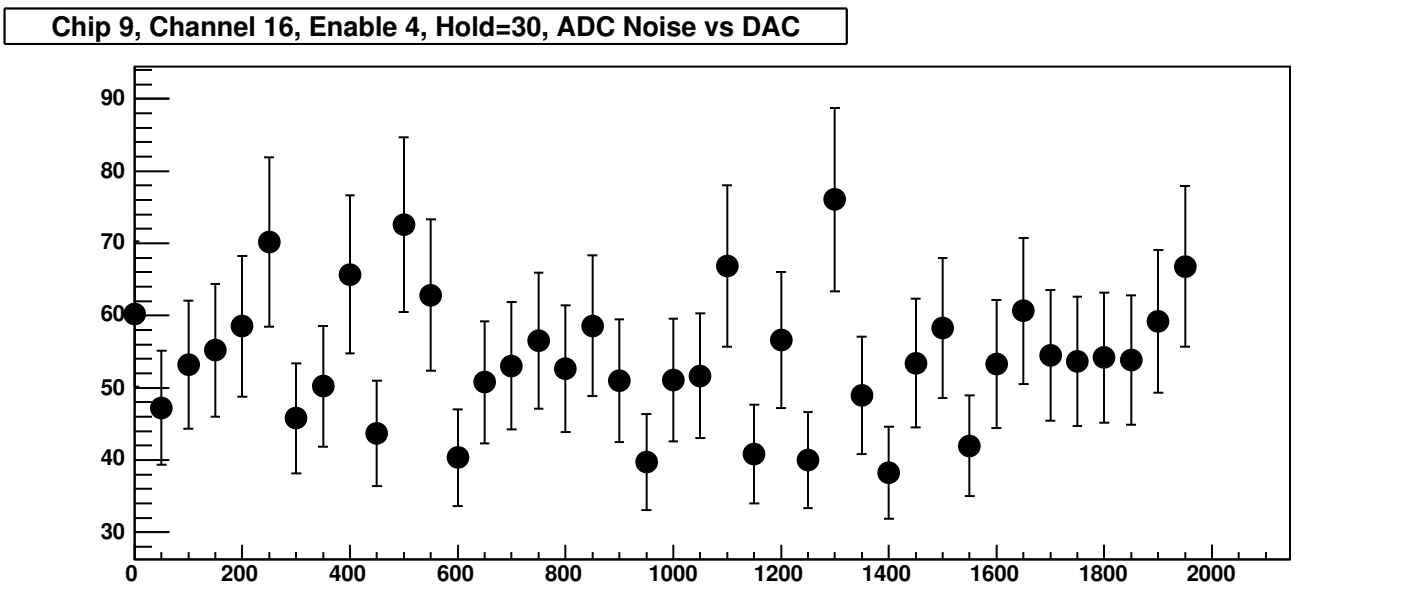

Chip 9, Channel 16, Enable 2!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

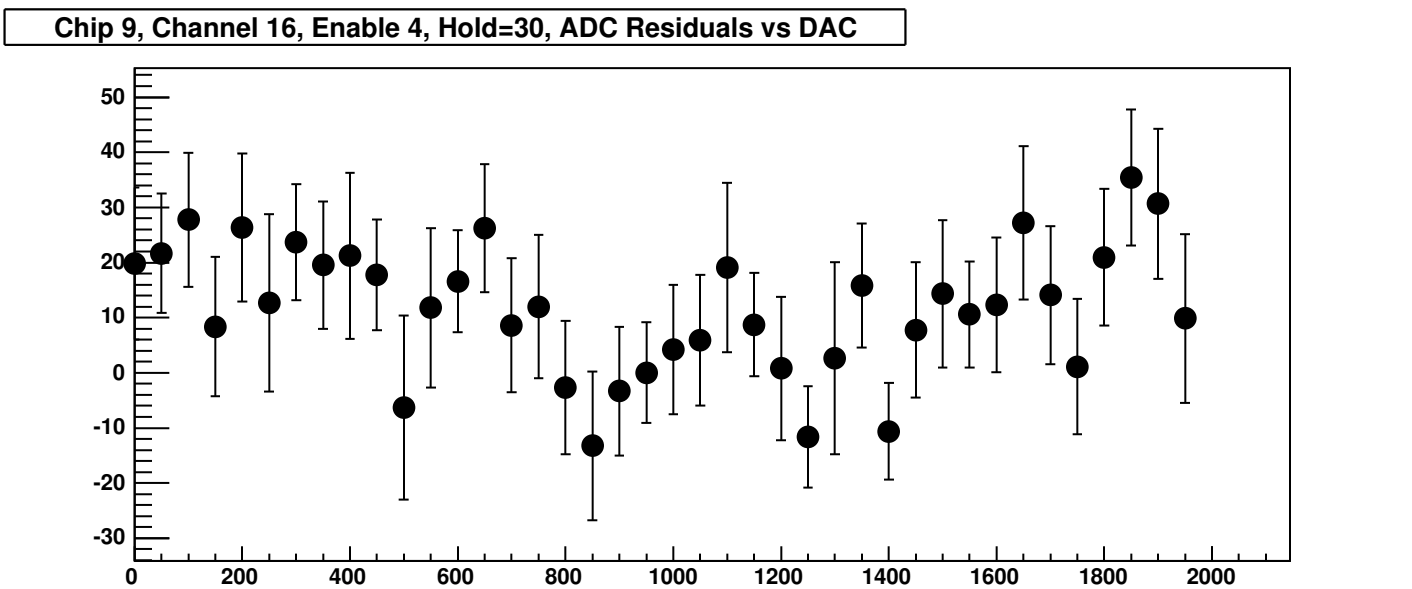

Chip 9, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4.896 / 11
p0 -1686 ± 21.4
p1 0.001018 ± 0.01922
-1630
-1640
-1650
-1660
-1670
-1680
-1690
-1700
-1710
-1720
0
200
400
600
800
1000
1200
1400
1600
1800
2000

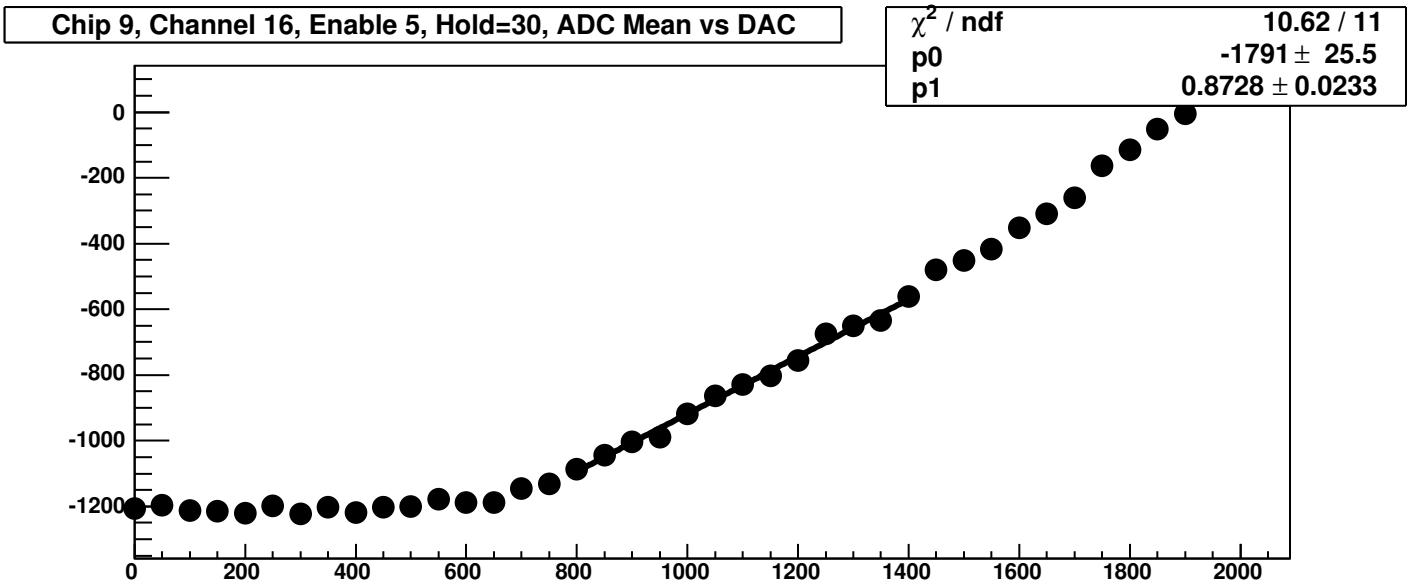

Chip 9, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

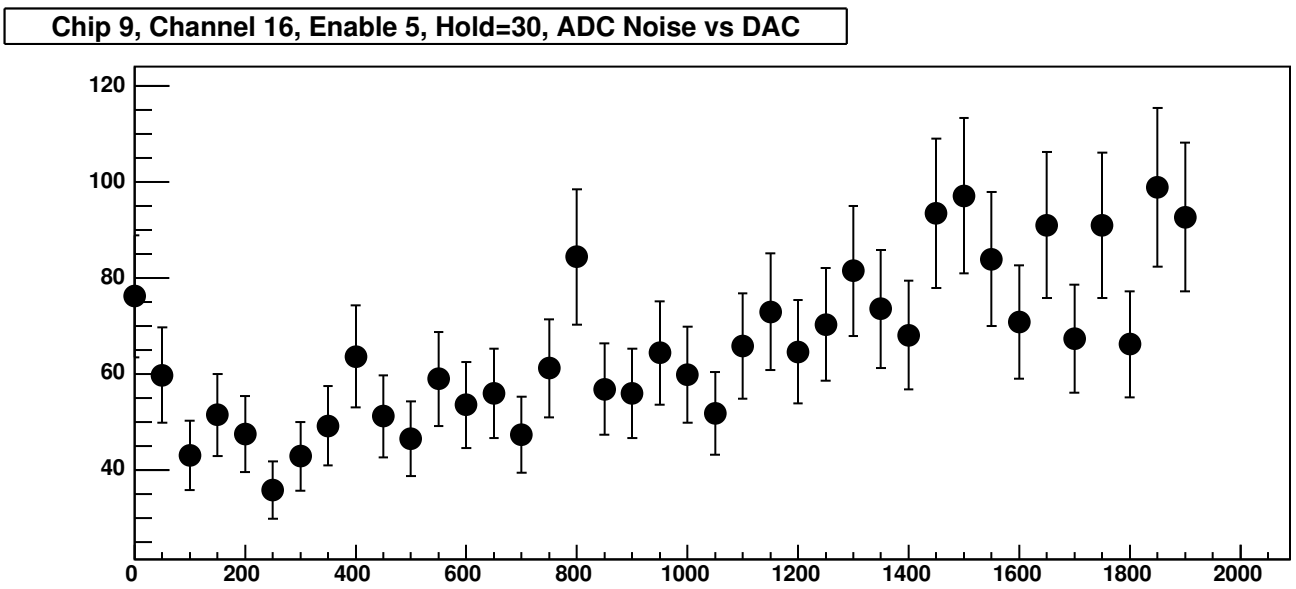

Chip 9, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC
50
40
30
20
10
0
-10
-20
-30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

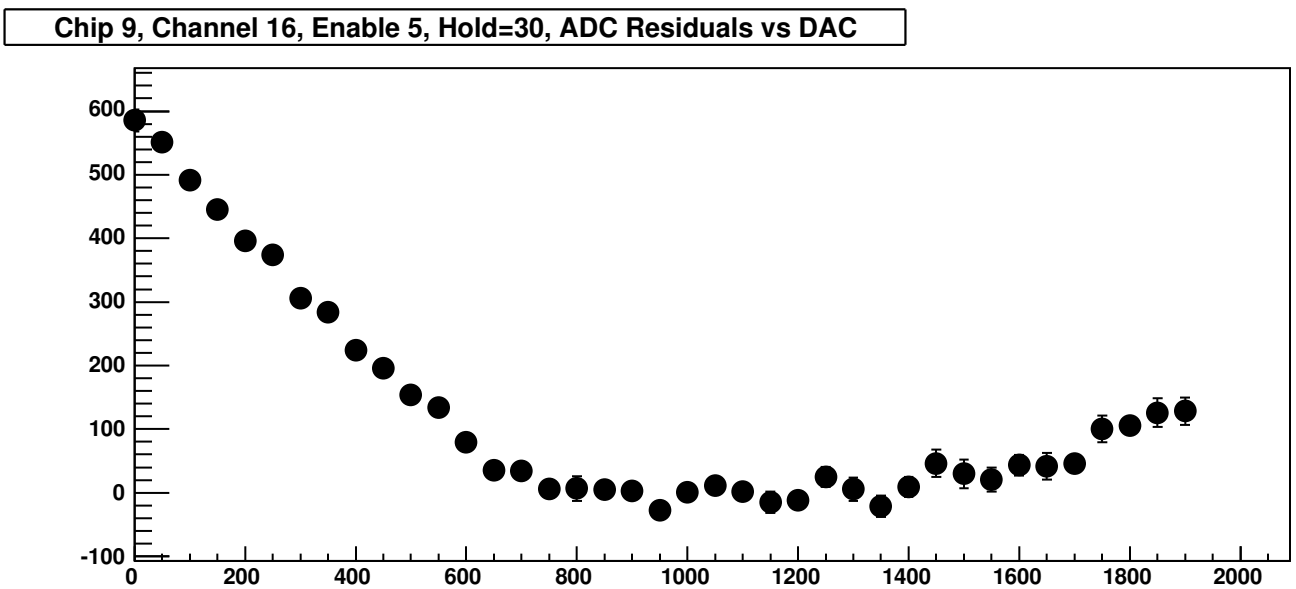

Chip 9, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 9.012 / 11
p0 -1688 ± 18.51
p1 0.03314 ± 0.01635
-1580
-1600
-1620
-1640
-1660
-1680
-1700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

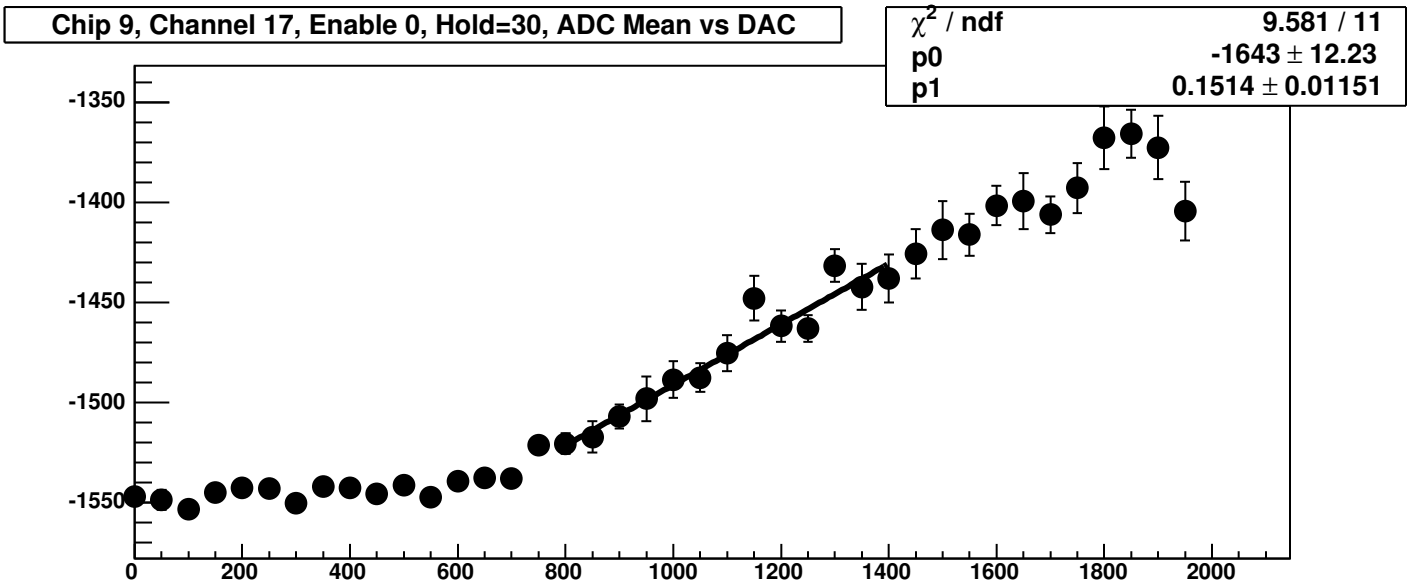

Chip 9, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

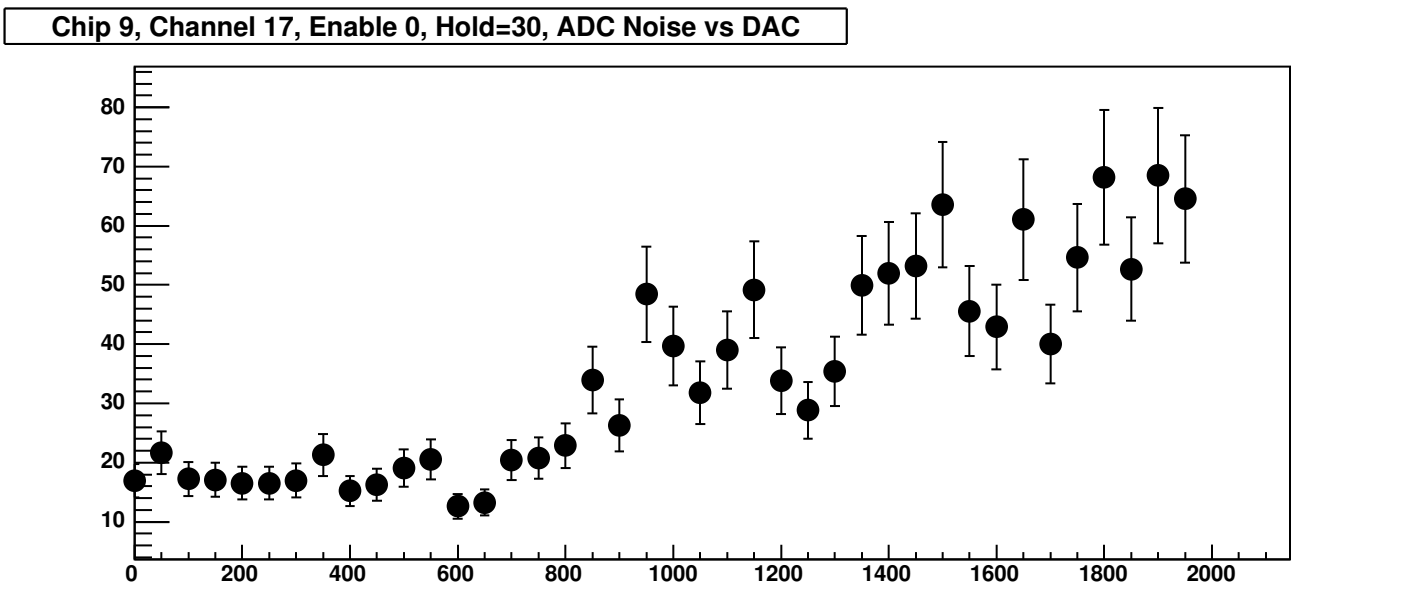

Chip 9, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC
50
40
30
20
10
0
-10
-20
-30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

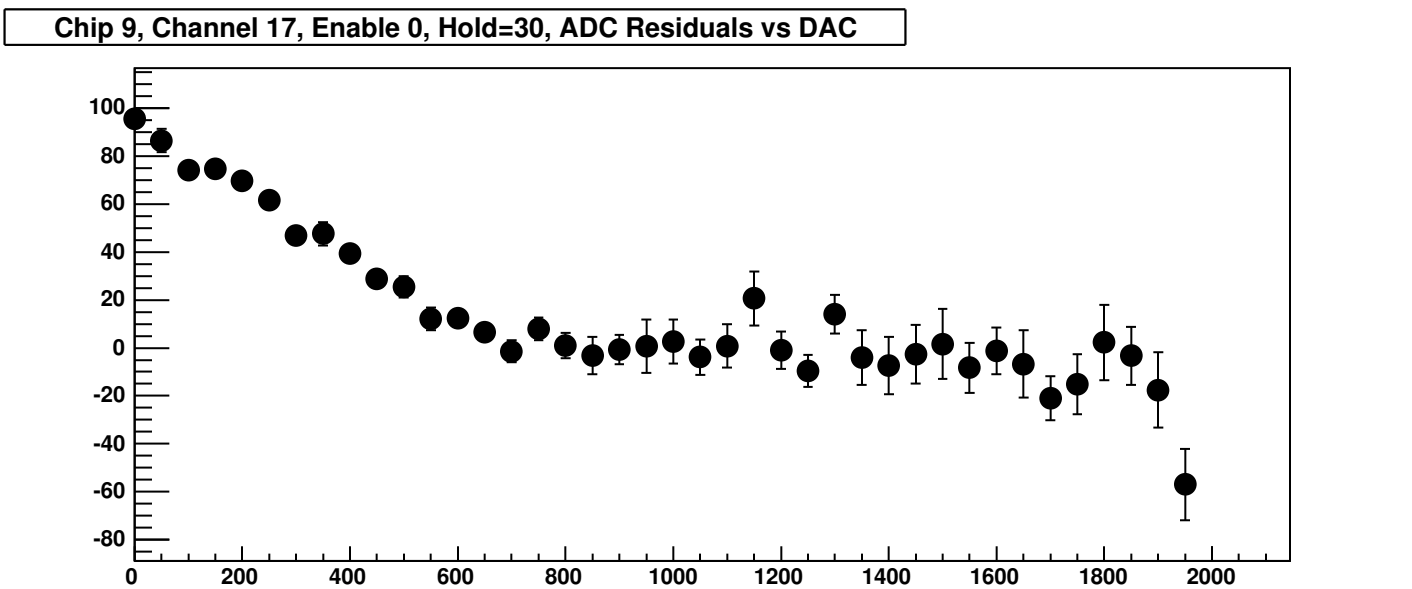

Chip 9, Channel 16, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 10.62 / 11
p0 -1791 ± 25.5
p1 0.8728 ± 0.0233
0
-200
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

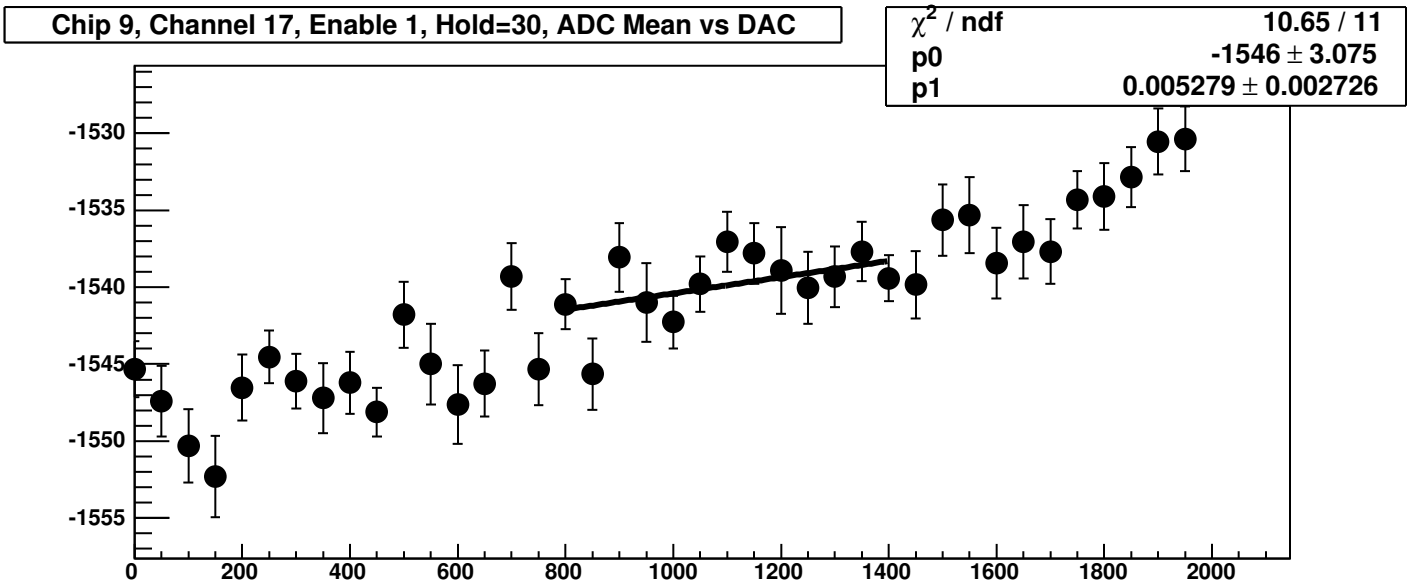

Chip 9, Channel 16, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

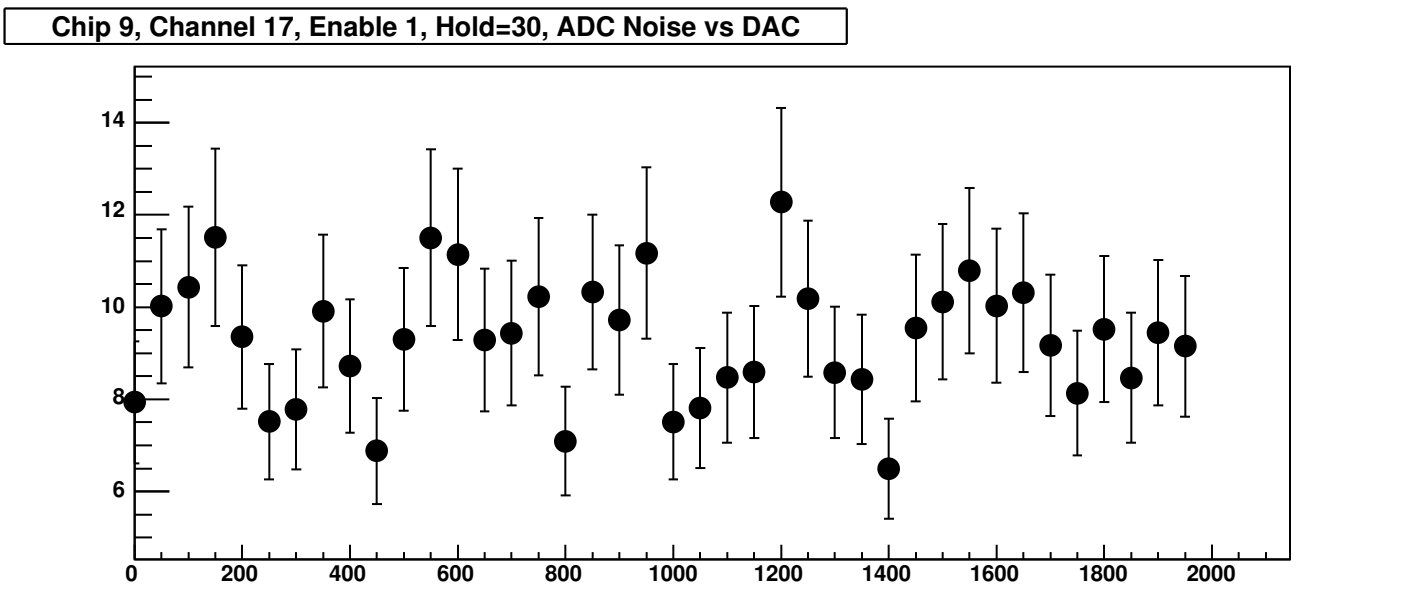

Chip 9, Channel 16, Enable 5, Hold=30, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

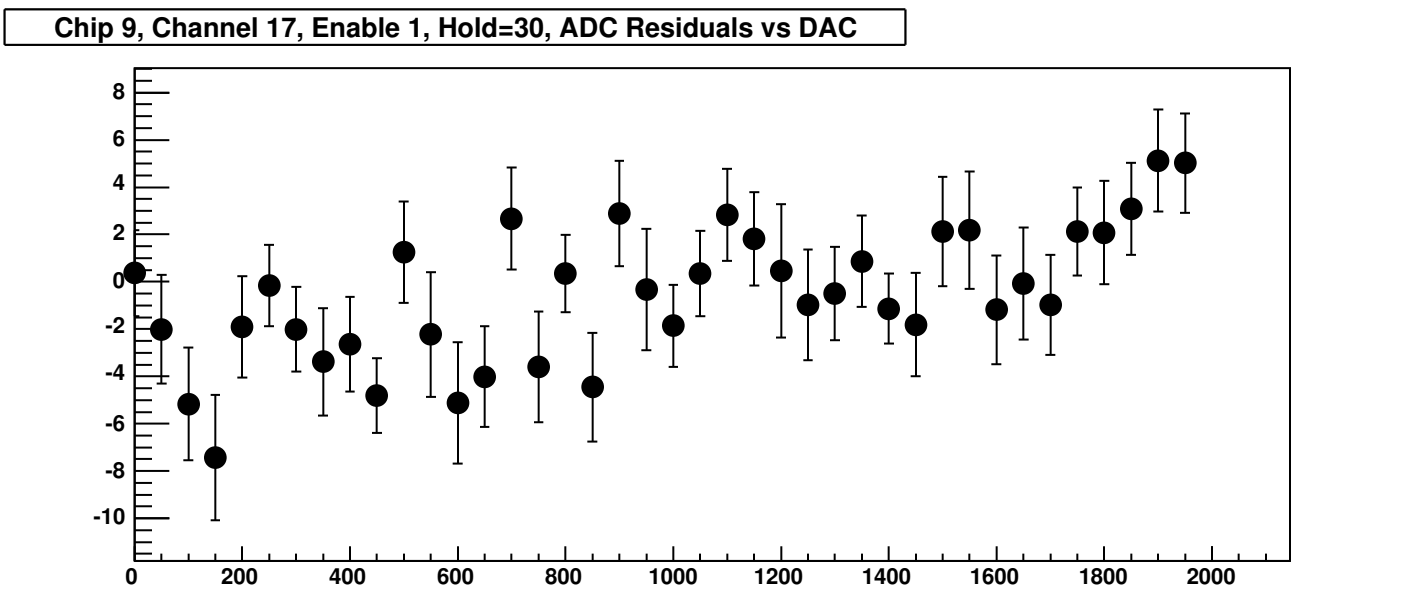

Chip 9, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 9.581 / 11
p0 -1643 ± 12.23
p1 0.1514 ± 0.01151
-1350
-1400
-1450
-1500
-1550
0
200
400
600
800
1000
1200
1400
1600
1800
2000

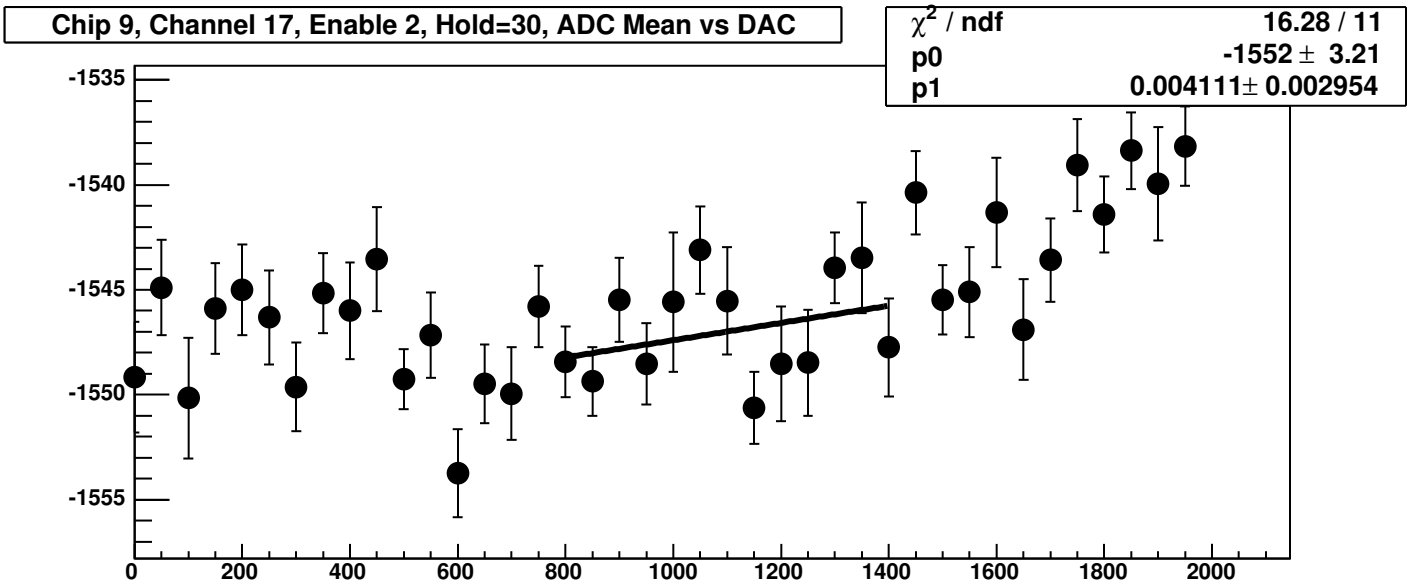

Chip 9, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

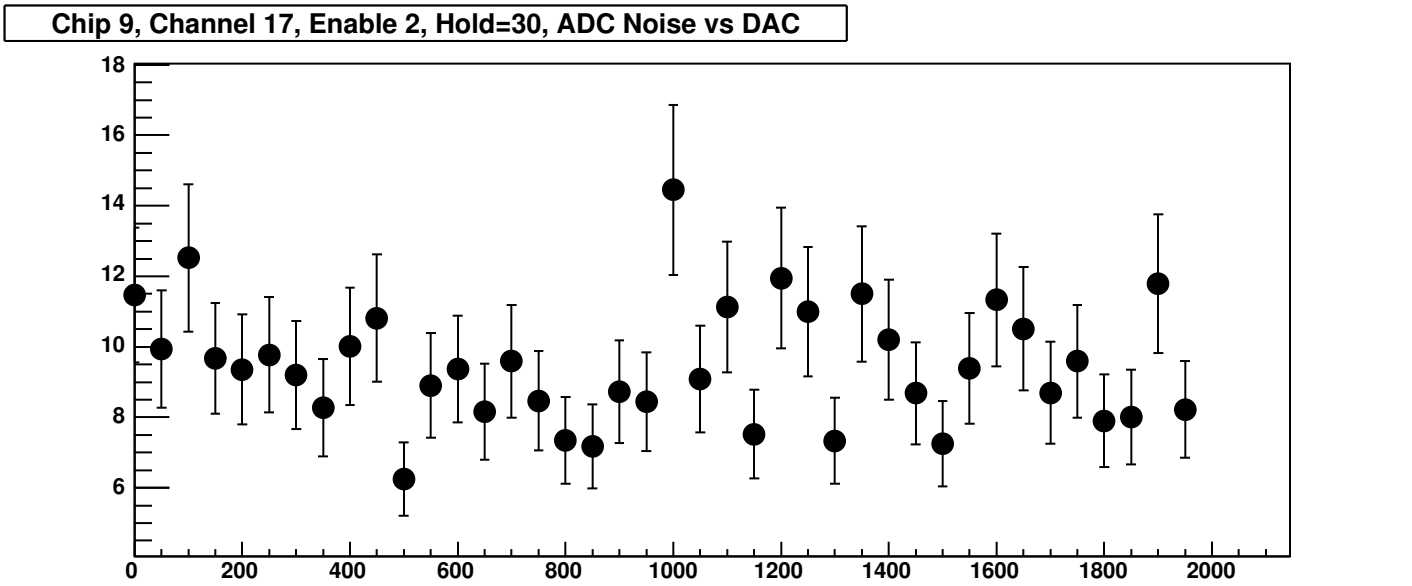

Chip 9, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
-60
-80
0
200
400
600
800
1000
1200
1400
1600
1800
2000

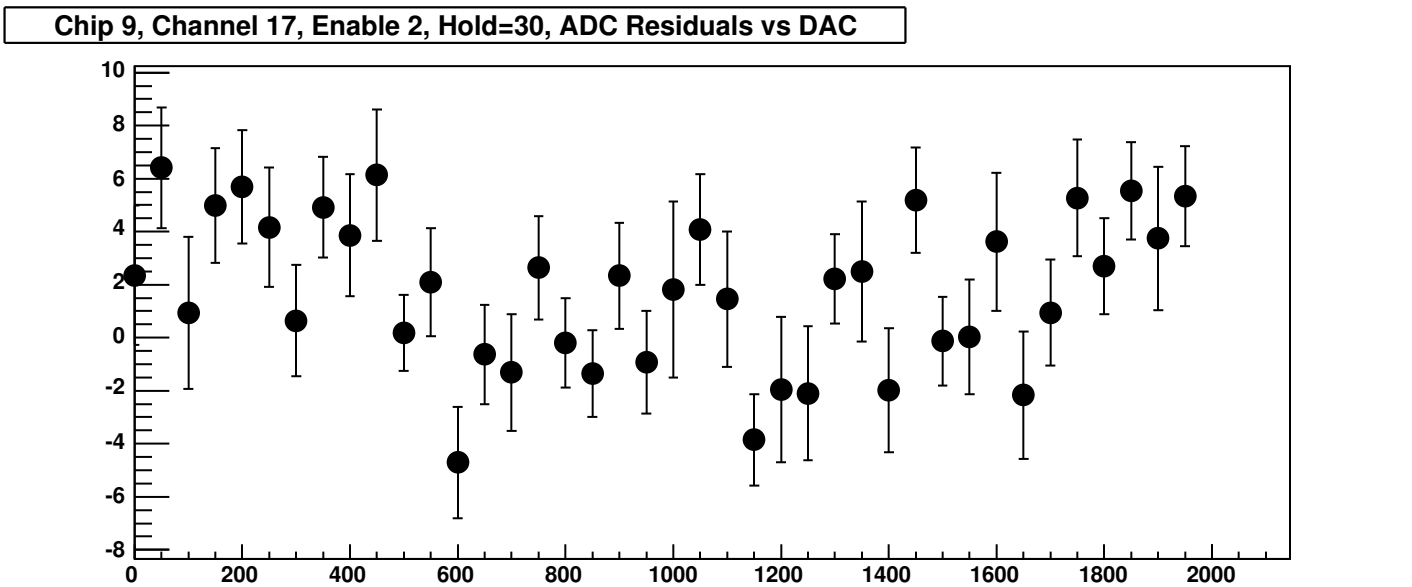

Chip 9, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 10.65 / 11
p0 -1546 ± 3.075
p1 0.005279 ± 0.002726
-1530
-1535
-1540
-1545
-1550
-1555
0
200
400
600
800
1000
1200
1400
1600
1800
2000

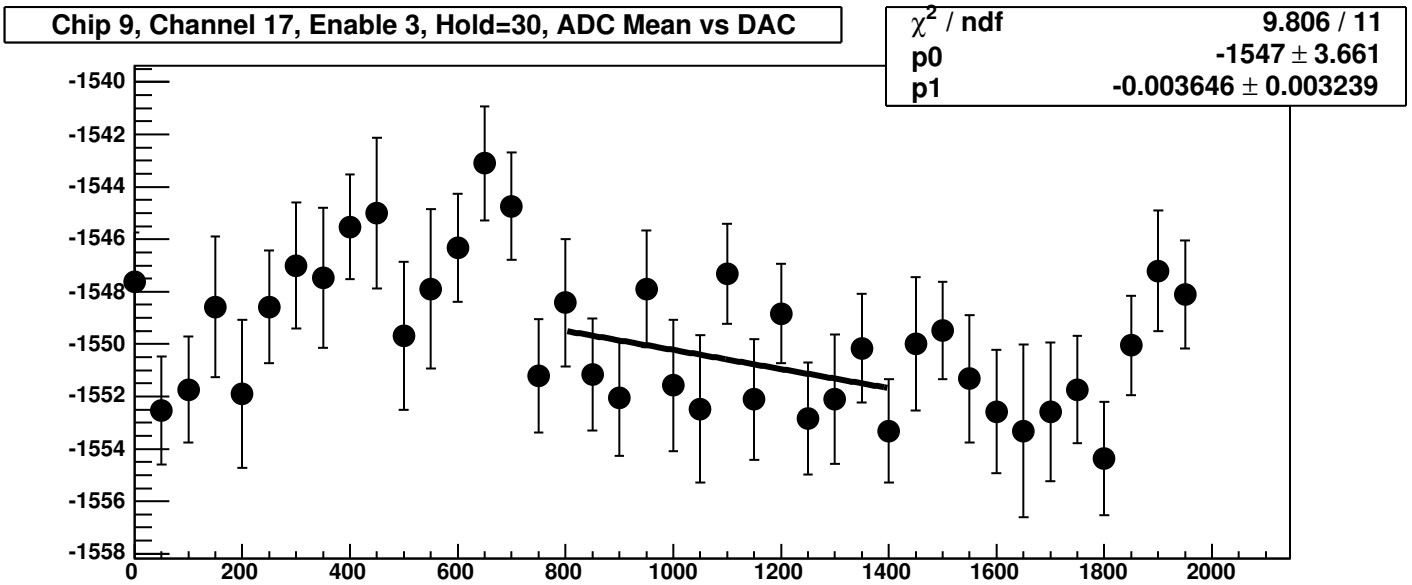

Chip 9, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

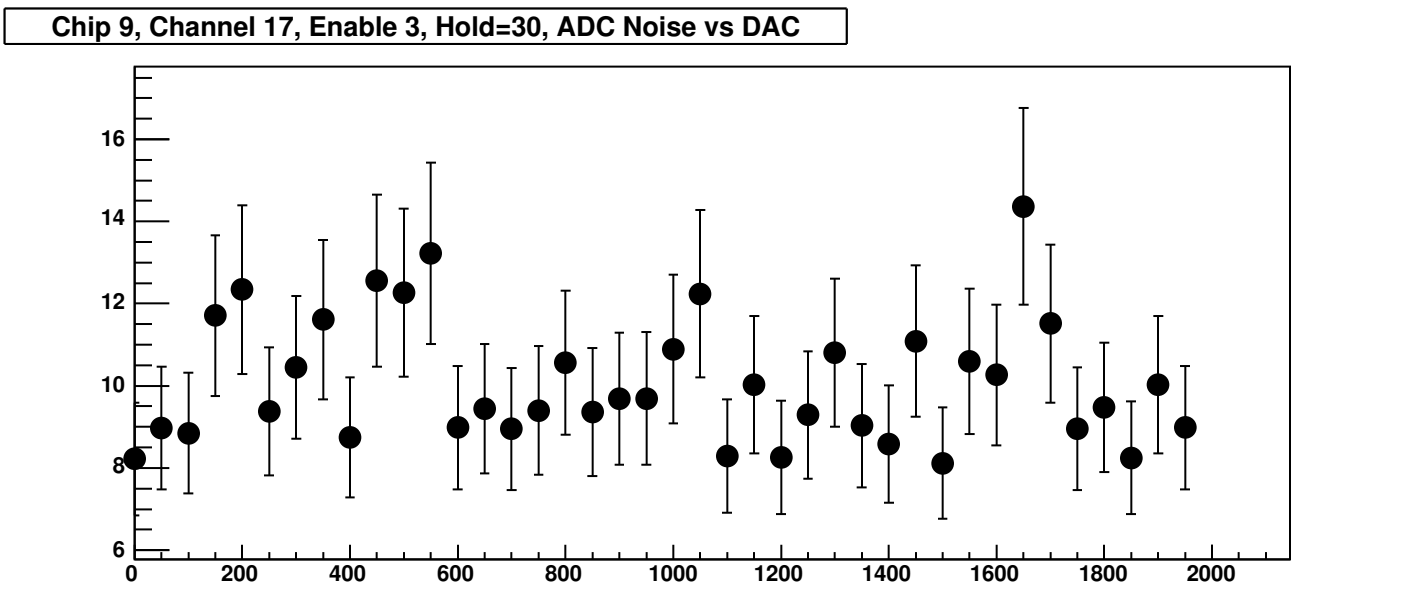

Chip 9, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

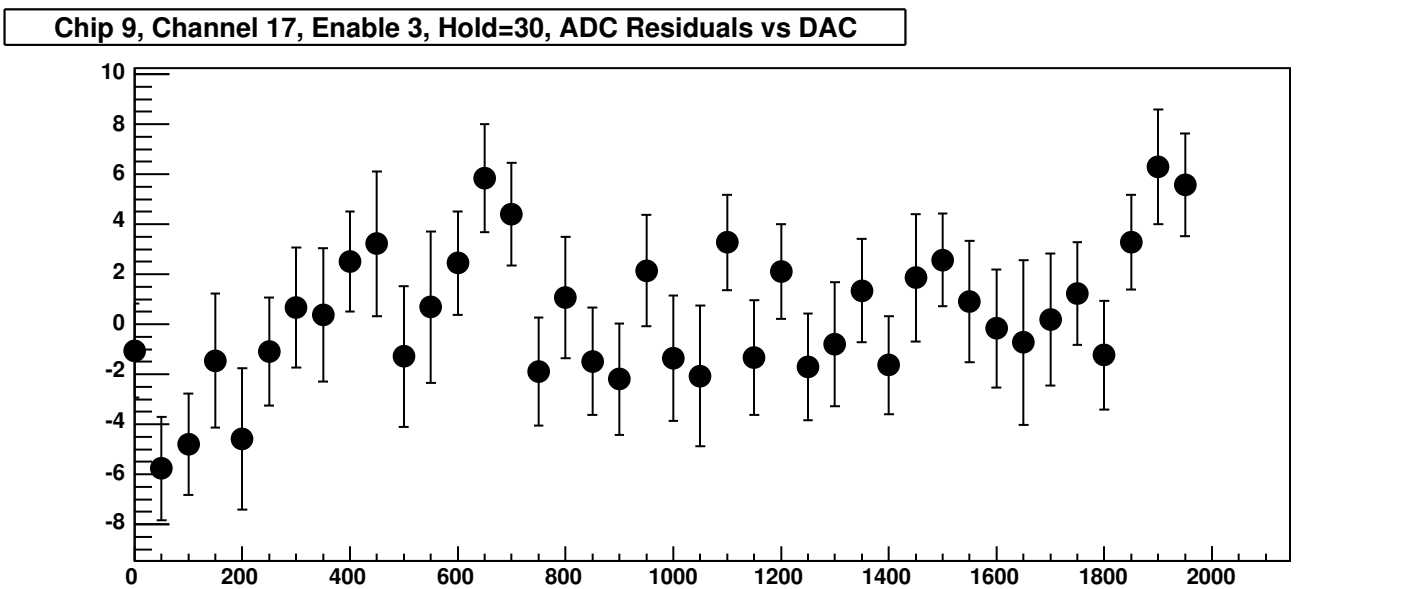

Chip 9, Channel 17, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 16.28 / 11
p0 -1552 ± 3.21
p1 0.004111 ± 0.002954
-1535
-1540
-1545
-1550
-1555
0
200
400
600
800
1000
1200
1400
1600
1800
2000

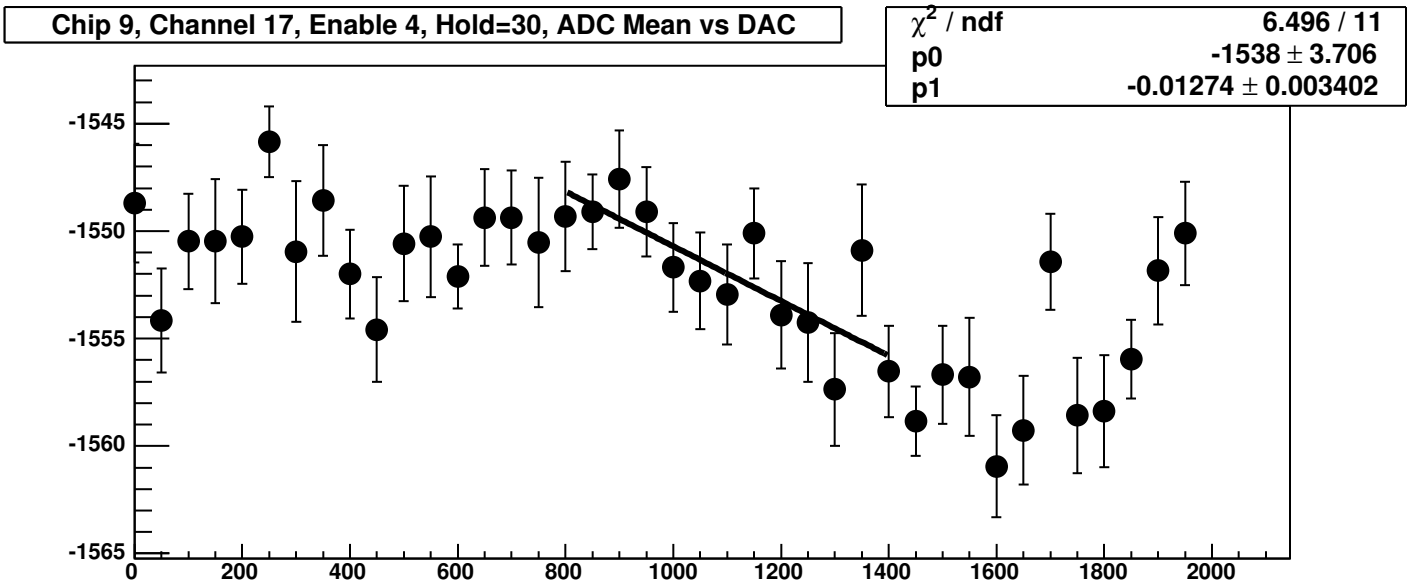

Chip 9, Channel 17, Enable 2, Hold=30, ADC Noise vs DAC
18
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

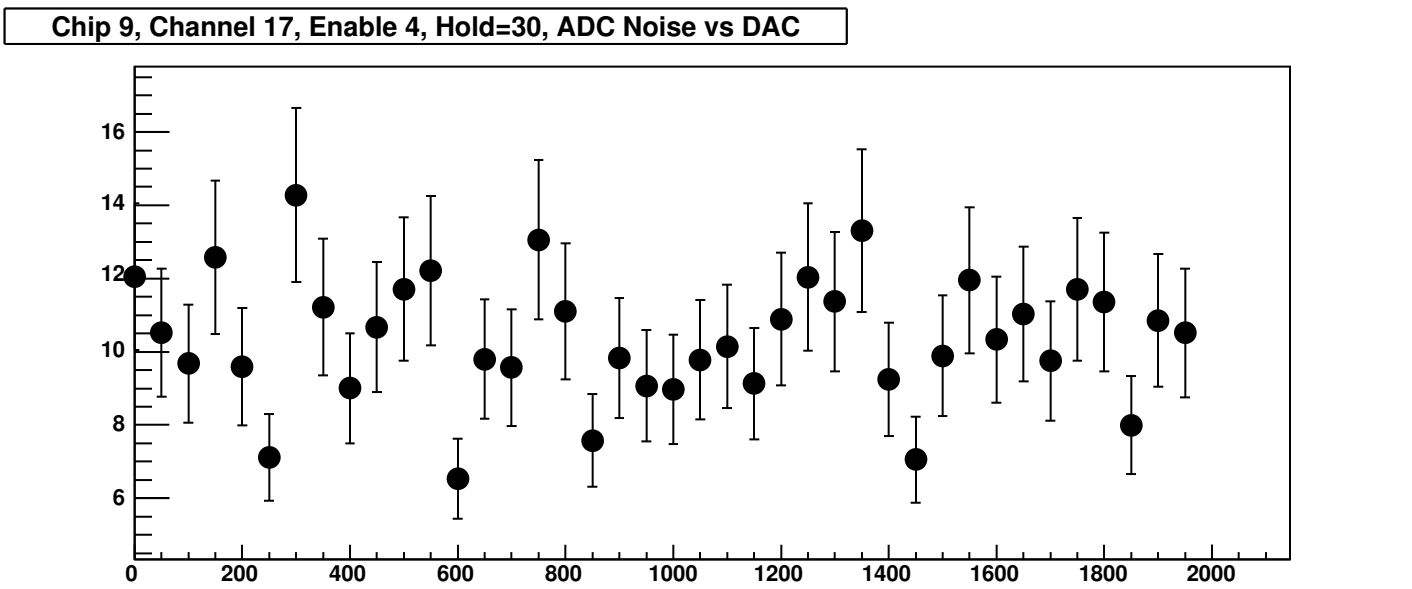

Chip 9, Channel 17, Enable 2, Hold=30, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
200
400
600
800
1000
1200
1400
1600
1800
2000

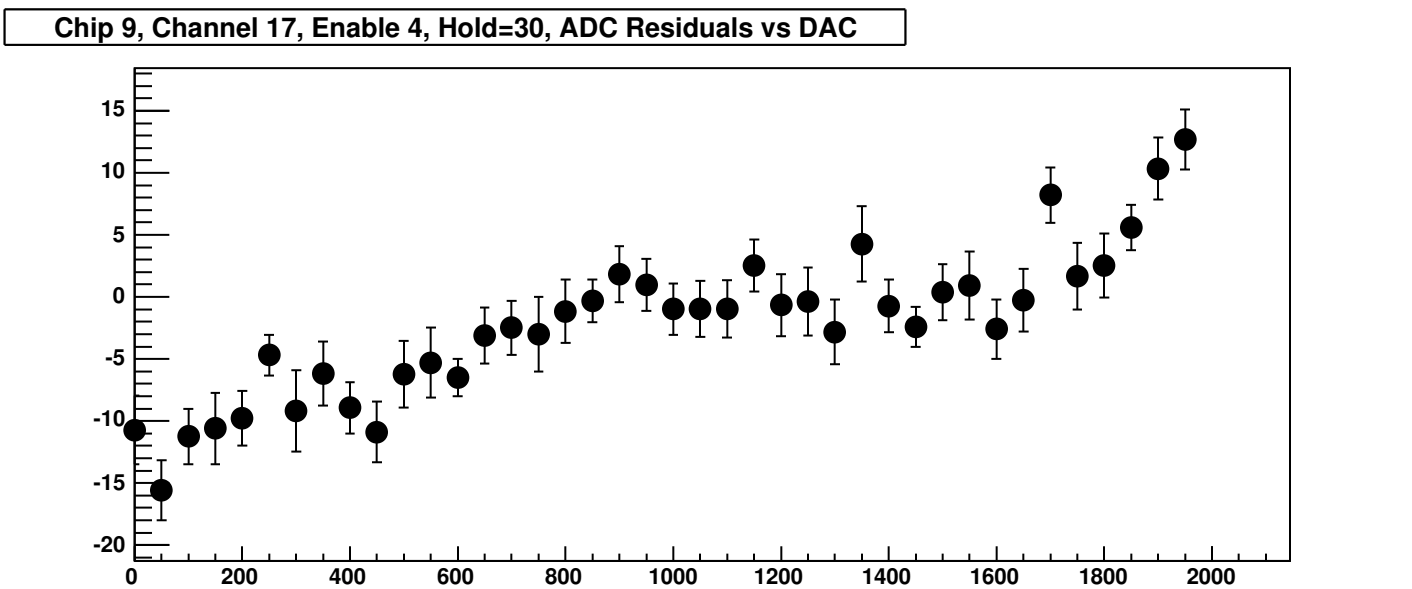

Chip 9, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 9.806 / 11
p0 -1547 ± 3.661
p1 -0.003646 ± 0.003239
-1540
-1542
-1544
-1546
-1548
-1550
-1552
-1554
-1556
-1558
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000

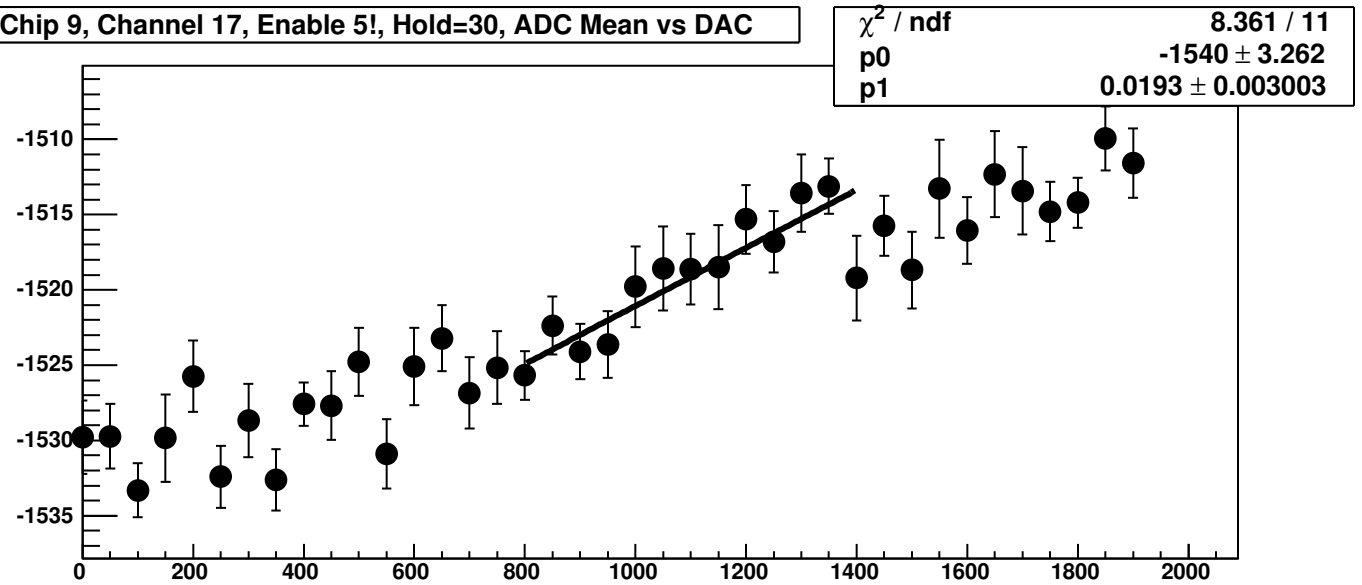

Chip 9, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 6.496 / 11
p0 -1538 ± 3.706
p1 -0.01274 ± 0.003402
-1545
-1550
-1555
-1560
-1565
0
200
400
600
800
1000
1200
1400
1600
1800
2000

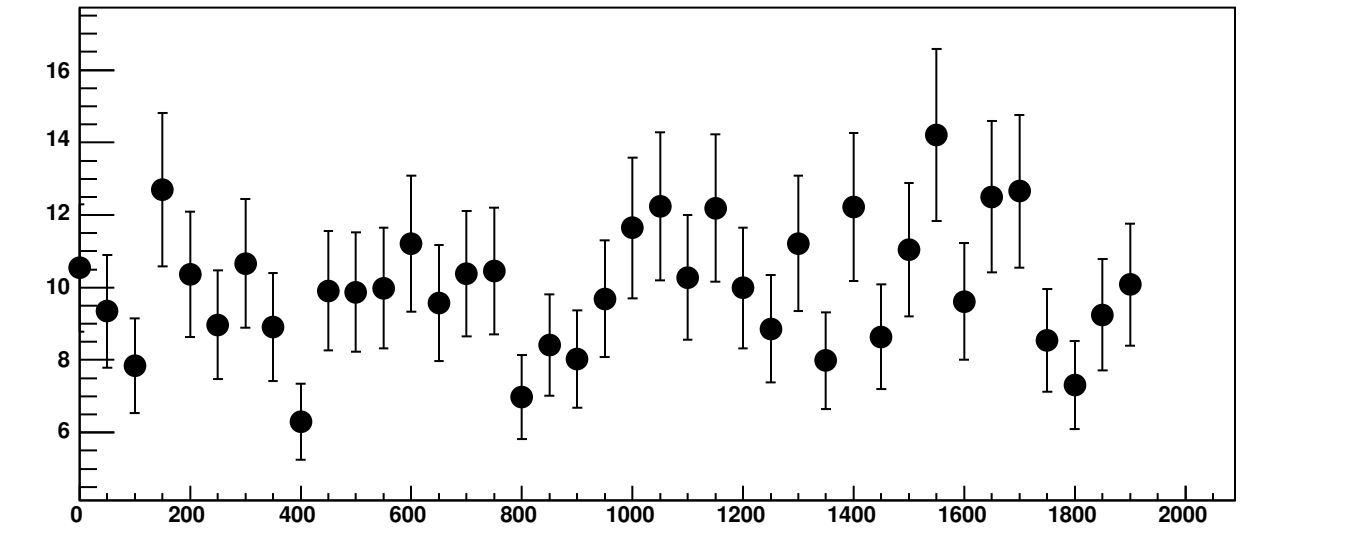

Chip 9, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 9, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

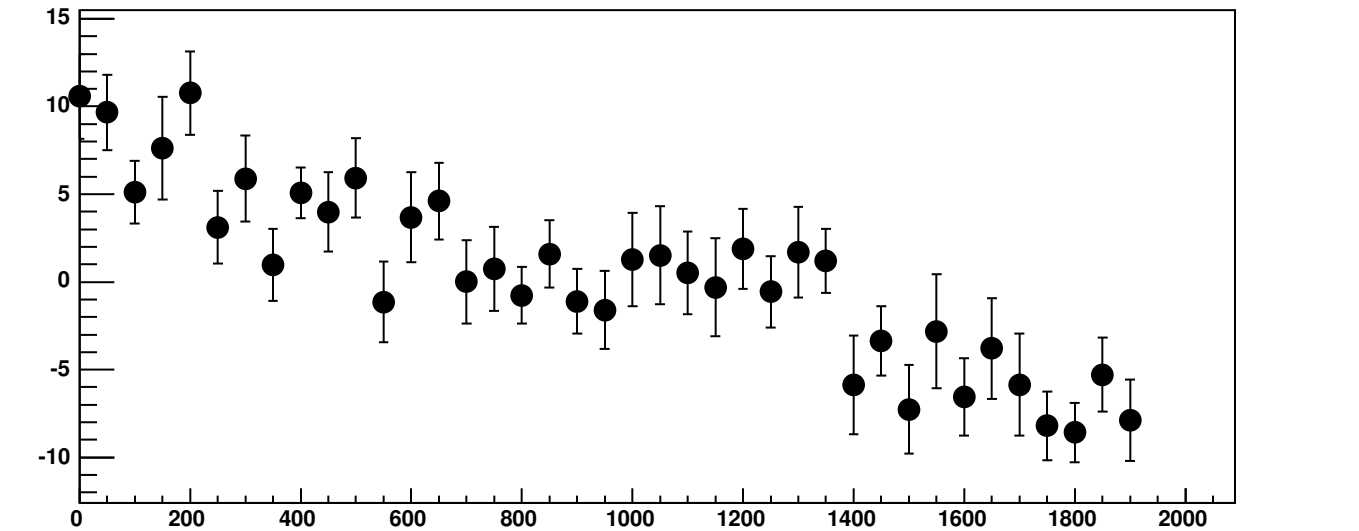
Chip 9, Channel 17, Enable 5!, Hold=30, ADC Mean vs DAC

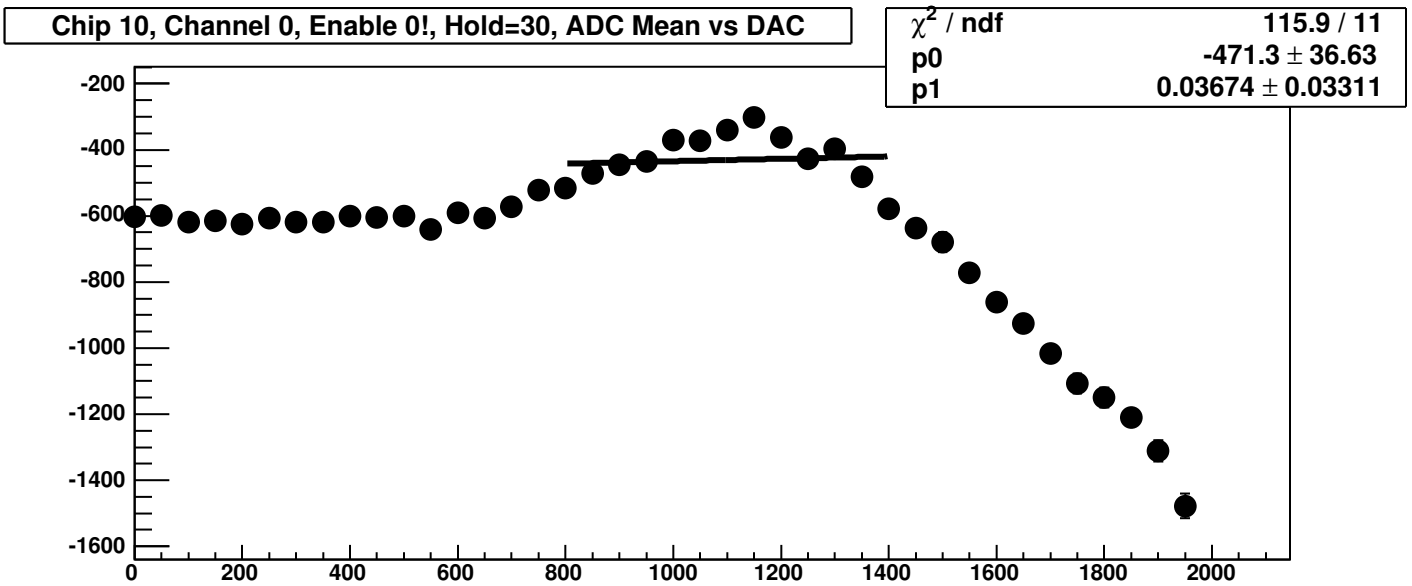


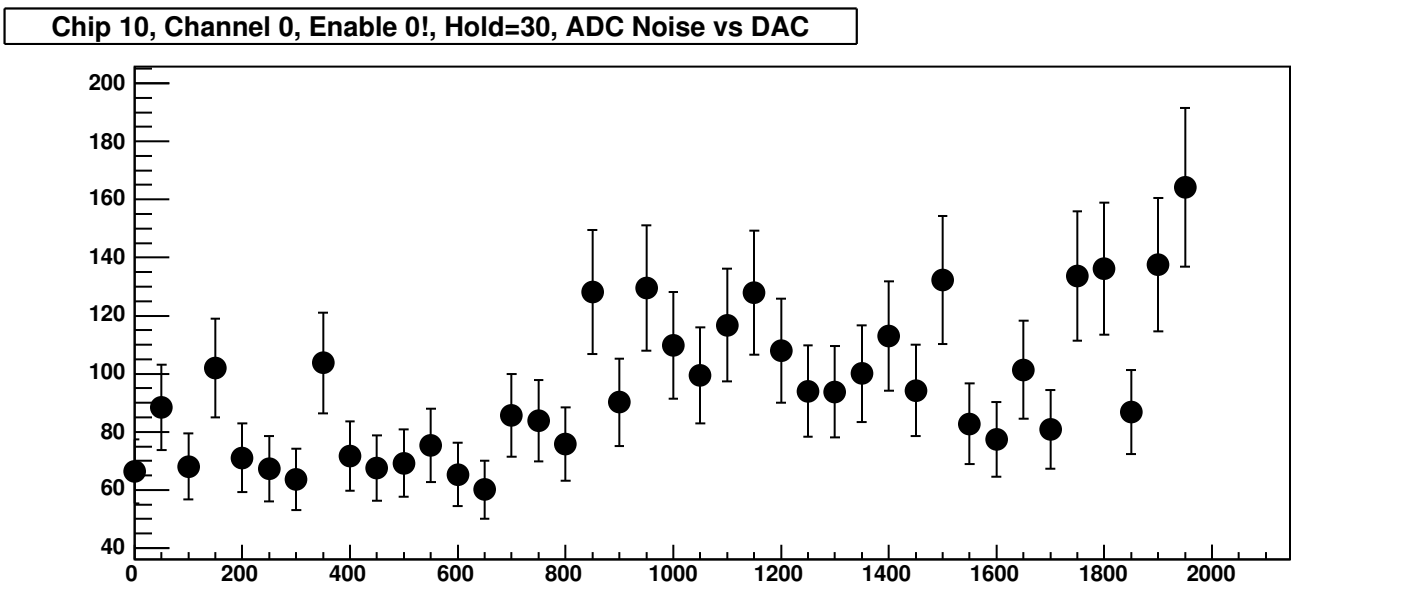
Chip 9, Channel 17, Enable 5!, Hold=30, ADC Noise vs DAC

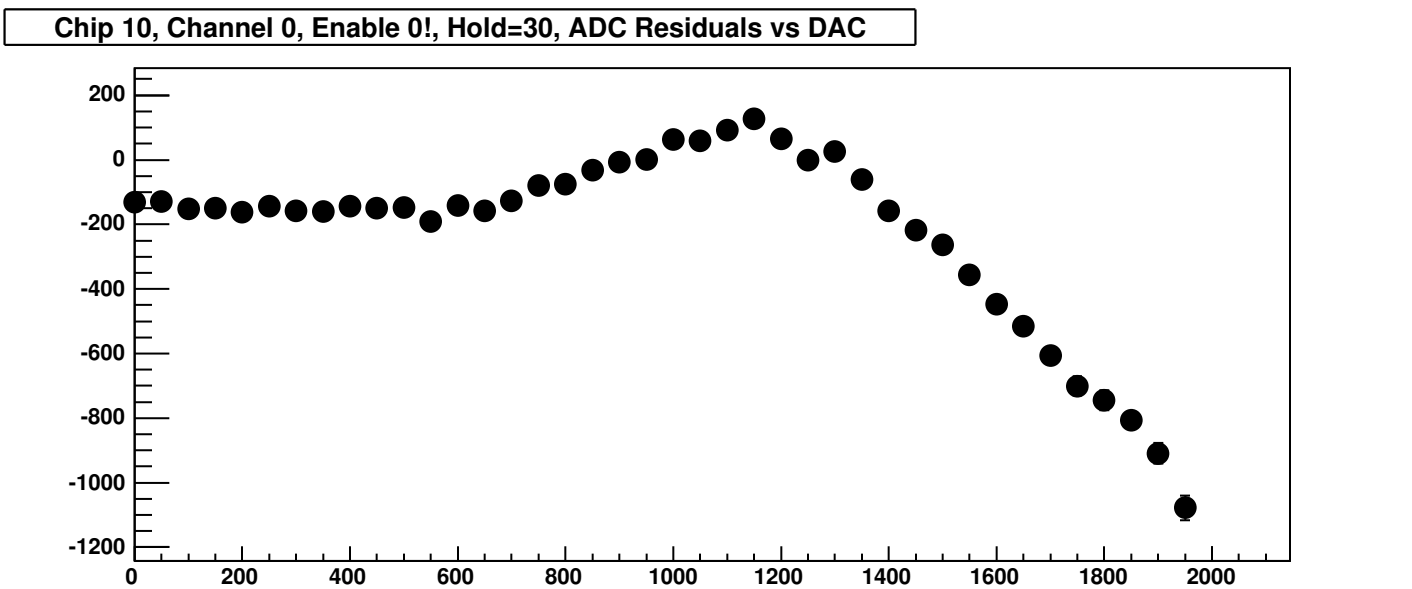


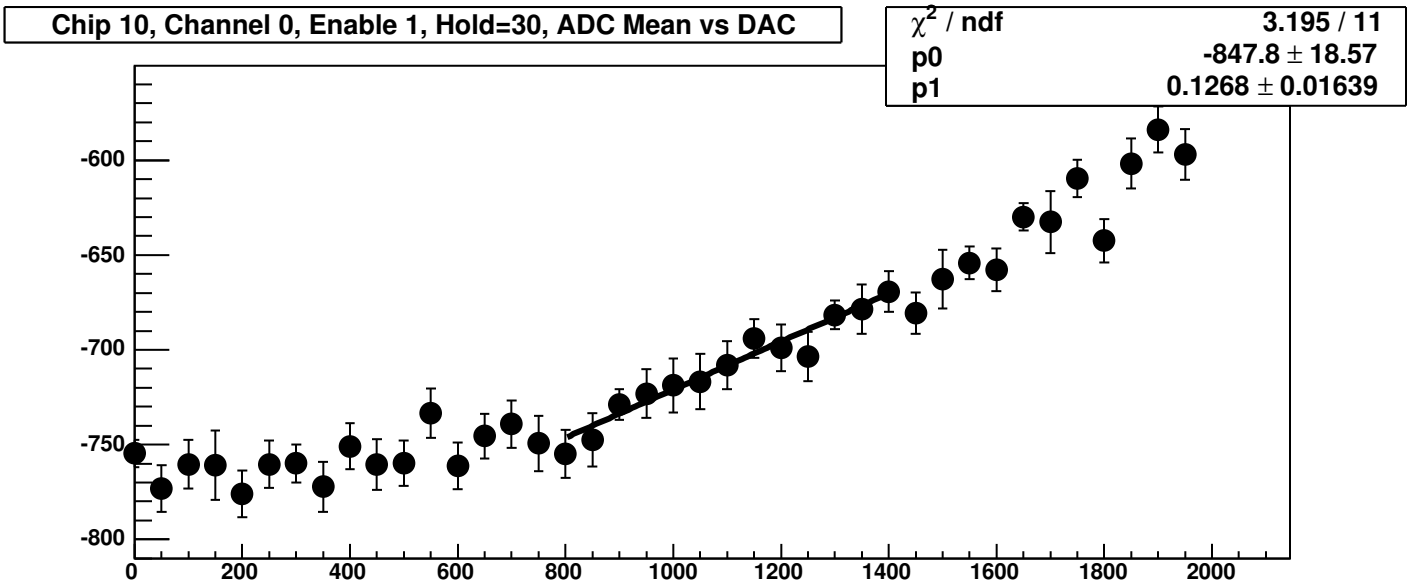
Chip 9, Channel 17, Enable 5!, Hold=30, ADC Residuals vs DAC

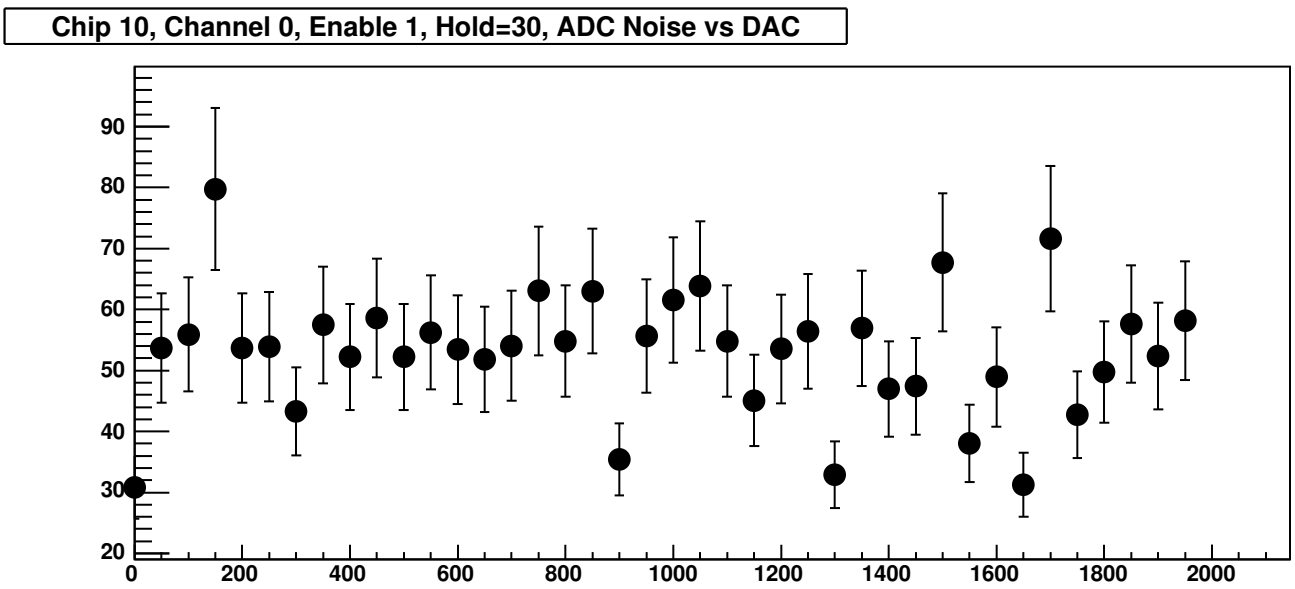


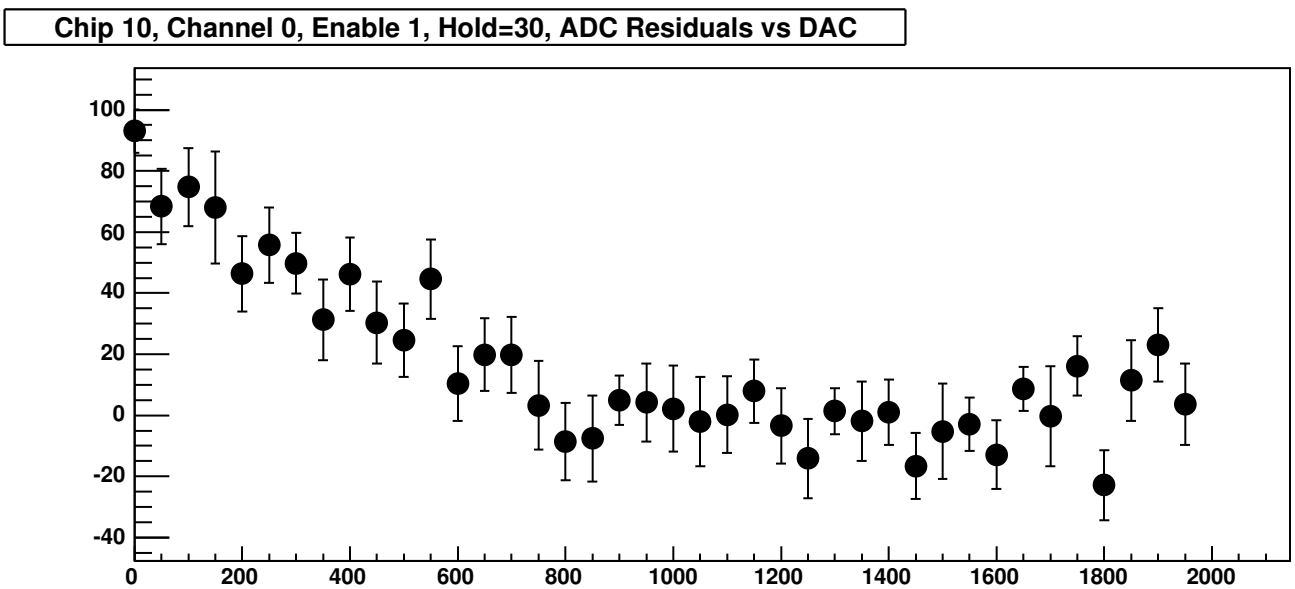

Chip 10, Channel 0, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 115.9 / 11
p0 -471.3 ± 36.63
p1 0.03674 ± 0.03311
-200
-400
-600
-800
-1000
-1200
-1400
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

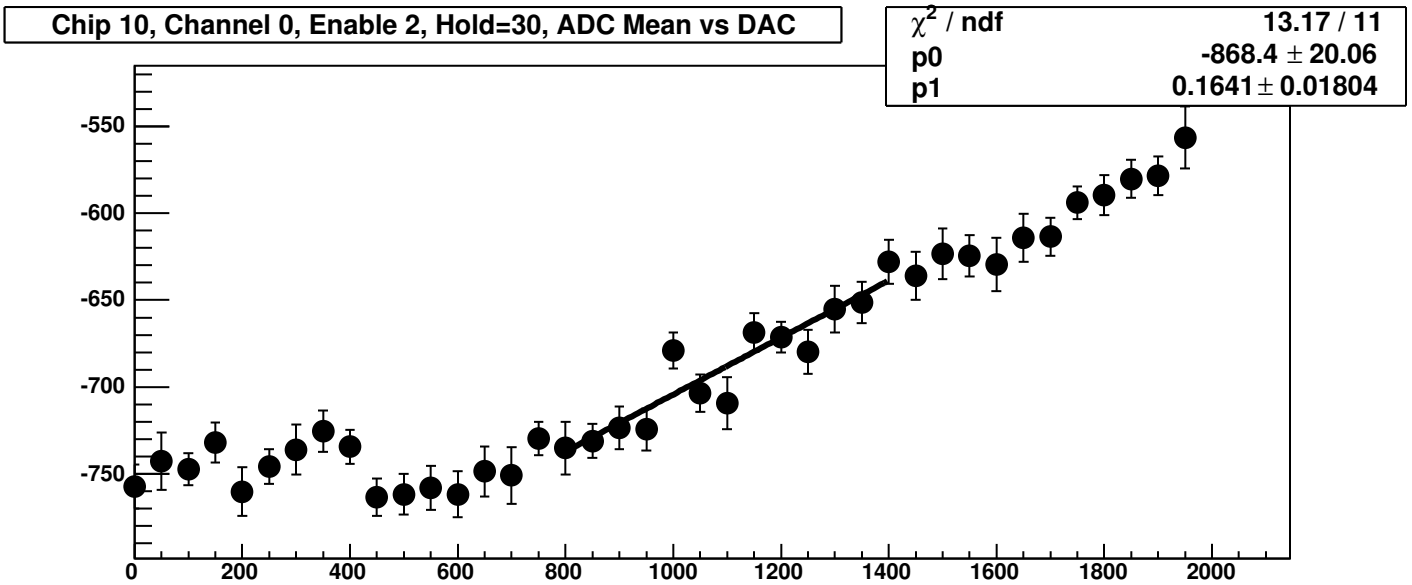

Chip 10, Channel 0, Enable 0!, Hold=30, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

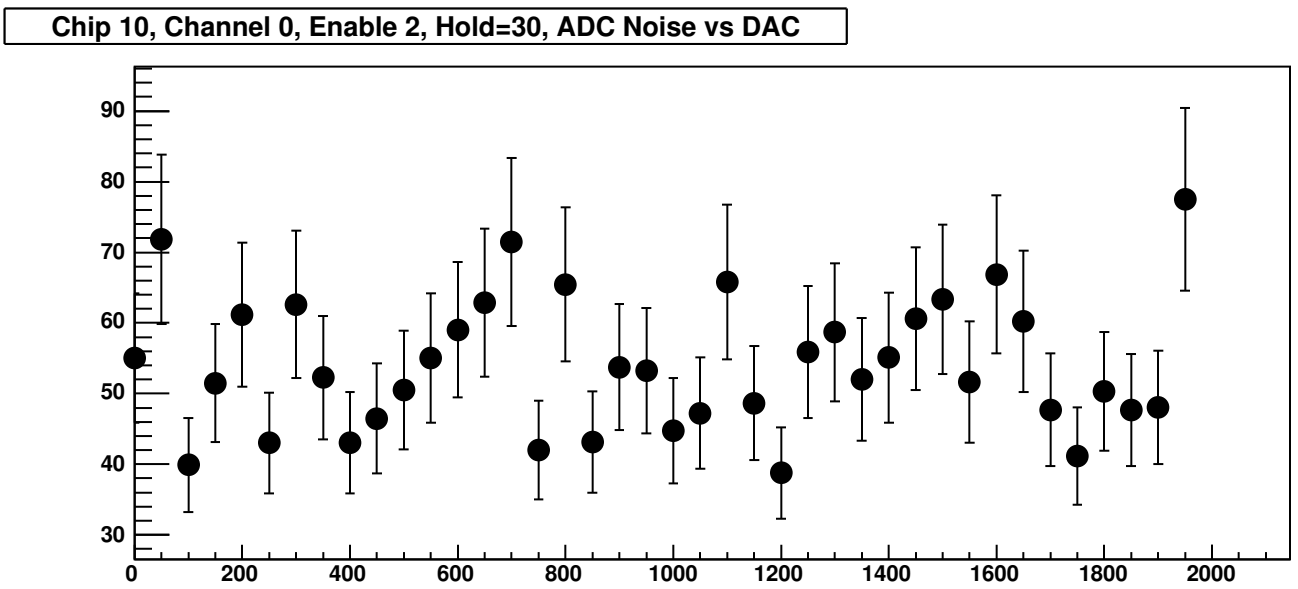

Chip 10, Channel 0, Enable 0!, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

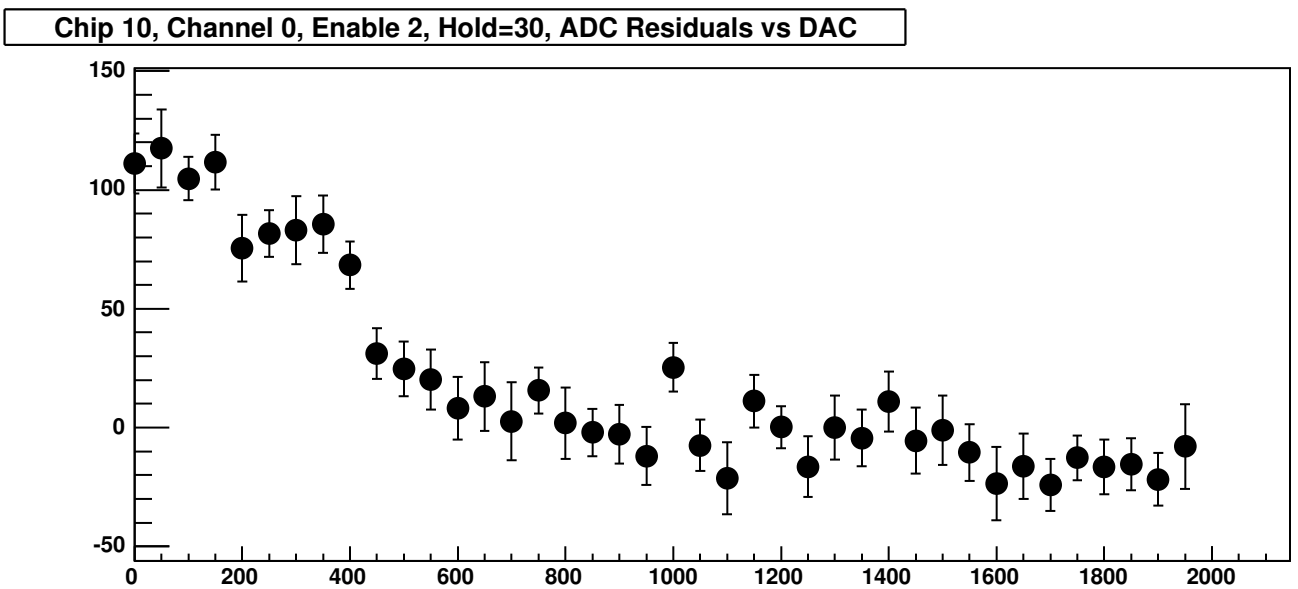

Chip 10, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3.195 / 11
p0 -847.8 ± 18.57
p1 0.1268 ± 0.01639
-600
-650
-700
-750
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

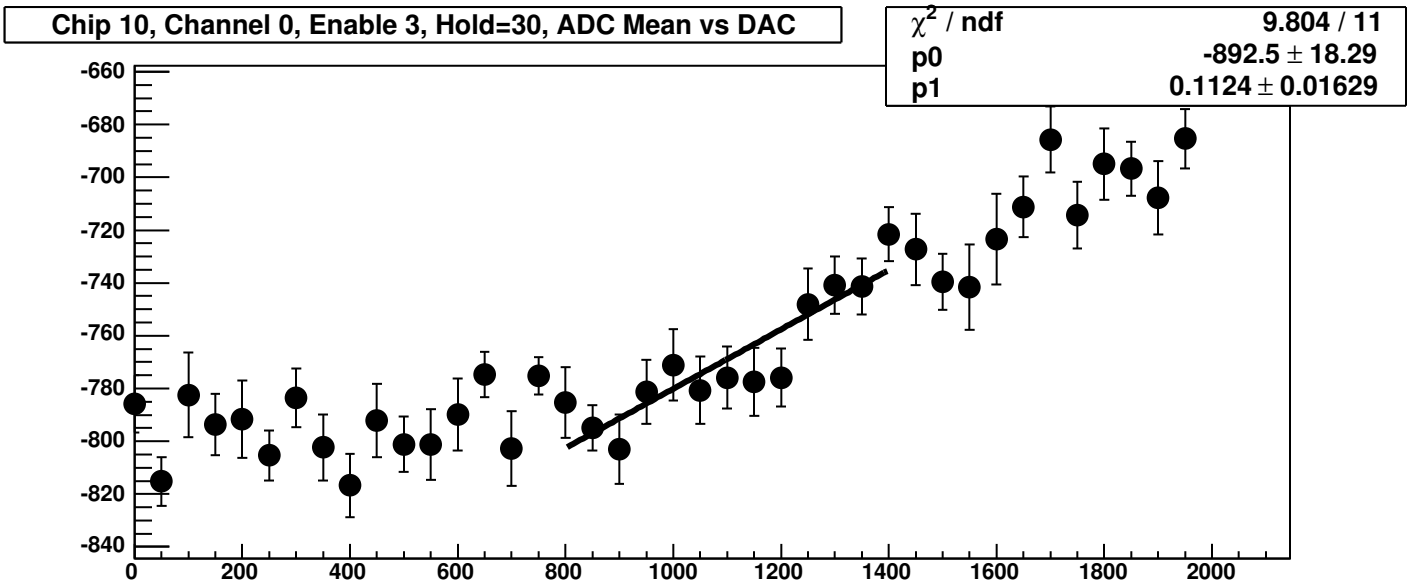

Chip 10, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

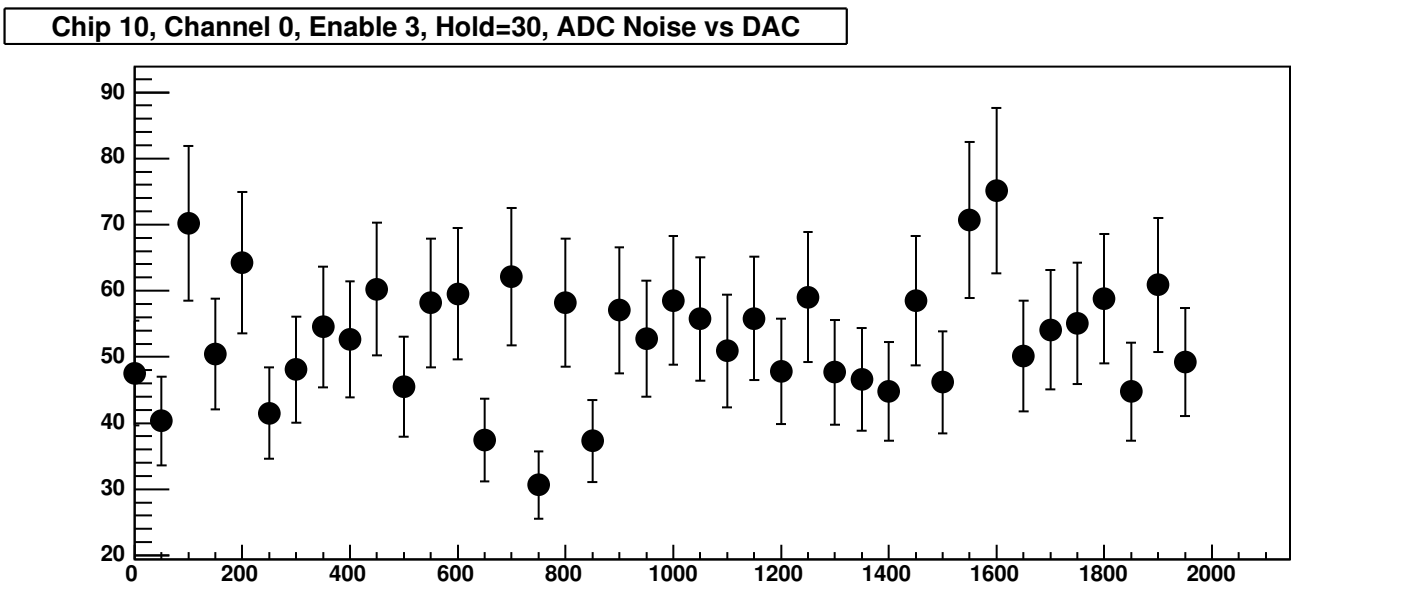

Chip 10, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

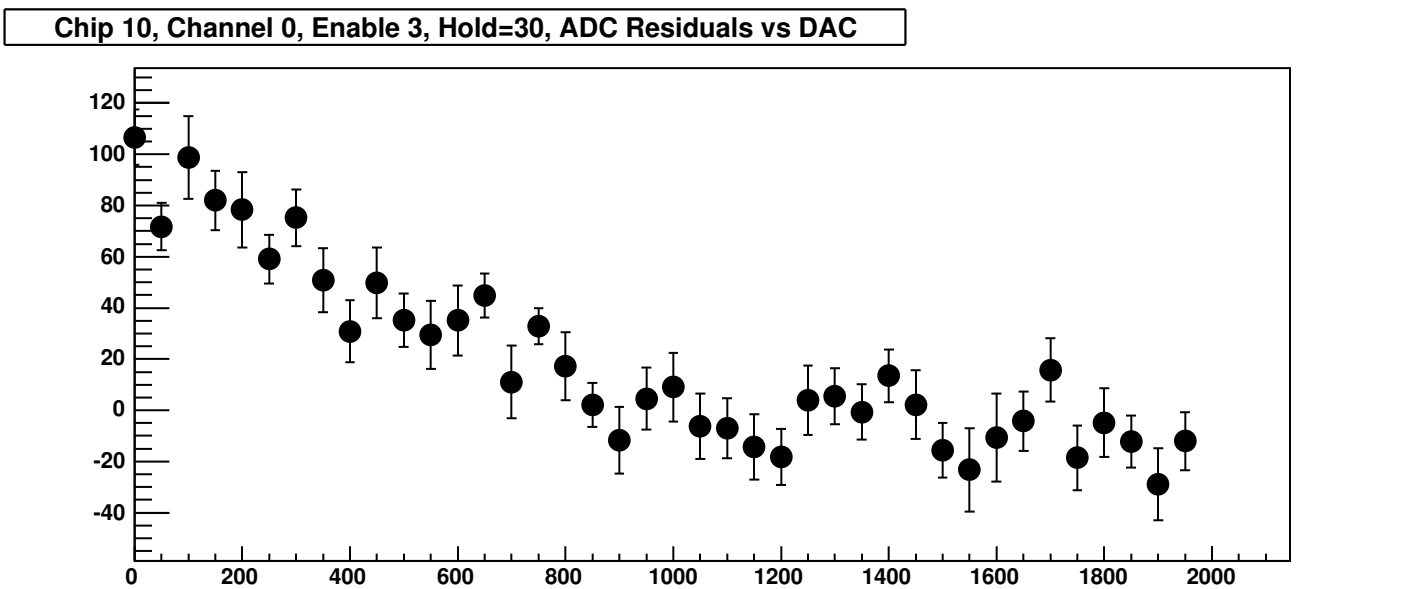

Chip 10, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 13.17 / 11
p0 -868.4 ± 20.06
p1 0.1641 ± 0.01804
-550
-600
-650
-700
-750
0
200
400
600
800
1000
1200
1400
1600
1800
2000

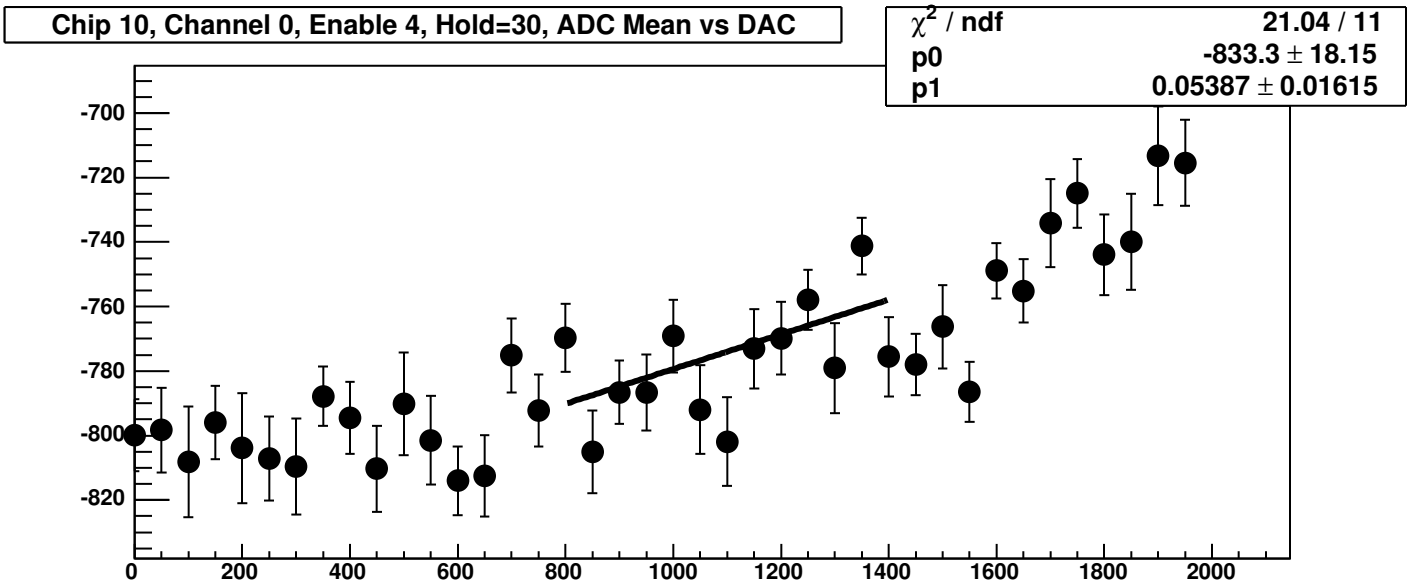

Chip 10, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

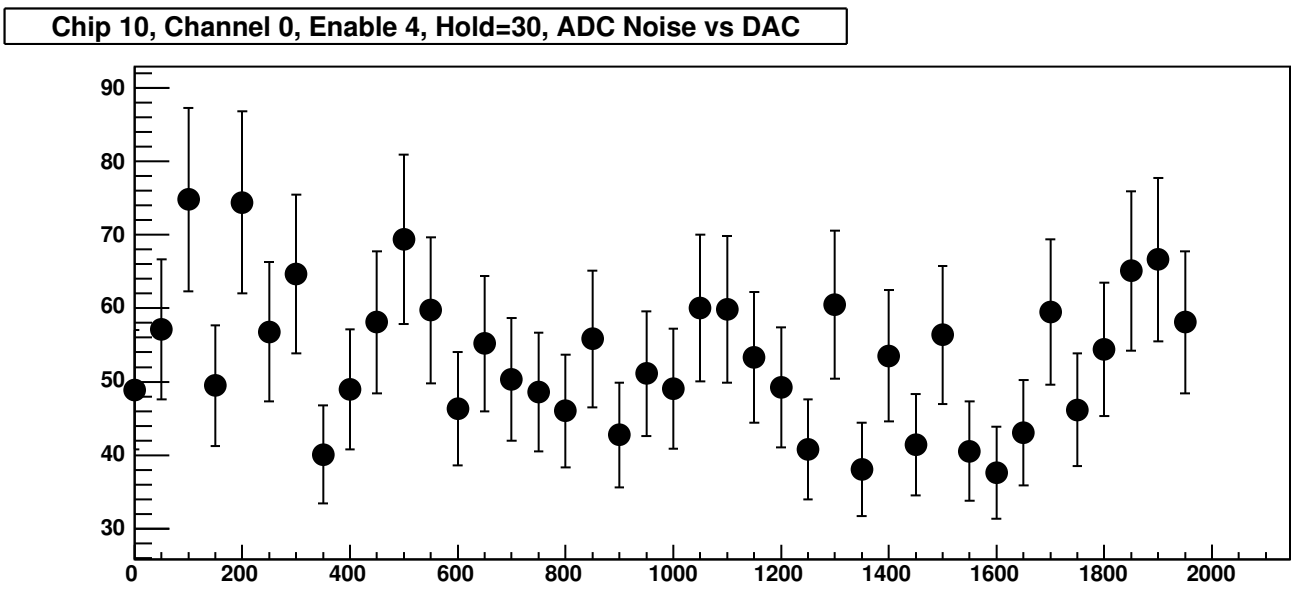

Chip 10, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

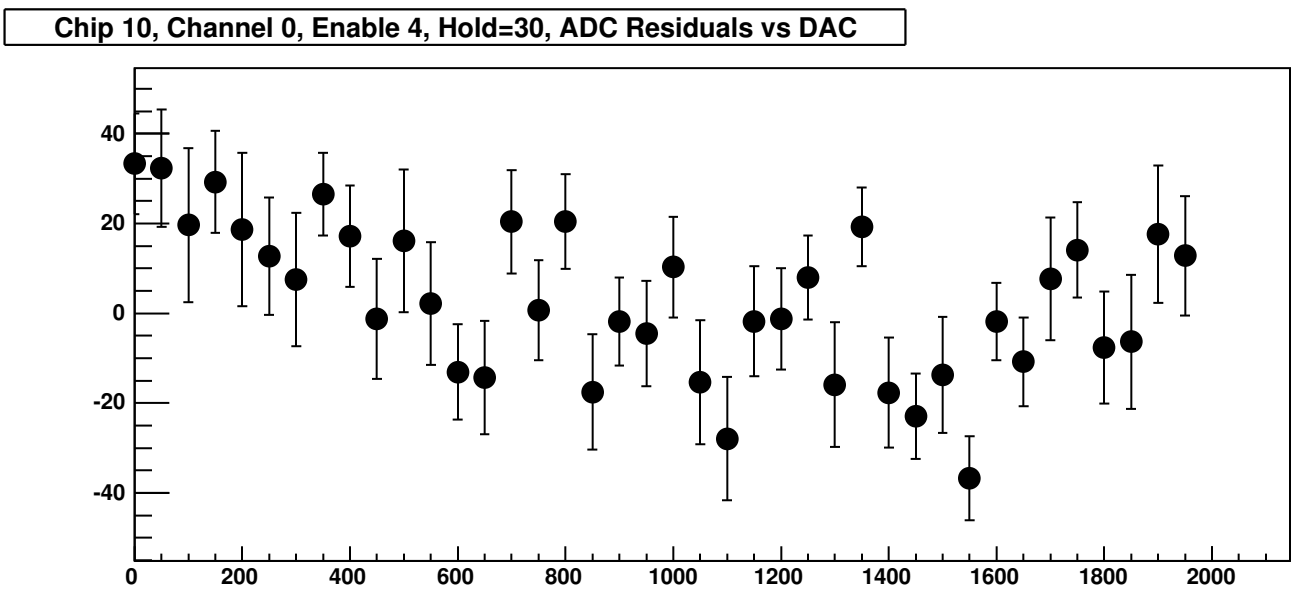

Chip 10, Channel 0, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 9.804 / 11
p0 -892.5 ± 18.29
p1 0.1124 ± 0.01629
-660
-680
-700
-720
-740
-760
-780
-800
-820
-840
0
200
400
600
800
1000
1200
1400
1600
1800
2000

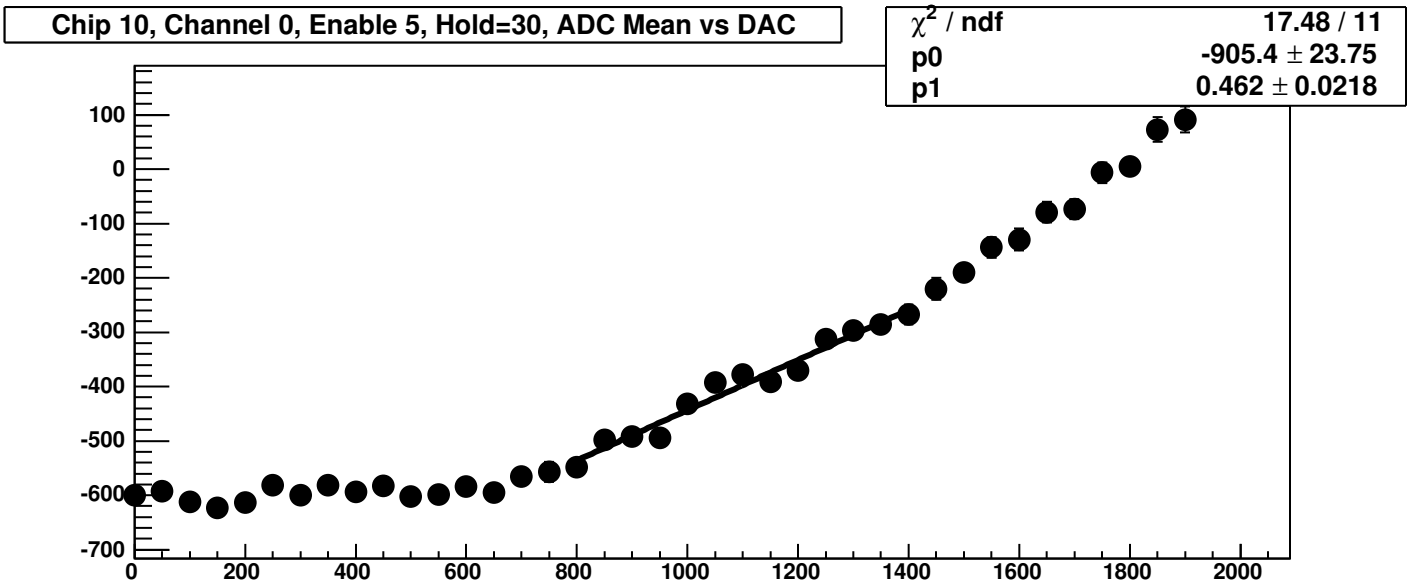

Chip 10, Channel 0, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

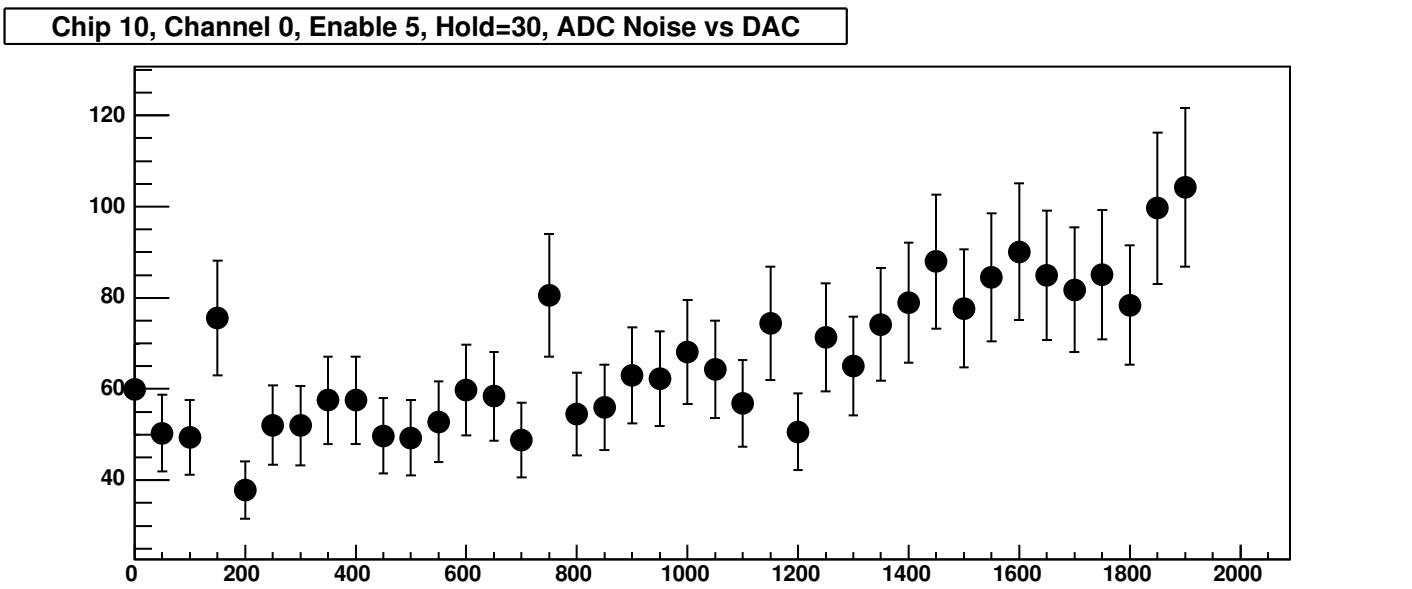

Chip 10, Channel 0, Enable 3, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

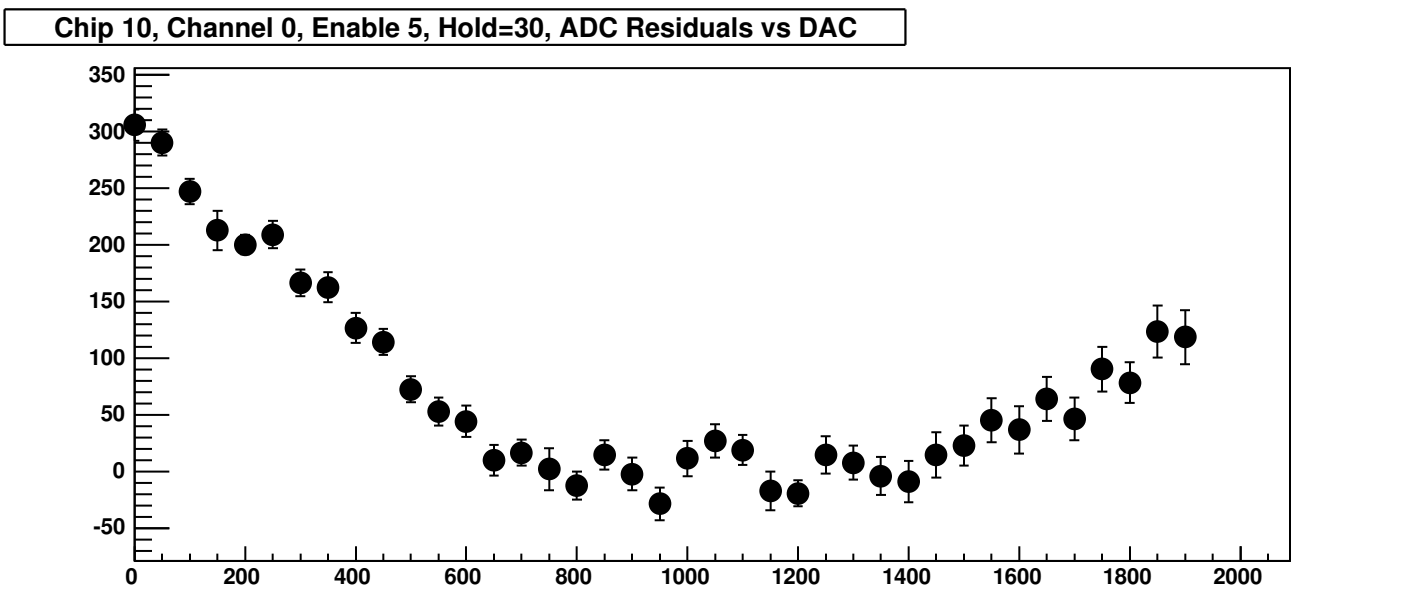

Chip 10, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 21.04 / 11
p0 -833.3 ± 18.15
p1 0.05387 ± 0.01615
-700
-720
-740
-760
-780
-800
-820
0
200
400
600
800
1000
1200
1400
1600
1800
2000

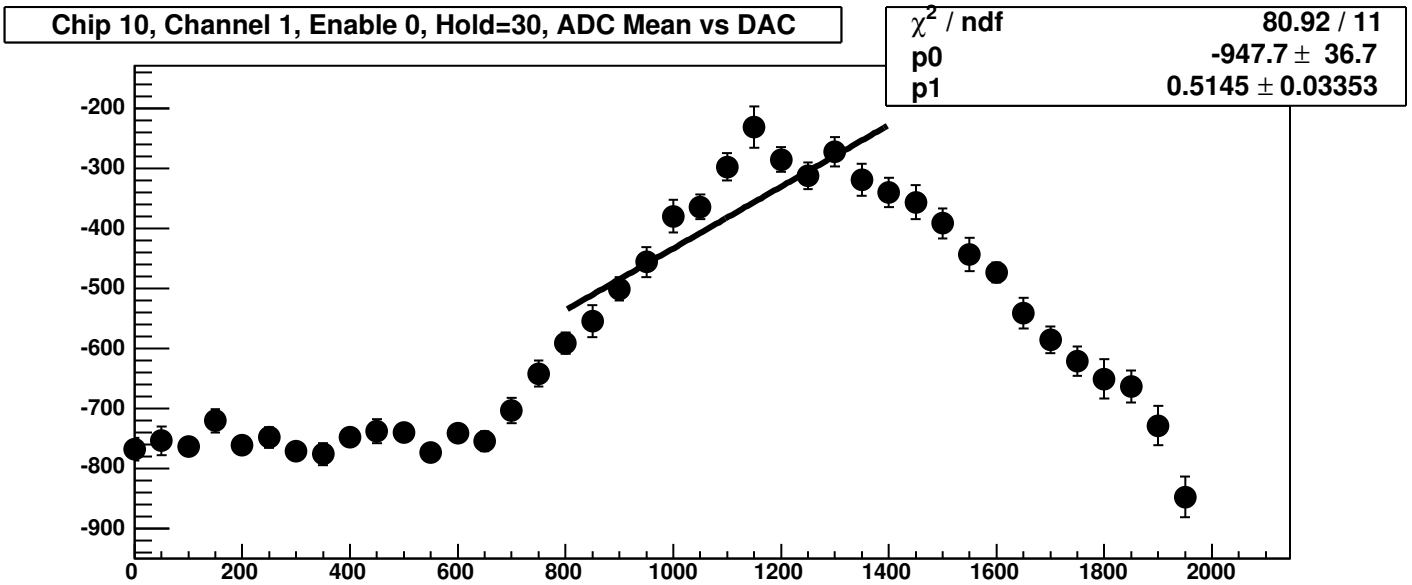

Chip 10, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

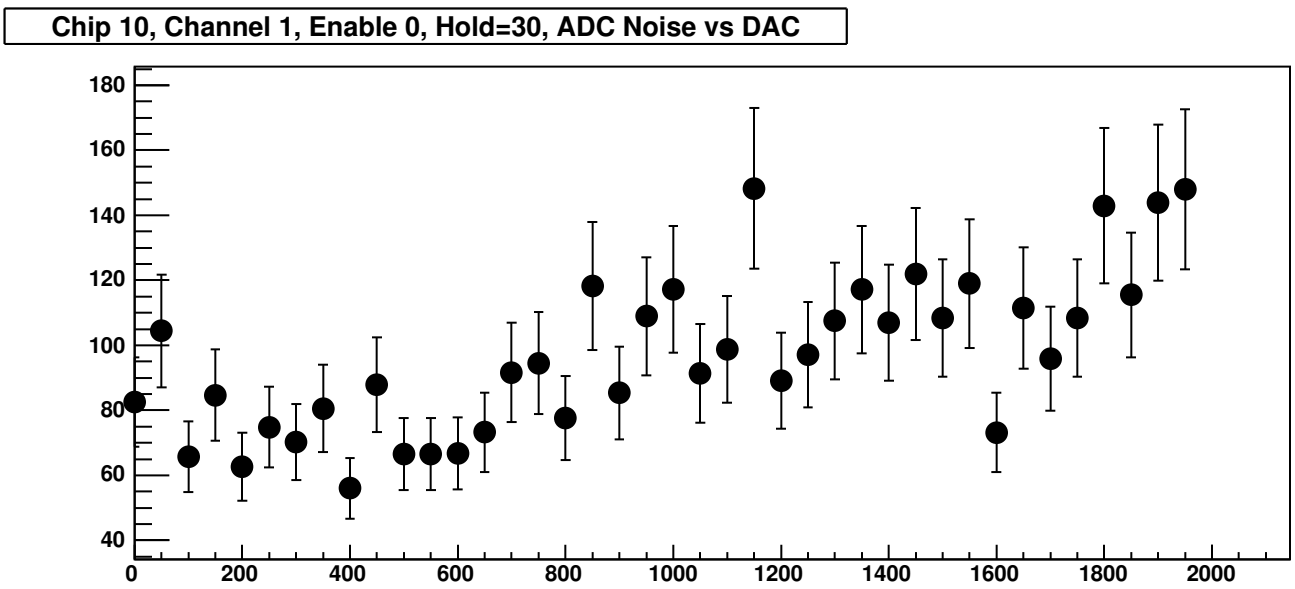

Chip 10, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

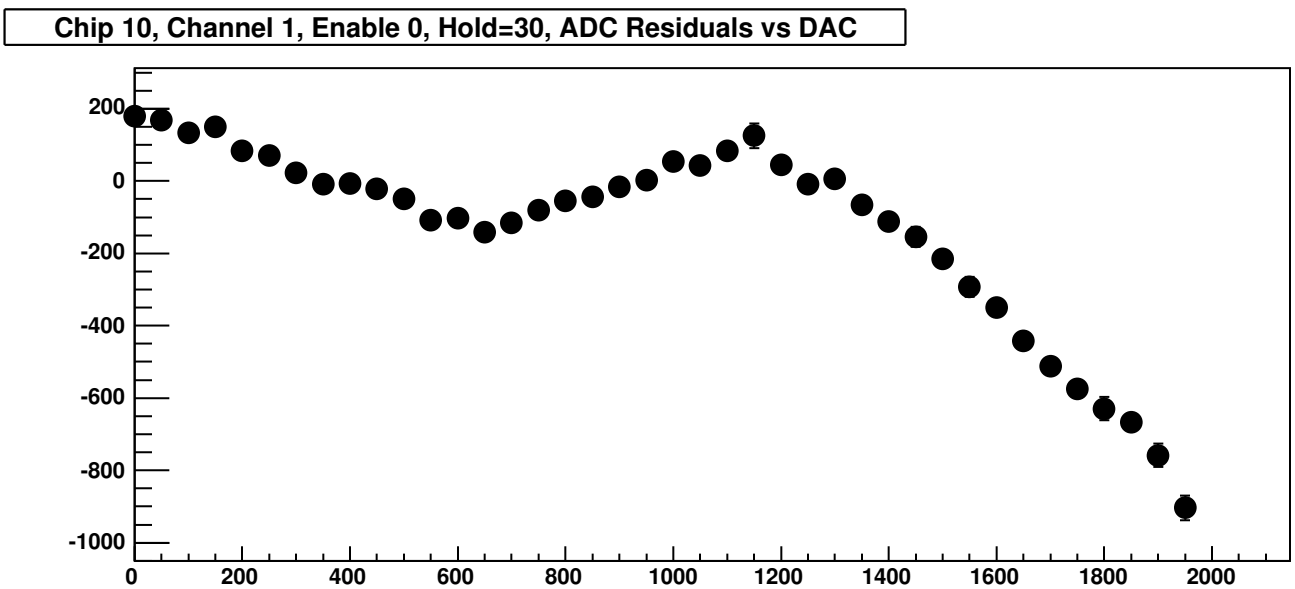

Chip 10, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 17.48 / 11
p0 -905.4 ± 23.75
p1 0.462 ± 0.0218
100
0
-100
-200
-300
-400
-500
-600
-700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

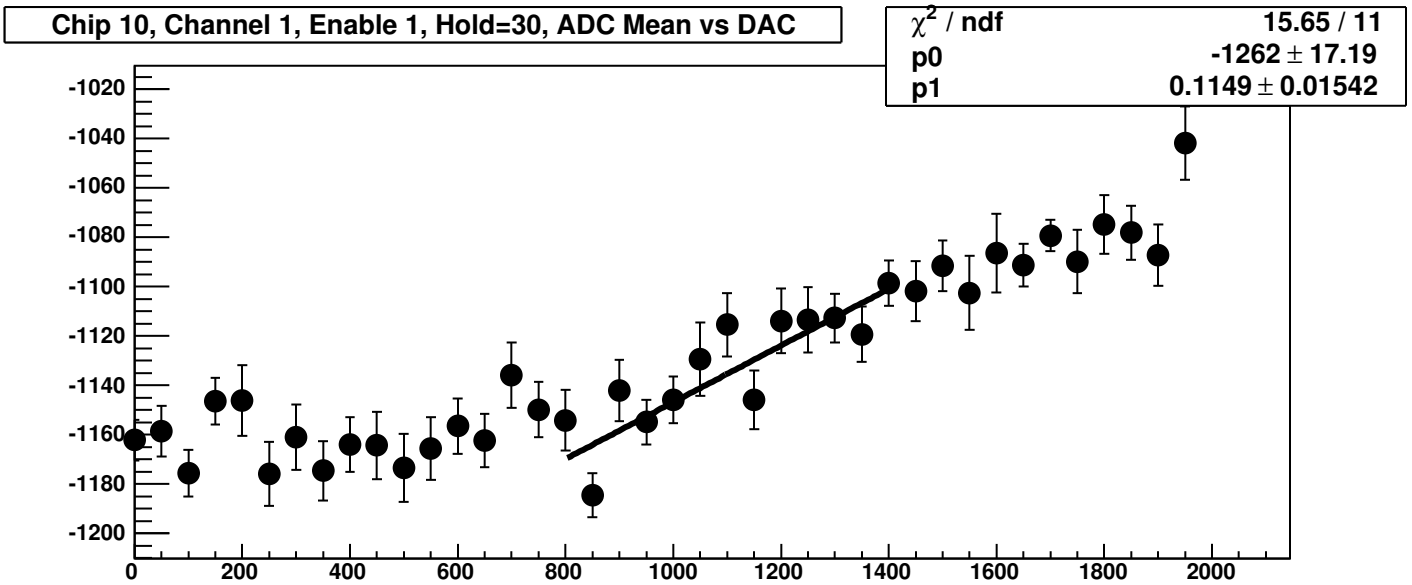

Chip 10, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

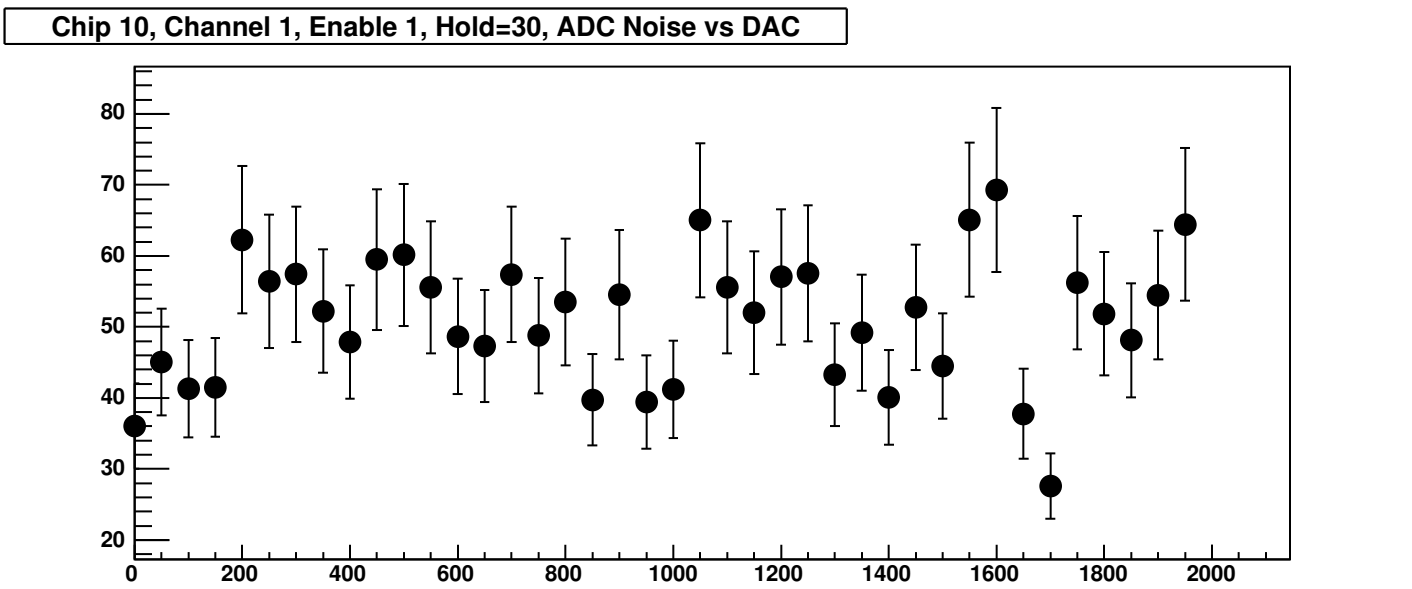

Chip 10, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

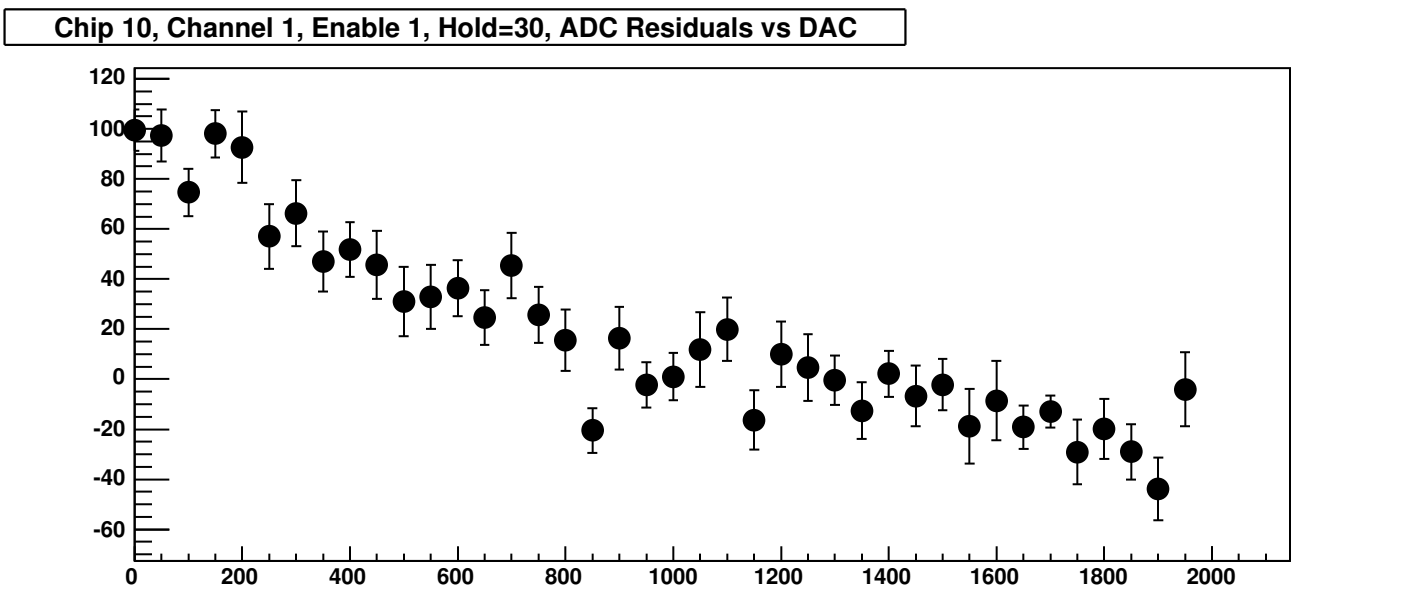

Chip 10, Channel 1, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 80.92 / 11
p0 -947.7 ± 36.7
p1 0.5145 ± 0.03353
-200
-300
-400
-500
-600
-700
-800
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

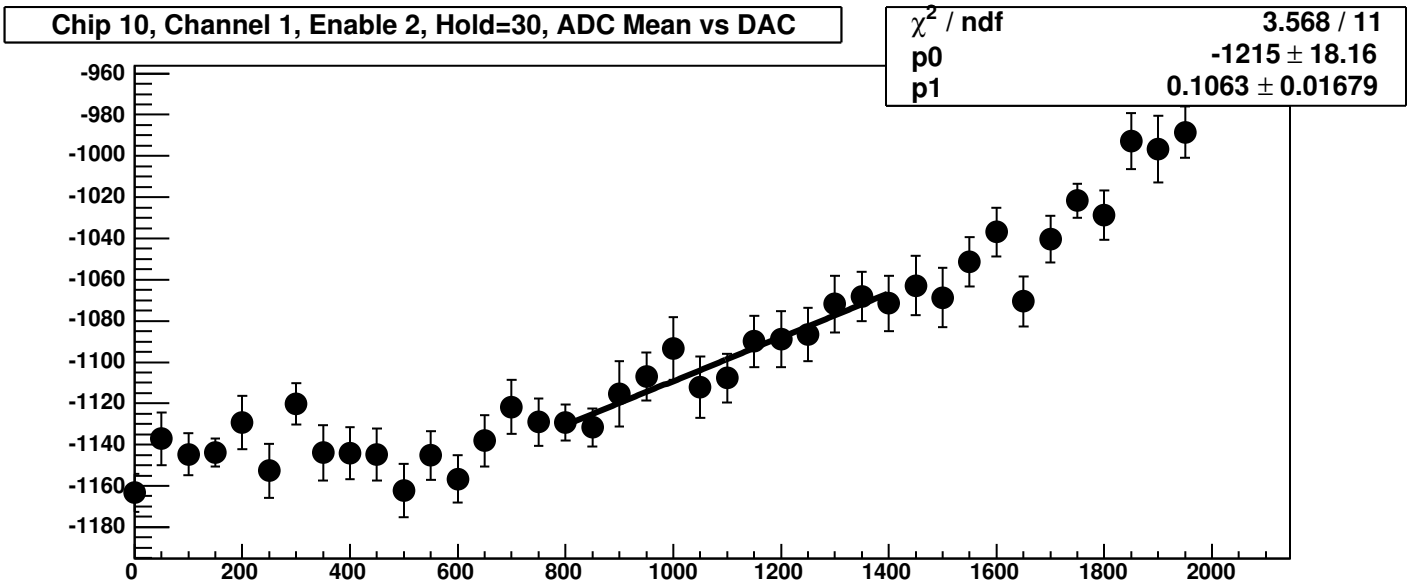

Chip 10, Channel 1, Enable 0, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

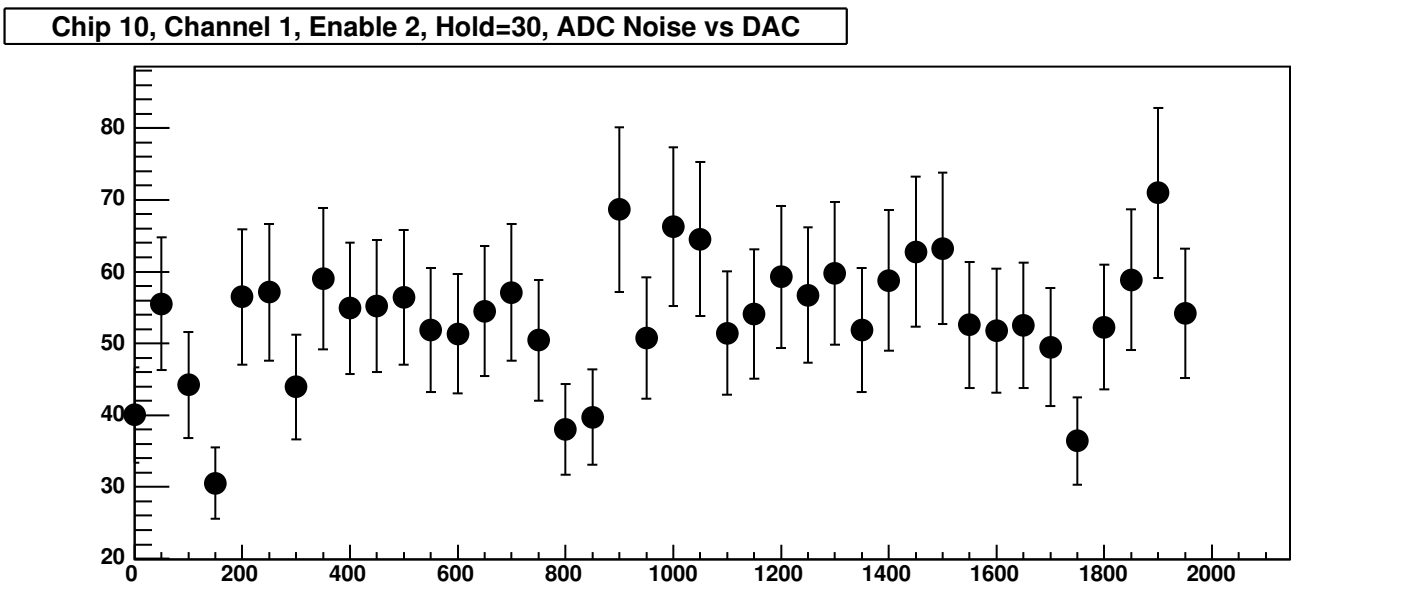

Chip 10, Channel 1, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

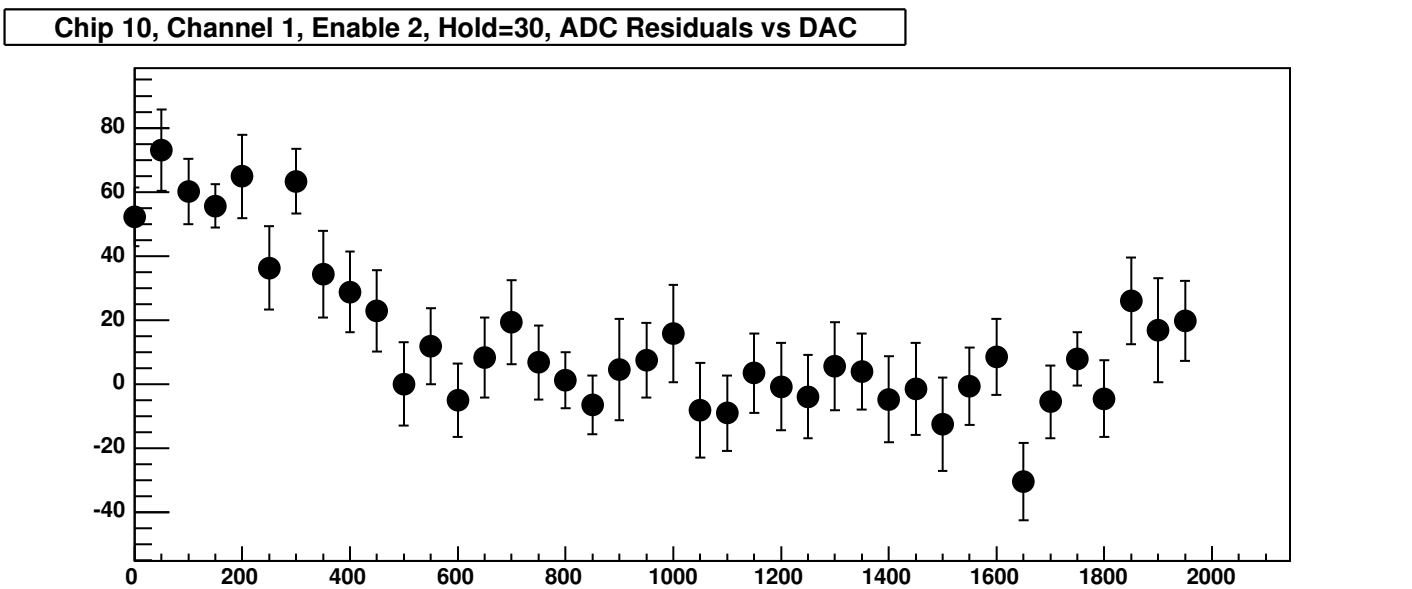

Chip 10, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 15.65 / 11
p0 -1262 ± 17.19
p1 0.1149 ± 0.01542
-1020
-1040
-1060
-1080
-1100
-1120
-1140
-1160
-1180
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

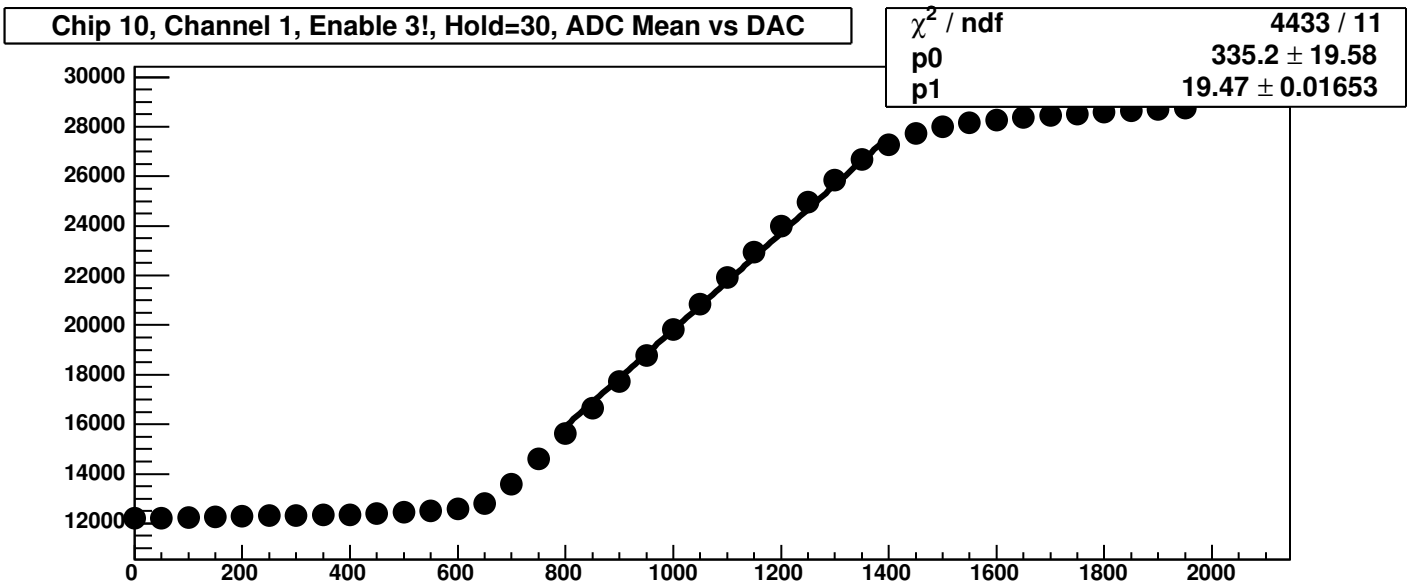

Chip 10, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

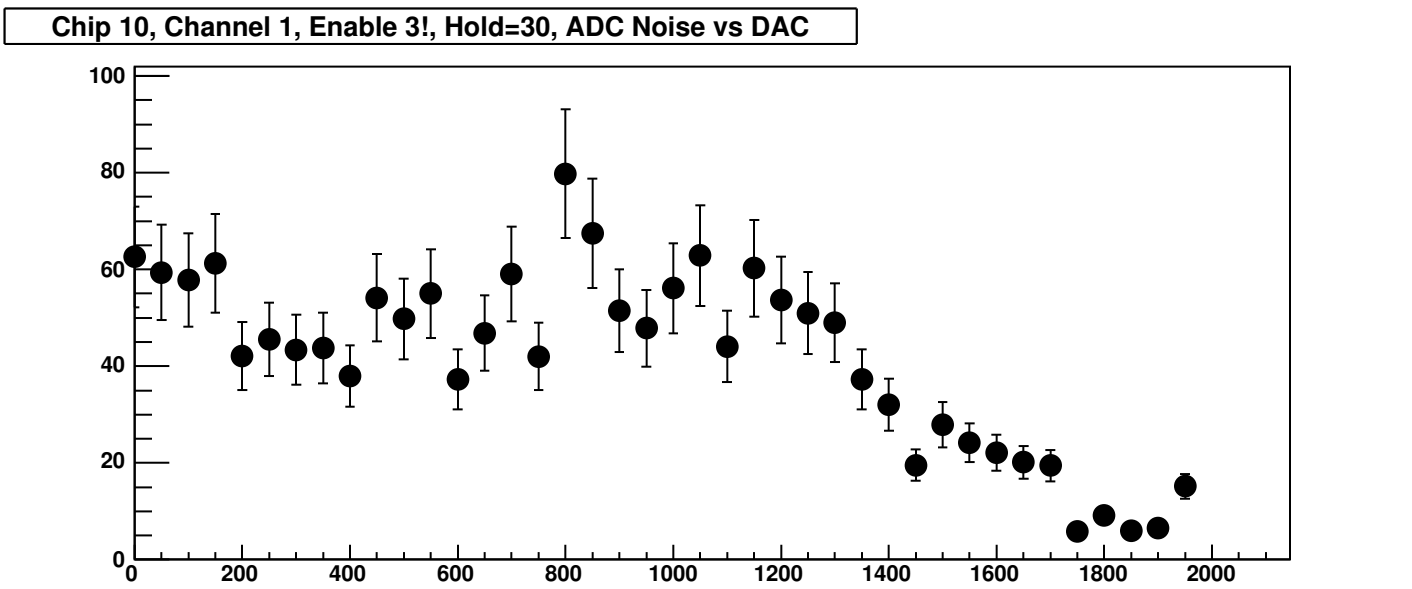

Chip 10, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

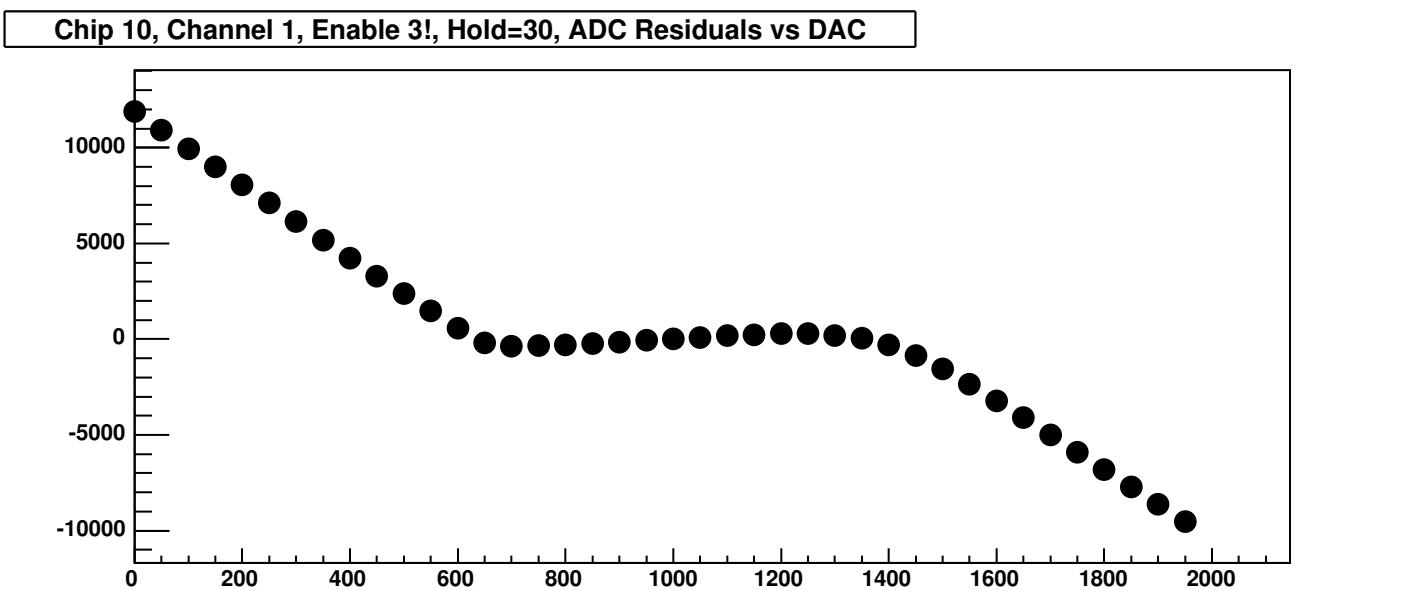

Chip 10, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 3.568 / 11
p0 -1215 ± 18.16
p1 0.1063 ± 0.01679
-960
-980
-1000
-1020
-1040
-1060
-1080
-1100
-1120
-1140
-1160
-1180
0
200
400
600
800
1000
1200
1400
1600
1800
2000

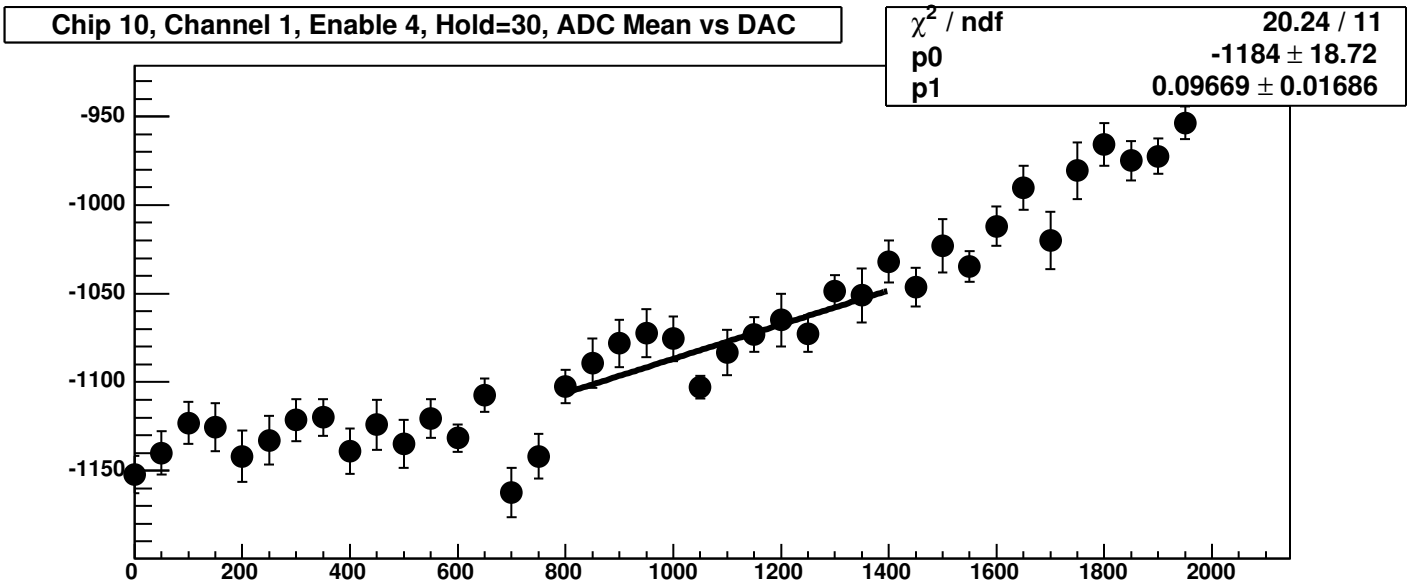

Chip 10, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

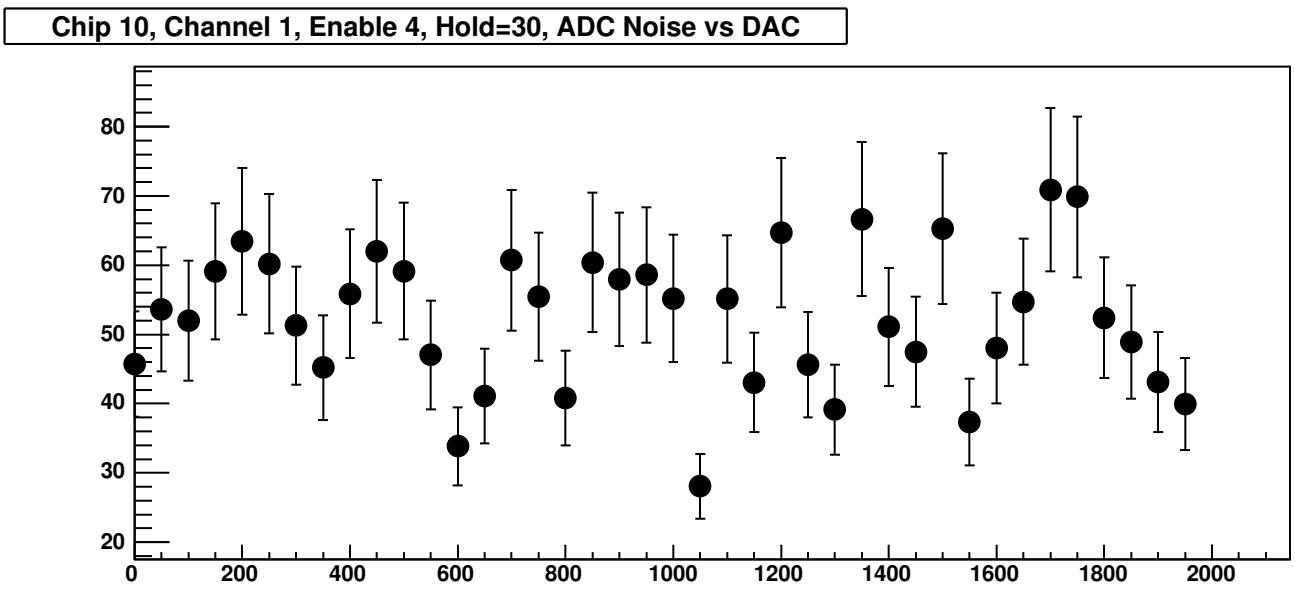

Chip 10, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

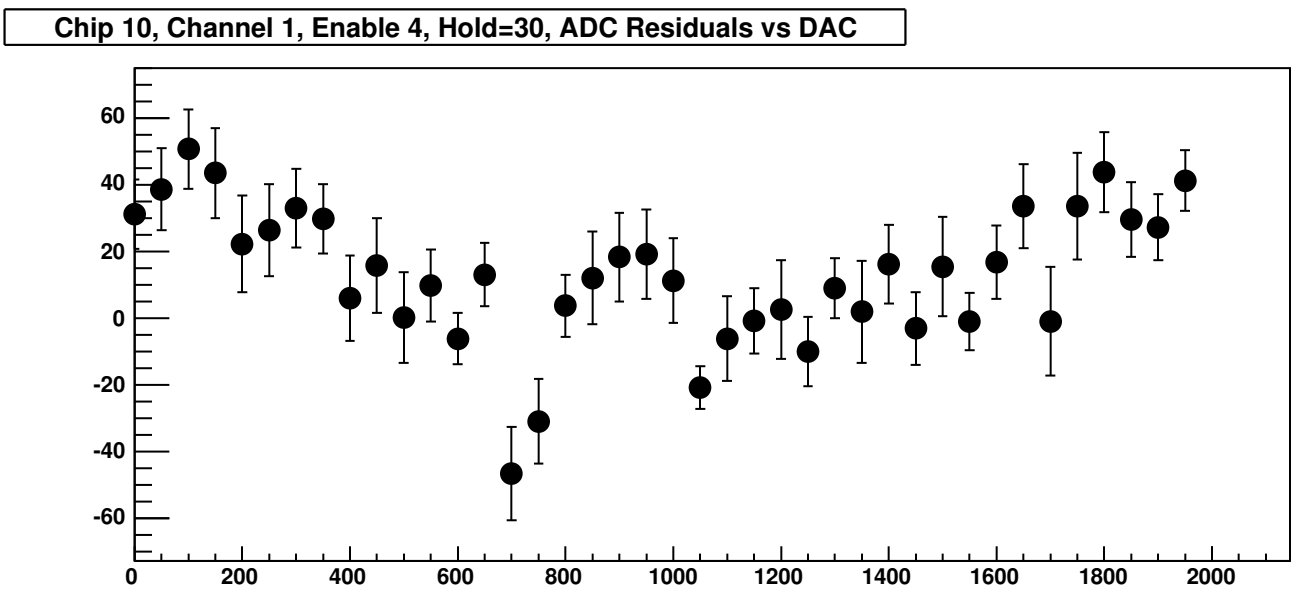

Chip 10, Channel 1, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 4433 / 11
p0 335.2 ± 19.58
p1 19.47 ± 0.01653
30000
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

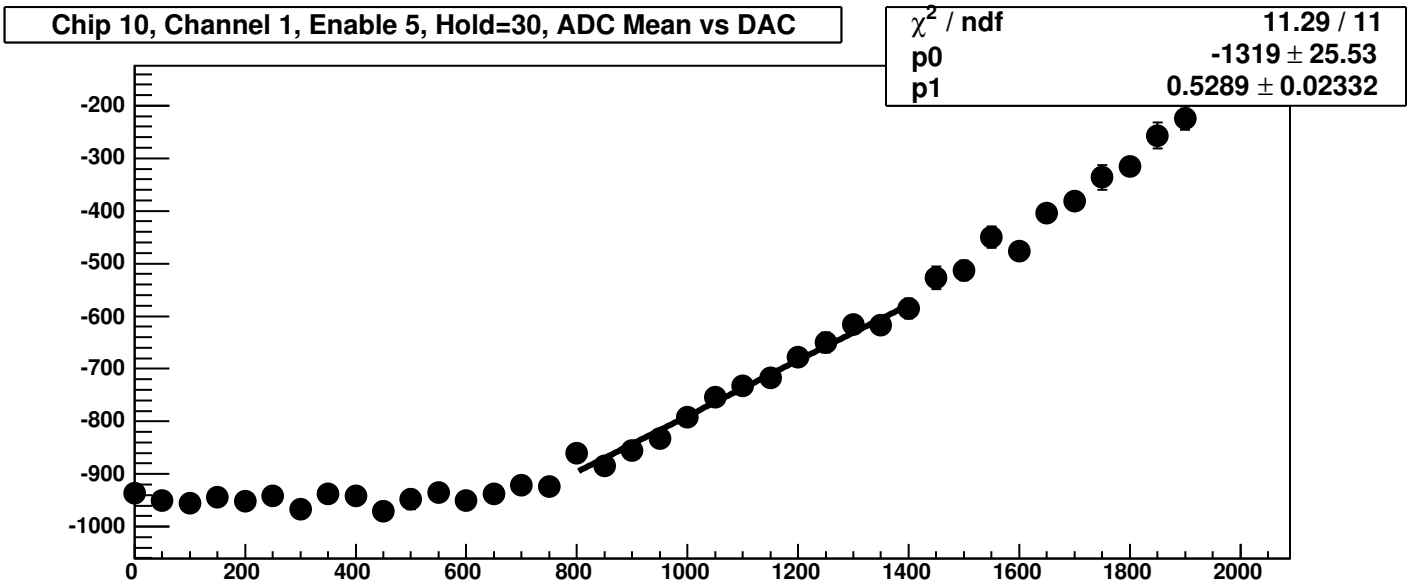

Chip 10, Channel 1, Enable 3!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

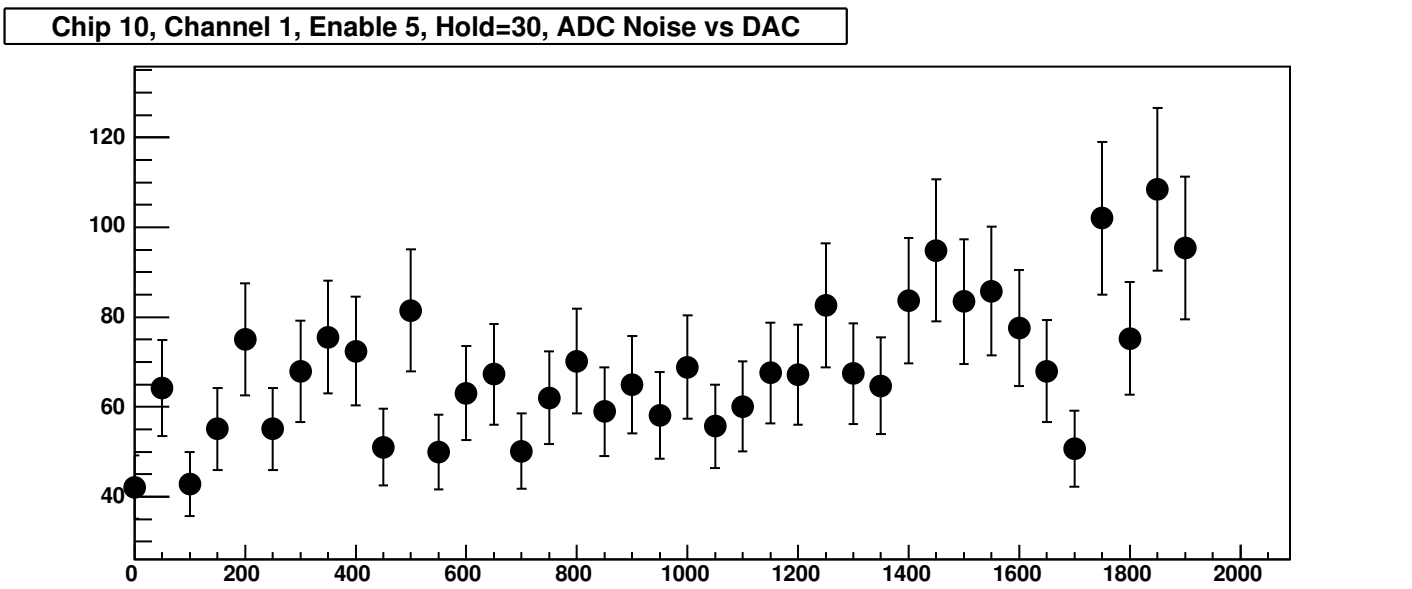

Chip 10, Channel 1, Enable 3!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

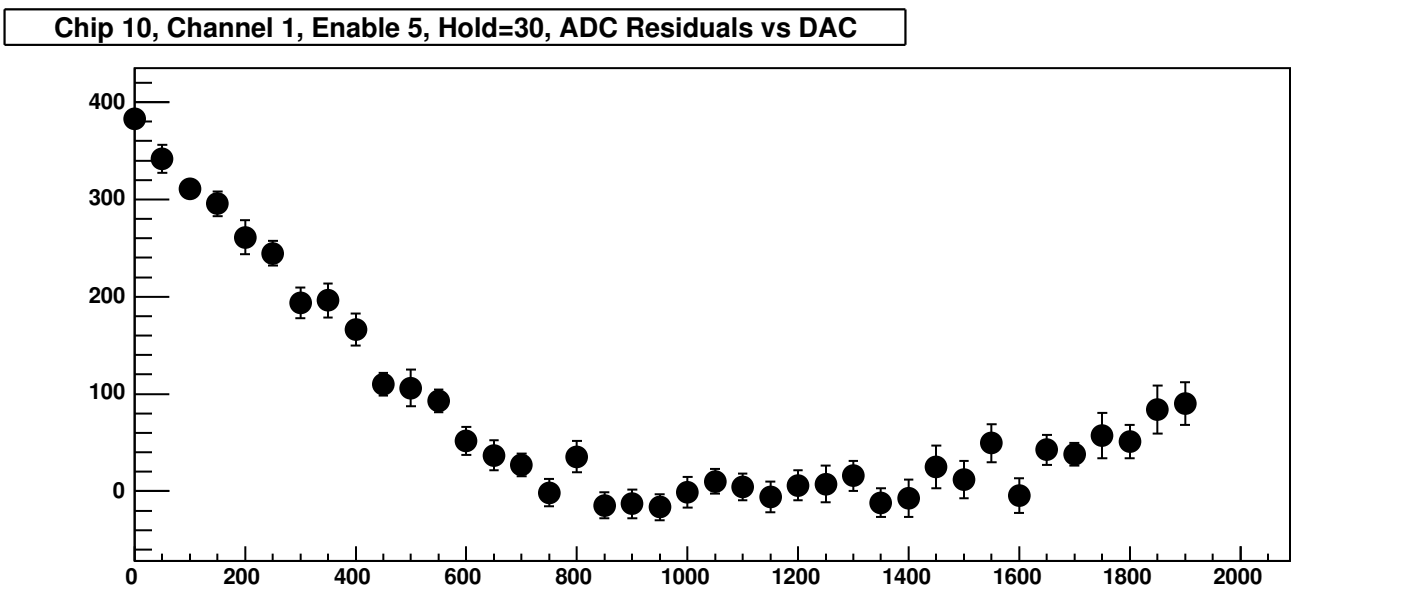

Chip 10, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 20.24 / 11
p0 -1184 ± 18.72
p1 0.09669 ± 0.01686
-950
-1000
-1050
-1100
-1150
0
200
400
600
800
1000
1200
1400
1600
1800
2000

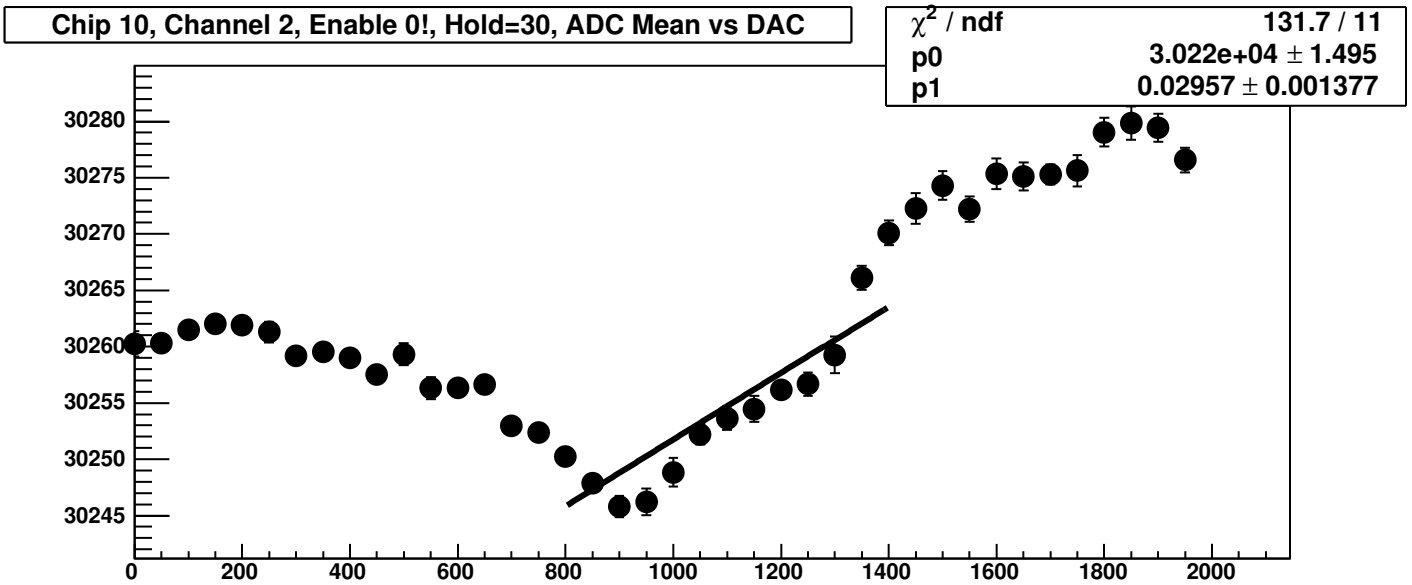

Chip 10, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

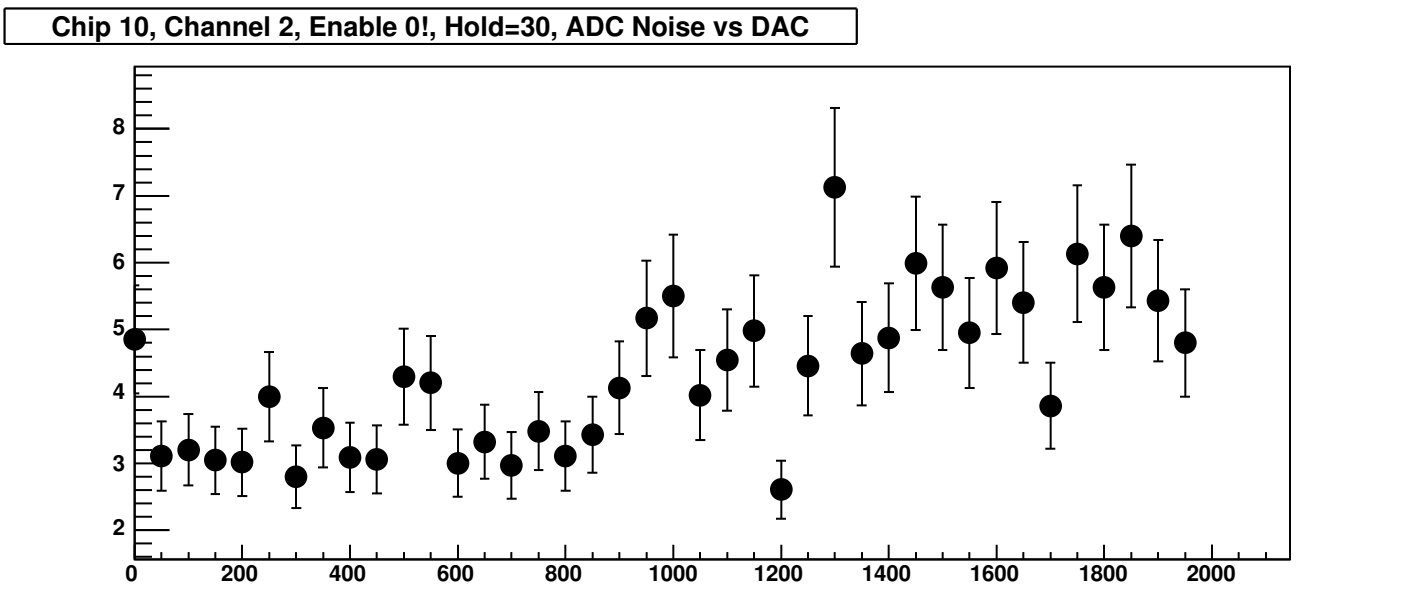

Chip 10, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

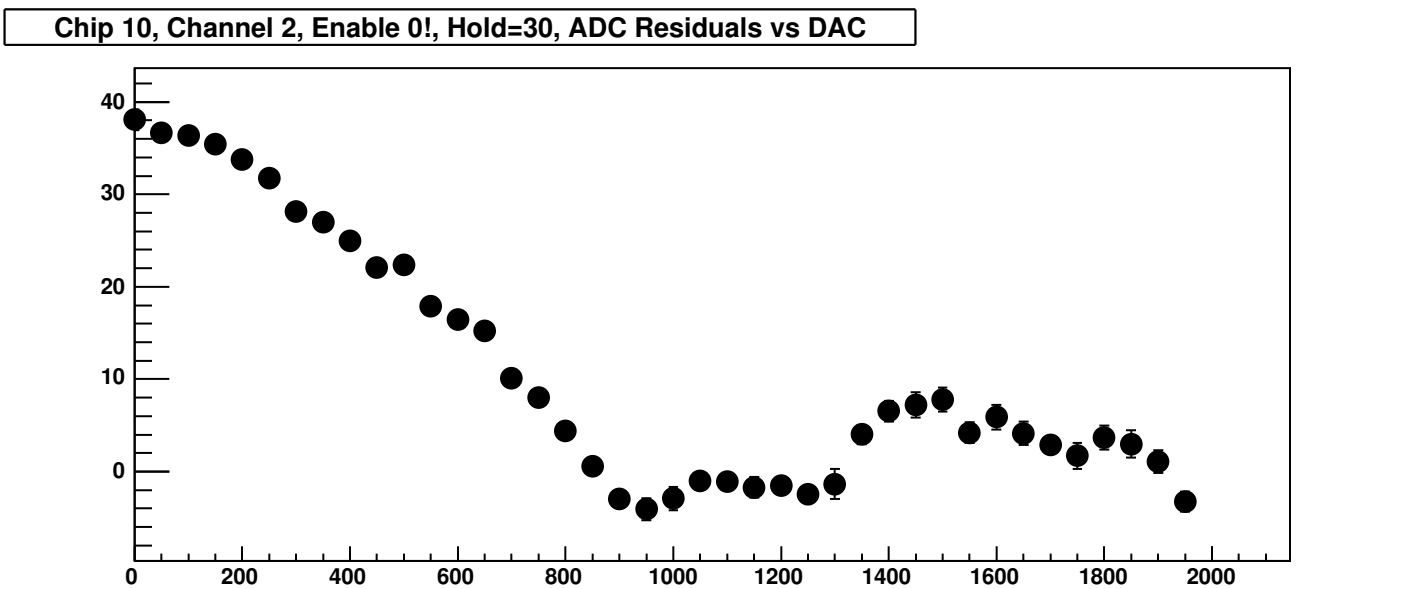

Chip 10, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 11.29 / 11
p0 -1319 ± 25.53
p1 0.5289 ± 0.02332
-200
-300
-400
-500
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

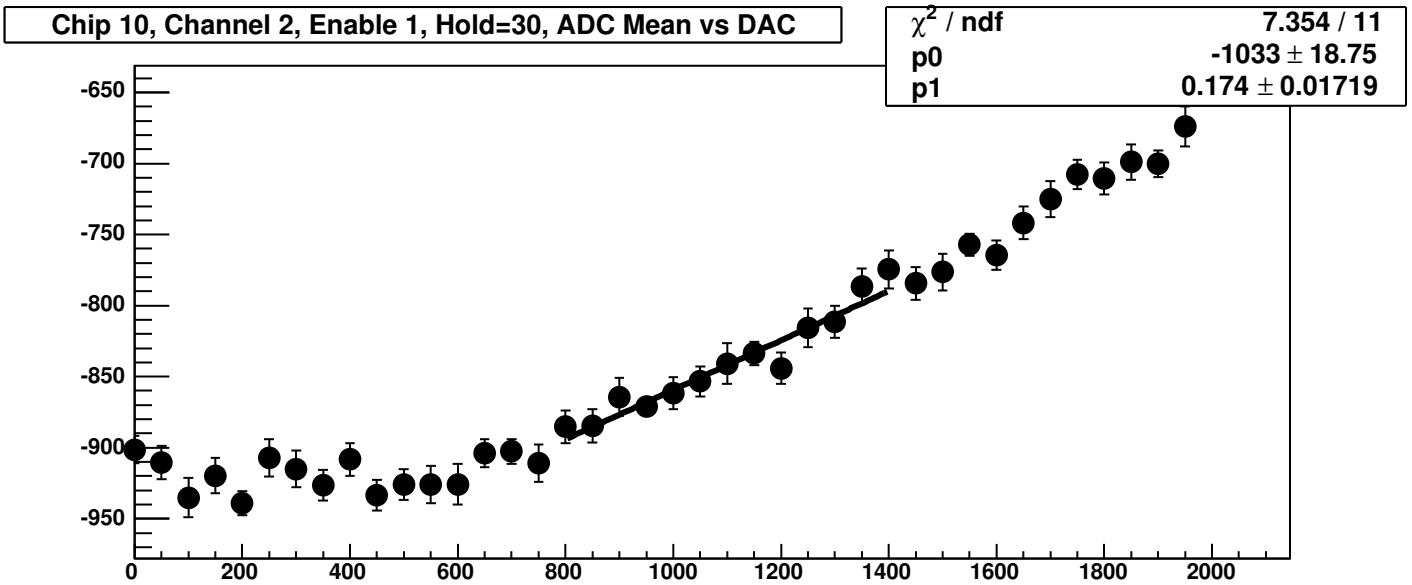

Chip 10, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

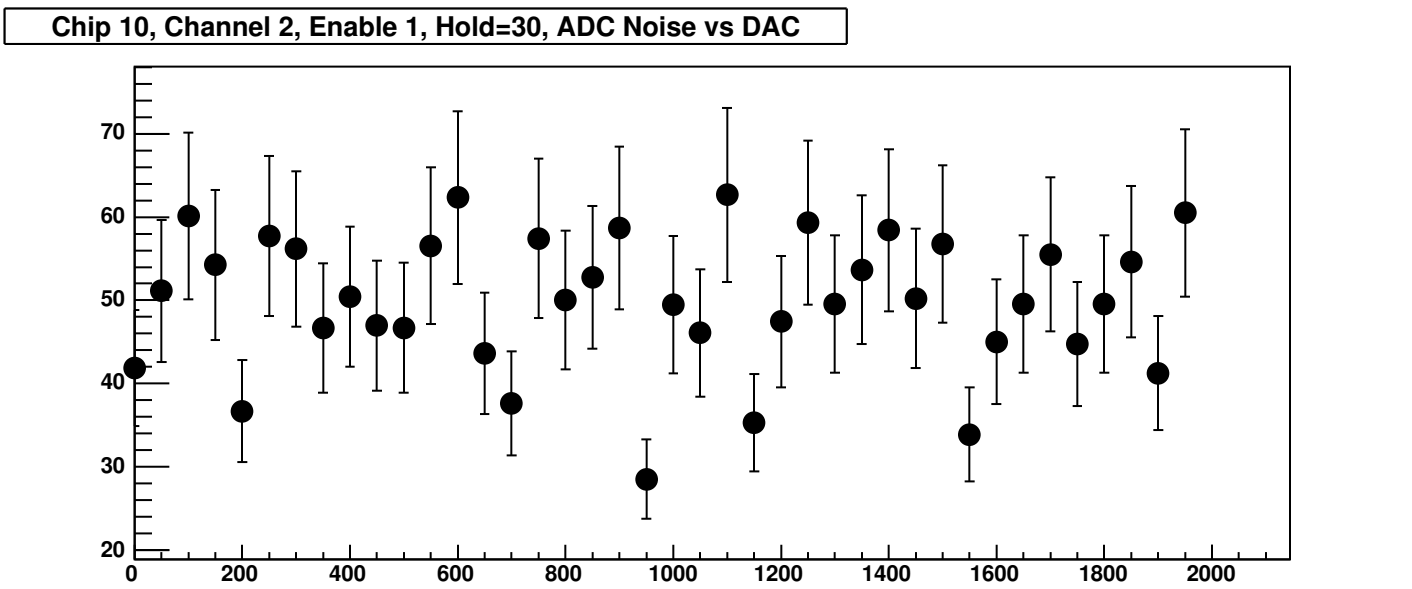

Chip 10, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
400
300
200
100
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

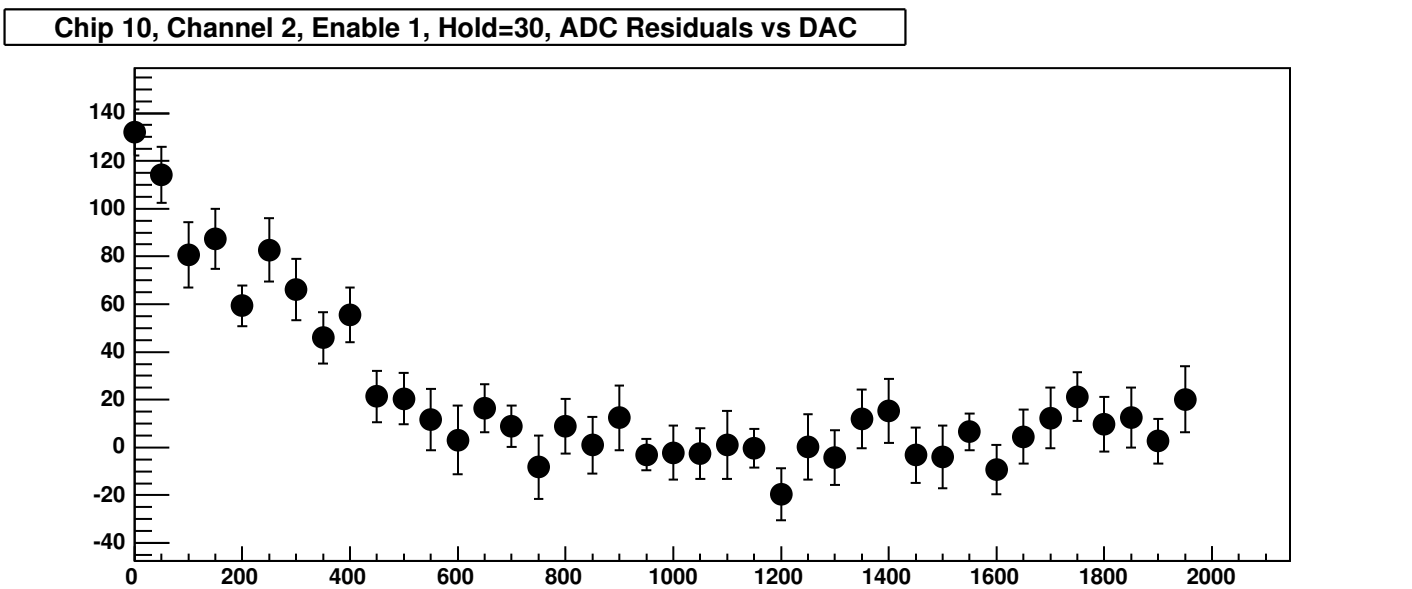

Chip 10, Channel 2, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 131.7 / 11
p0 3.022e+04 ± 1.495
p1 0.02957 ± 0.001377
30280
30275
30270
30265
30260
30255
30250
30245
0
200
400
600
800
1000
1200
1400
1600
1800
2000

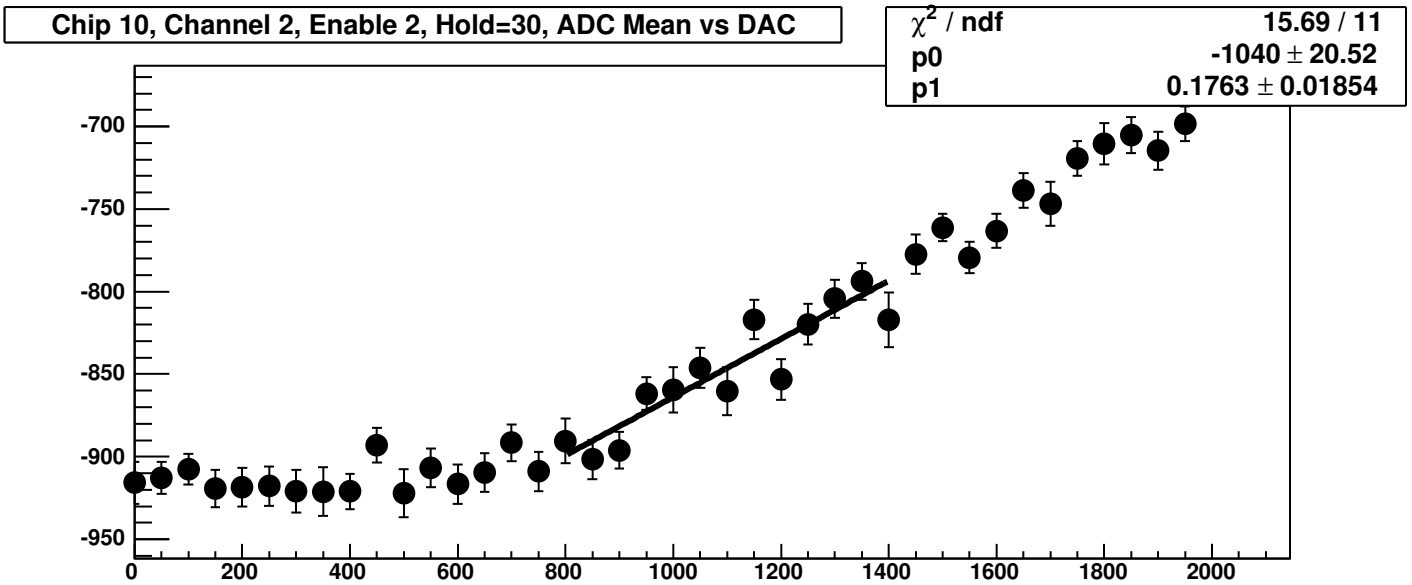

Chip 10, Channel 2, Enable 0!, Hold=30, ADC Noise vs DAC
8
7
6
5
4
3
2
0
200
400
600
800
1000
1200
1400
1600
1800
2000

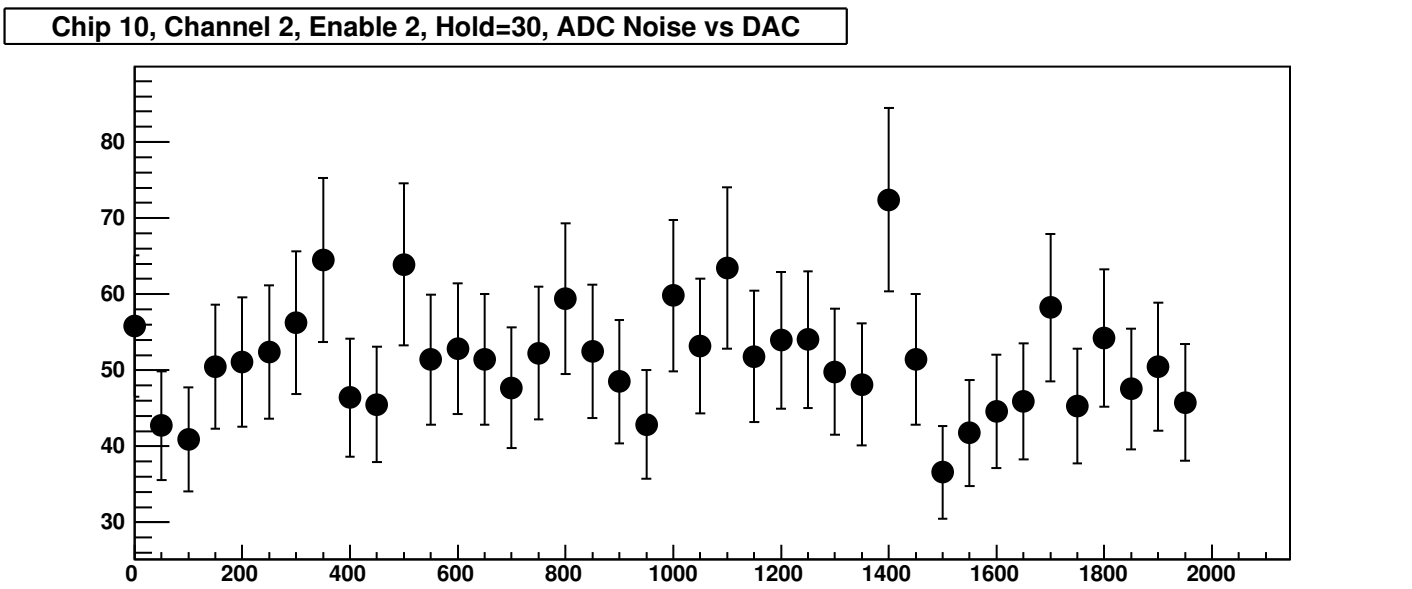

Chip 10, Channel 2, Enable 0!, Hold=30, ADC Residuals vs DAC
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

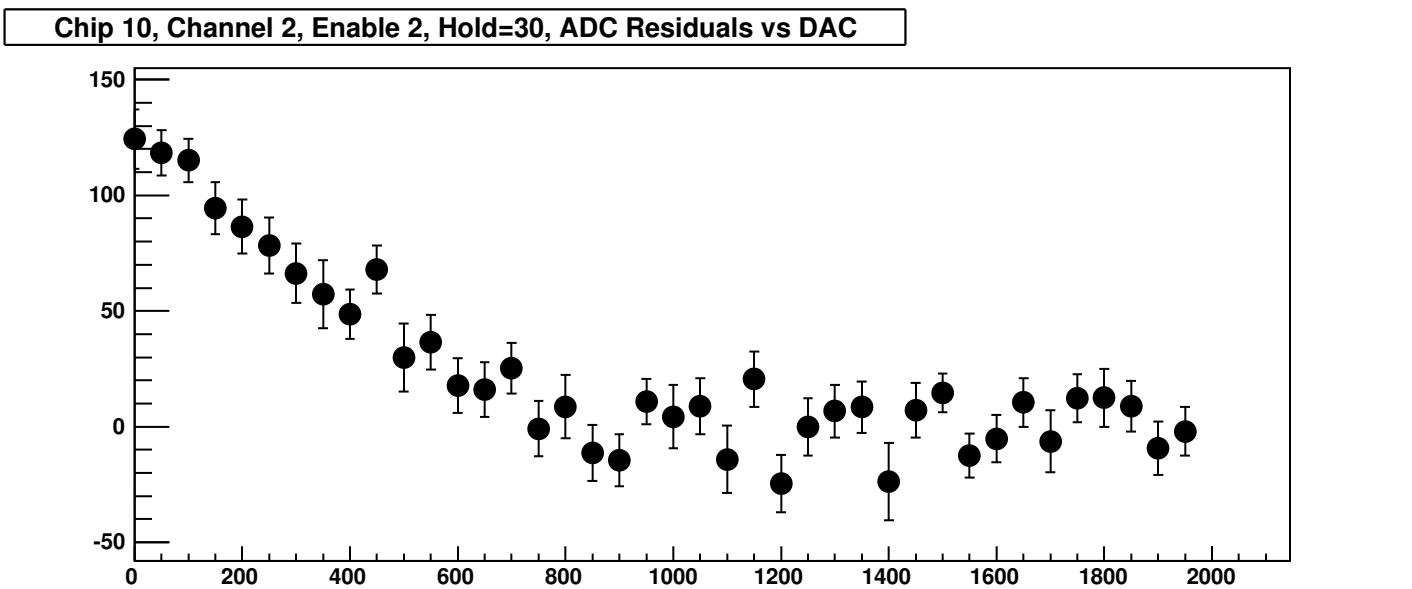

Chip 10, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 7.354 / 11
p0 -1033 ± 18.75
p1 0.174 ± 0.01719
-650
-700
-750
-800
-850
-900
-950
0
200
400
600
800
1000
1200
1400
1600
1800
2000

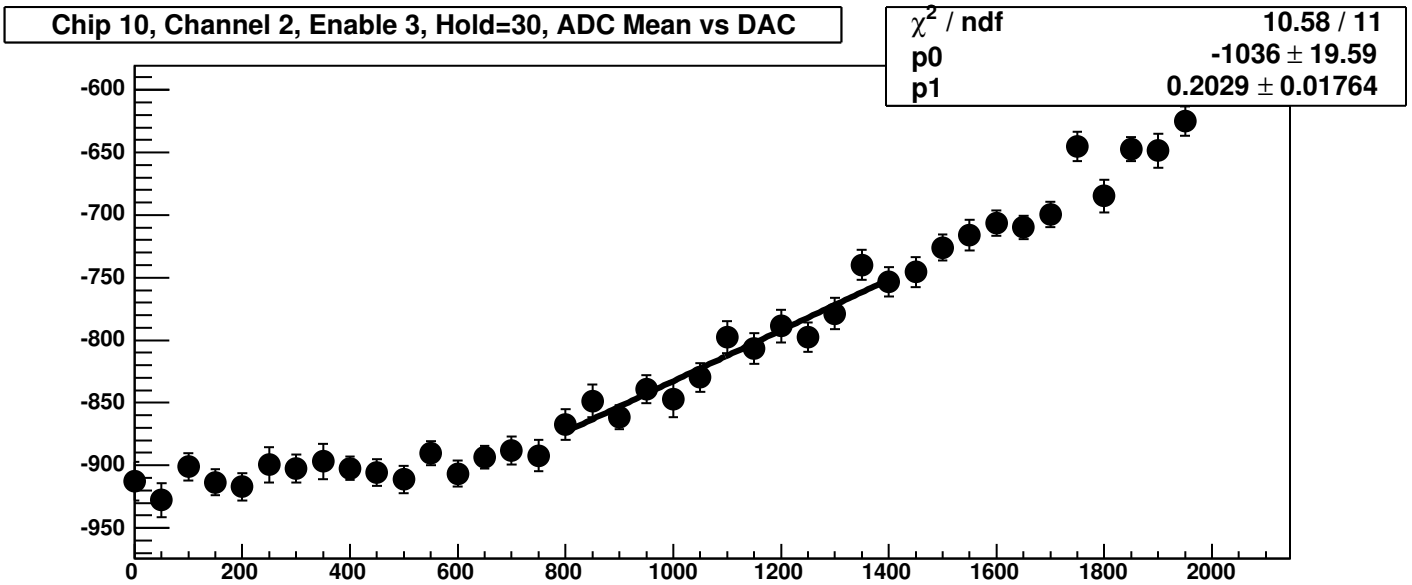

Chip 10, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

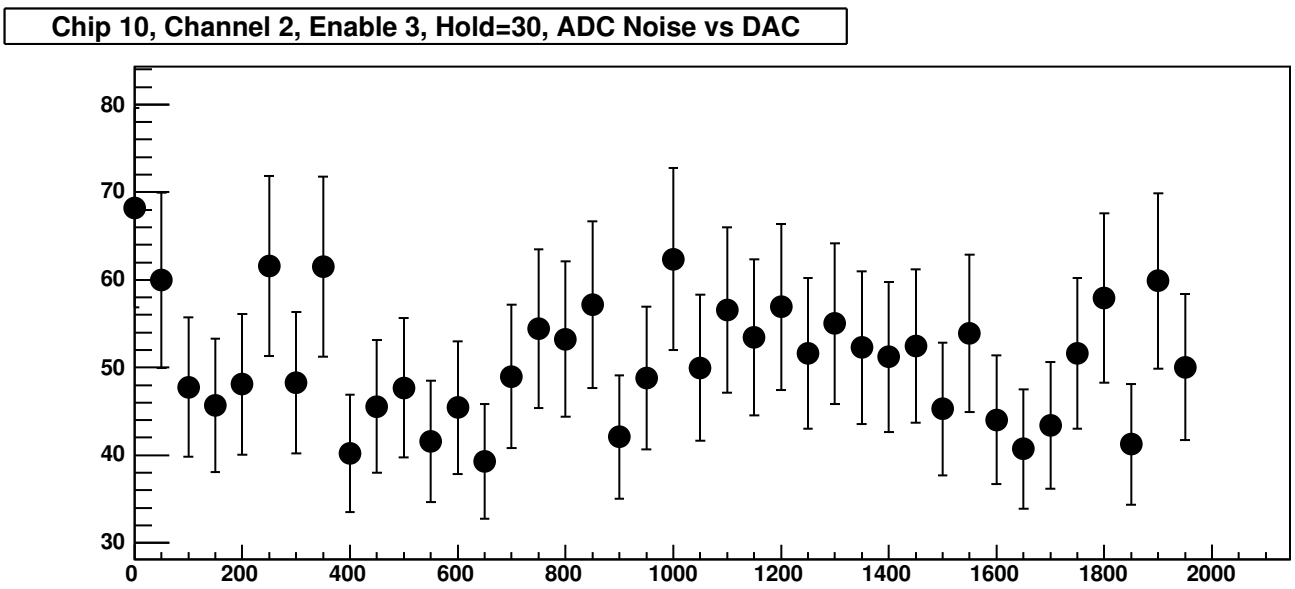

Chip 10, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

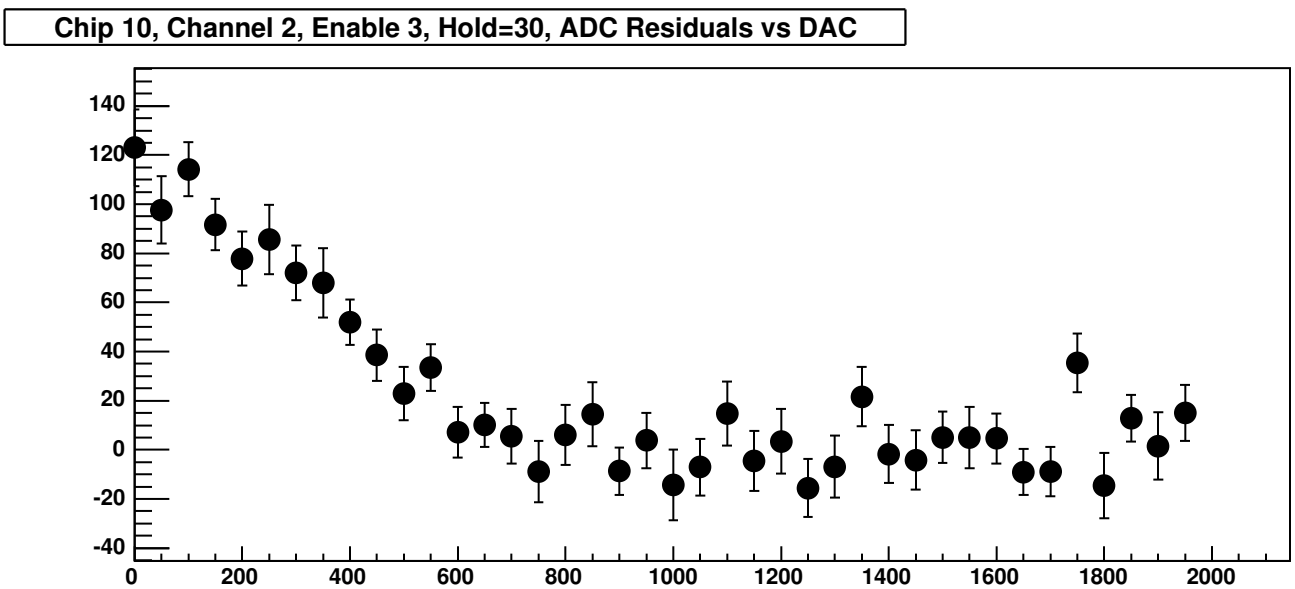

Chip 10, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 15.69 / 11
p0 -1040 ± 20.52
p1 0.1763 ± 0.01854
-700
-750
-800
-850
-900
-950
0
200
400
600
800
1000
1200
1400
1600
1800
2000

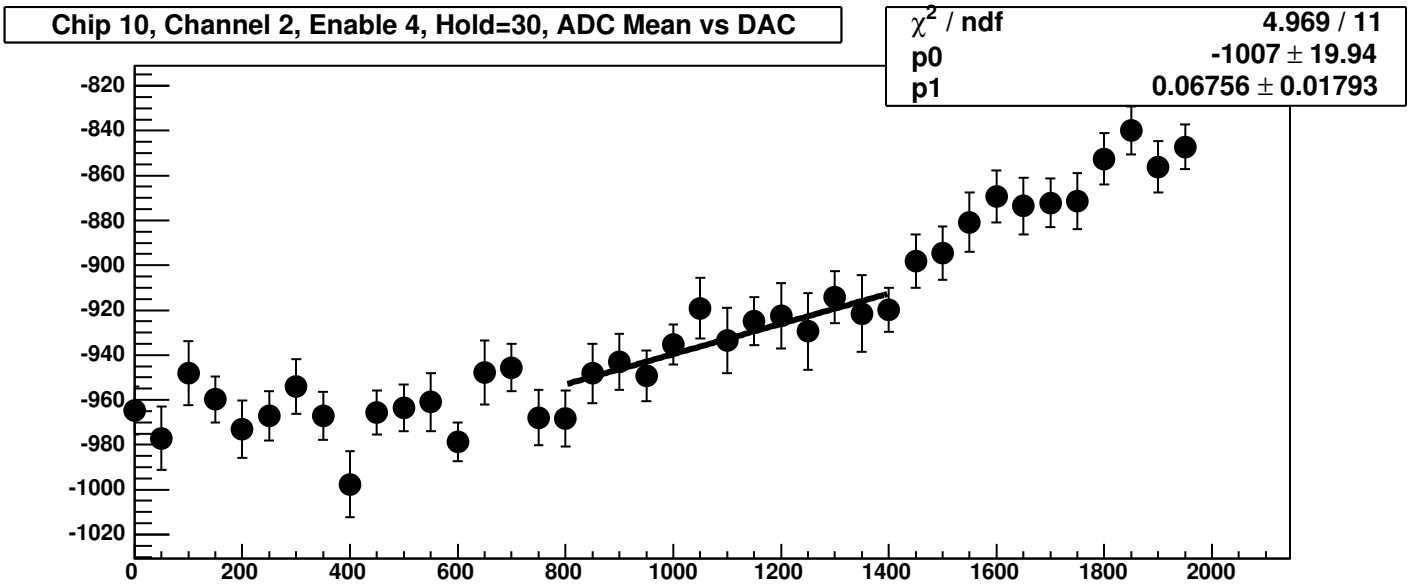

Chip 10, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

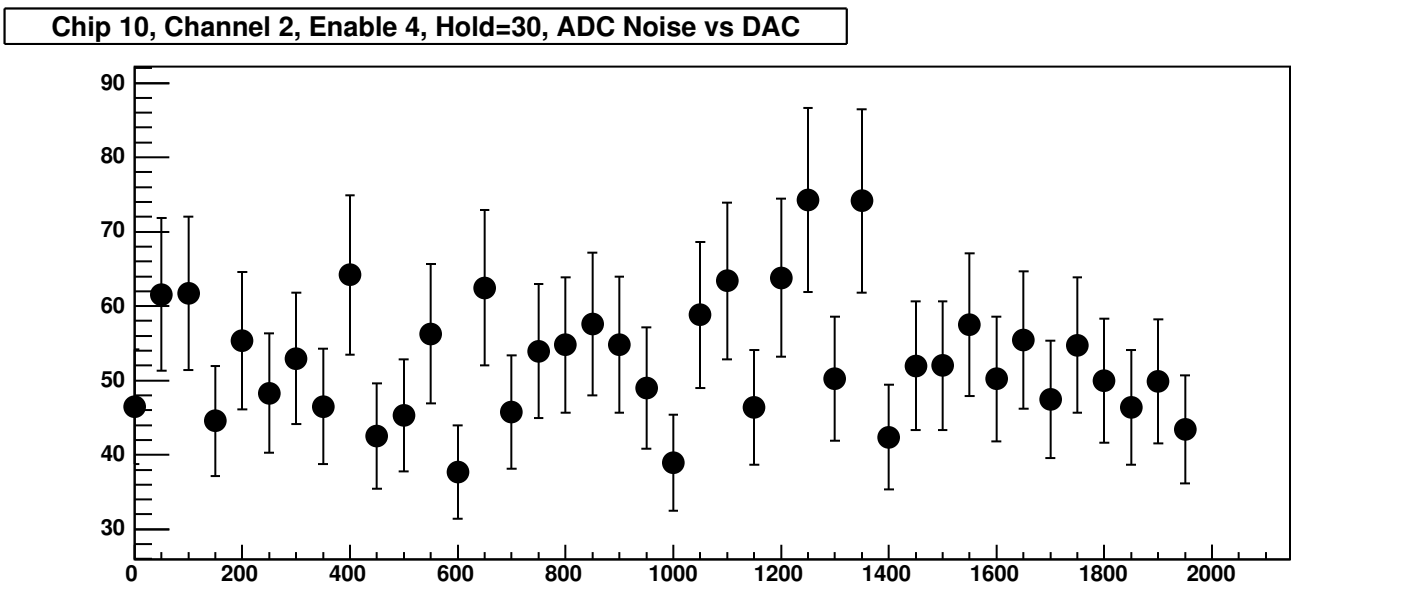

Chip 10, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

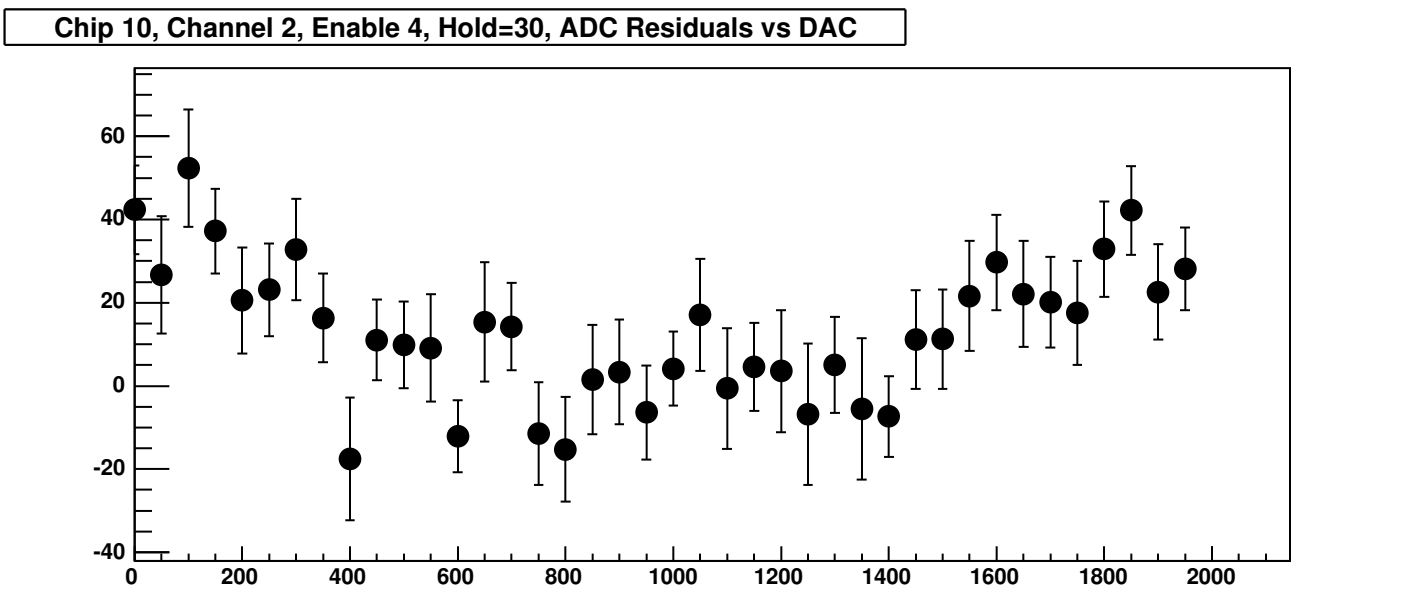

Chip 10, Channel 2, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 10.58 / 11
p0 -1036 ± 19.59
p1 0.2029 ± 0.01764
-600
-650
-700
-750
-800
-850
-900
-950
0
200
400
600
800
1000
1200
1400
1600
1800
2000

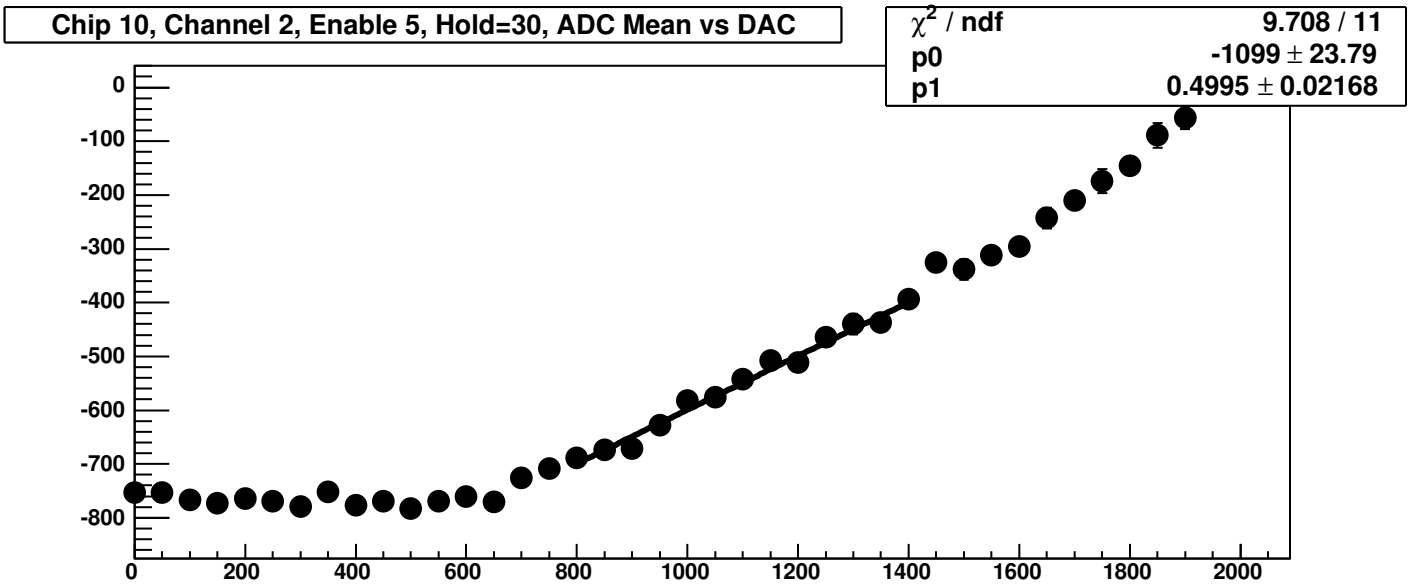

Chip 10, Channel 2, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

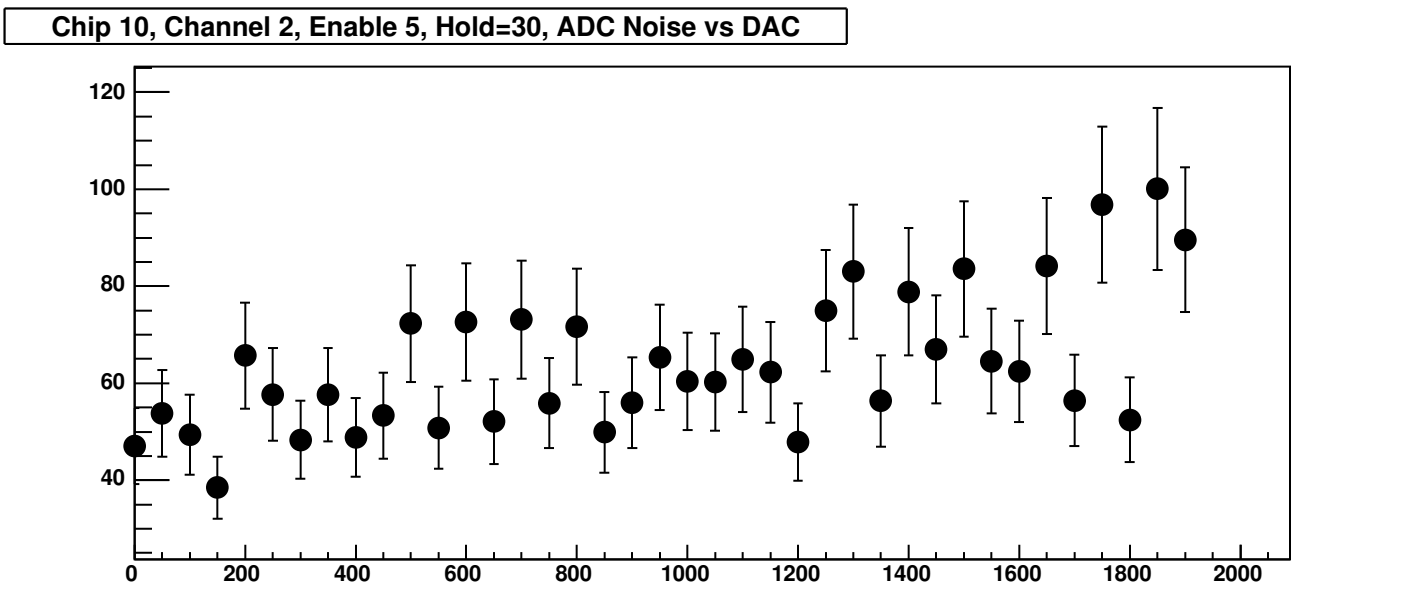

Chip 10, Channel 2, Enable 3, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

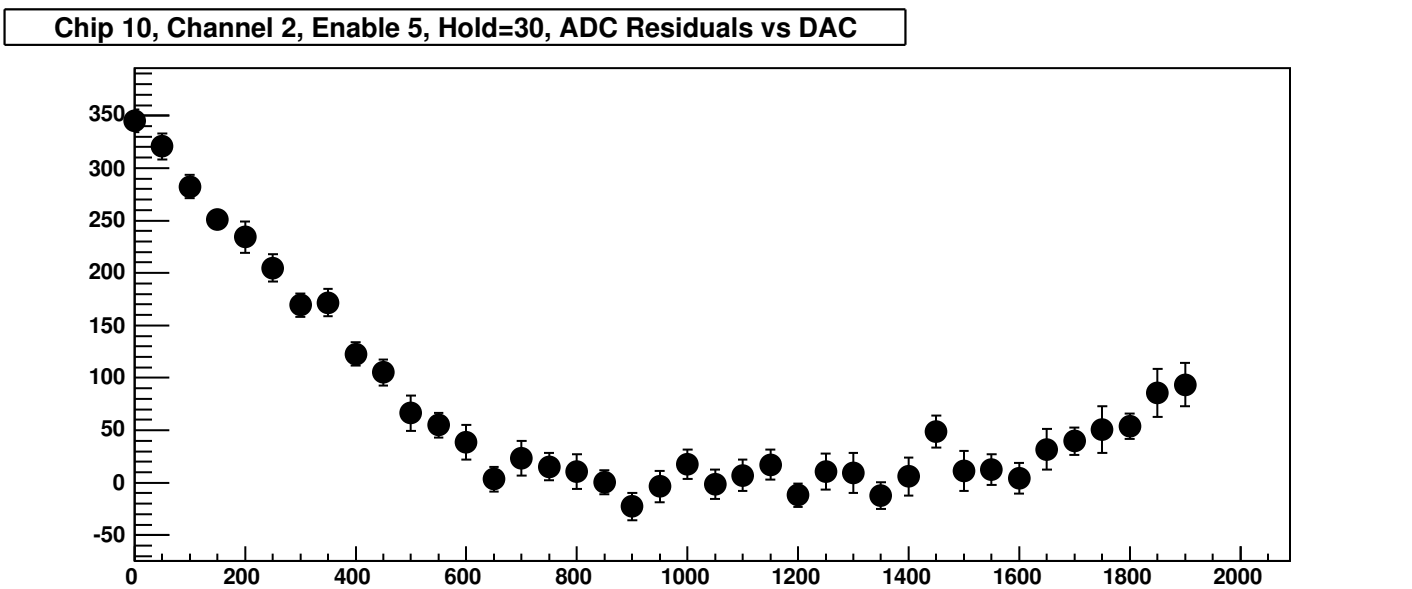

Chip 10, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4.969 / 11
p0 -1007 ± 19.94
p1 0.06756 ± 0.01793
-820
-840
-860
-880
-900
-920
-940
-960
-980
-1000
-1020
0
200
400
600
800
1000
1200
1400
1600
1800
2000

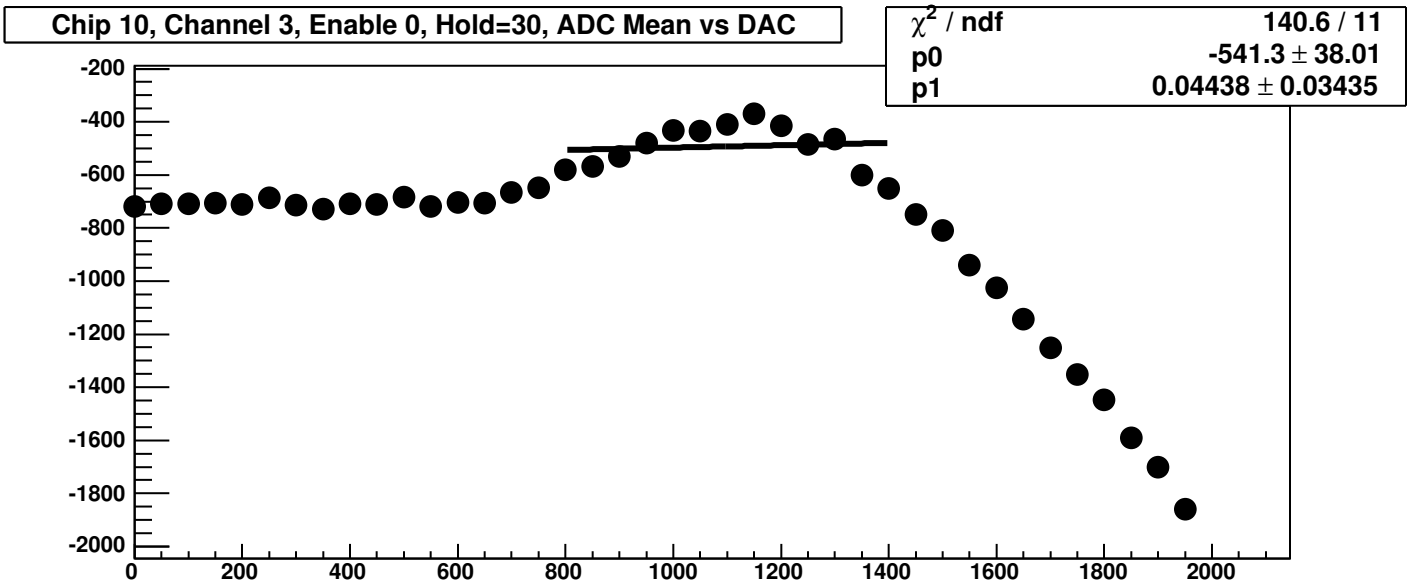

Chip 10, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

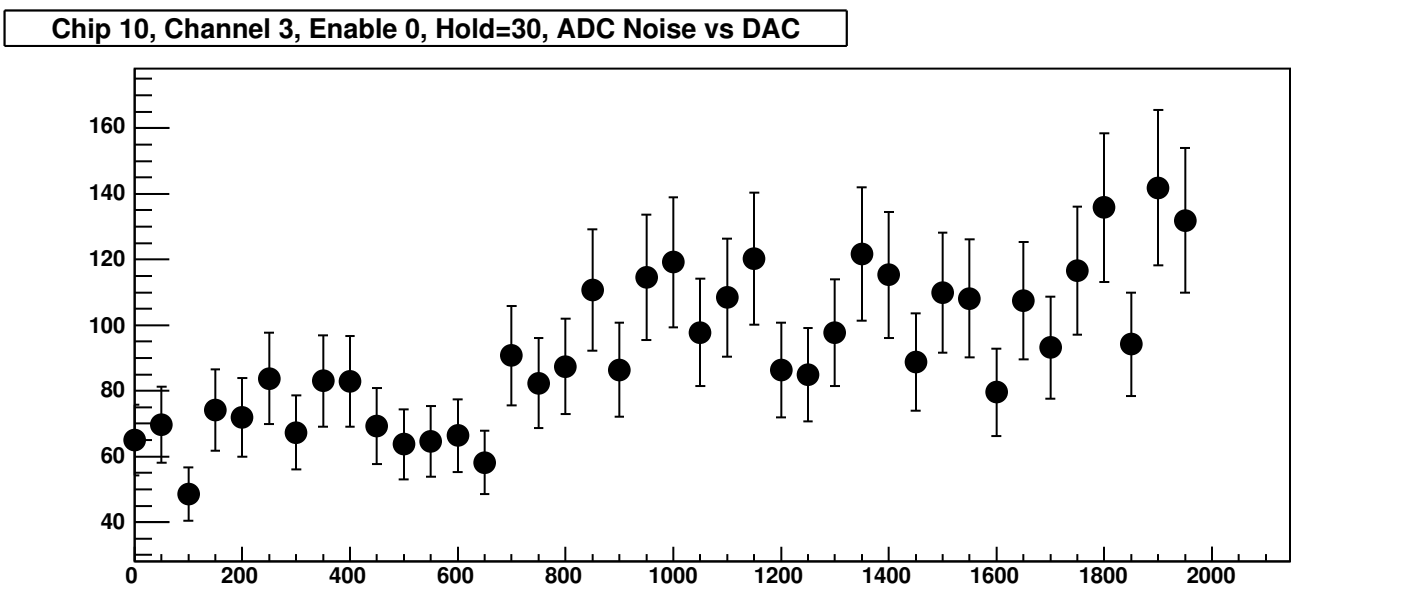

Chip 10, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

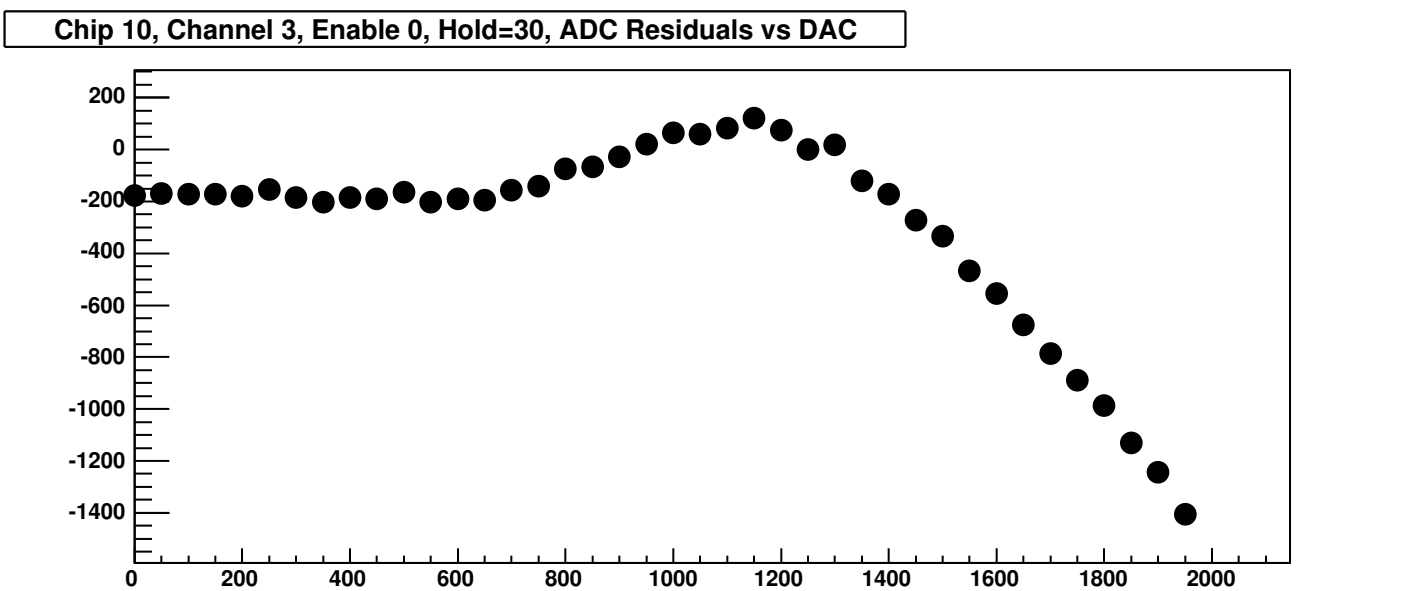

Chip 10, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9.708 / 11
p0 -1099 ± 23.79
p1 0.4995 ± 0.02168
0
-100
-200
-300
-400
-500
-600
-700
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

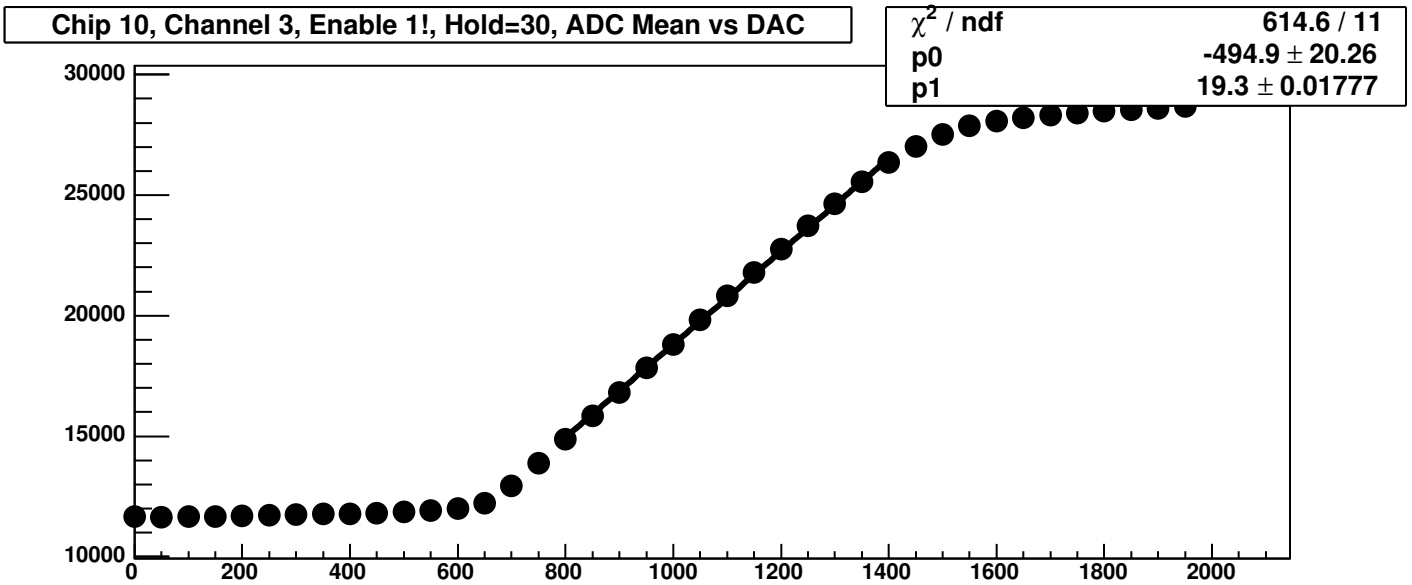

Chip 10, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

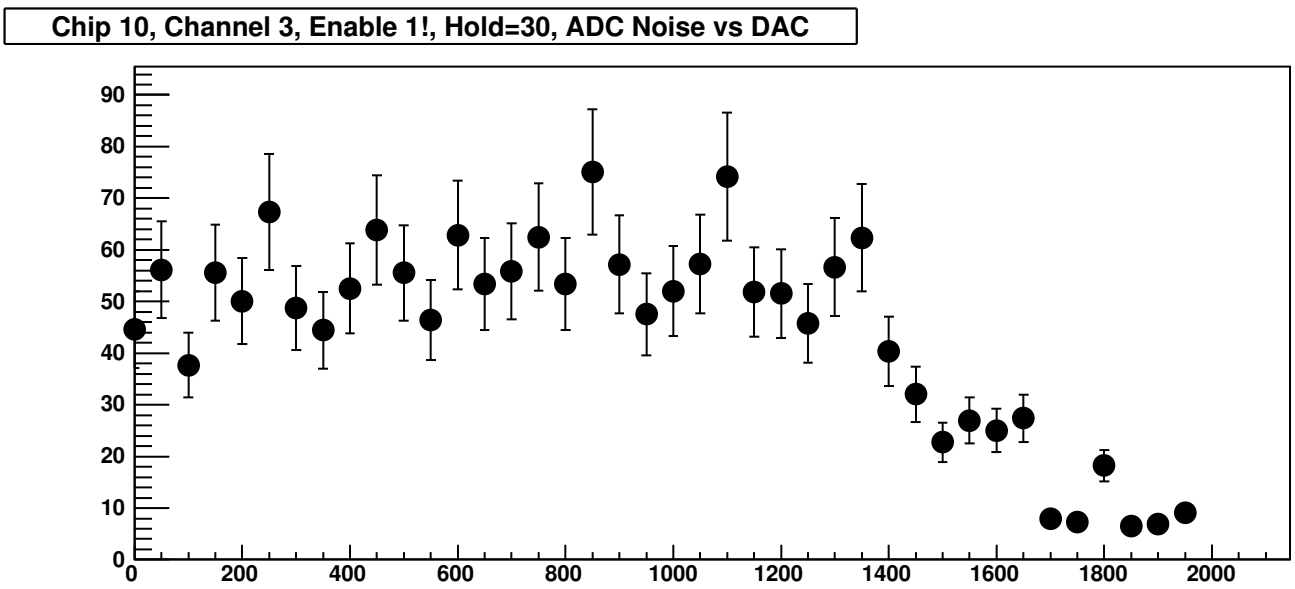

Chip 10, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

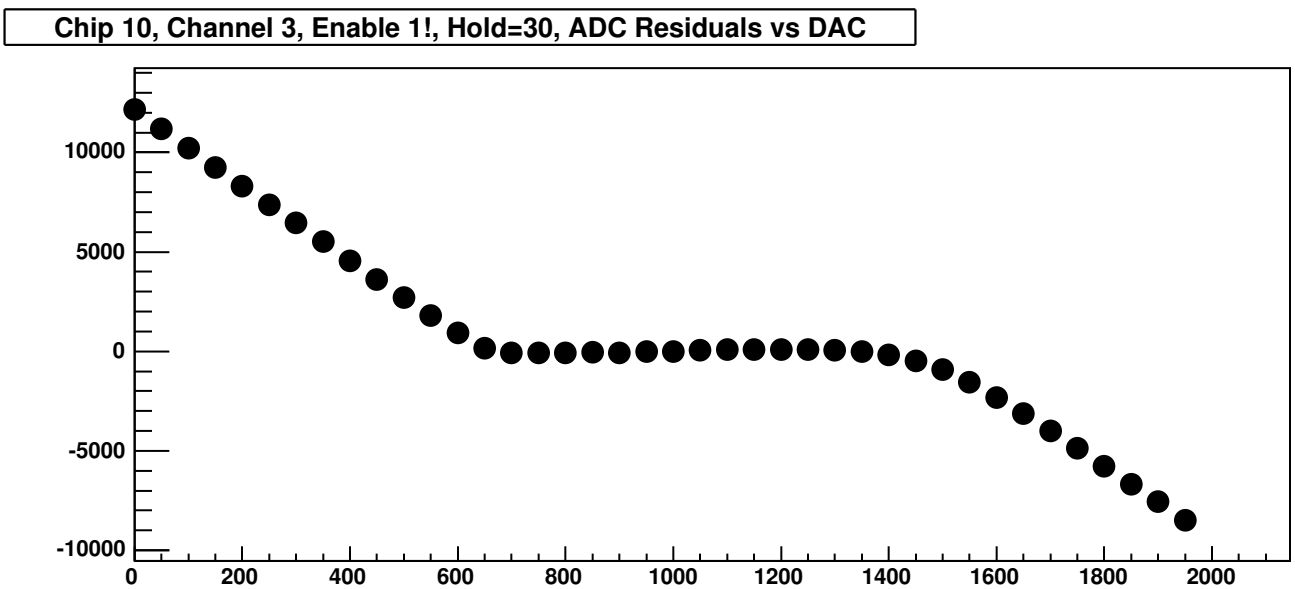

Chip 10, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 140.6 / 11
p0 -541.3 ± 38.01
p1 0.04438 ± 0.03435
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
-2000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

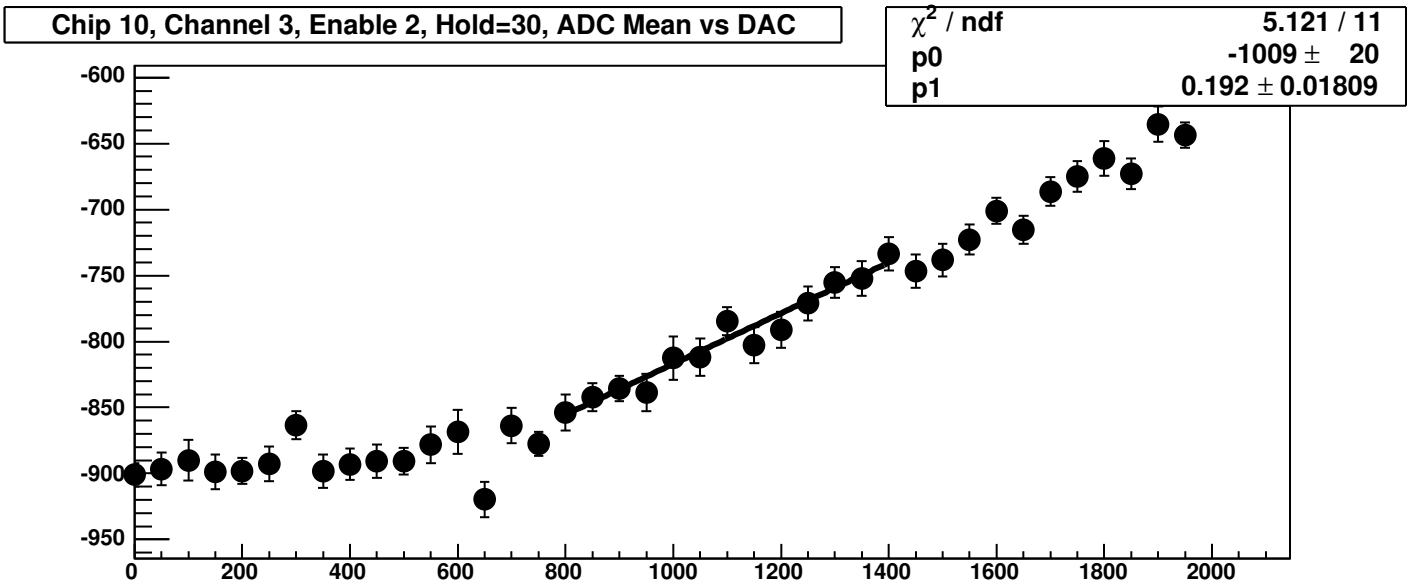

Chip 10, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

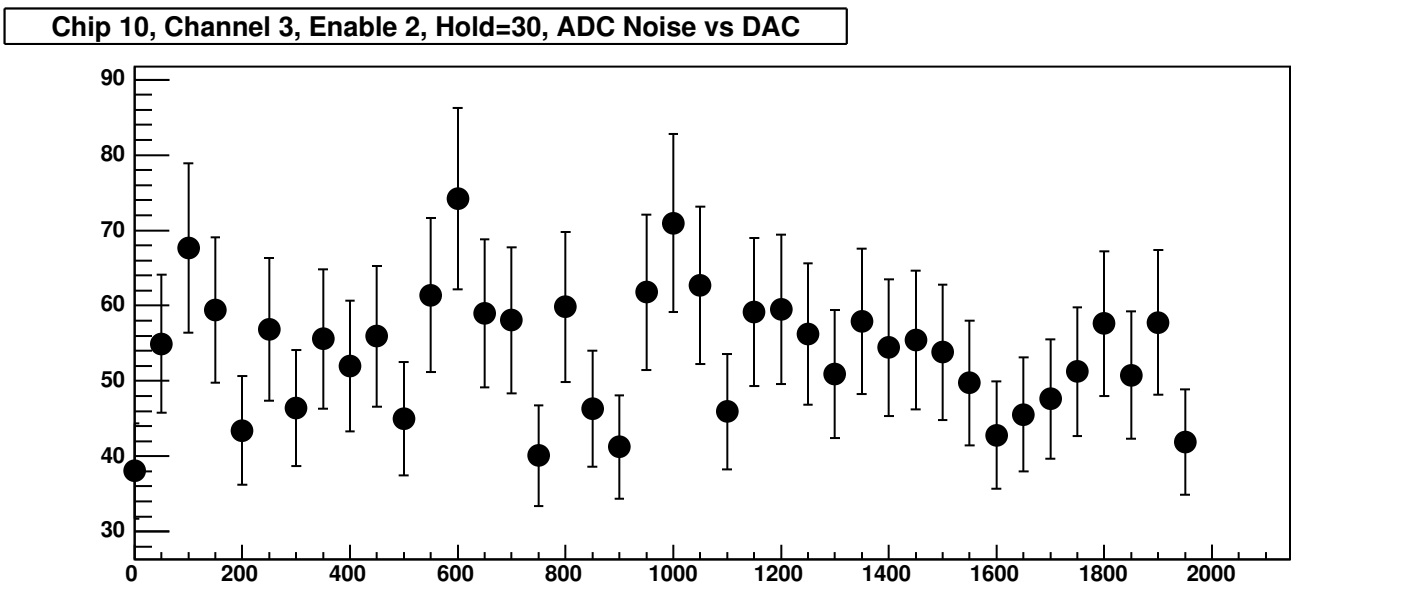

Chip 10, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

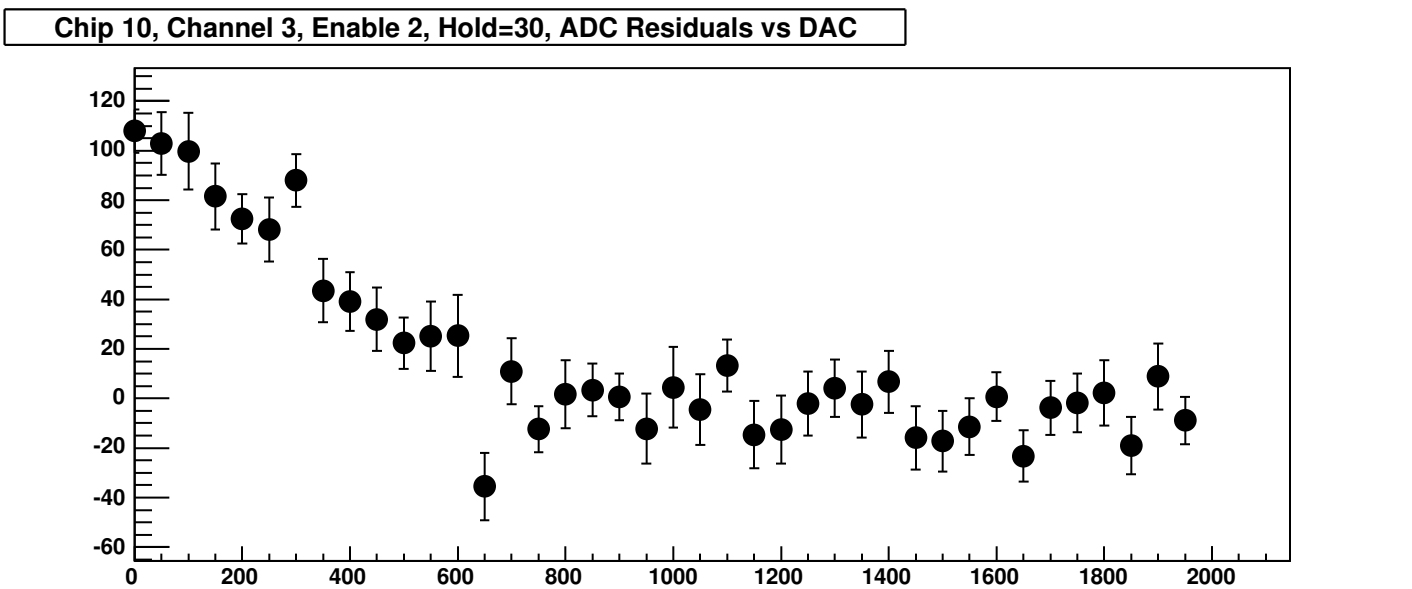

Chip 10, Channel 3, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 614.6 / 11
p0 -494.9 ± 20.26
p1 19.3 ± 0.01777
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

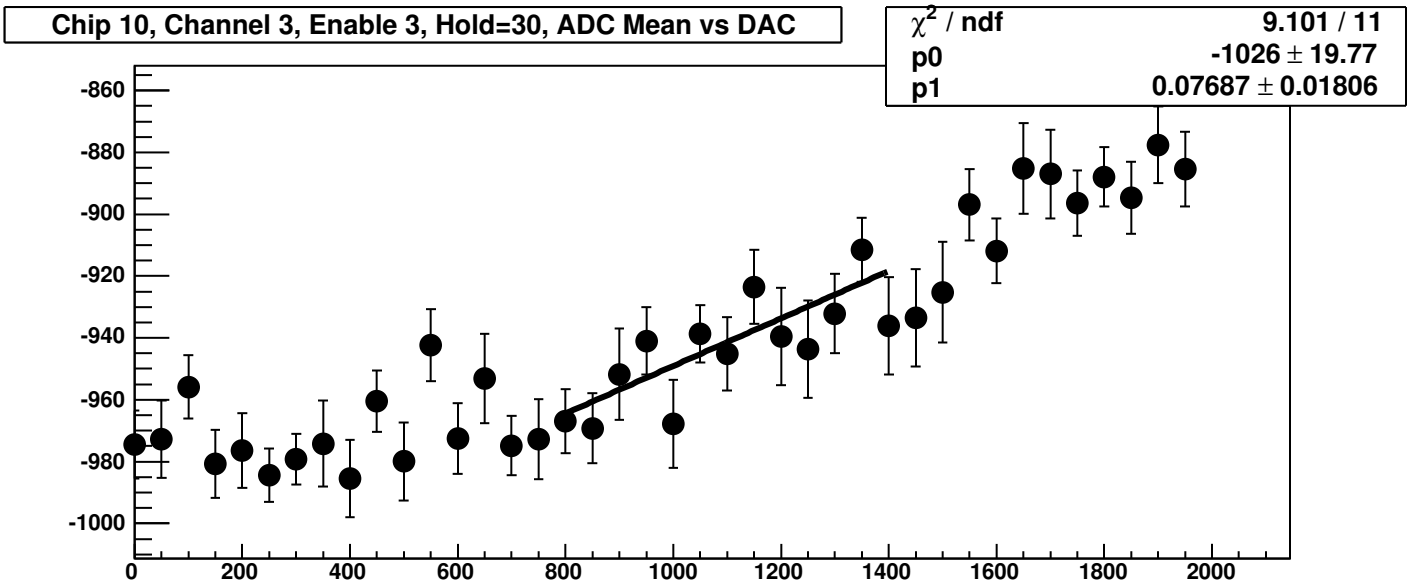

Chip 10, Channel 3, Enable 1!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

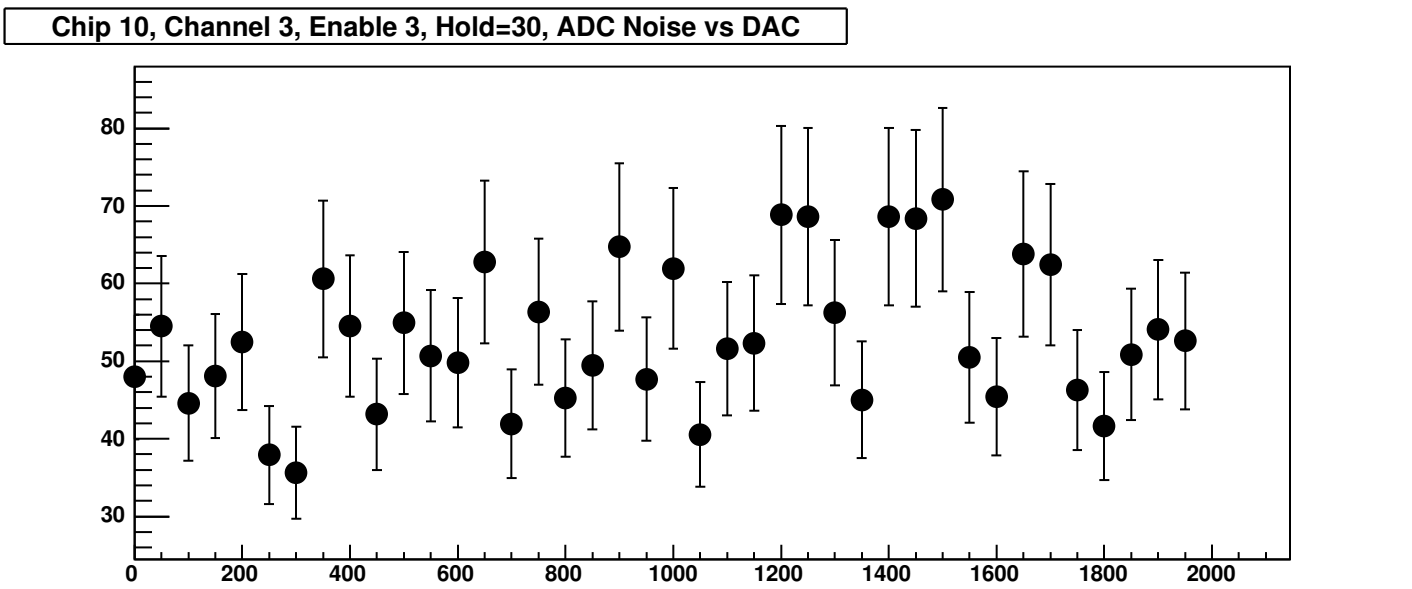

Chip 10, Channel 3, Enable 1!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

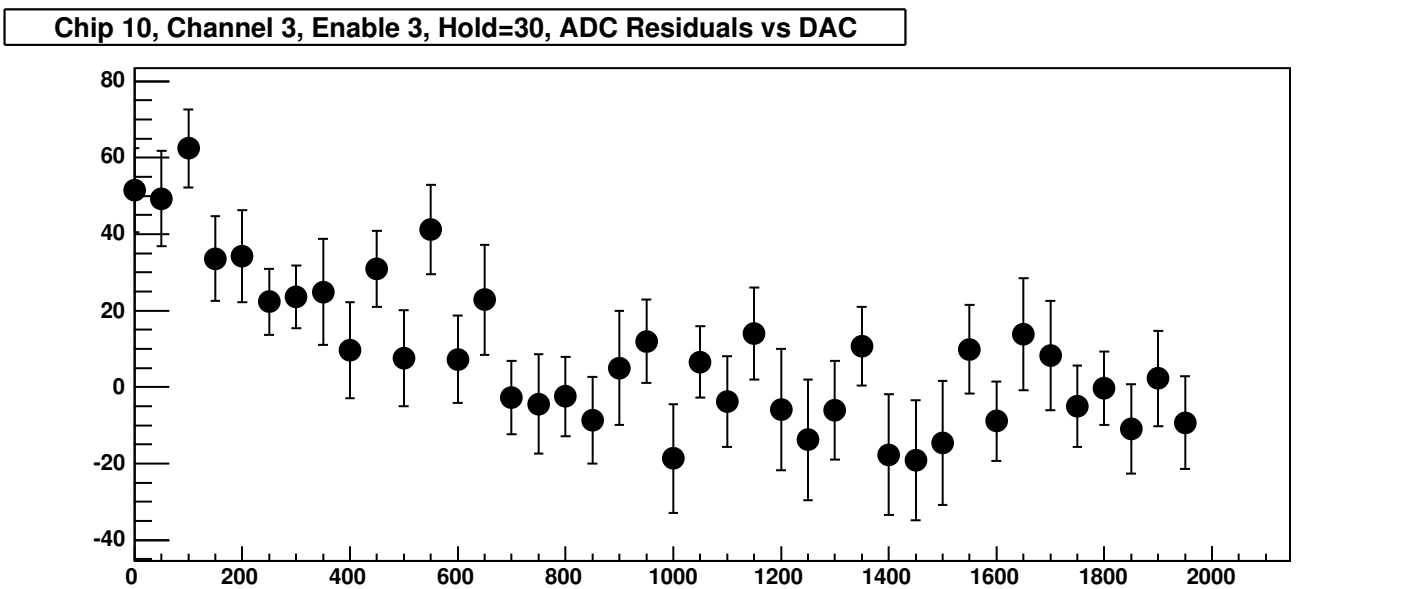

Chip 10, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 5.121 / 11
p0 -1009 ± 20
p1 0.192 ± 0.01809
-600
-650
-700
-750
-800
-850
-900
-950
0
200
400
600
800
1000
1200
1400
1600
1800
2000

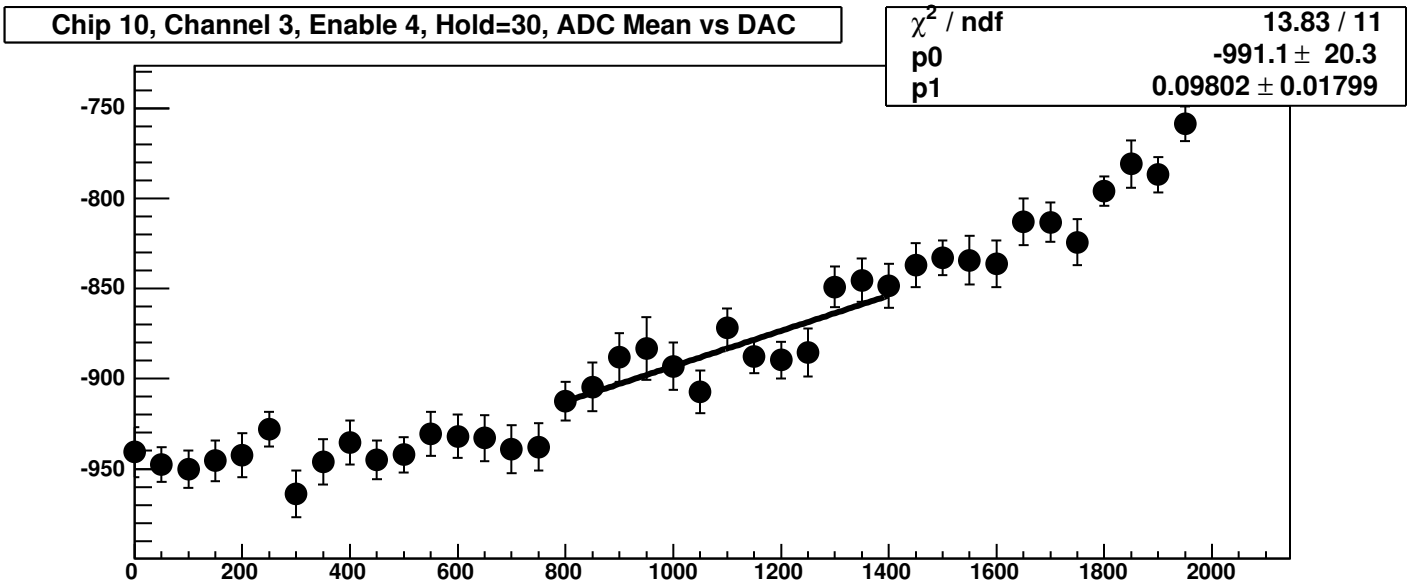

Chip 10, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

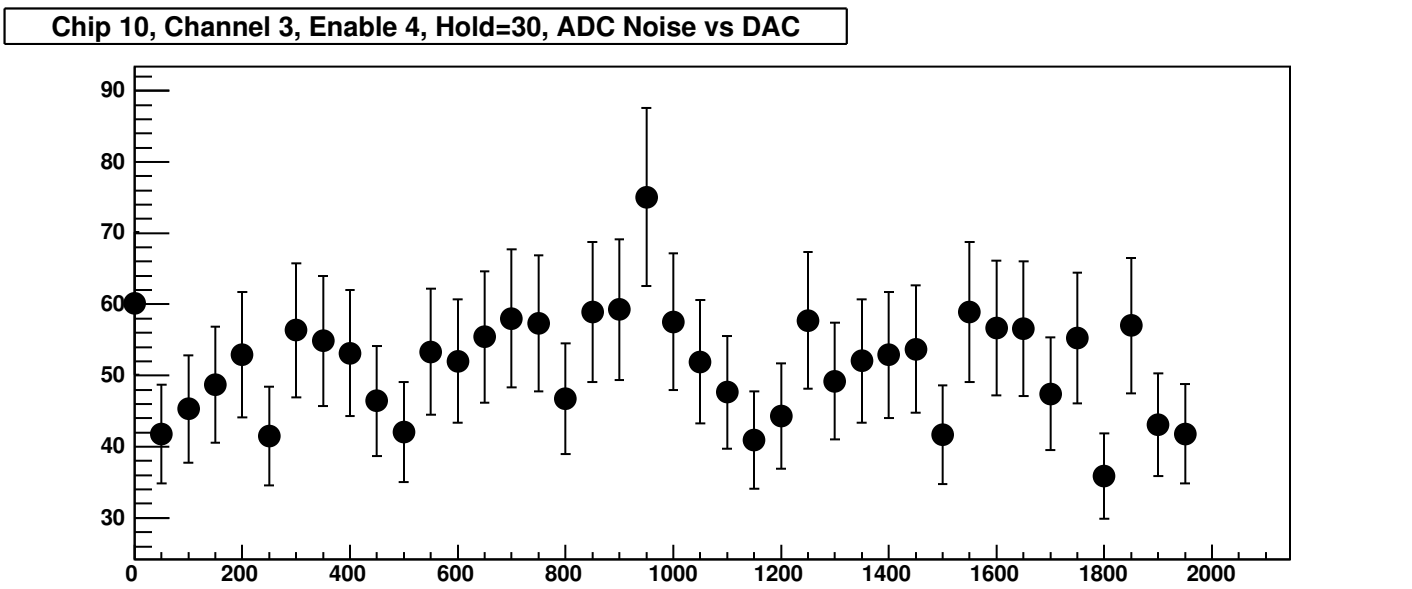

Chip 10, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

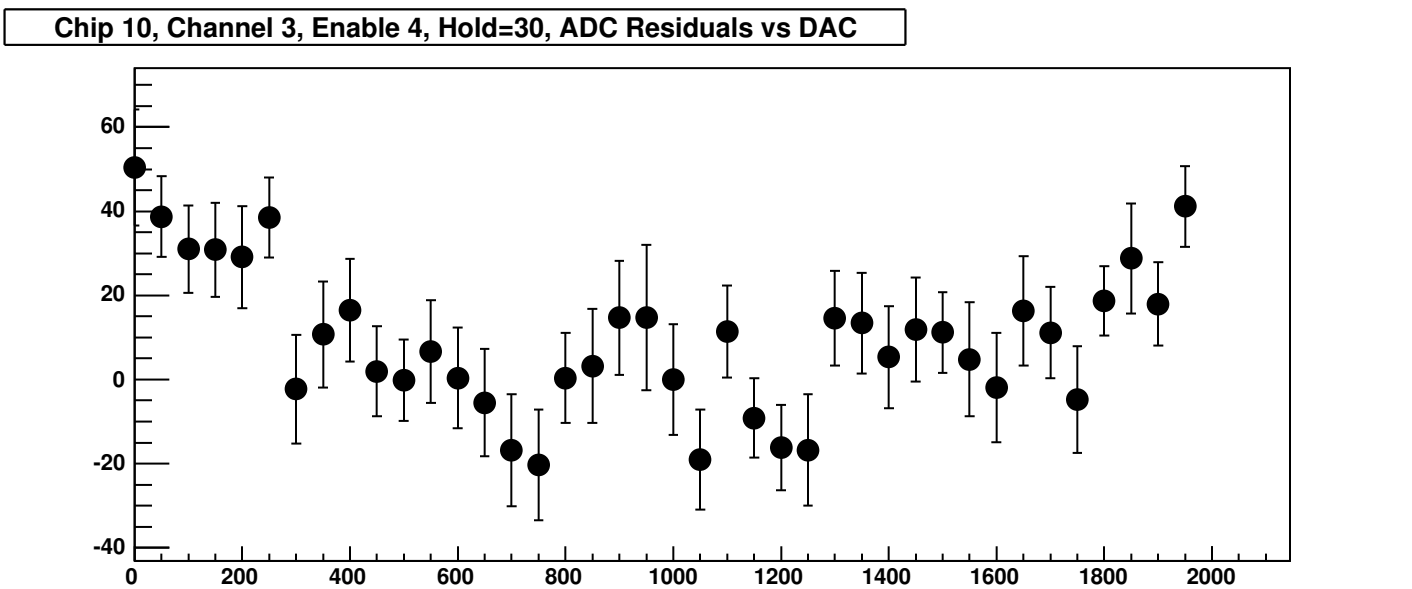

Chip 10, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 9.101 / 11
p0 -1026 ± 19.77
p1 0.07687 ± 0.01806
-860
-880
-900
-920
-940
-960
-980
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

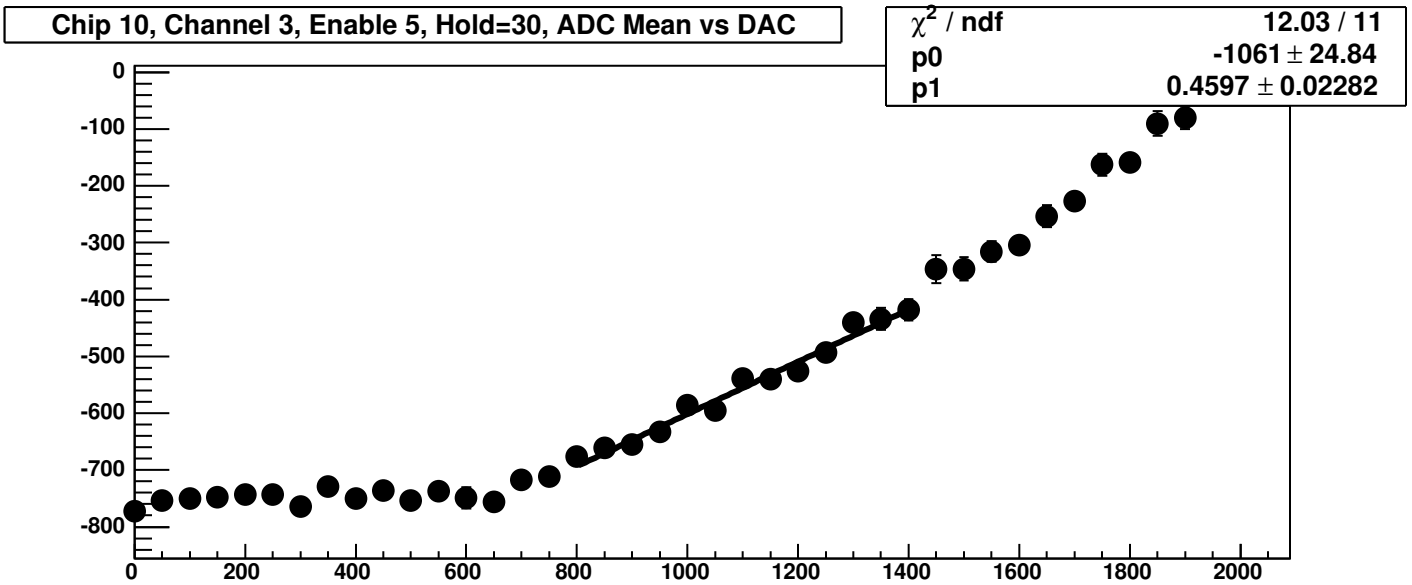

Chip 10, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

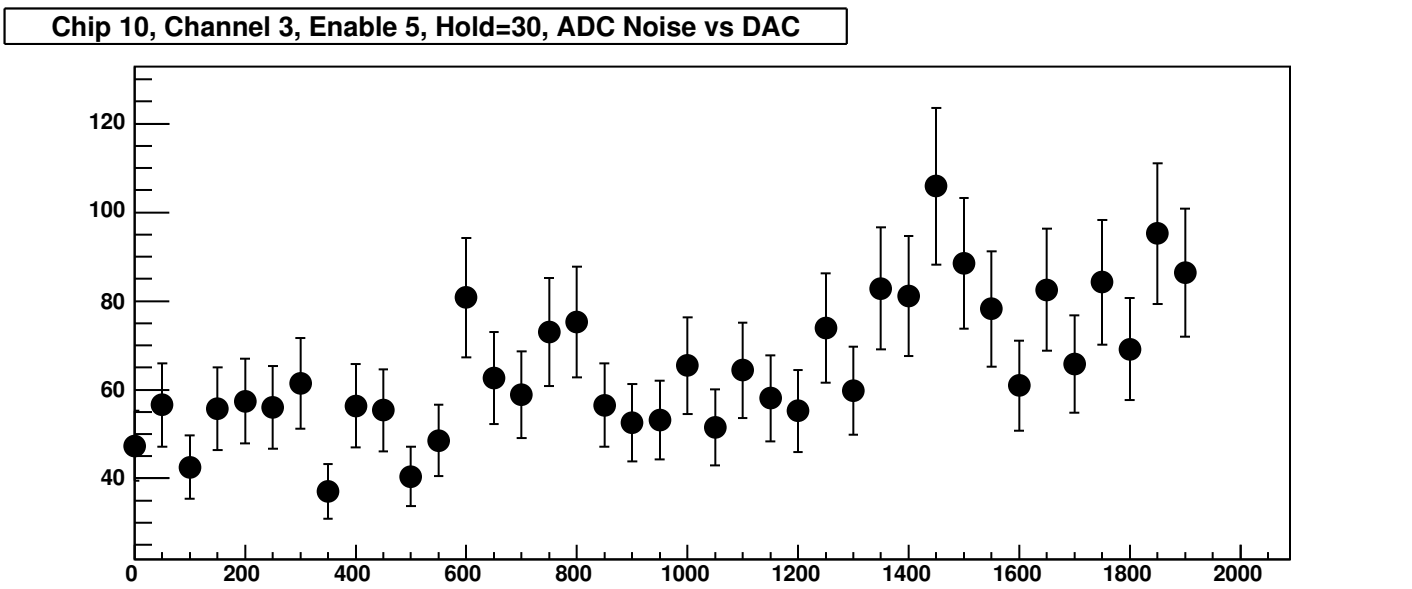

Chip 10, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

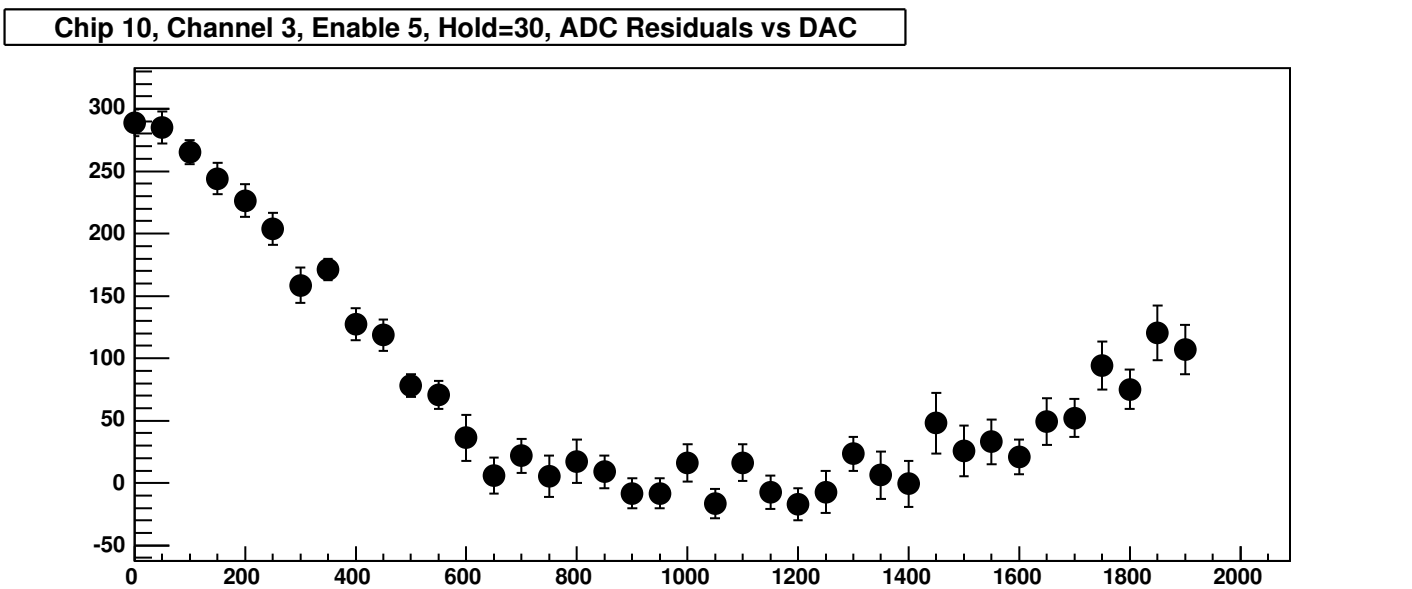

Chip 10, Channel 3, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 13.83 / 11
p0 -991.1 ± 20.3
p1 0.09802 ± 0.01799
-750
-800
-850
-900
-950
0
200
400
600
800
1000
1200
1400
1600
1800
2000

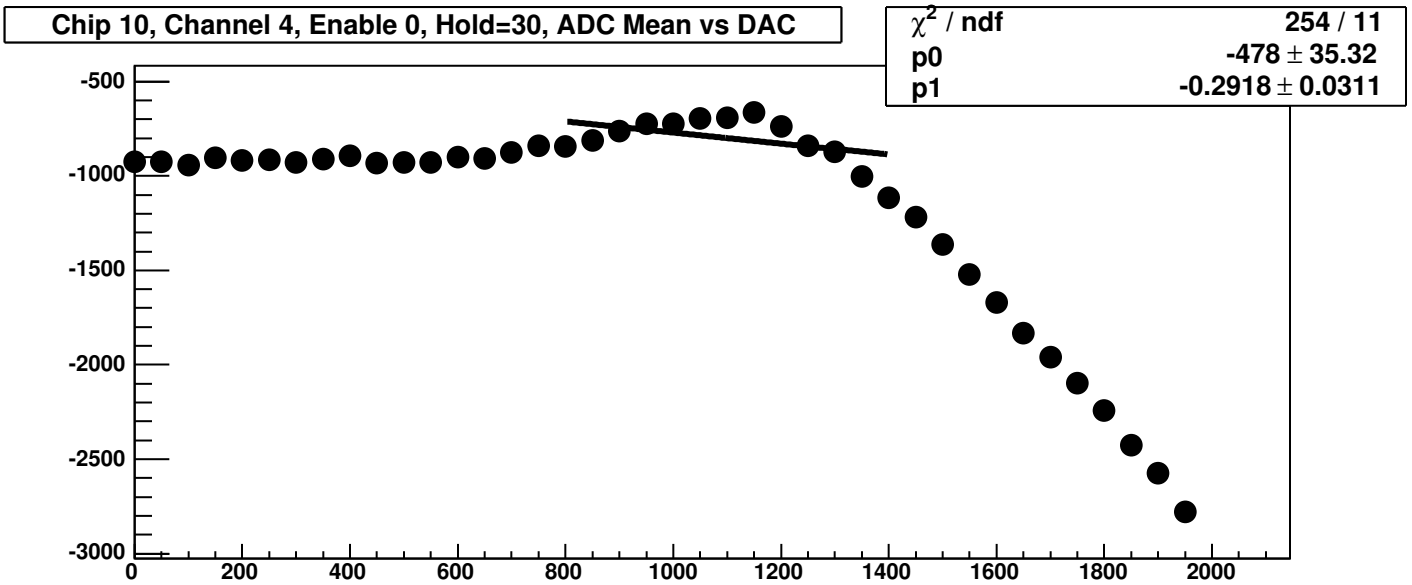

Chip 10, Channel 3, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

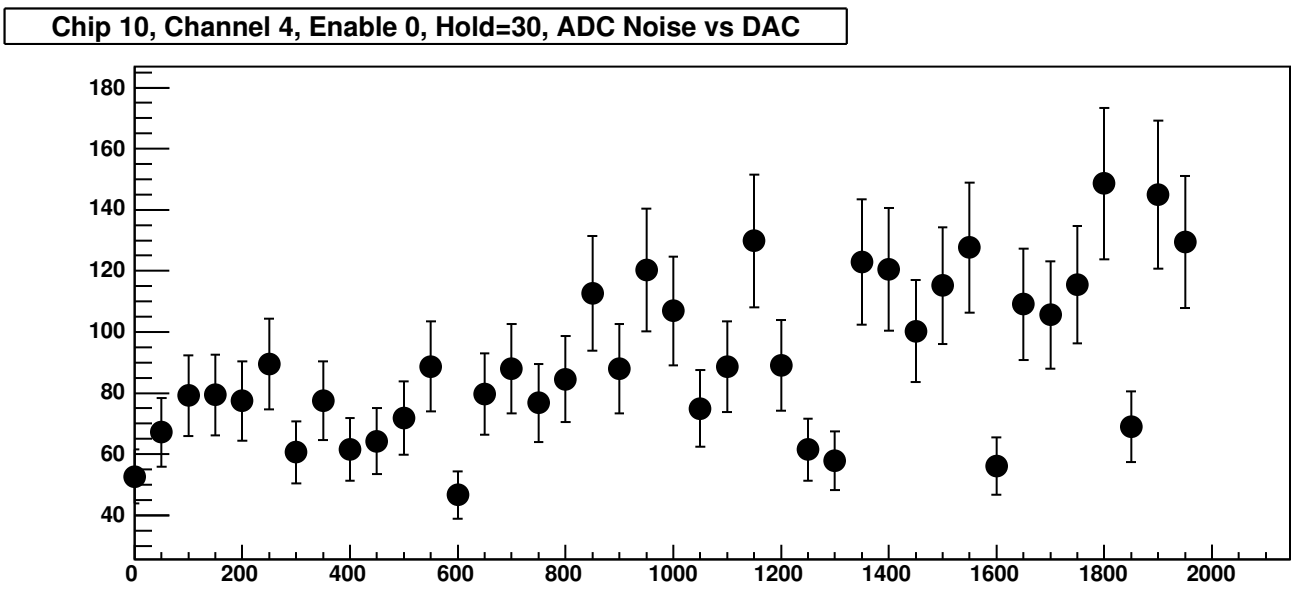

Chip 10, Channel 3, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

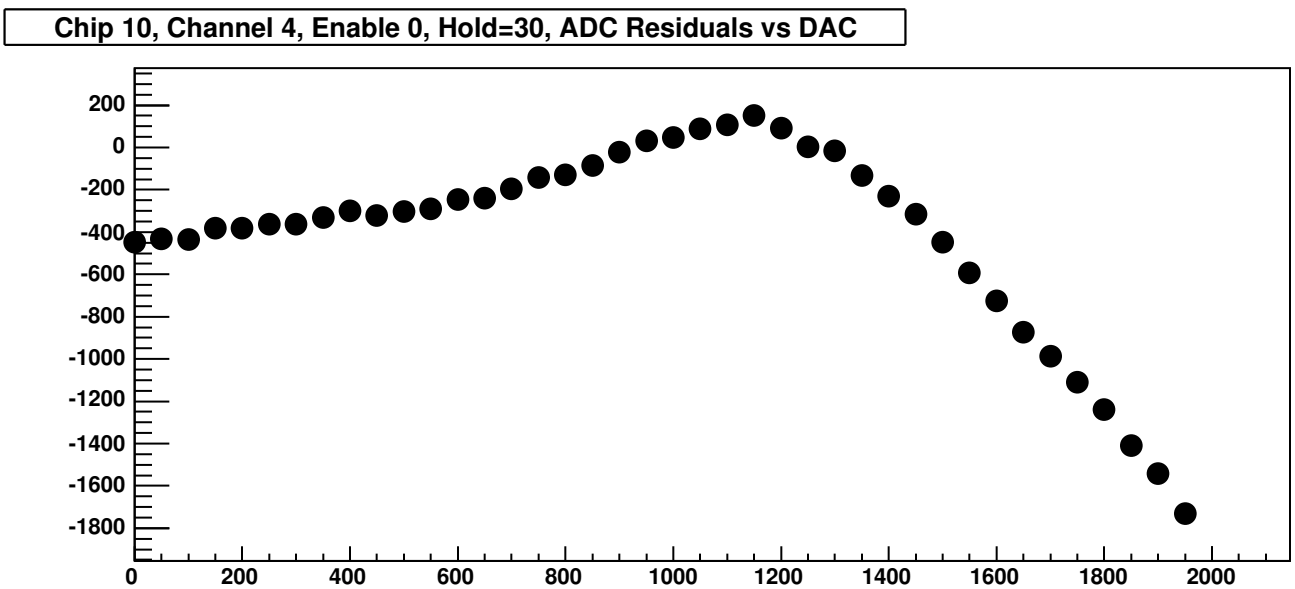

Chip 10, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 12.03 / 11
p0 -1061 ± 24.84
p1 0.4597 ± 0.02282
0
-100
-200
-300
-400
-500
-600
-700
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

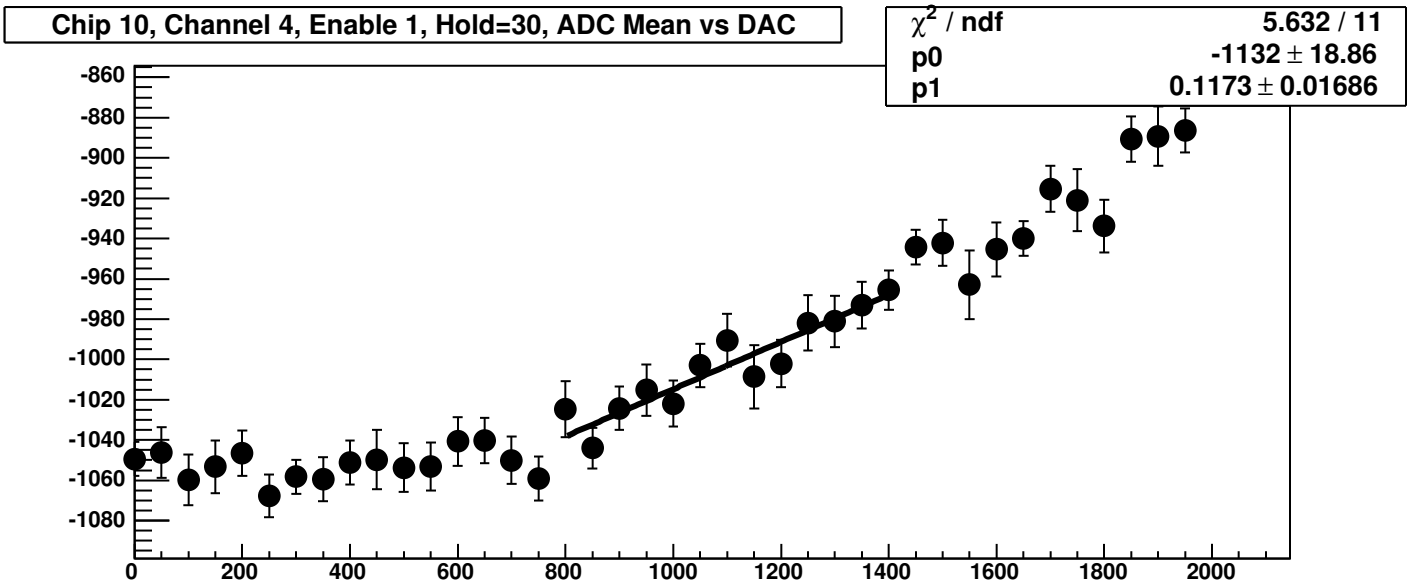

Chip 10, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

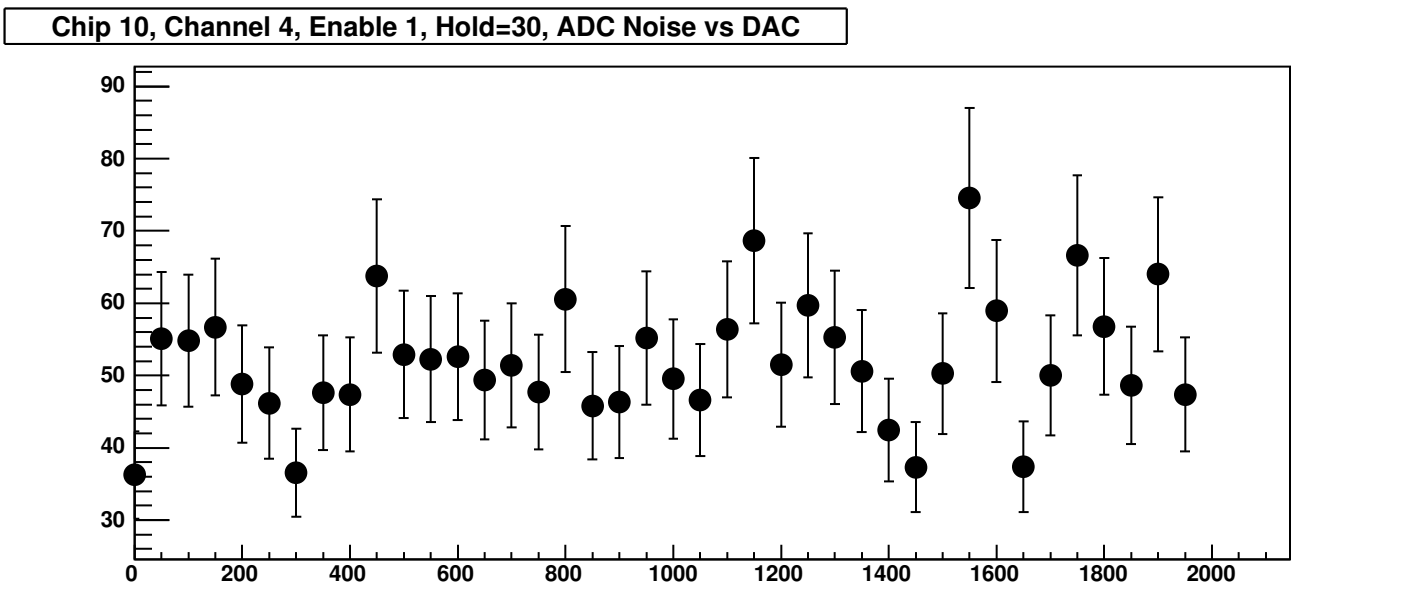

Chip 10, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

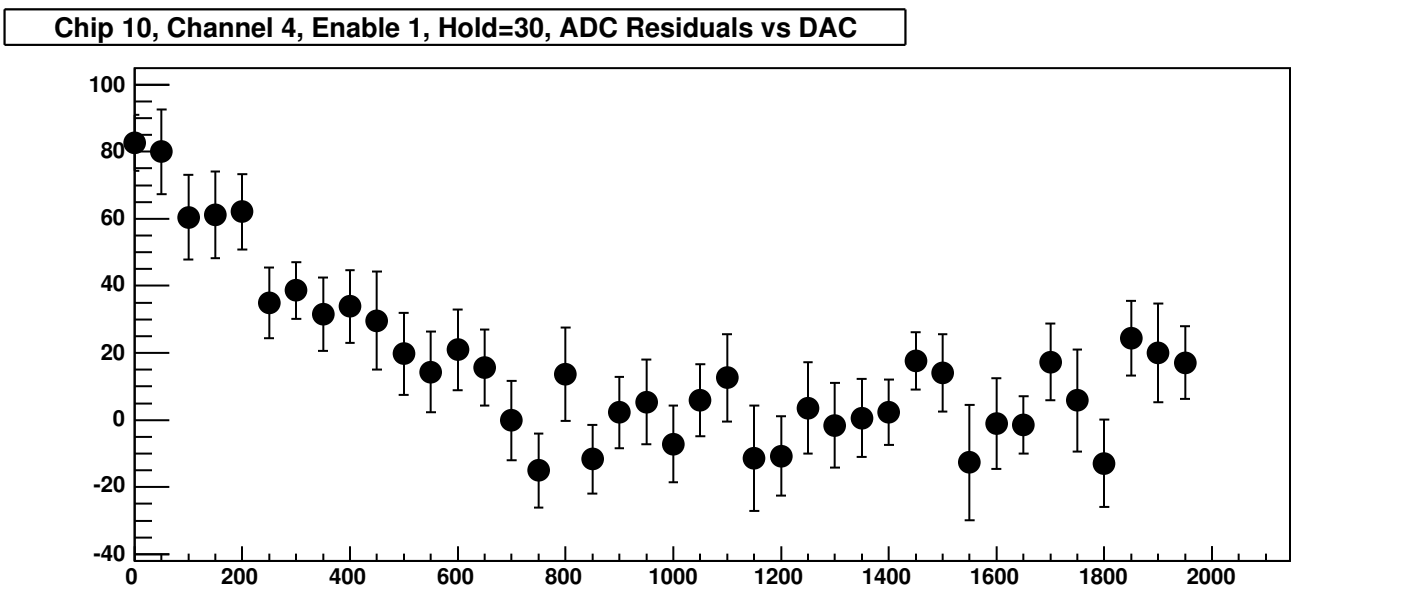

Chip 10, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 254 / 11
p0 -478 ± 35.32
p1 -0.2918 ± 0.0311
-500
-1000
-1500
-2000
-2500
-3000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

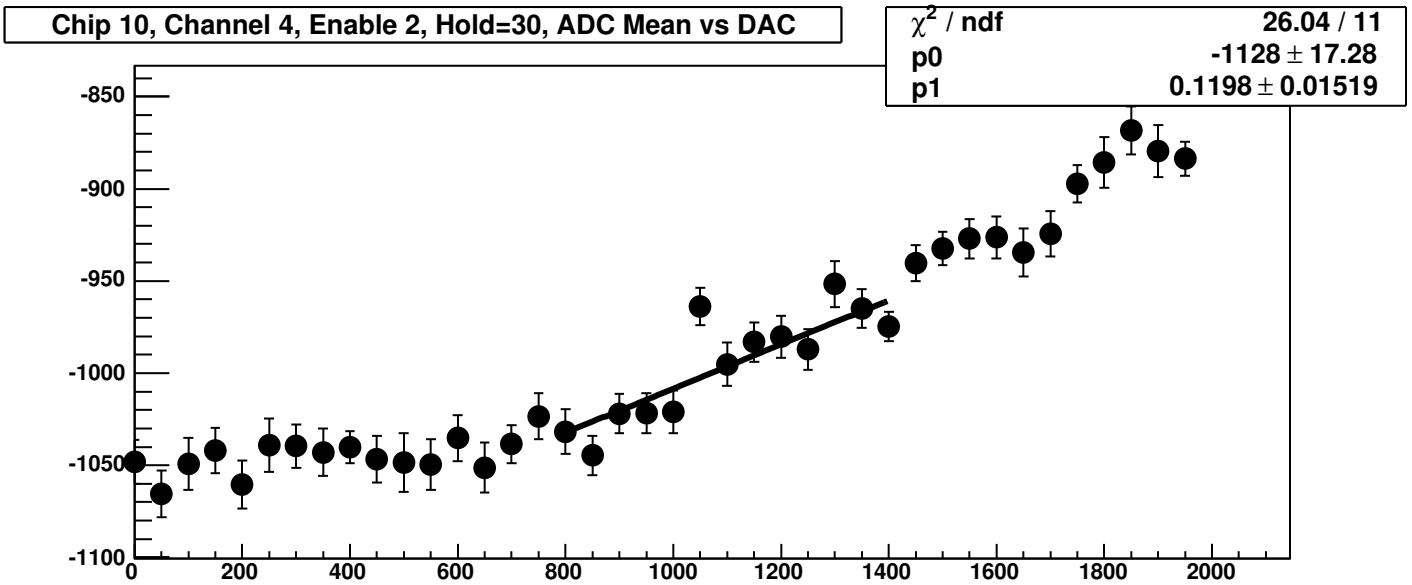

Chip 10, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

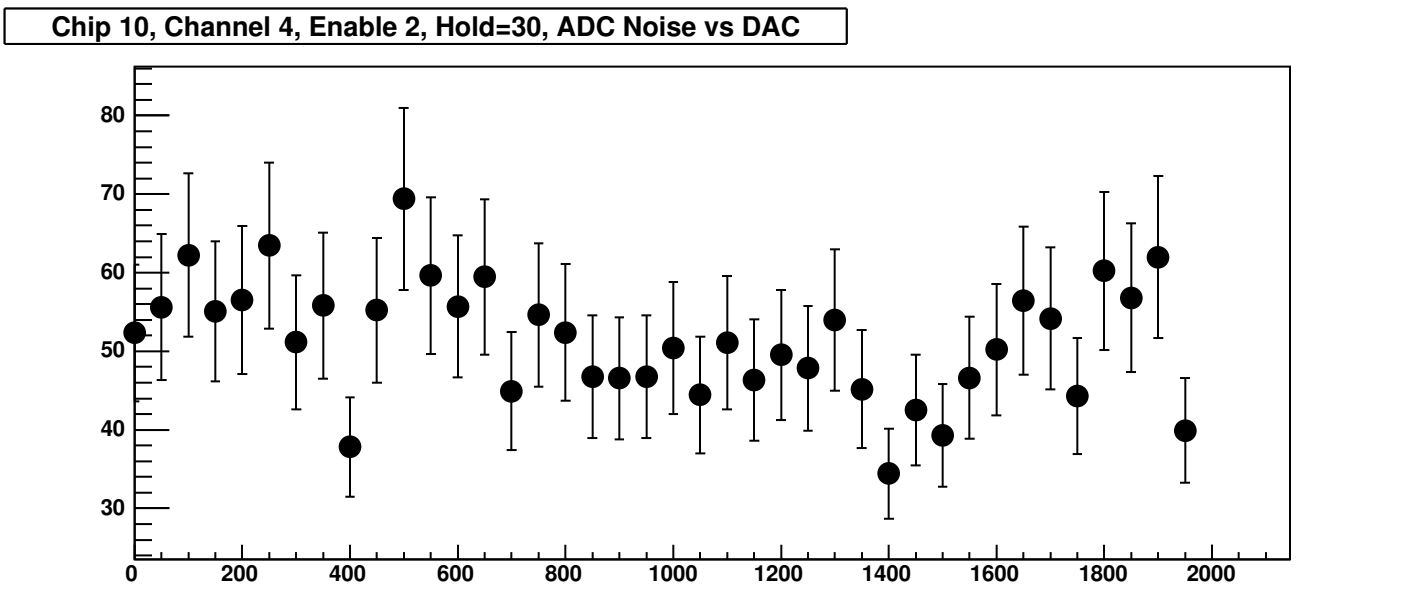

Chip 10, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

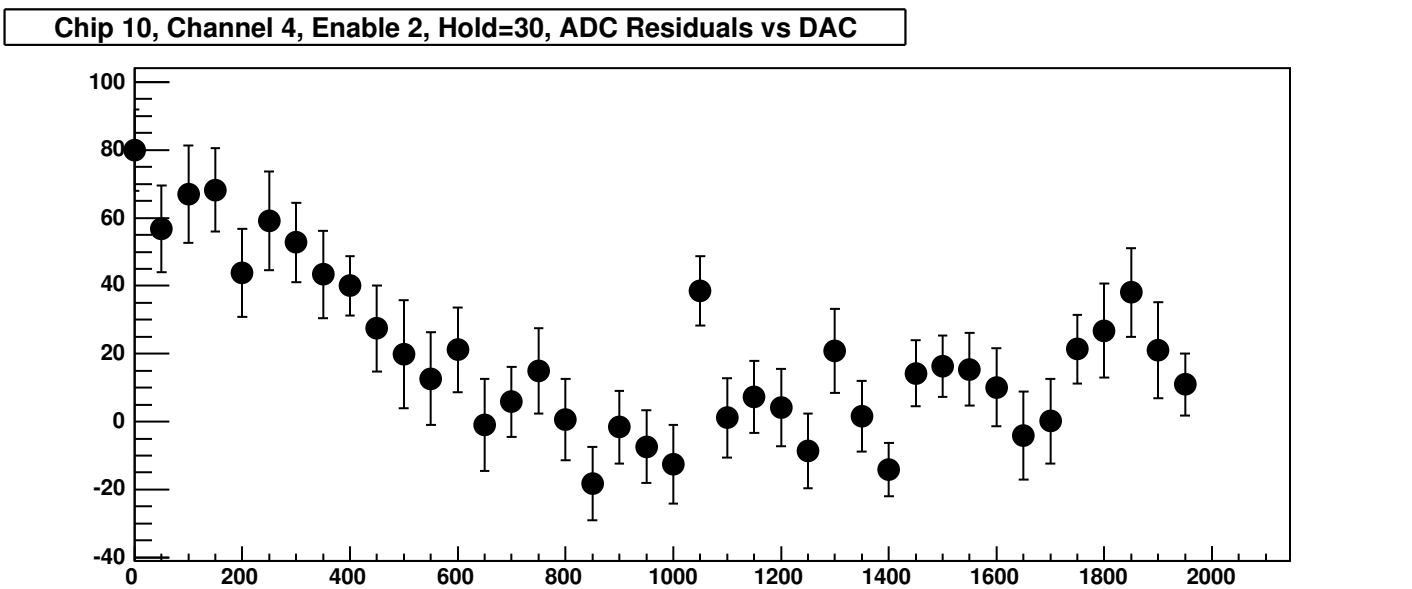

Chip 10, Channel 4, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 5.632 / 11
p0 -1132 ± 18.86
p1 0.1173 ± 0.01686
-860
-880
-900
-920
-940
-960
-980
-1000
-1020
-1040
-1060
-1080
0
200
400
600
800
1000
1200
1400
1600
1800
2000

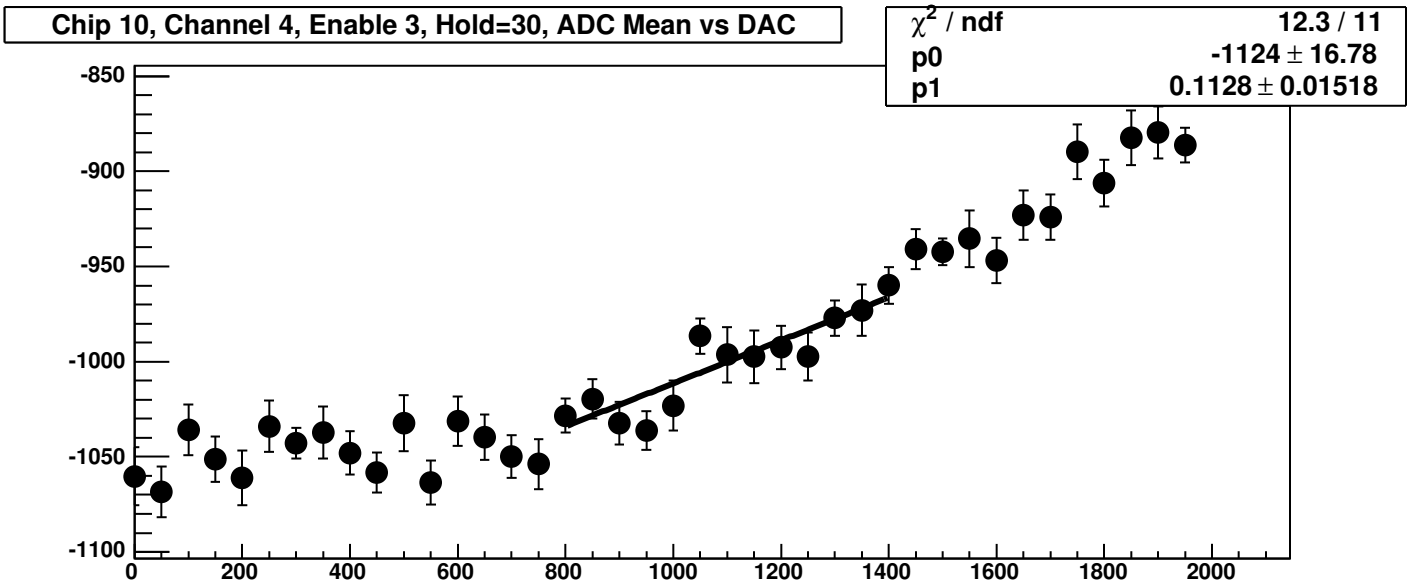

Chip 10, Channel 4, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

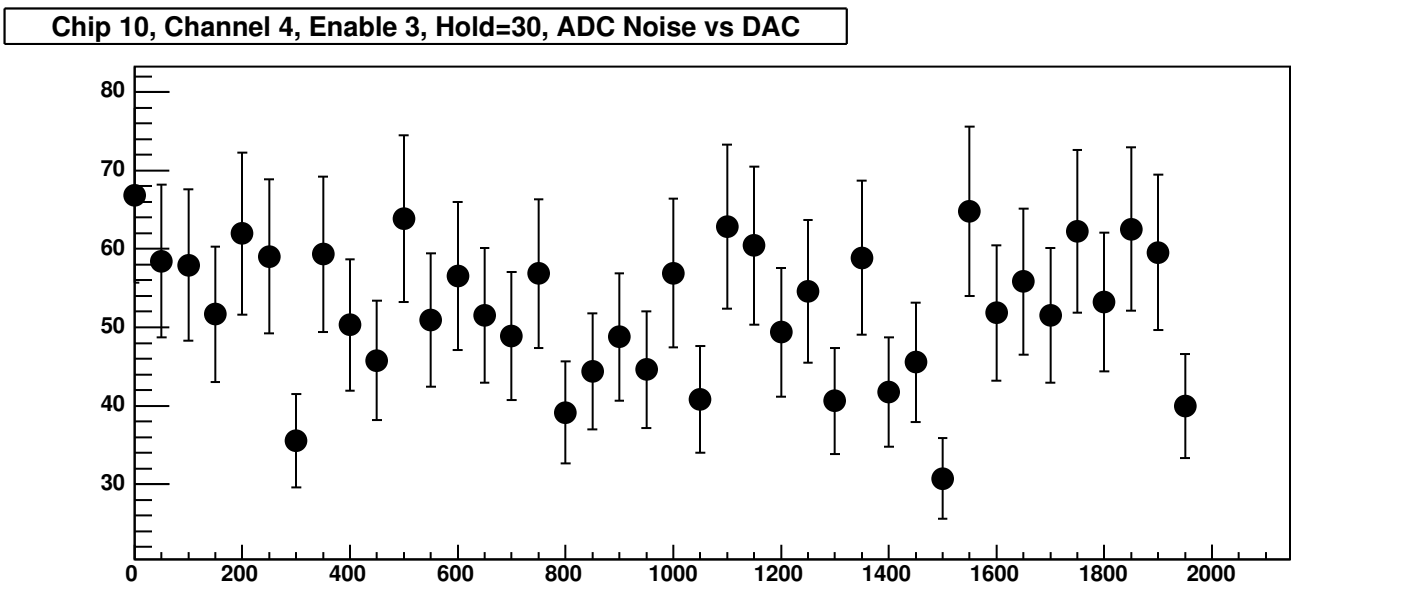

Chip 10, Channel 4, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

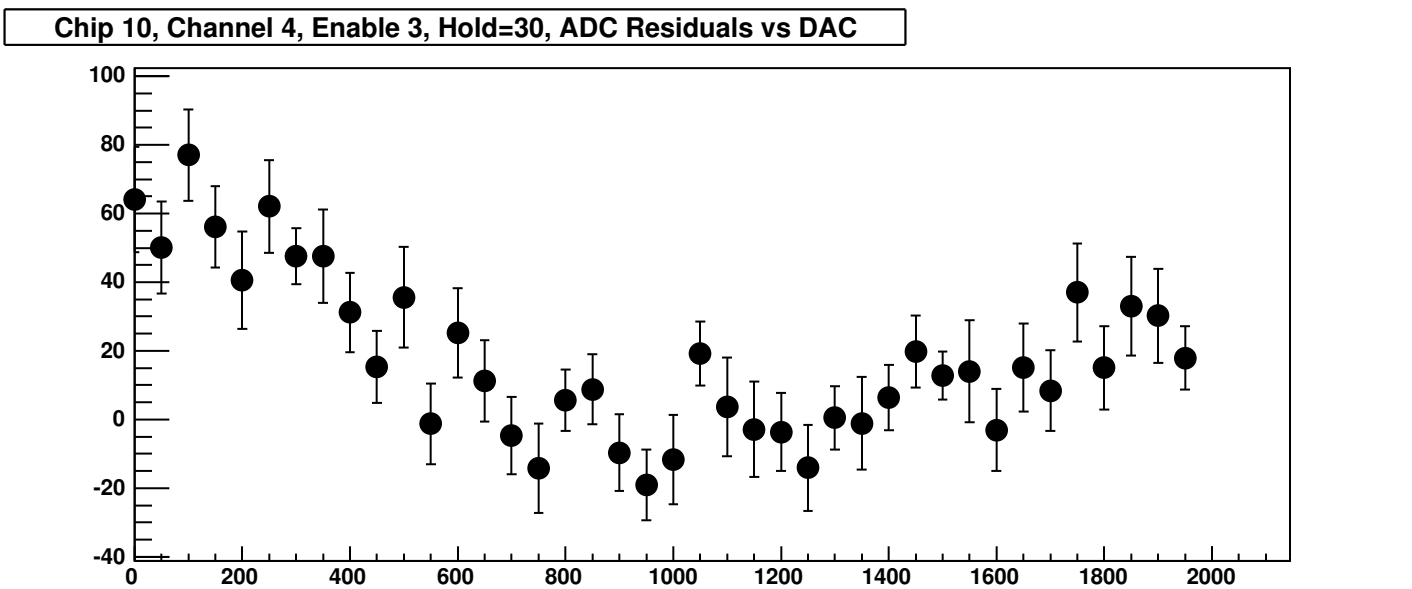

Chip 10, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 26.04 / 11
p0 -1128 ± 17.28
p1 0.1198 ± 0.01519
-850
-900
-950
-1000
-1050
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

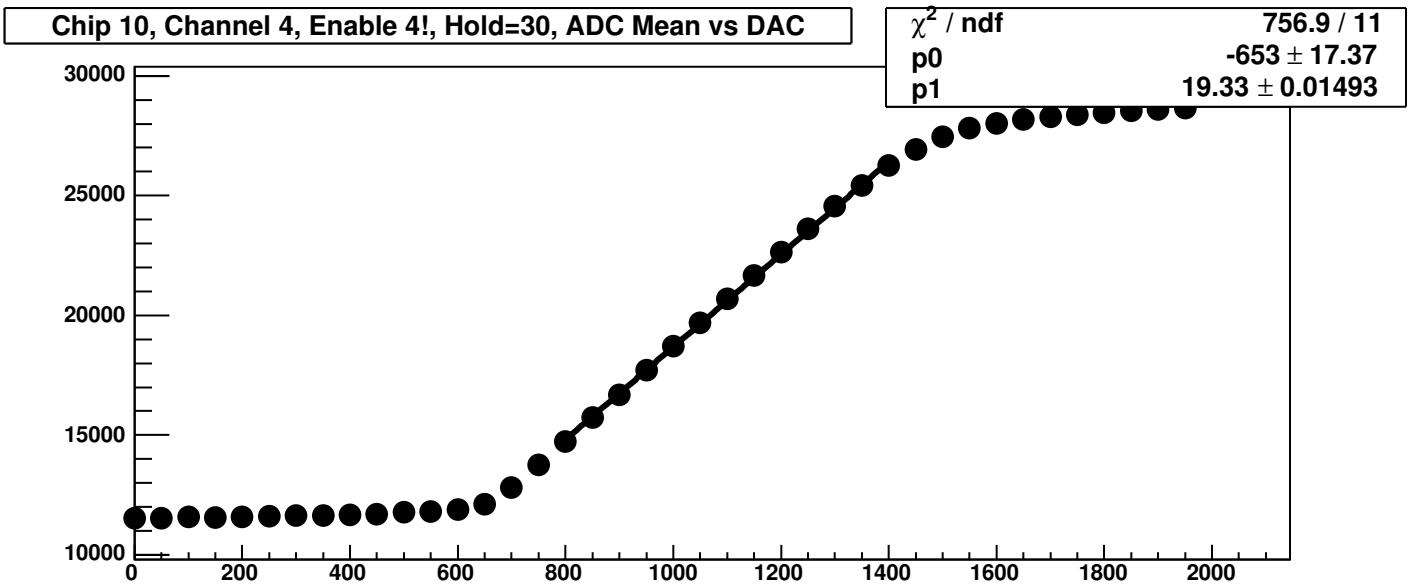

Chip 10, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

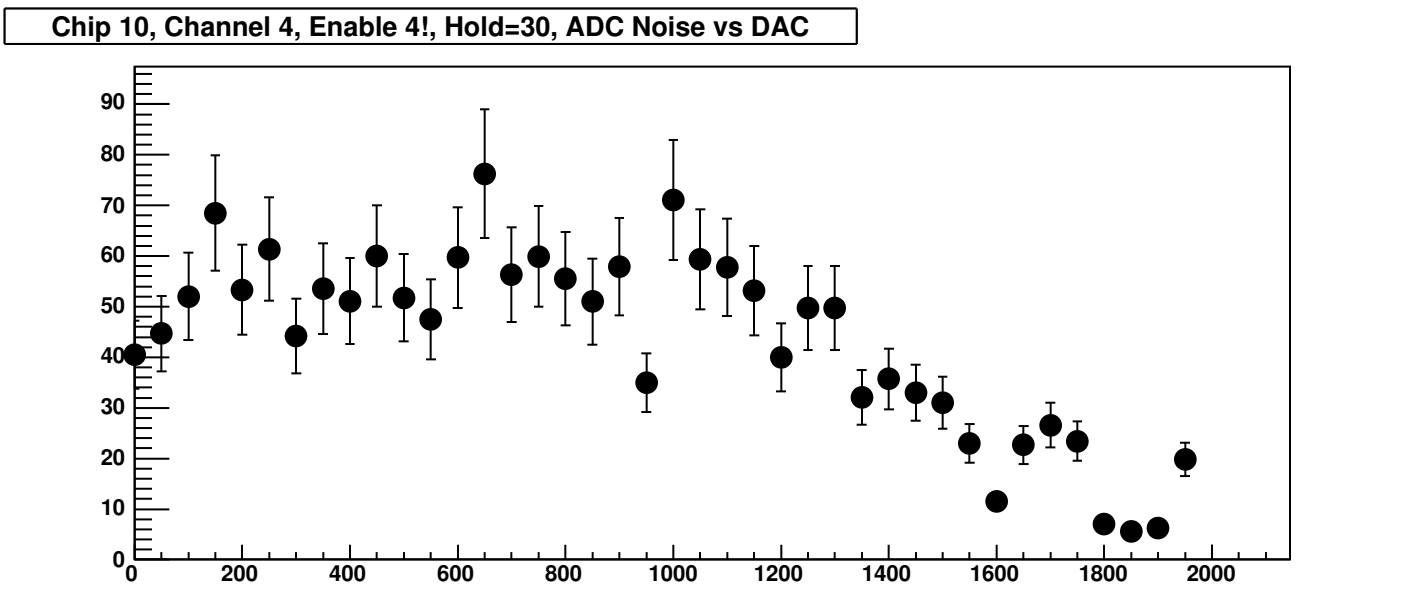

Chip 10, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

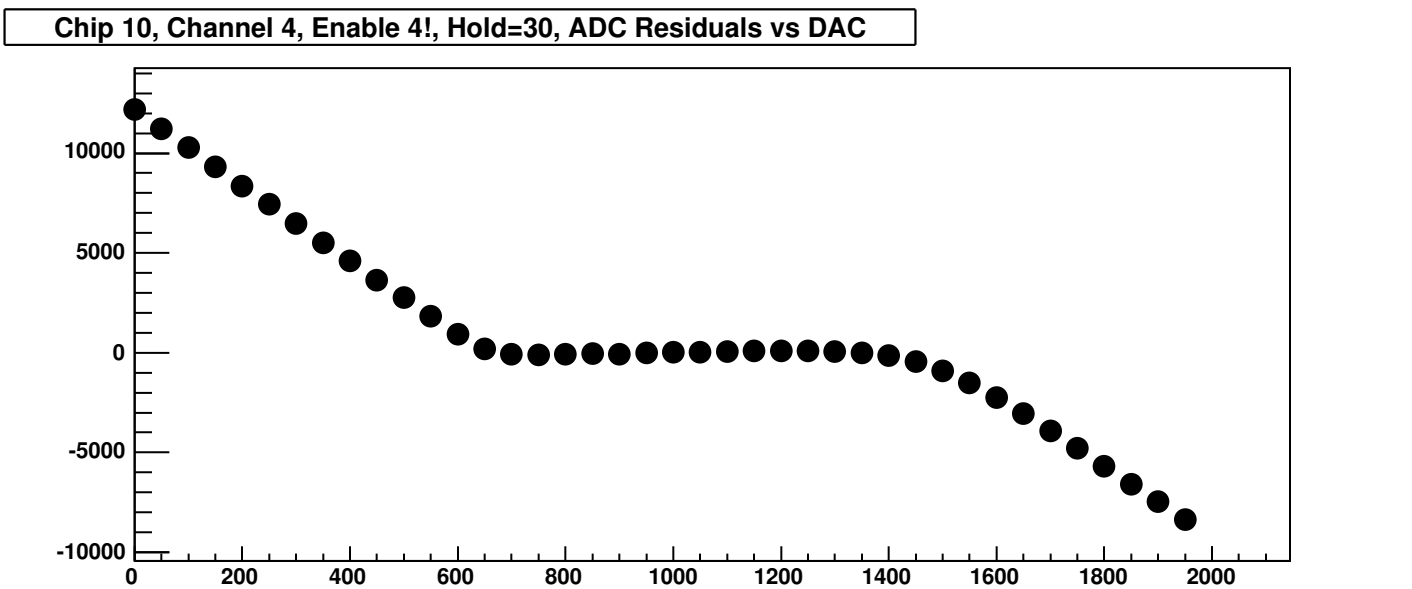

Chip 10, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 12.3 / 11
p0 -1124 ± 16.78
p1 0.1128 ± 0.01518
-850
-900
-950
-1000
-1050
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

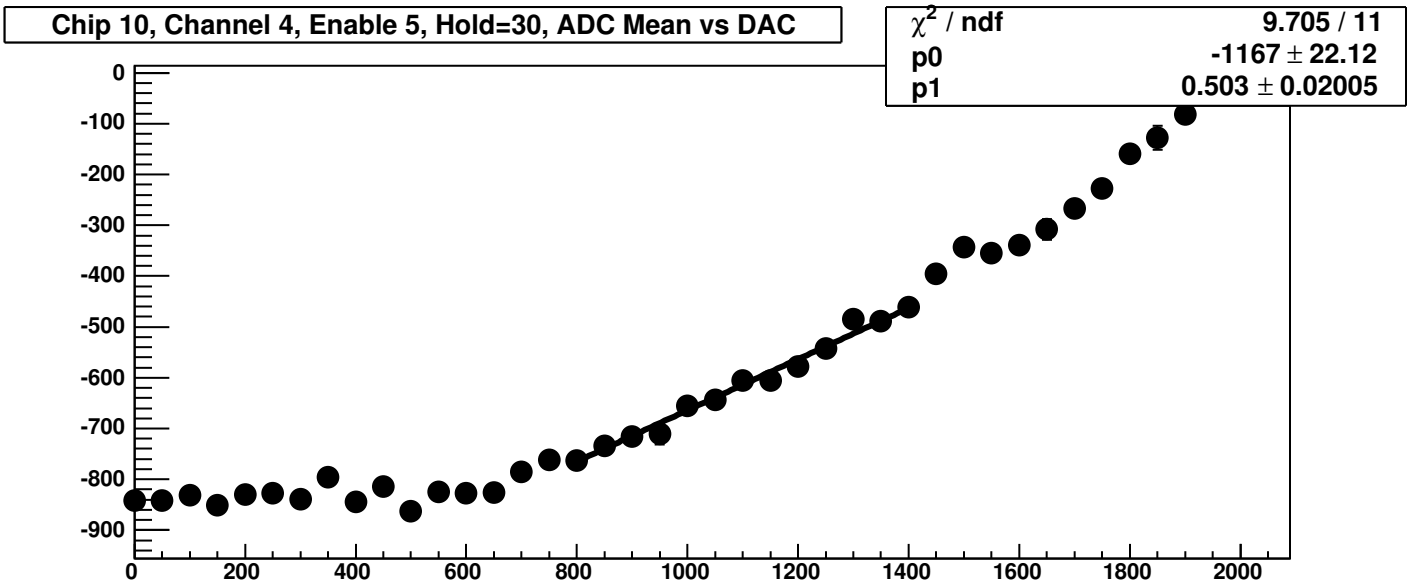

Chip 10, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

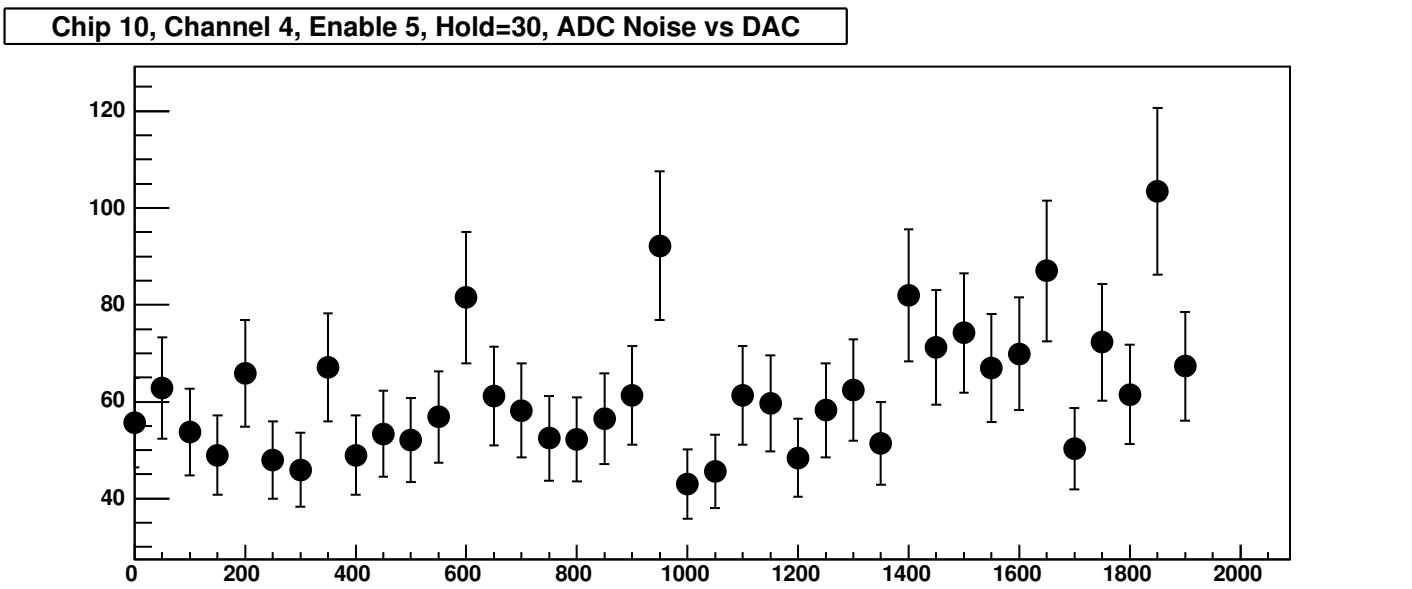

Chip 10, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

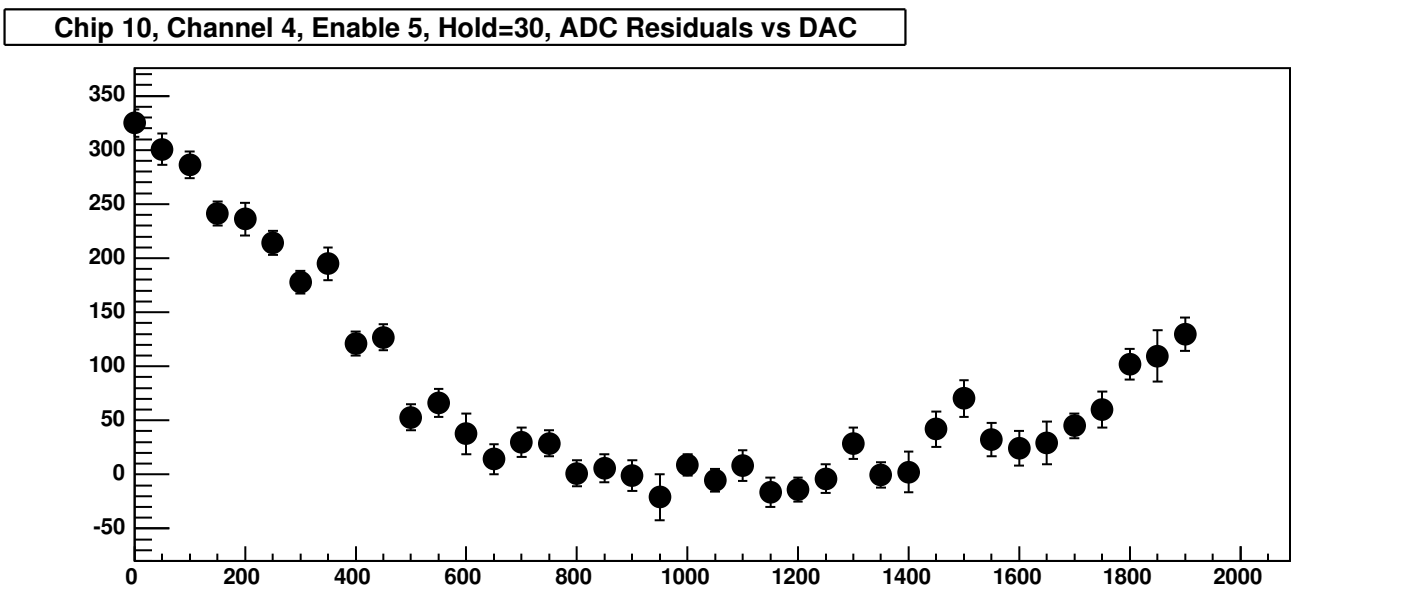

Chip 10, Channel 4, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 756.9 / 11
p0 -653 ± 17.37
p1 19.33 ± 0.01493
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

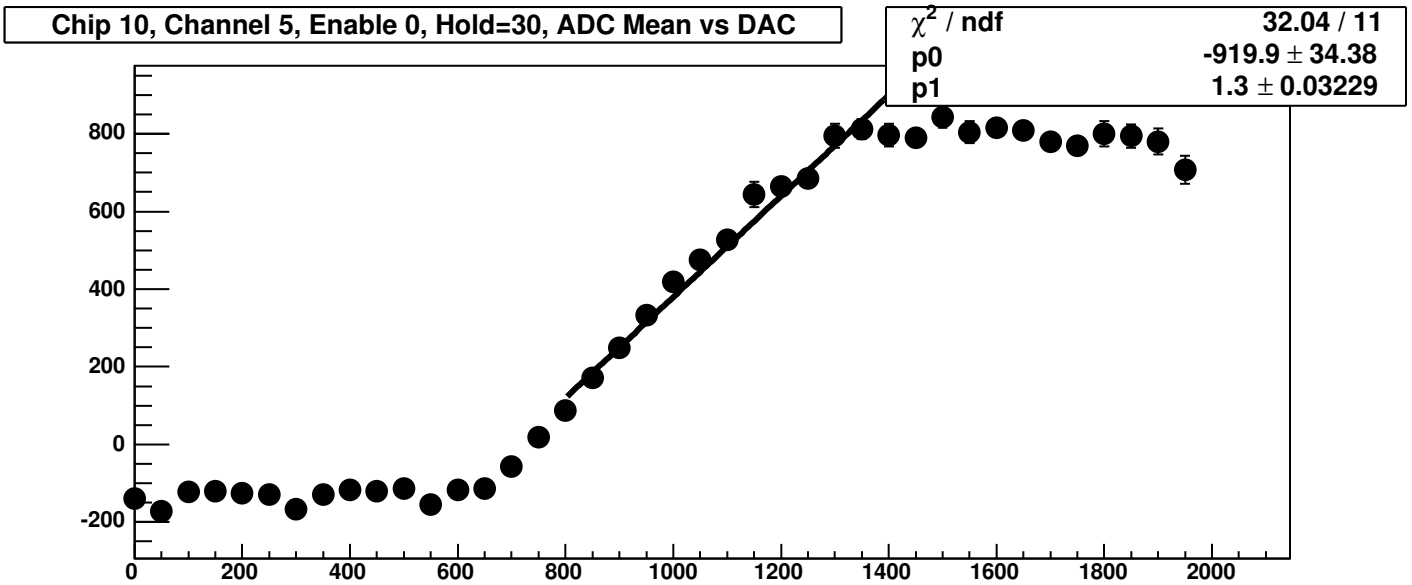

Chip 10, Channel 4, Enable 4!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

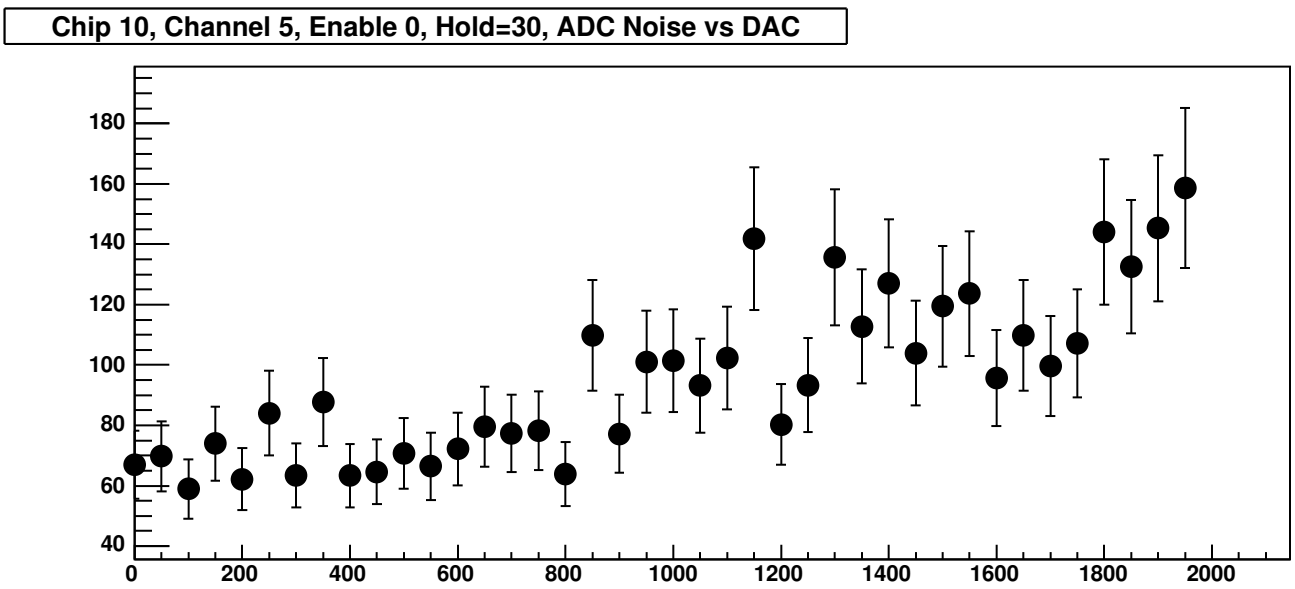

Chip 10, Channel 4, Enable 4!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

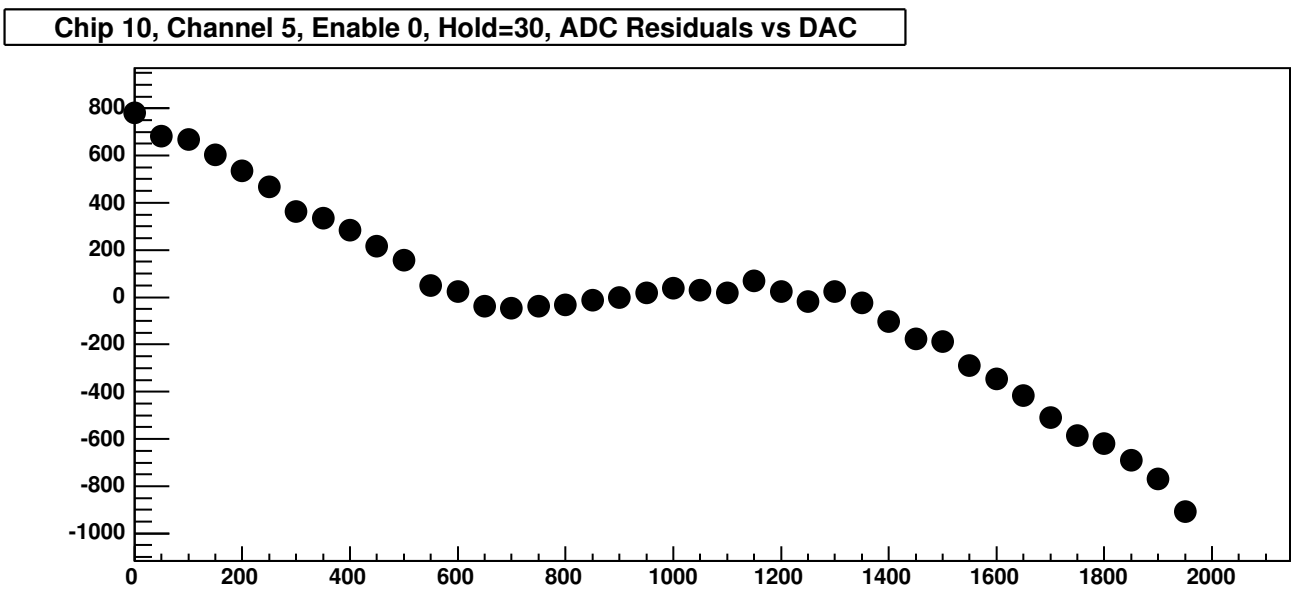

Chip 10, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9.705 / 11
p0 -1167 ± 22.12
p1 0.503 ± 0.02005
0
-100
-200
-300
-400
-500
-600
-700
-800
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

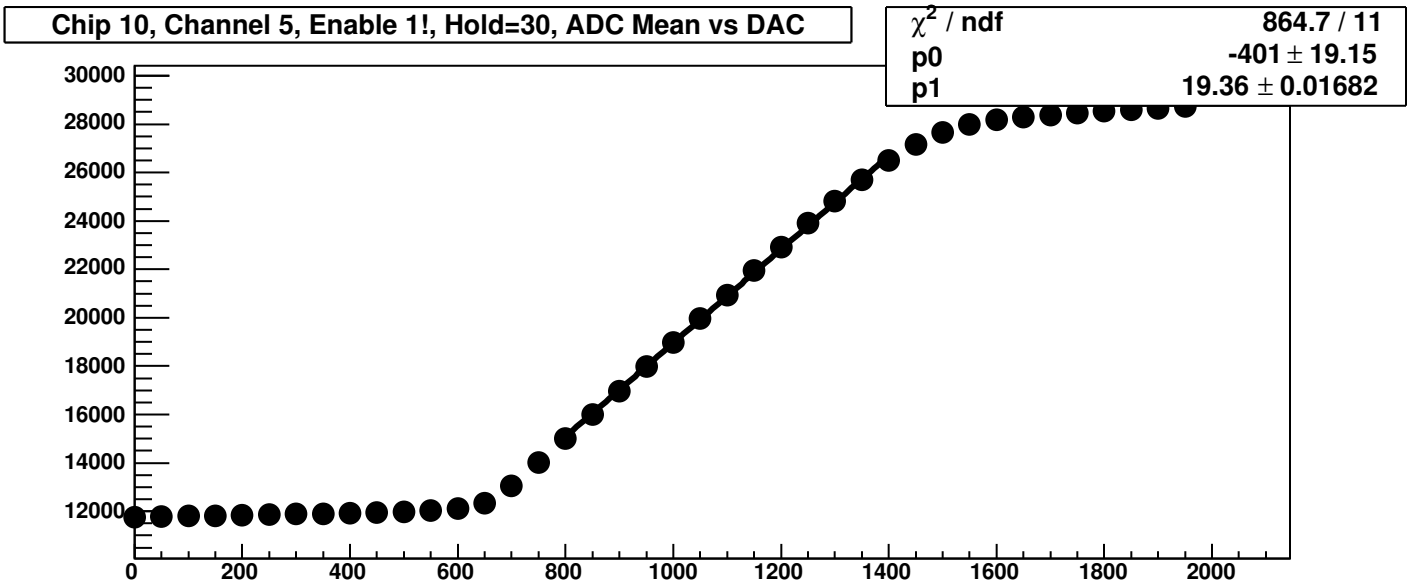

Chip 10, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

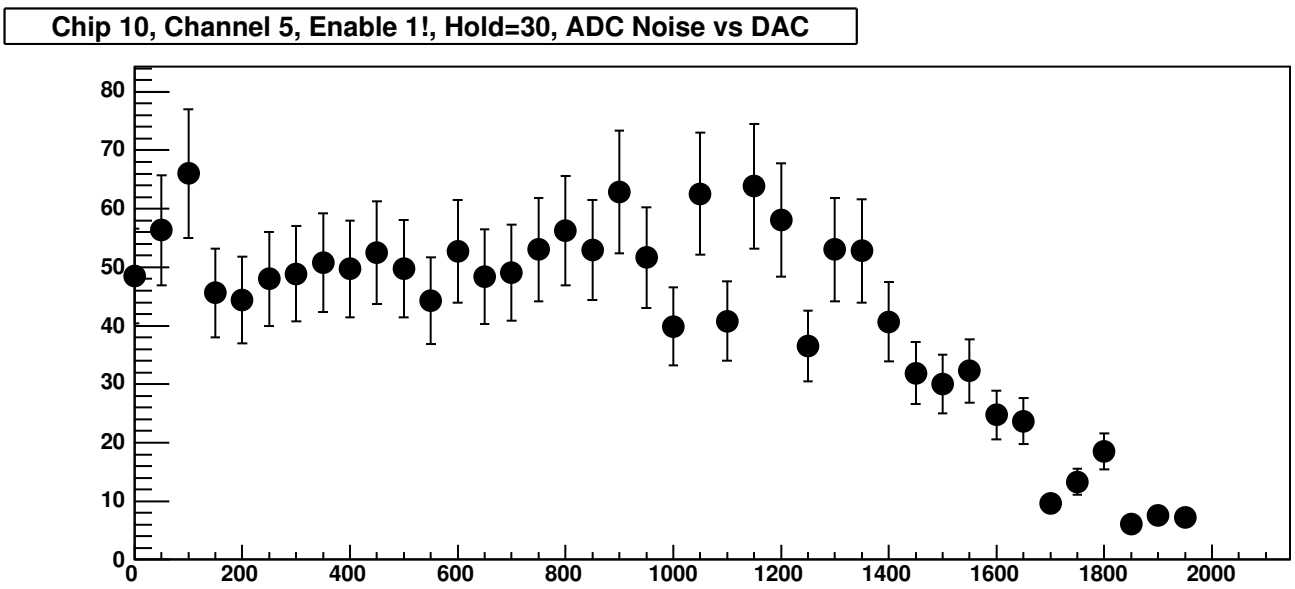

Chip 10, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

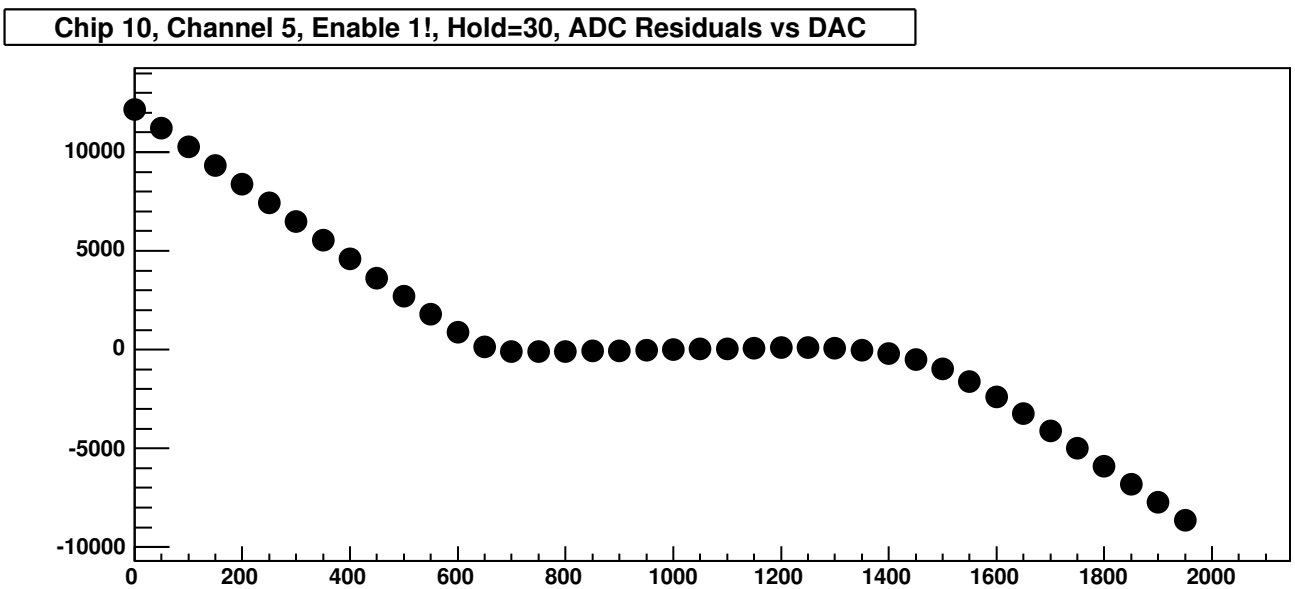

Chip 10, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 32.04 / 11
p0 -919.9 ± 34.38
p1 1.3 ± 0.03229
800
600
400
200
0
-200
0 200 400 600 800 1000 1200 1400 1600 1800 2000

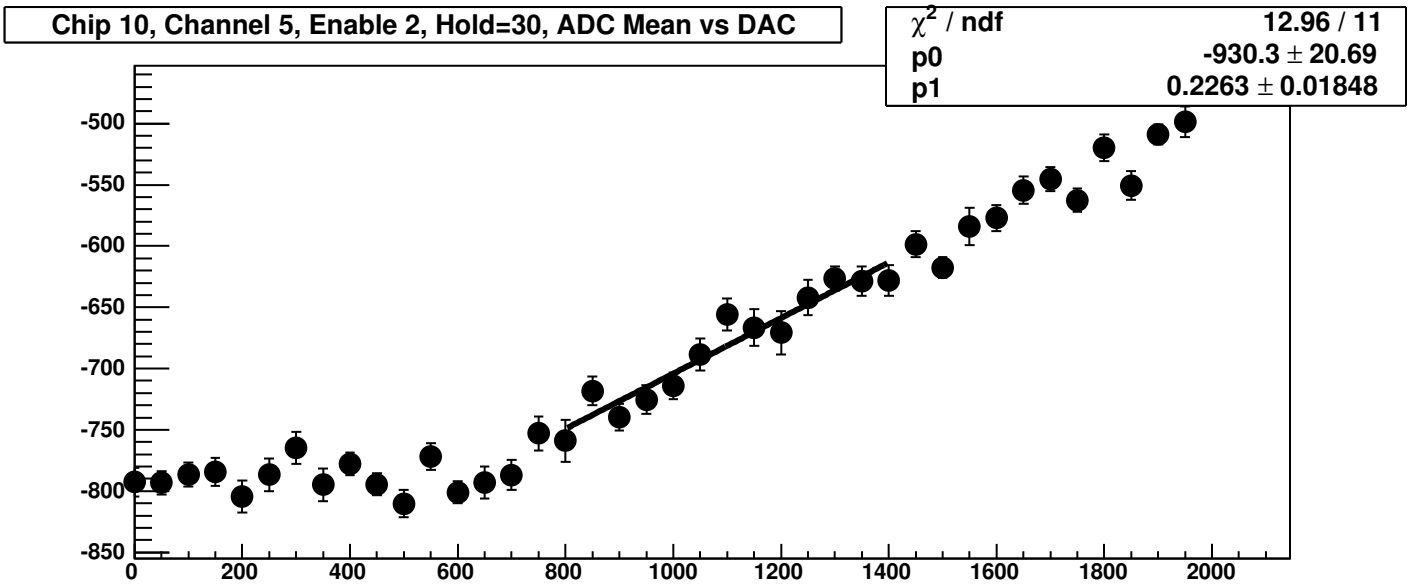

Chip 10, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0 200 400 600 800 1000 1200 1400 1600 1800 2000

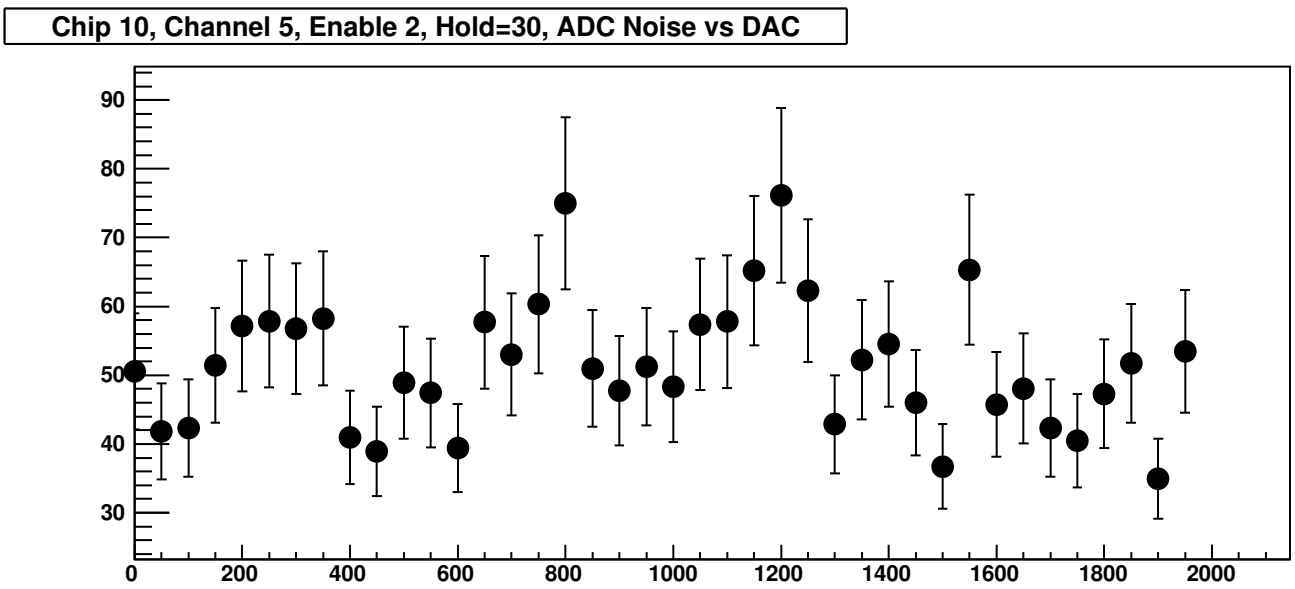

Chip 10, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
-600
-800
-1000
0 200 400 600 800 1000 1200 1400 1600 1800 2000

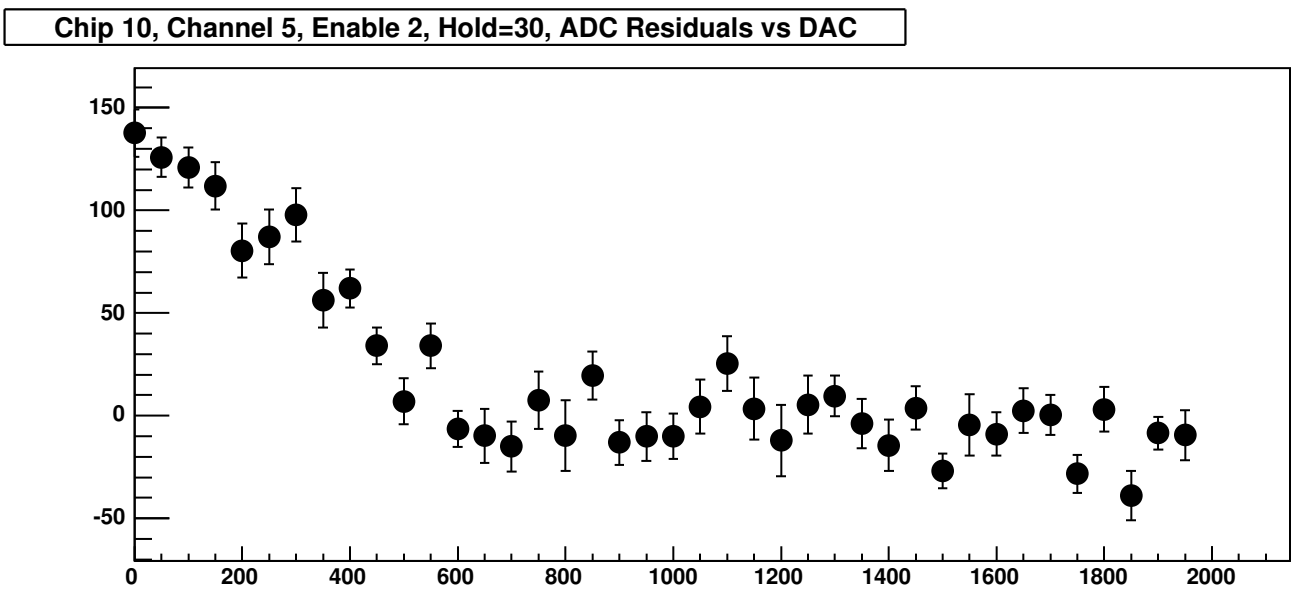

Chip 10, Channel 5, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 864.7 / 11
p0 -401 ± 19.15
p1 19.36 ± 0.01682
30000
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

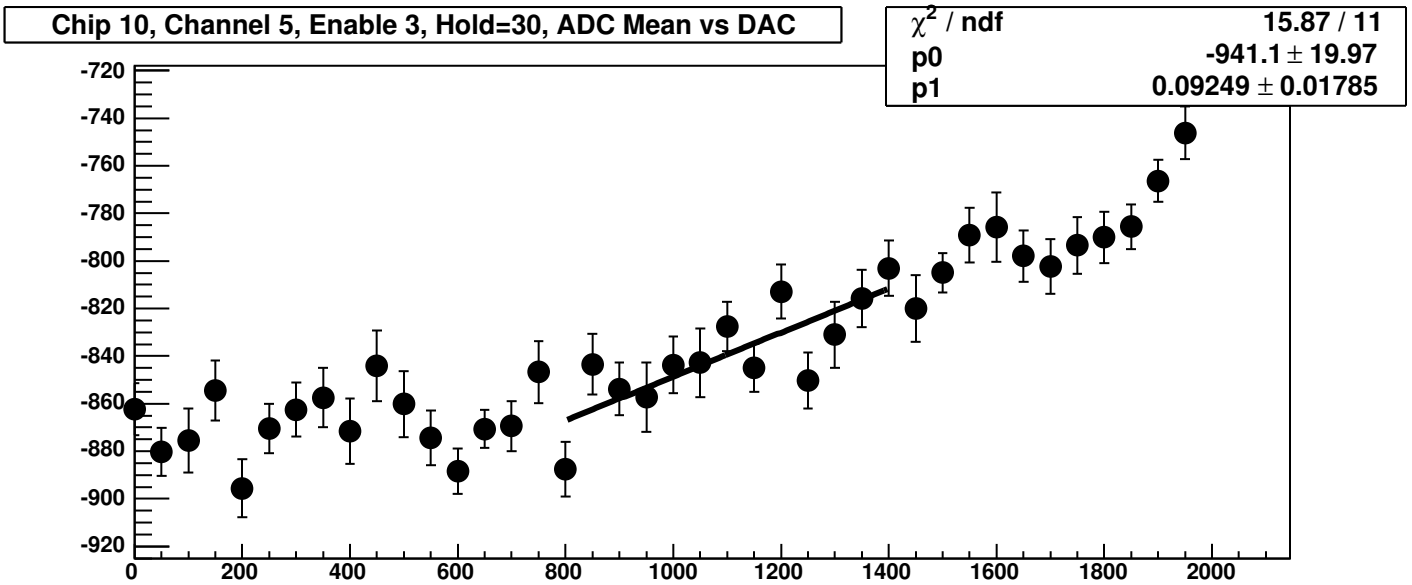

Chip 10, Channel 5, Enable 1!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

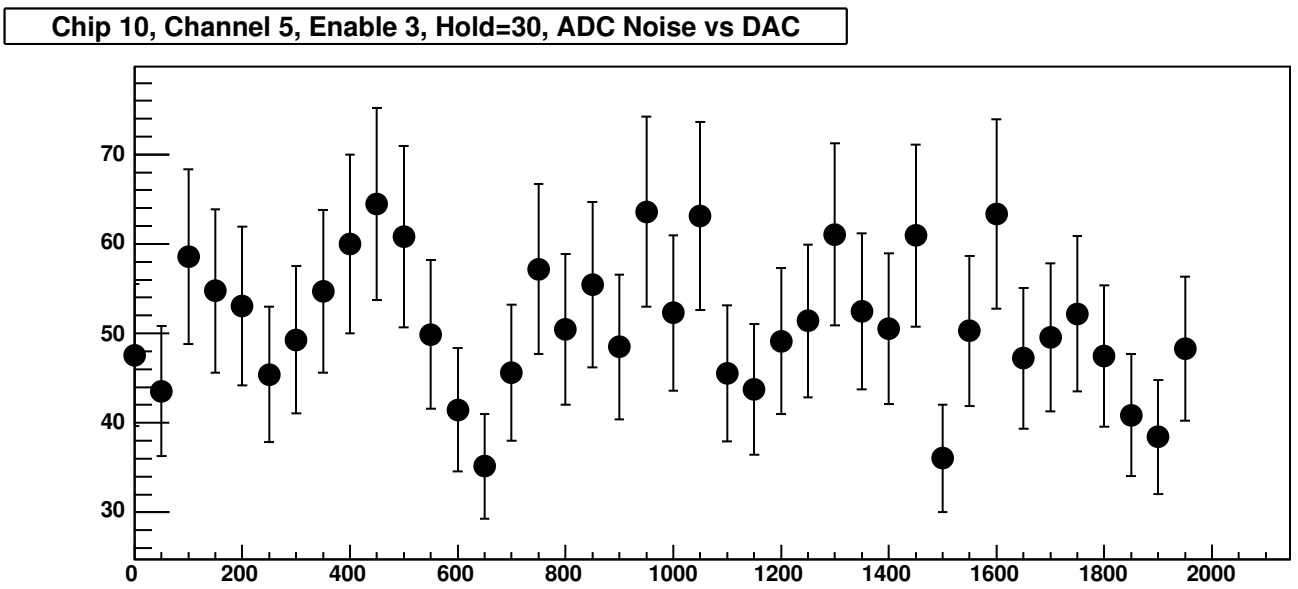

Chip 10, Channel 5, Enable 1!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

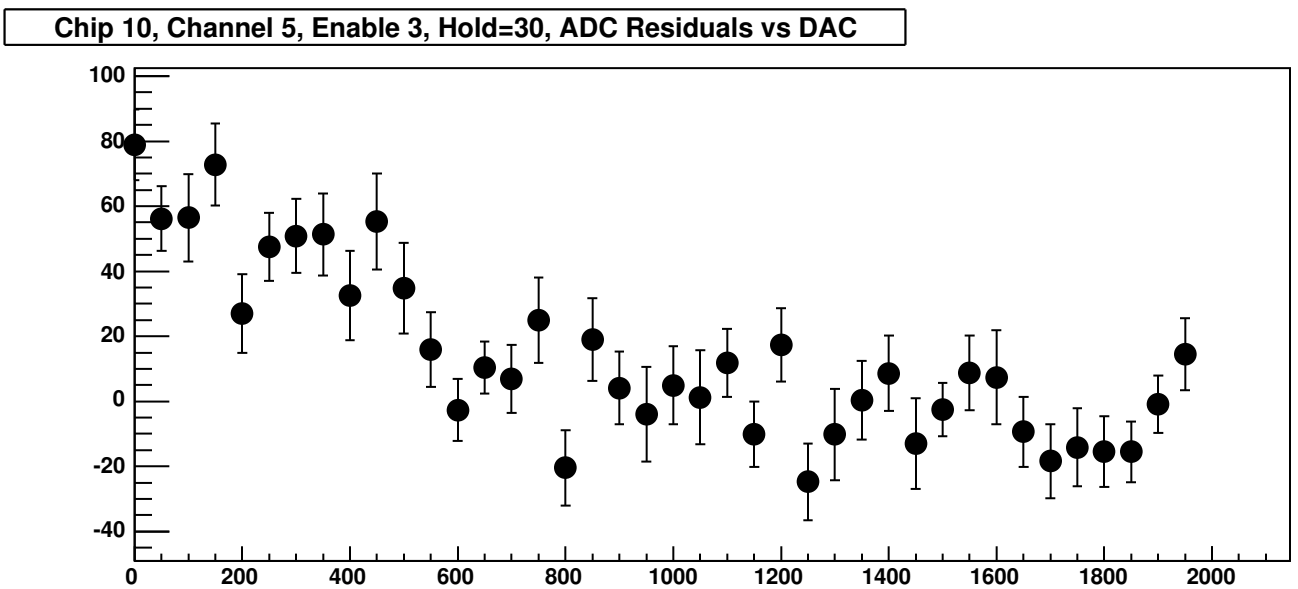

Chip 10, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 12.96 / 11
p0 -930.3 ± 20.69
p1 0.2263 ± 0.01848
-500
-550
-600
-650
-700
-750
-800
-850
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

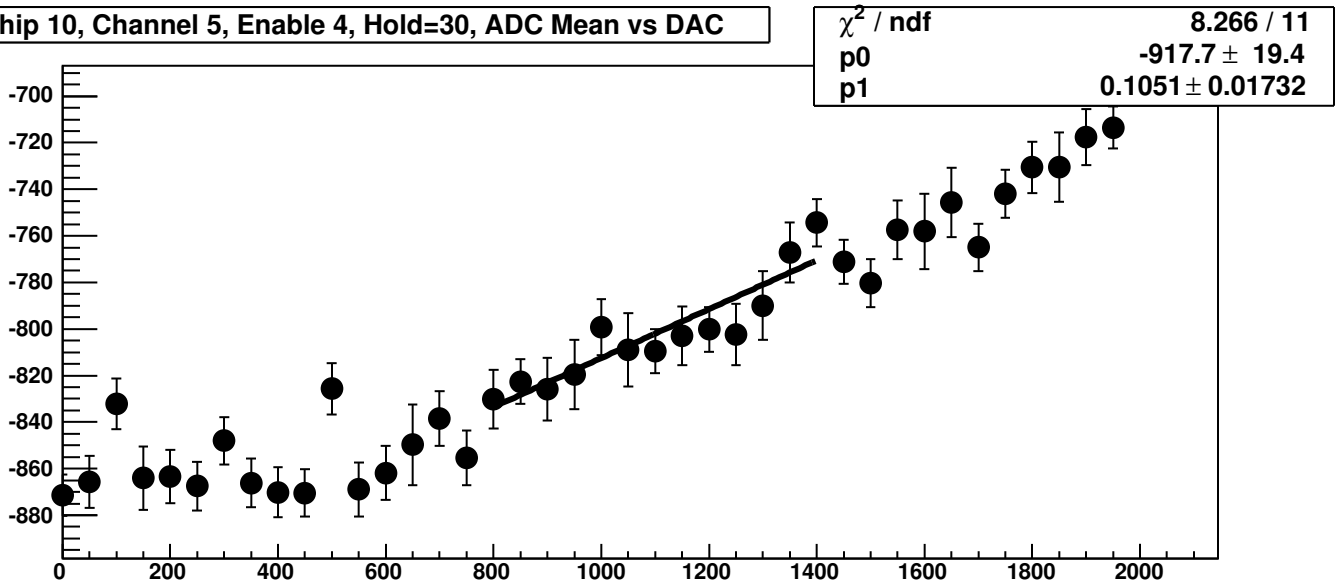

Chip 10, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 15.87 / 11
p0 -941.1 ± 19.97
p1 0.09249 ± 0.01785
-720
-740
-760
-780
-800
-820
-840
-860
-880
-900
-920
0
200
400
600
800
1000
1200
1400
1600
1800
2000

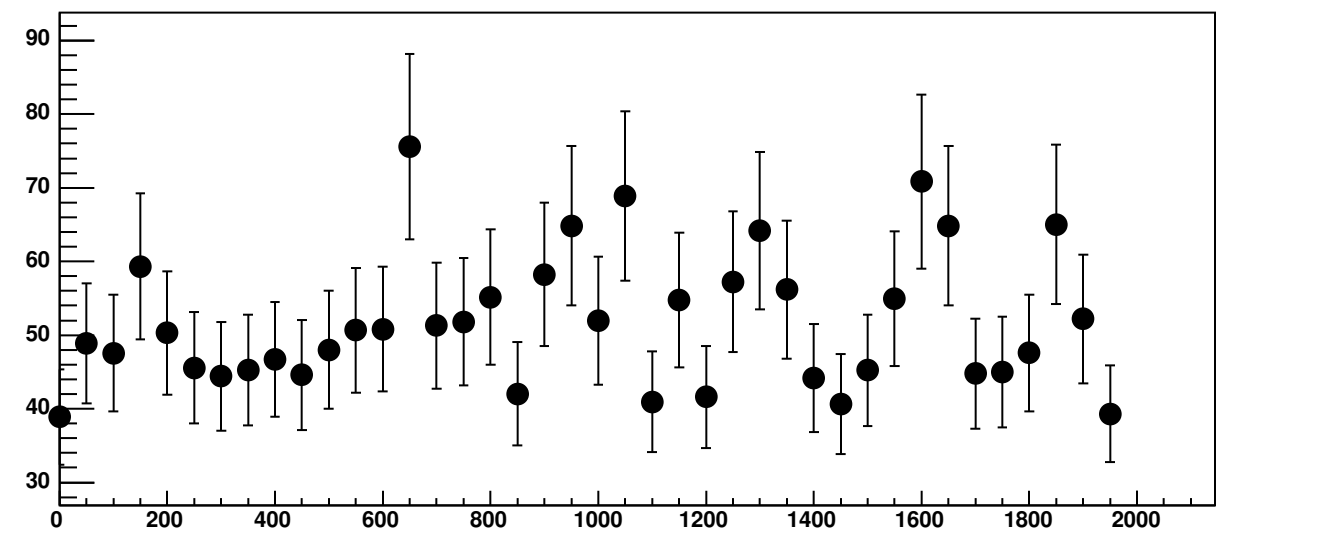

Chip 10, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

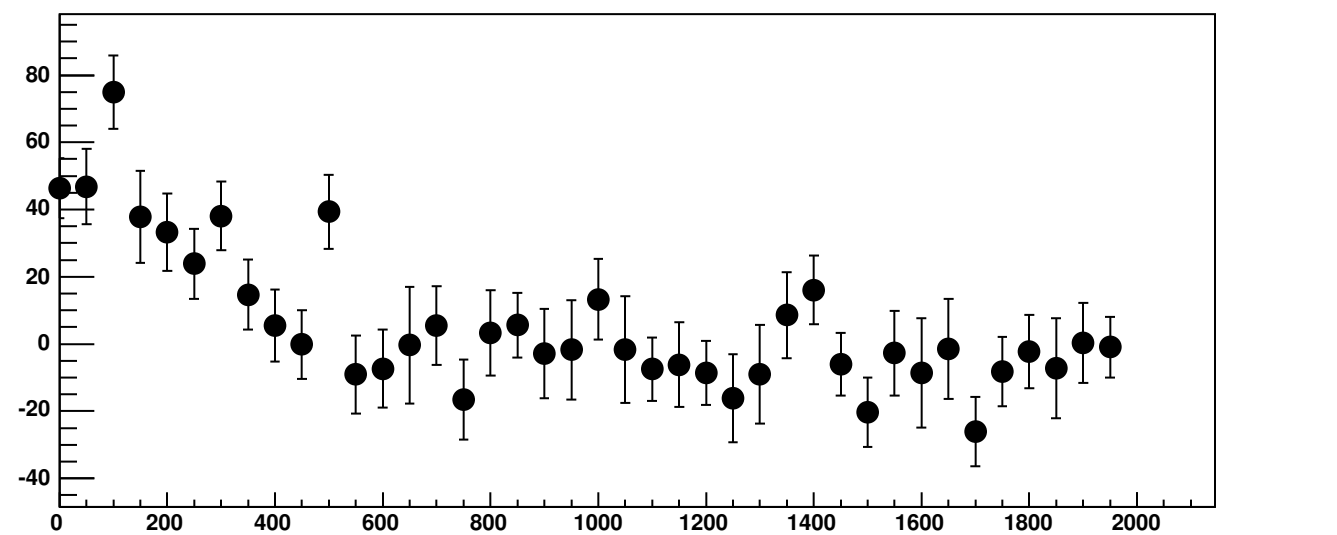
Chip 10, Channel 5, Enable 4, Hold=30, ADC Mean vs DAC

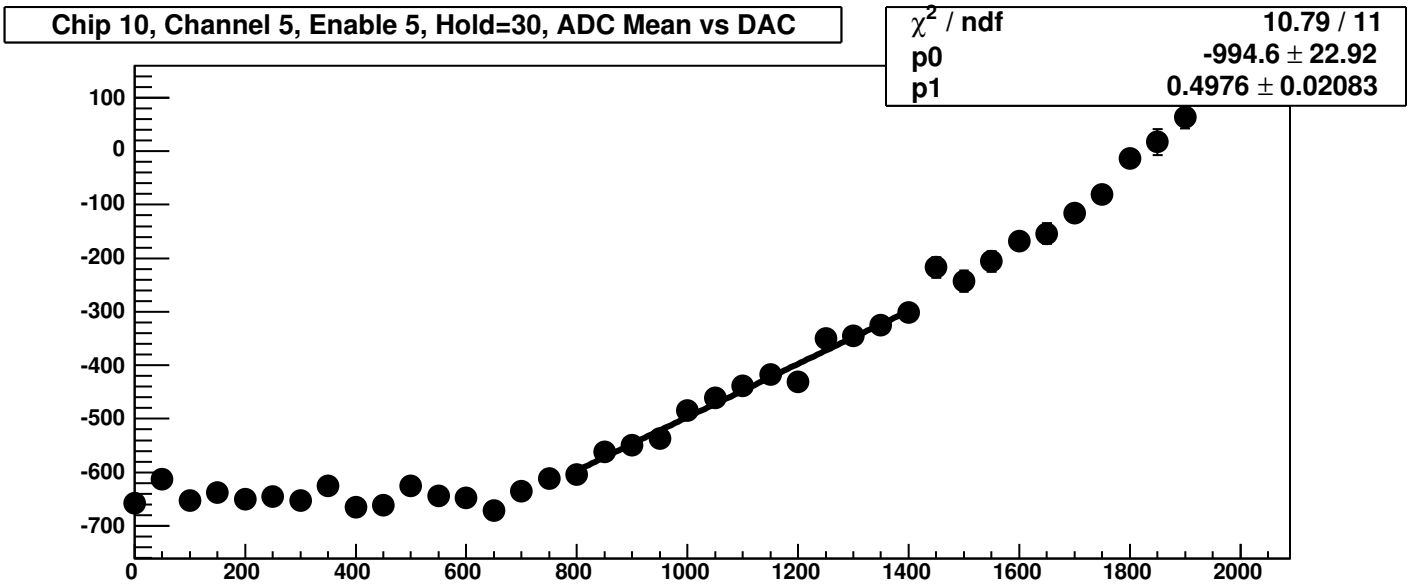


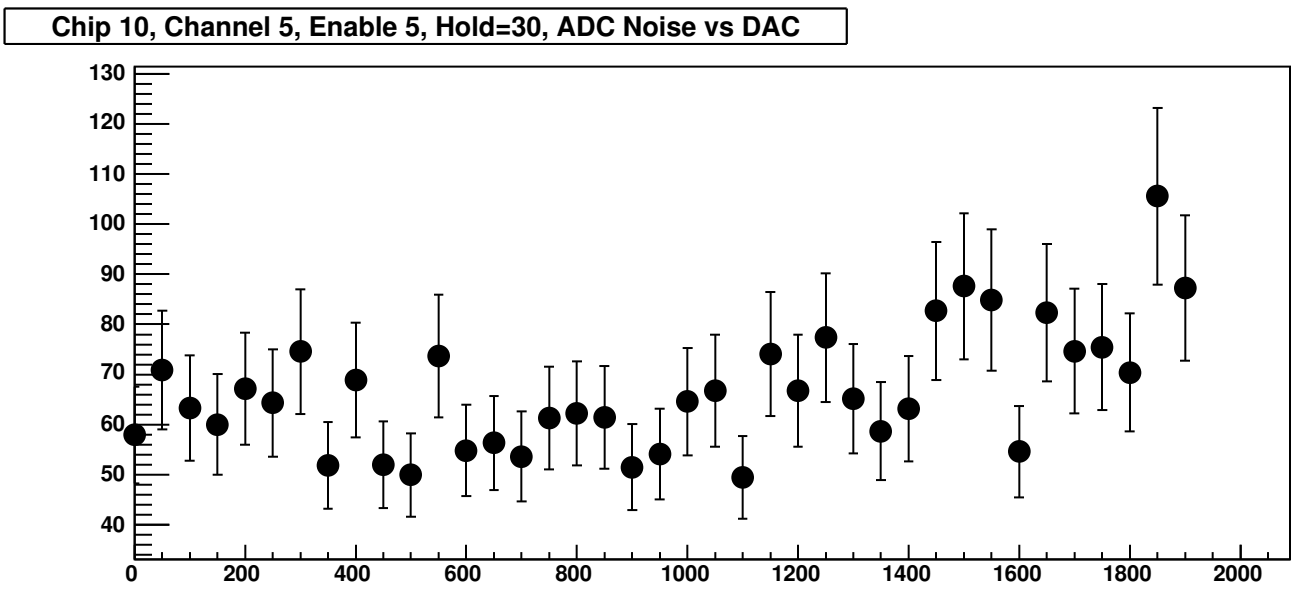
Chip 10, Channel 5, Enable 4, Hold=30, ADC Noise vs DAC

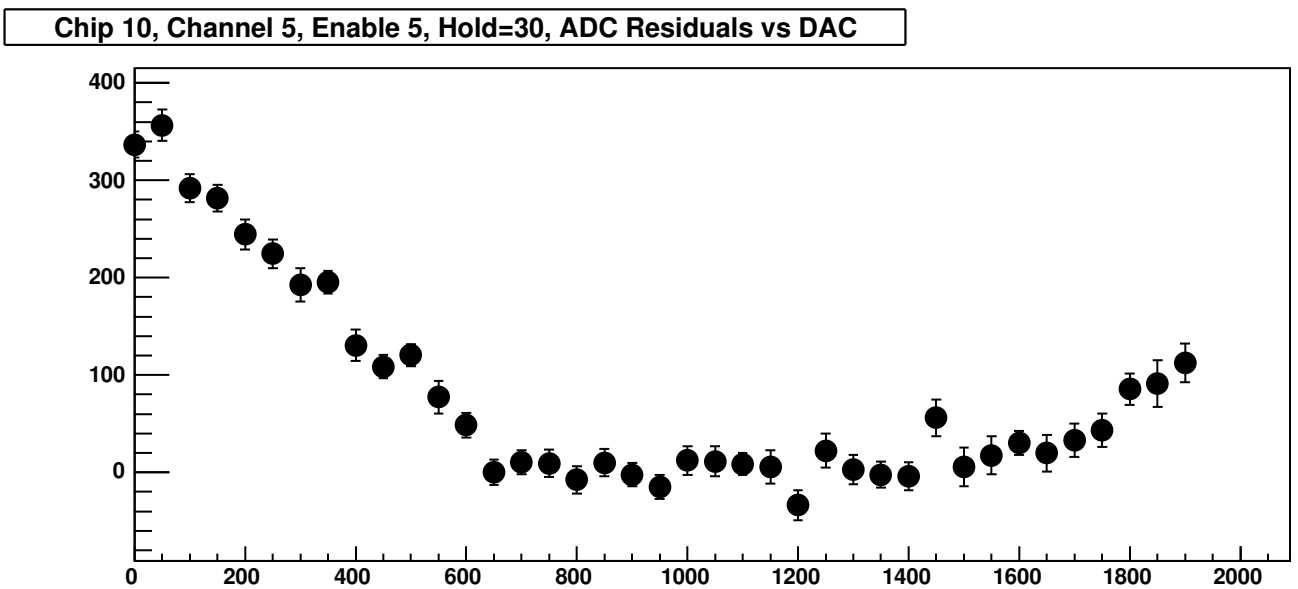


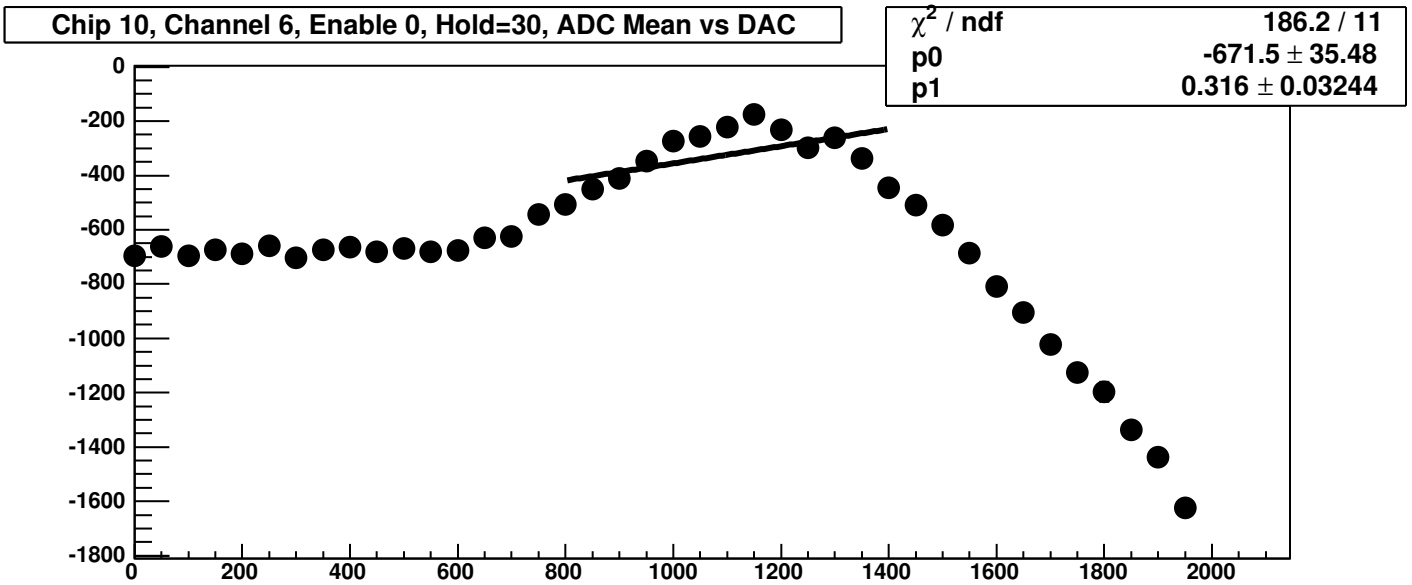
Chip 10, Channel 5, Enable 4, Hold=30, ADC Residuals vs DAC

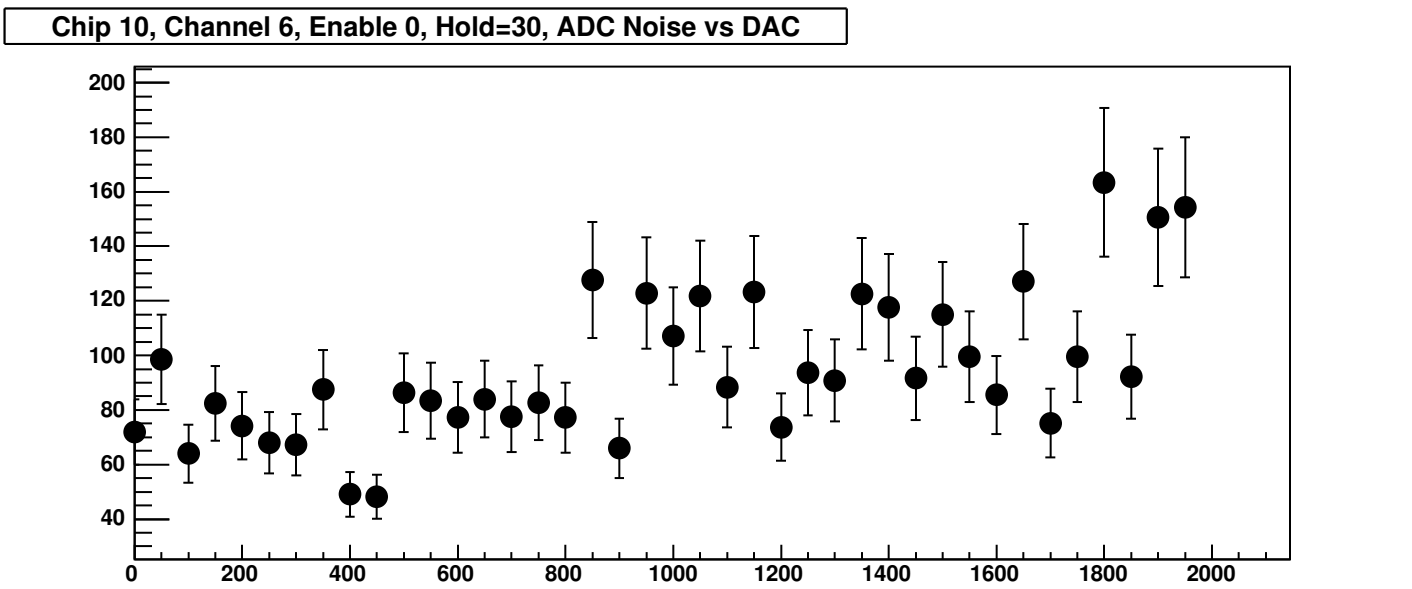


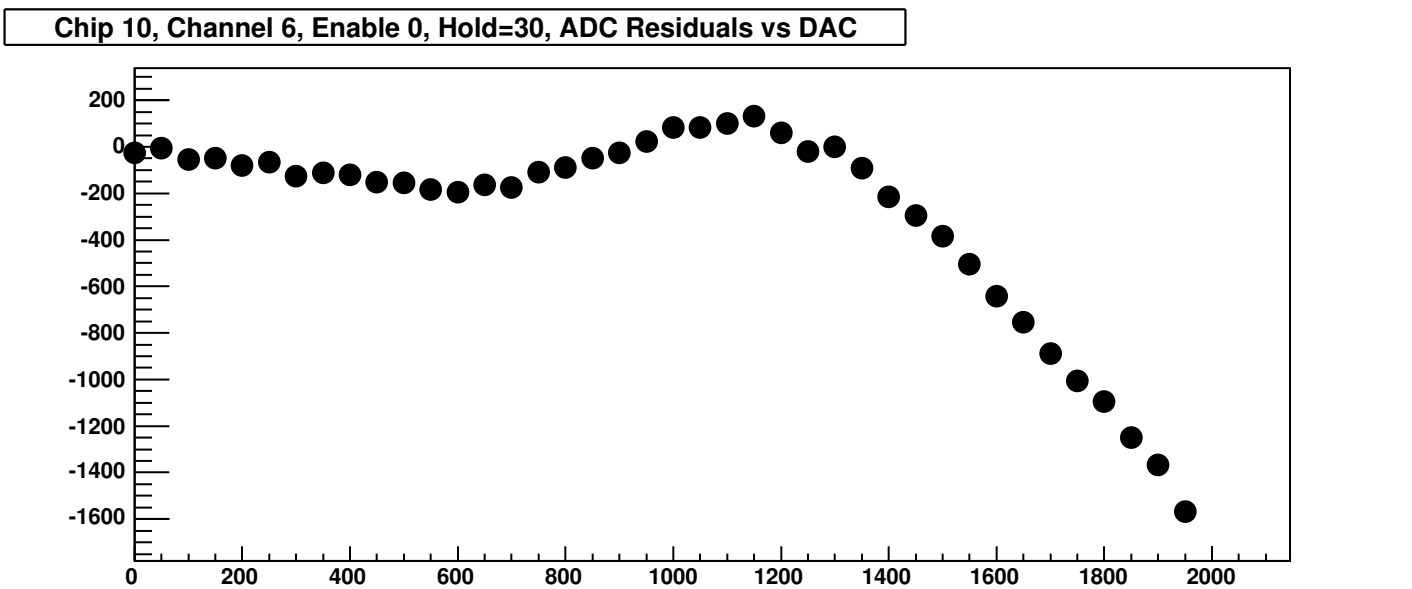

Chip 10, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 10.79 / 11
p0 -994.6 ± 22.92
p1 0.4976 ± 0.02083
100
0
-100
-200
-300
-400
-500
-600
-700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

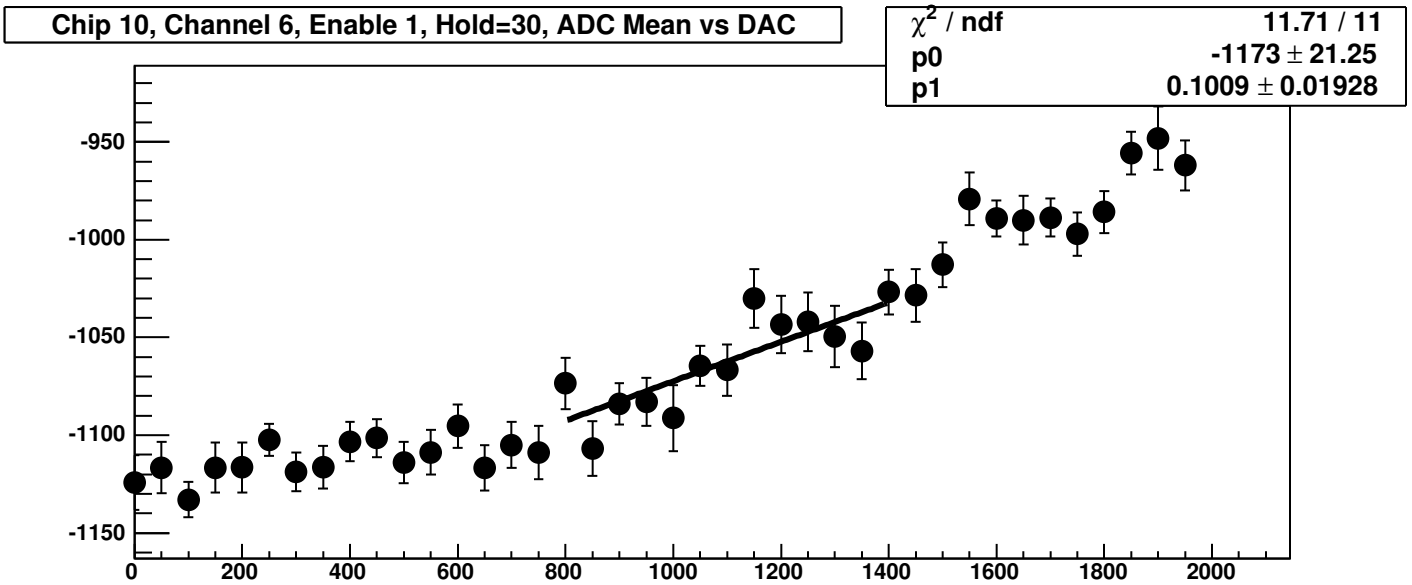

Chip 10, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
130
120
110
100
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

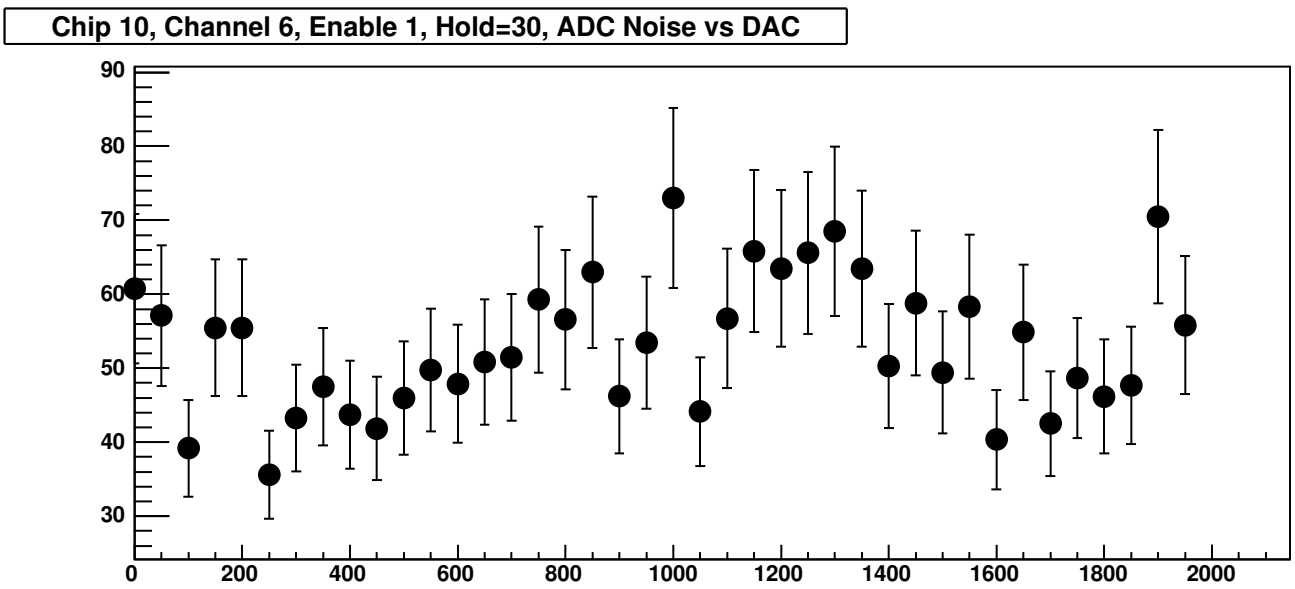

Chip 10, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
400
300
200
100
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

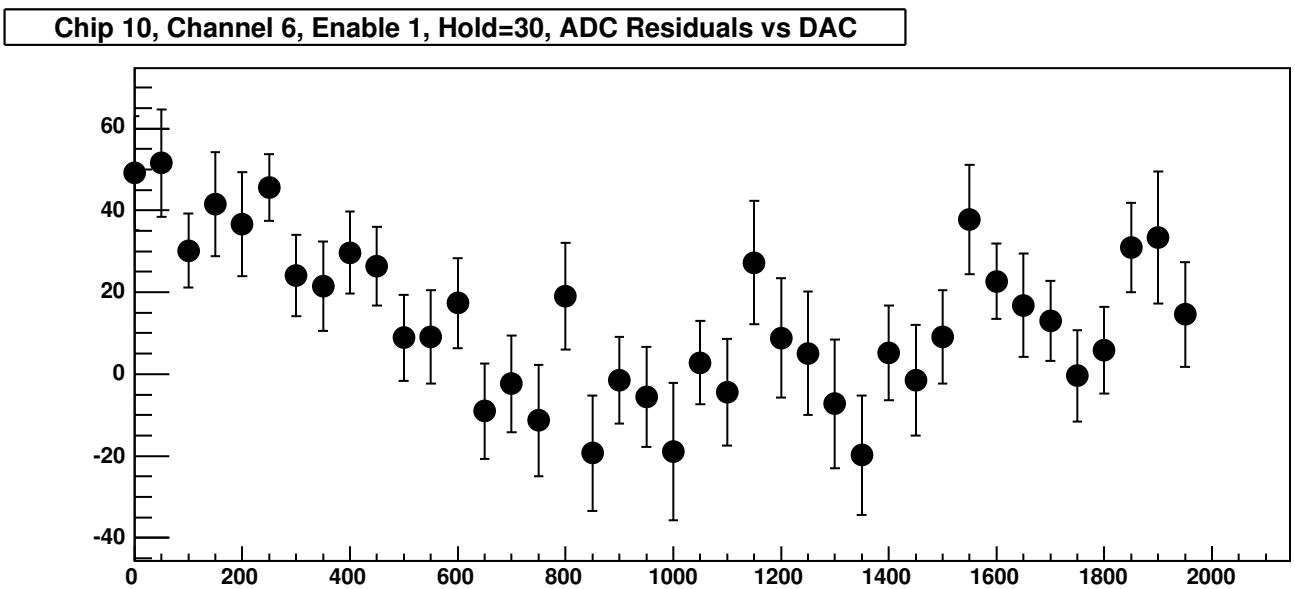

Chip 10, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 186.2 / 11
p0 -671.5 ± 35.48
p1 0.316 ± 0.03244
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

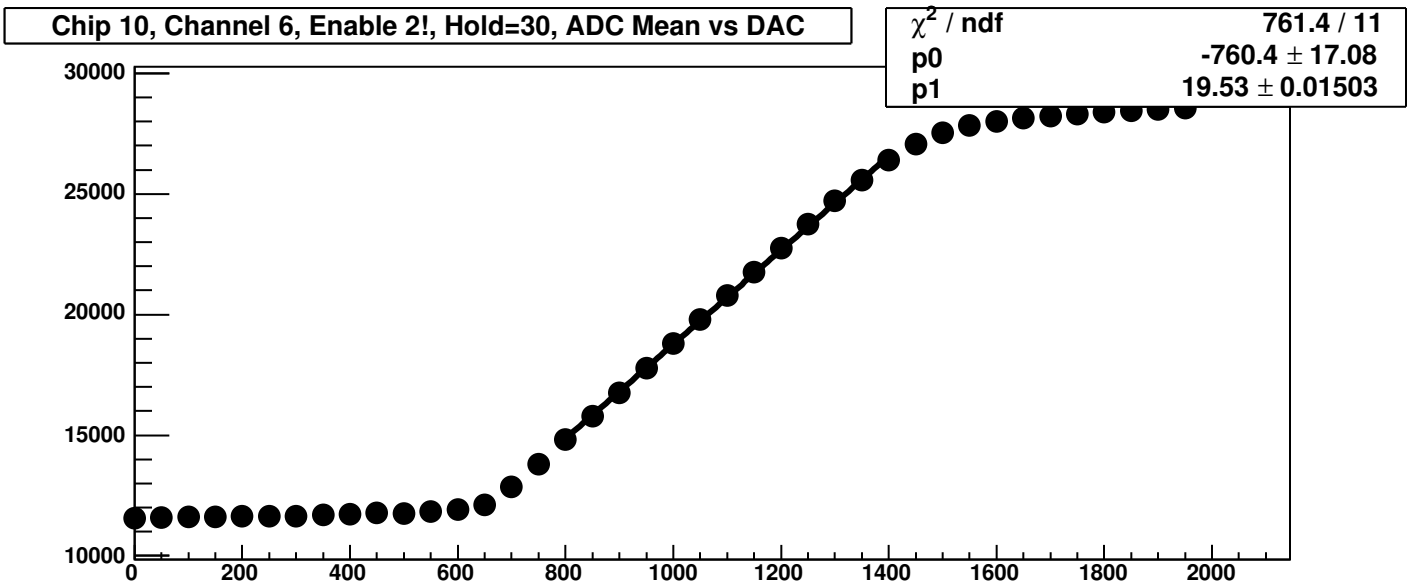

Chip 10, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

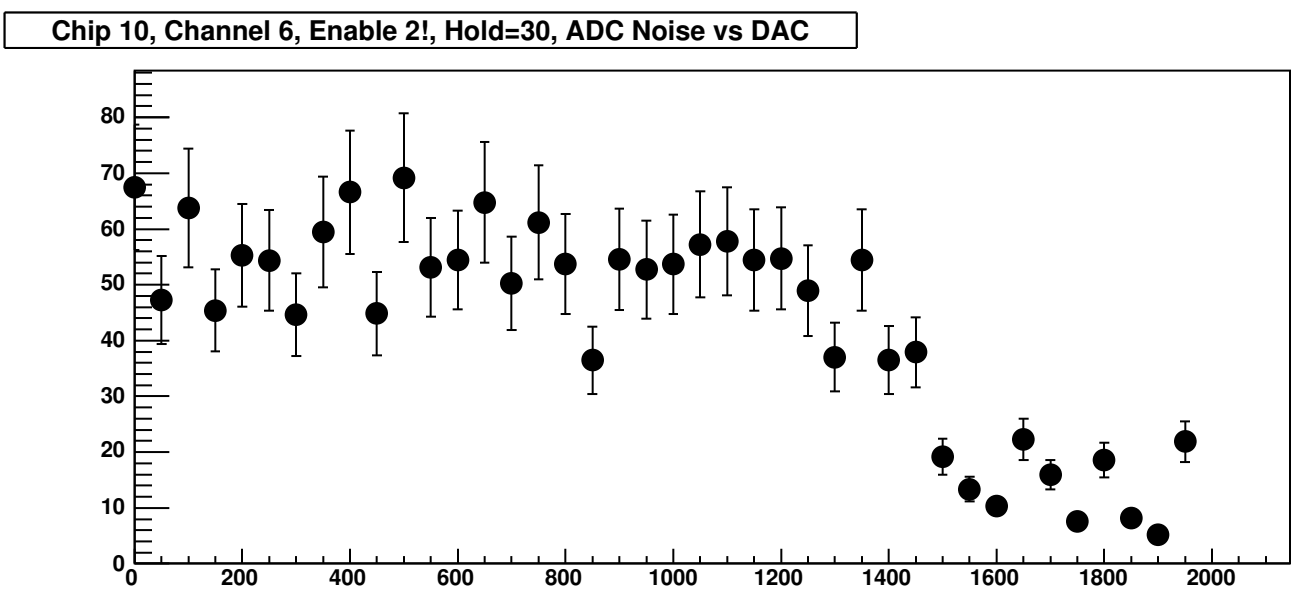

Chip 10, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

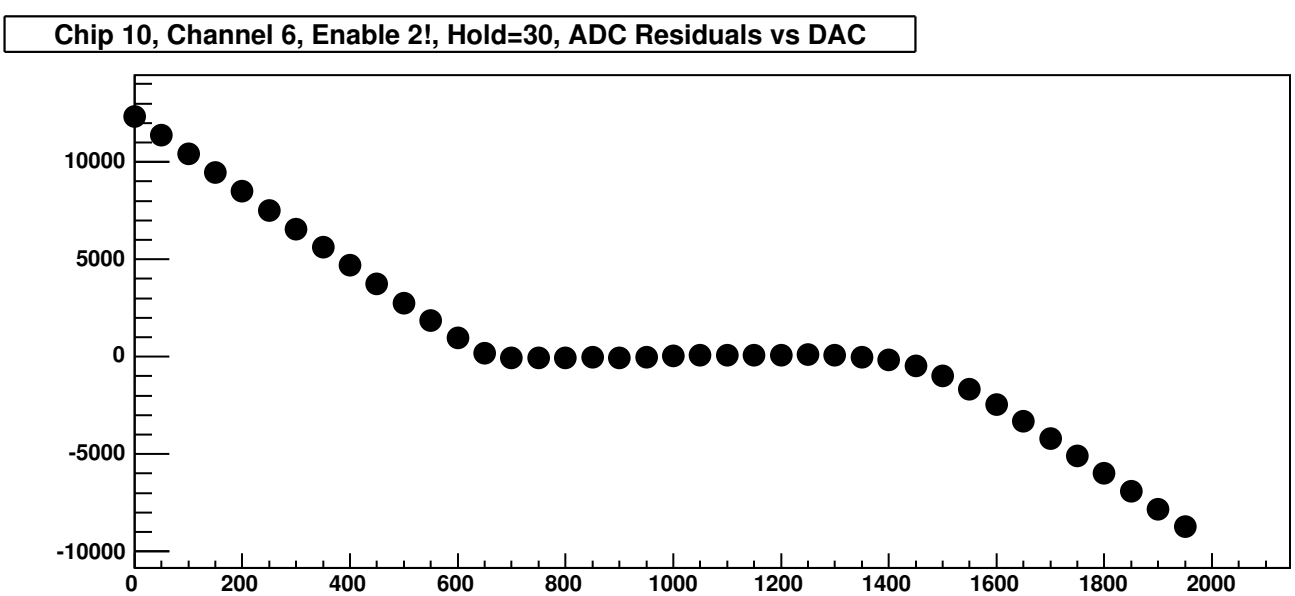

Chip 10, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 11.71 / 11
p0 -1173 ± 21.25
p1 0.1009 ± 0.01928
-950
-1000
-1050
-1100
-1150
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

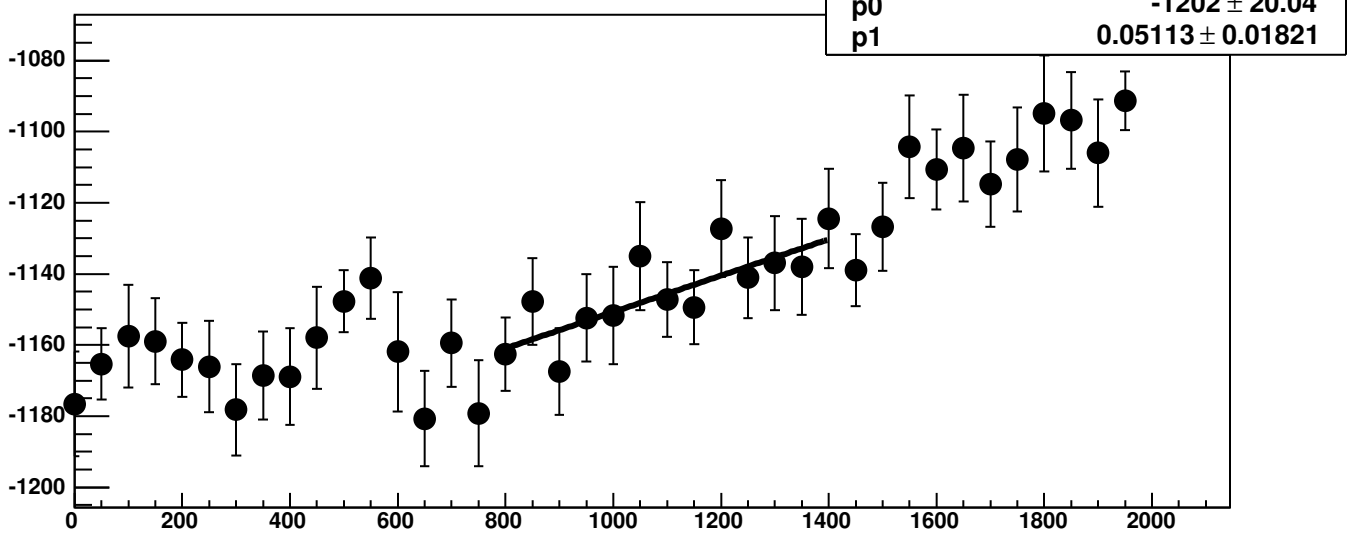

Chip 10, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 6, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 761.4 / 11
p0 -760.4 ± 17.08
p1 19.53 ± 0.01503
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

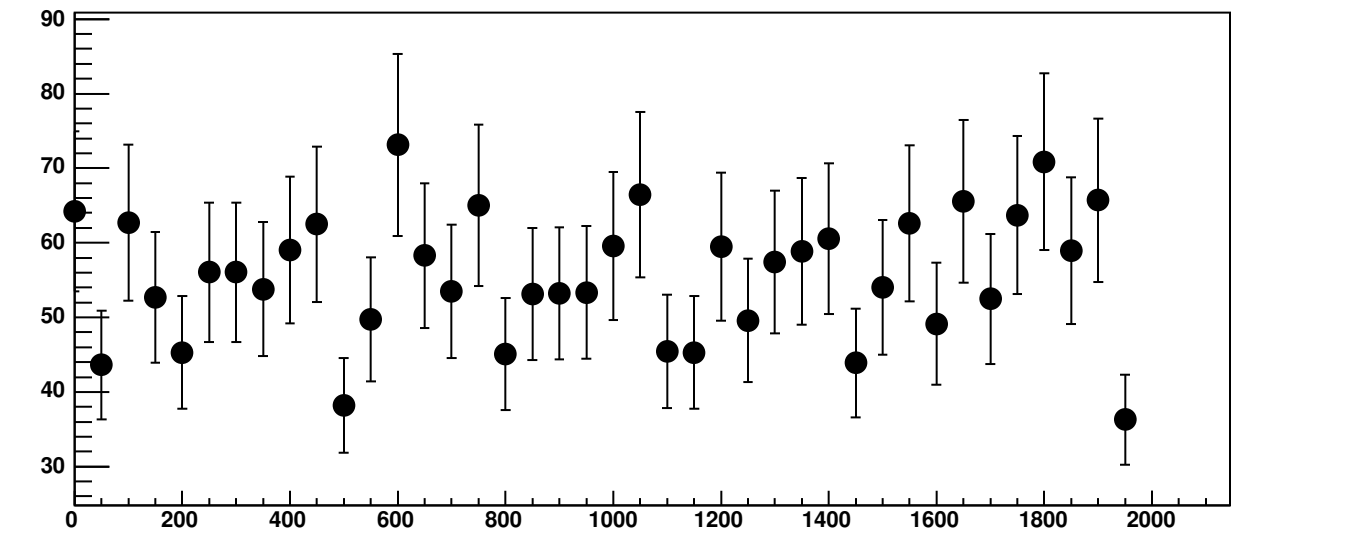

Chip 10, Channel 6, Enable 2!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 6, Enable 2!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

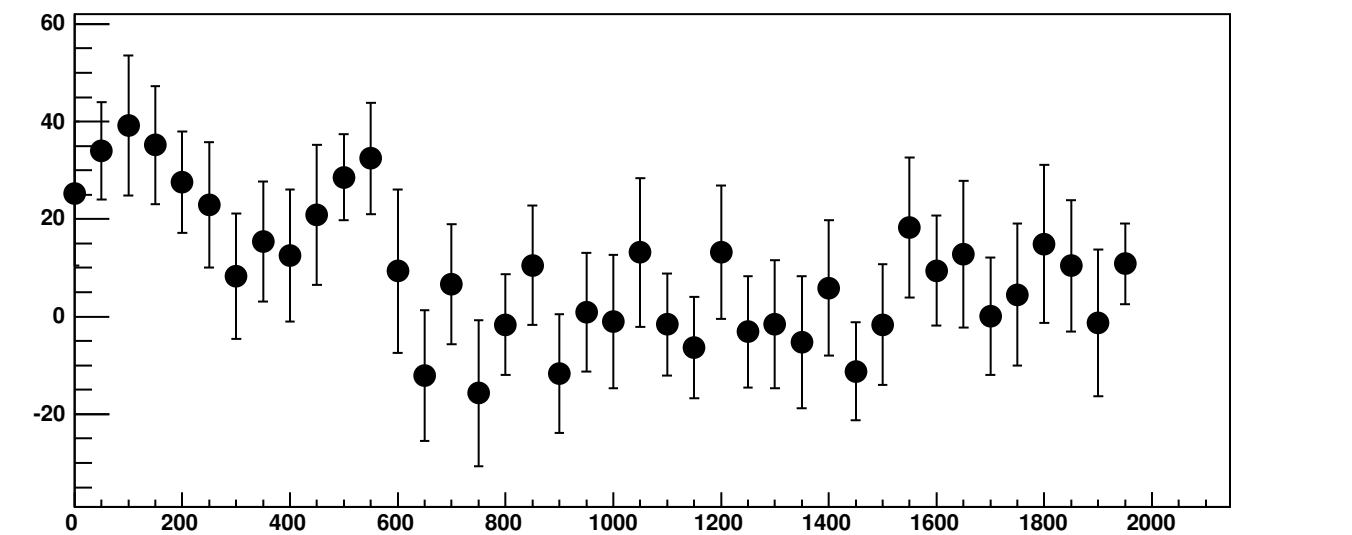
Chip 10, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC

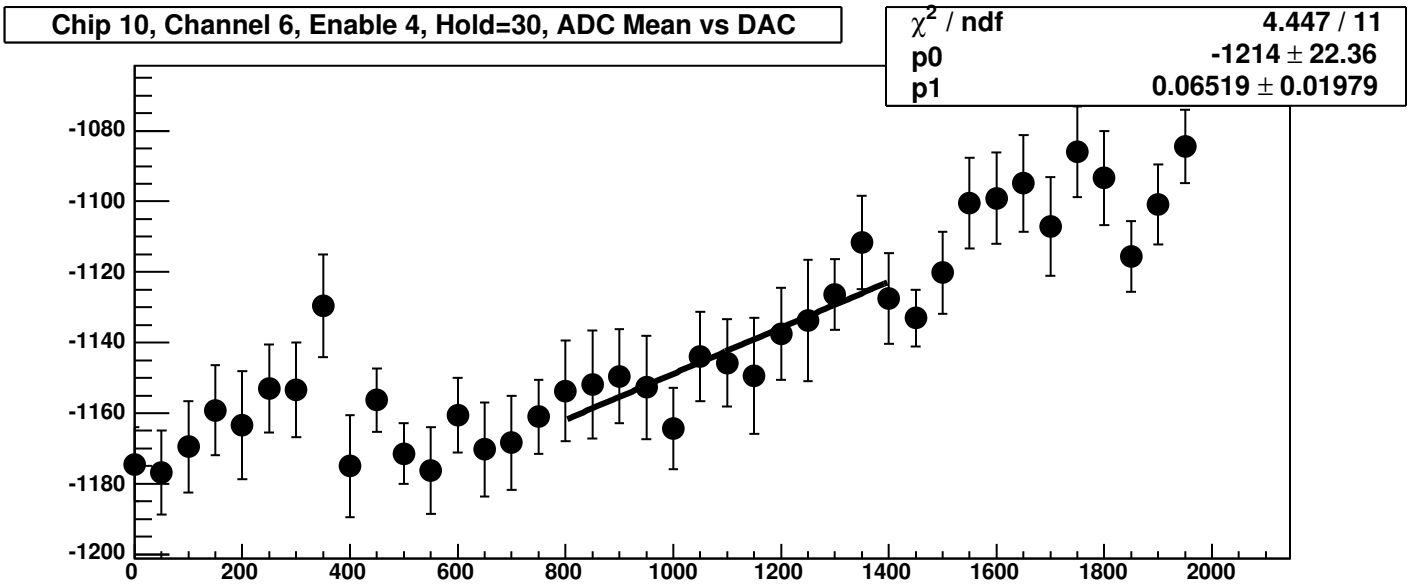


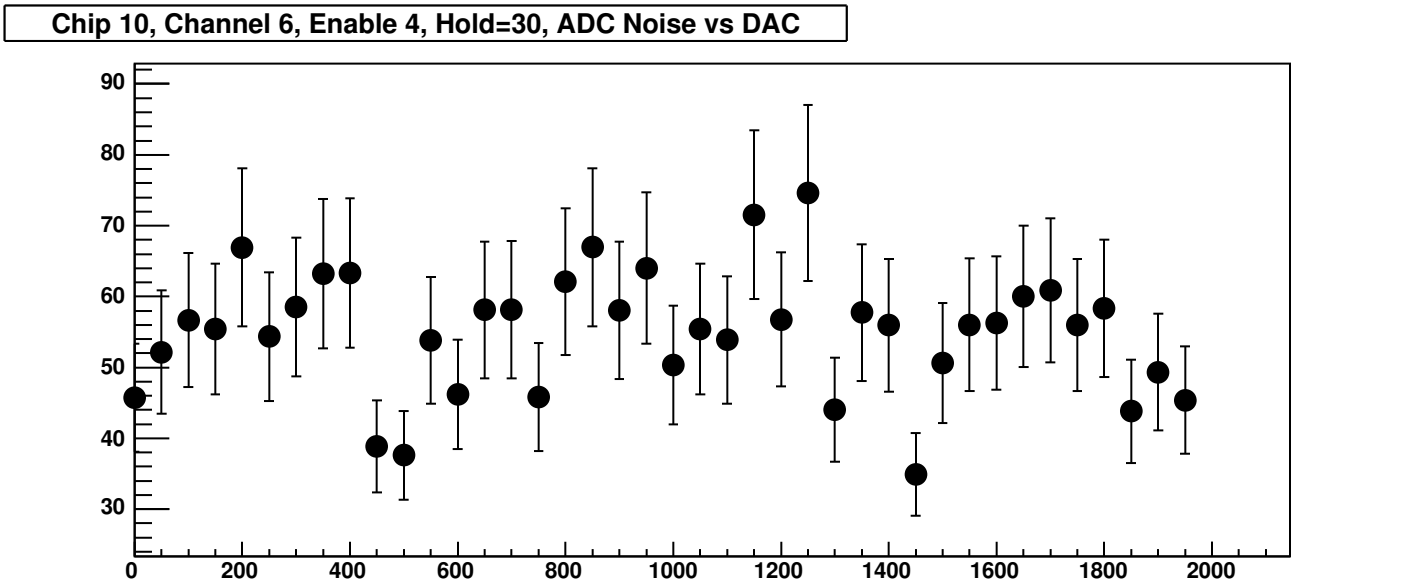
Chip 10, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC

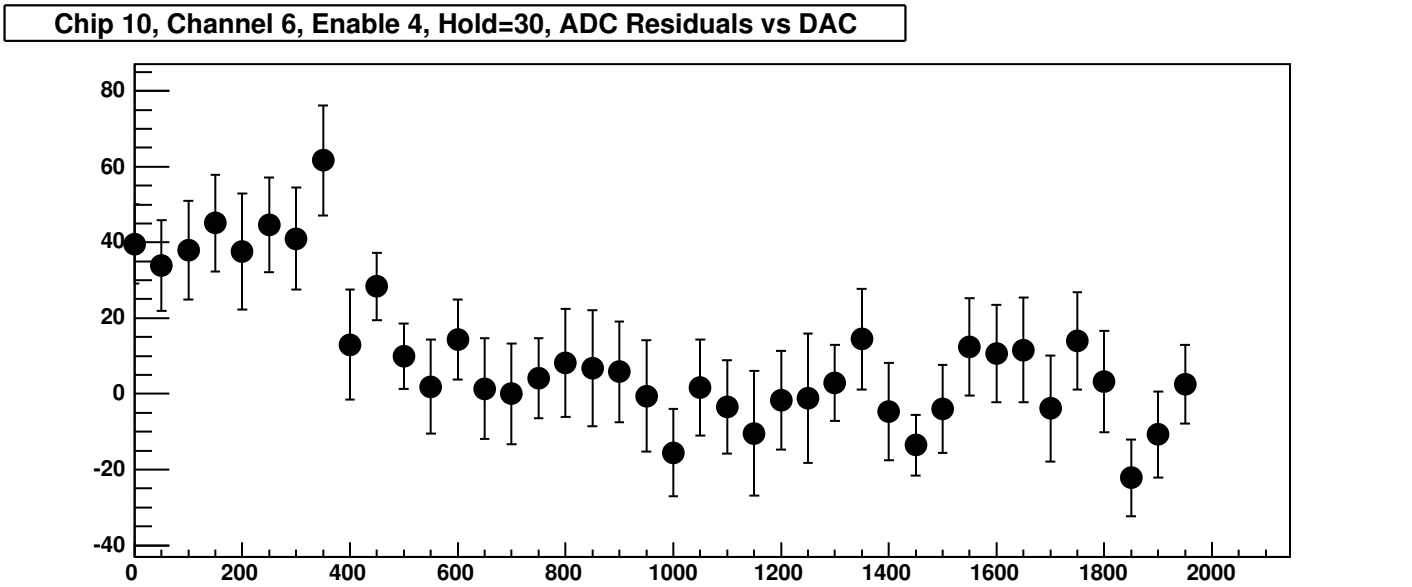


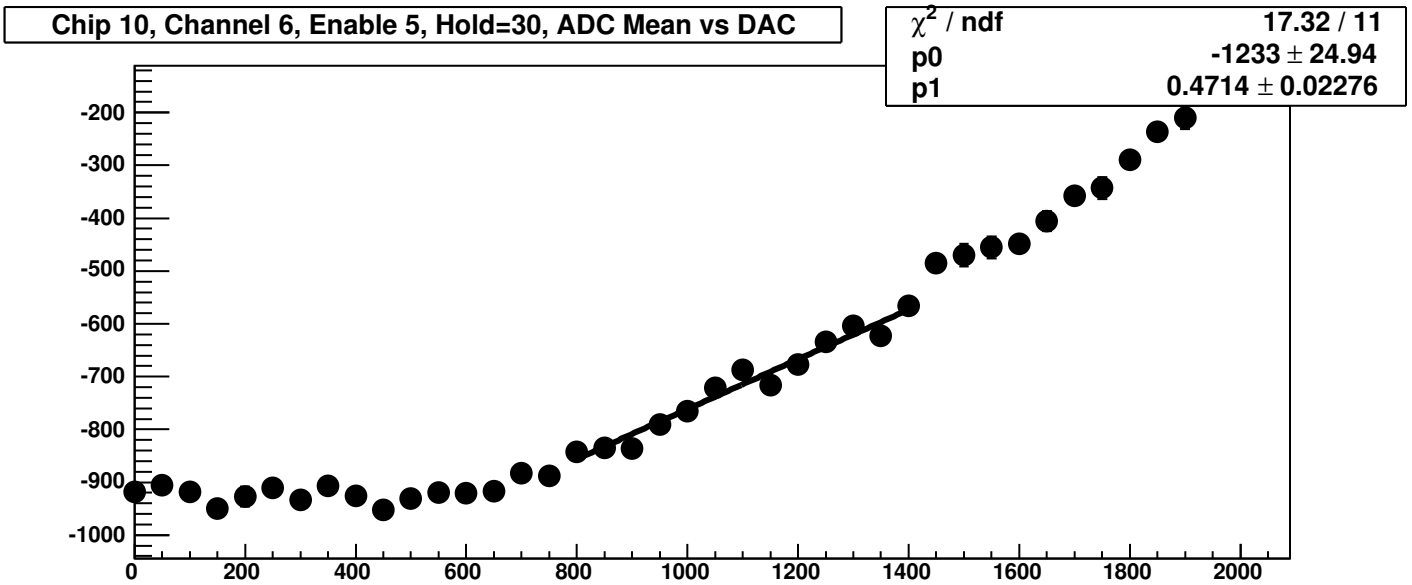
Chip 10, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC

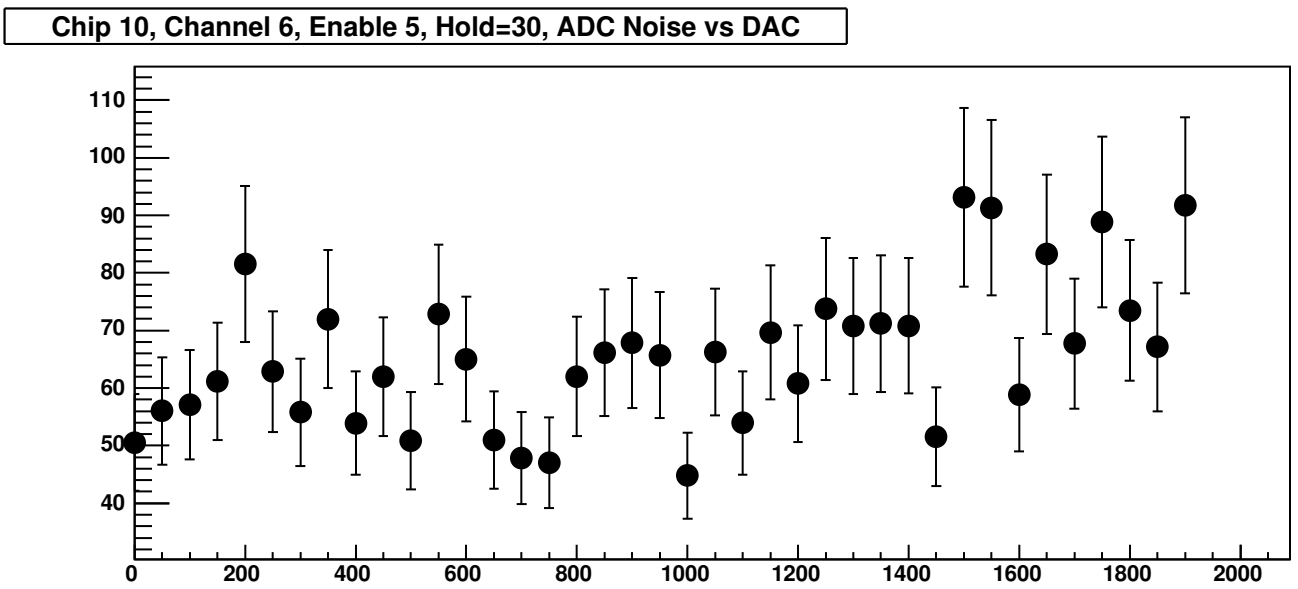


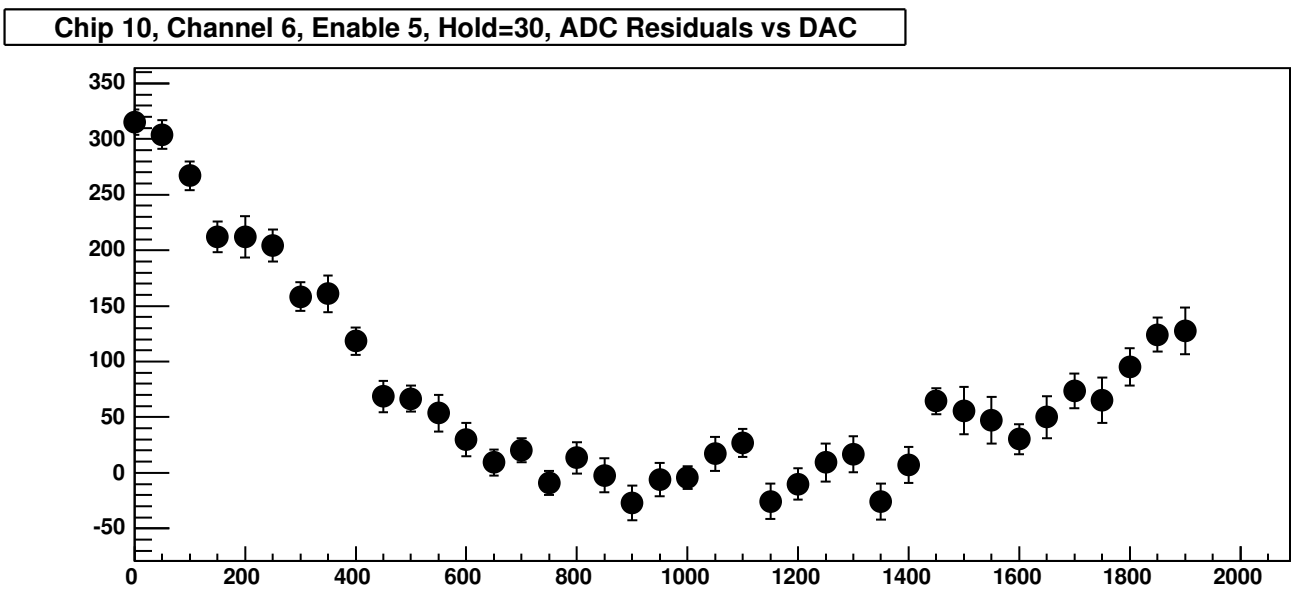

Chip 10, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4.447 / 11
p0 -1214 ± 22.36
p1 0.06519 ± 0.01979
-1080
-1100
-1120
-1140
-1160
-1180
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

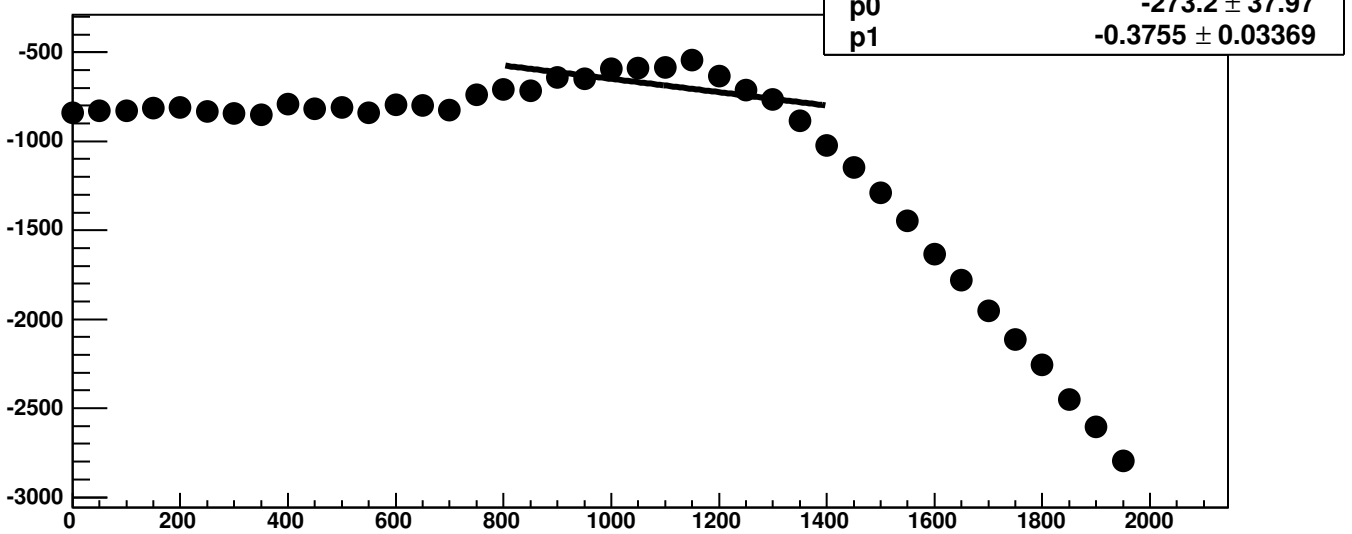

Chip 10, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 6, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 17.32 / 11
p0 -1233 ± 24.94
p1 0.4714 ± 0.02276
-200
-300
-400
-500
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

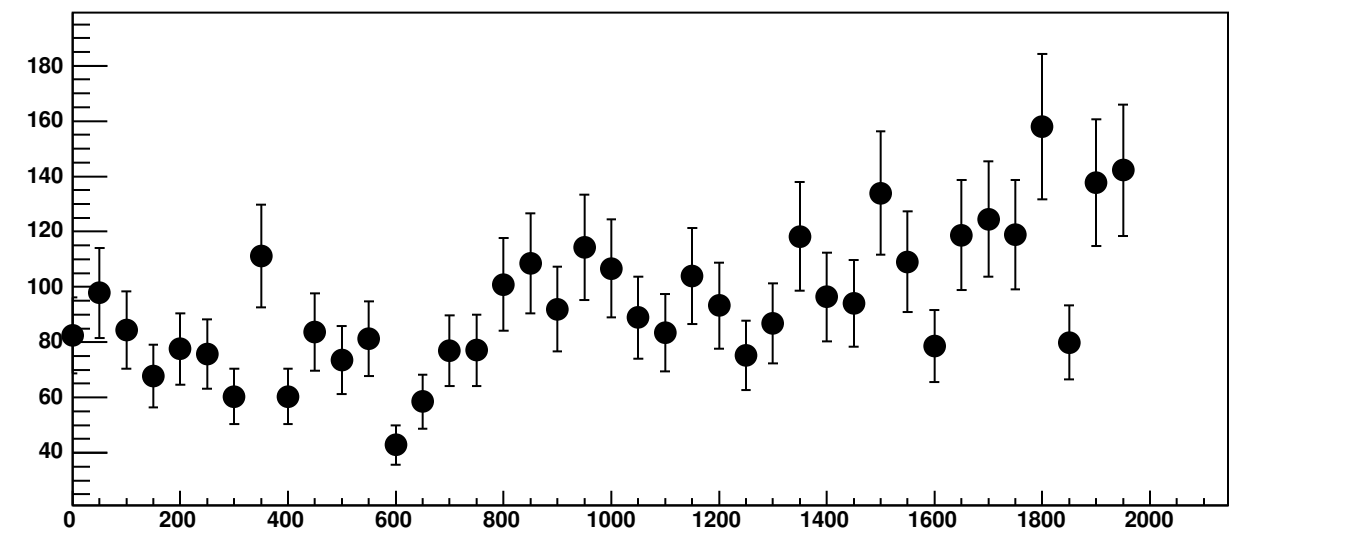

Chip 10, Channel 6, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 6, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

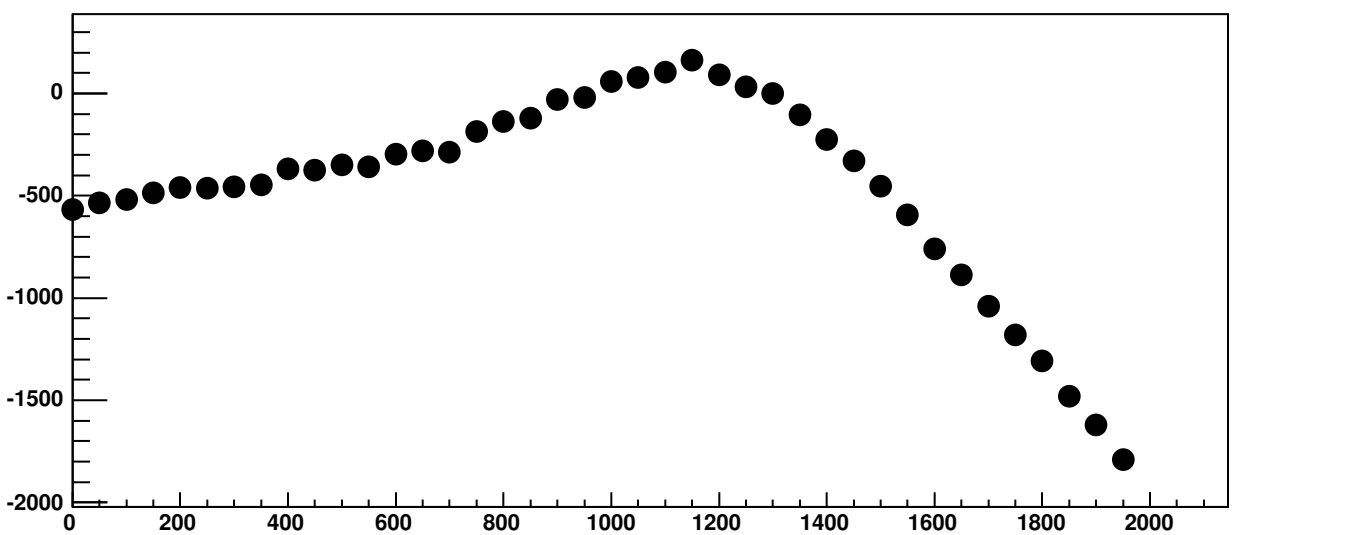
Chip 10, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC

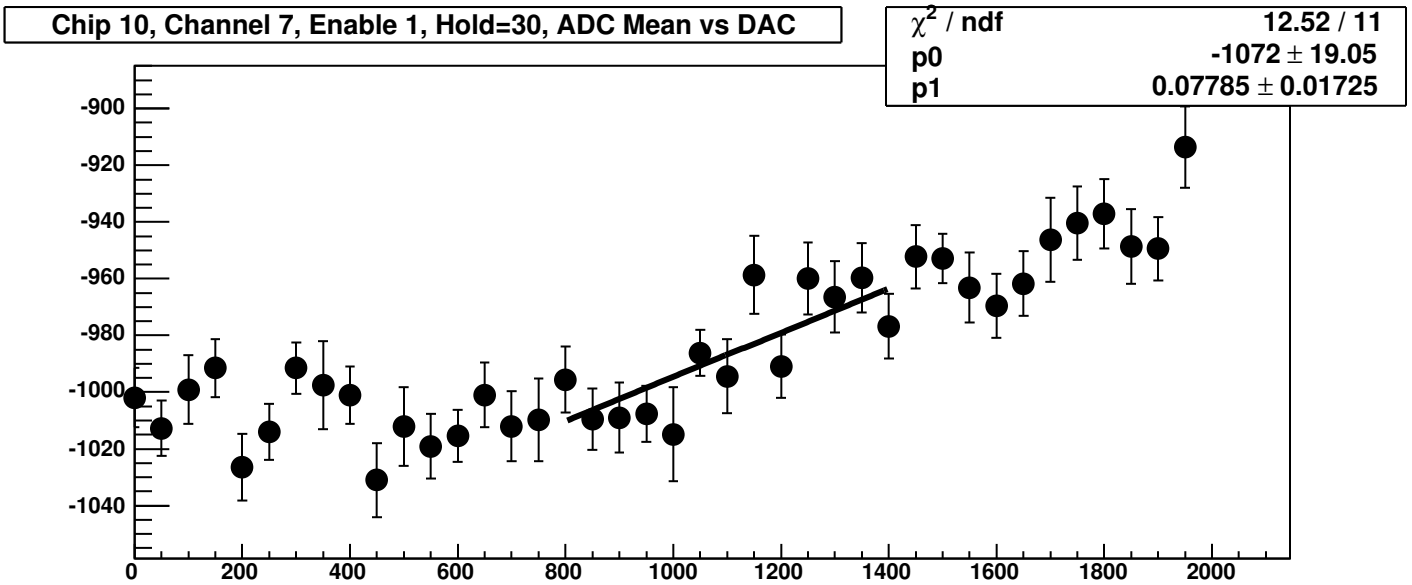


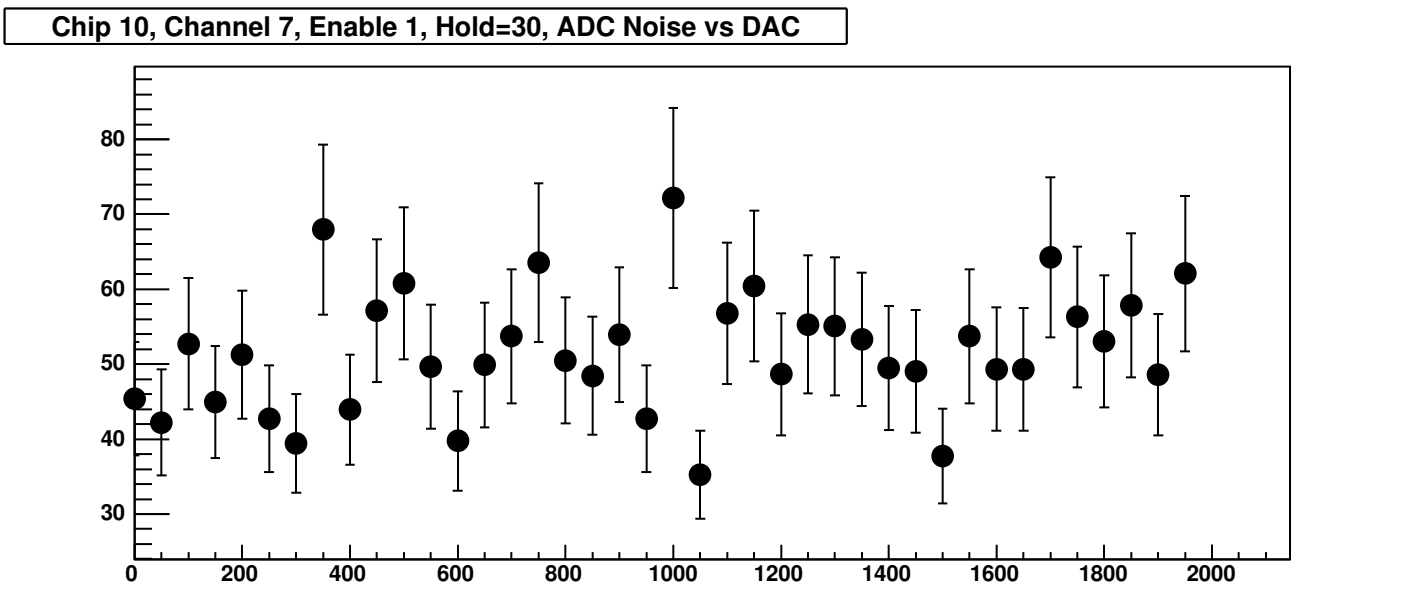
Chip 10, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC

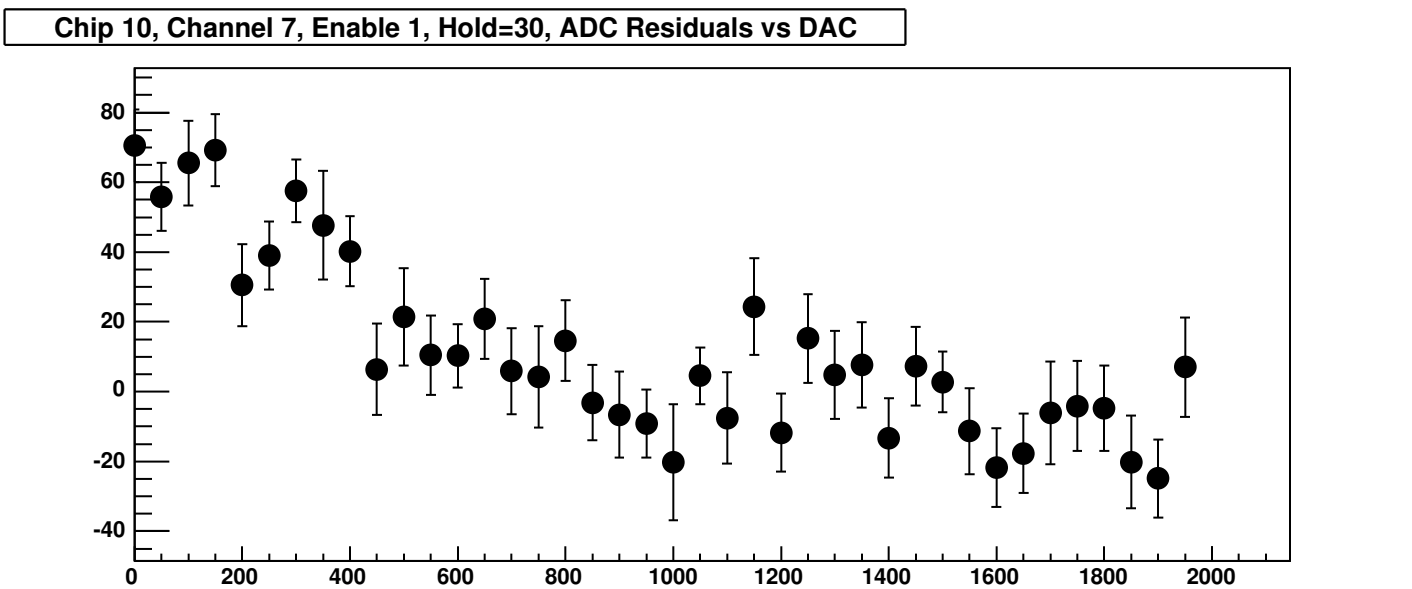


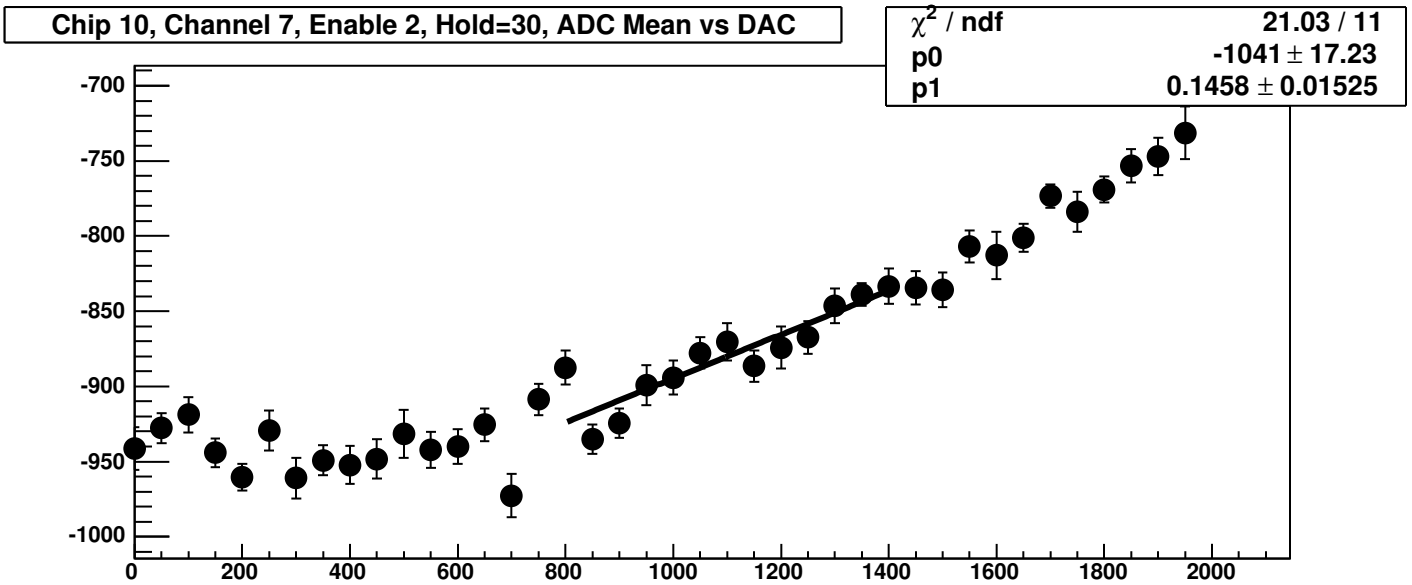
Chip 10, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC

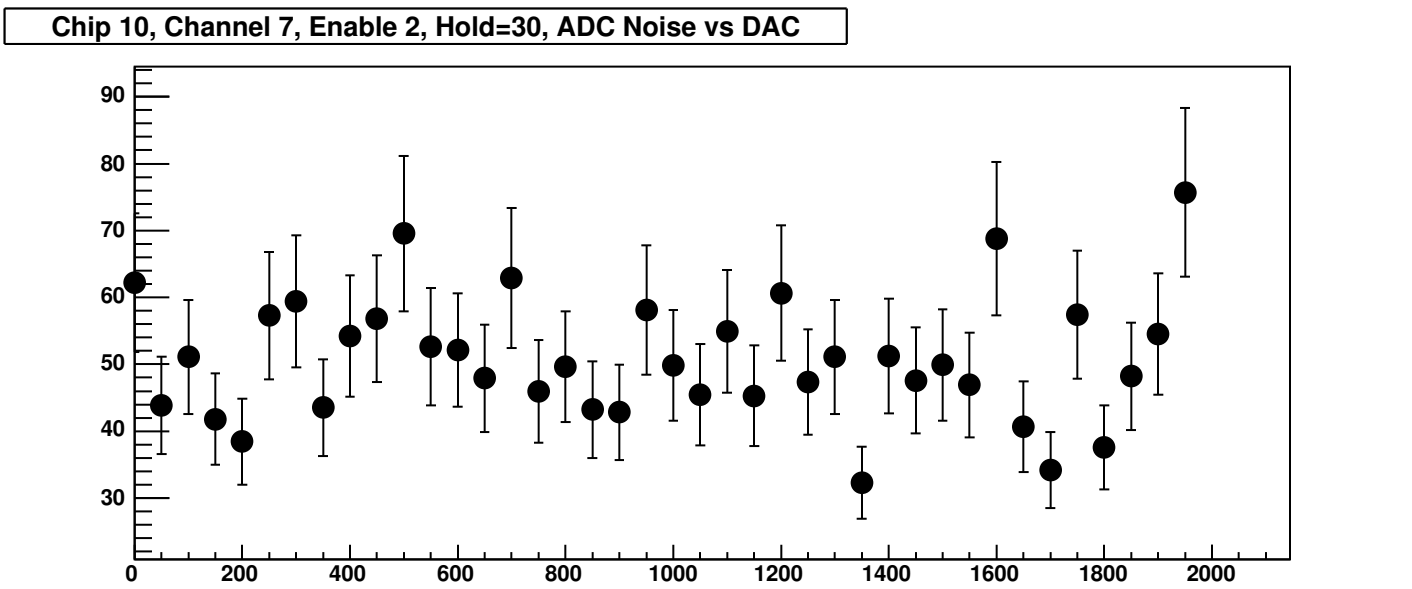


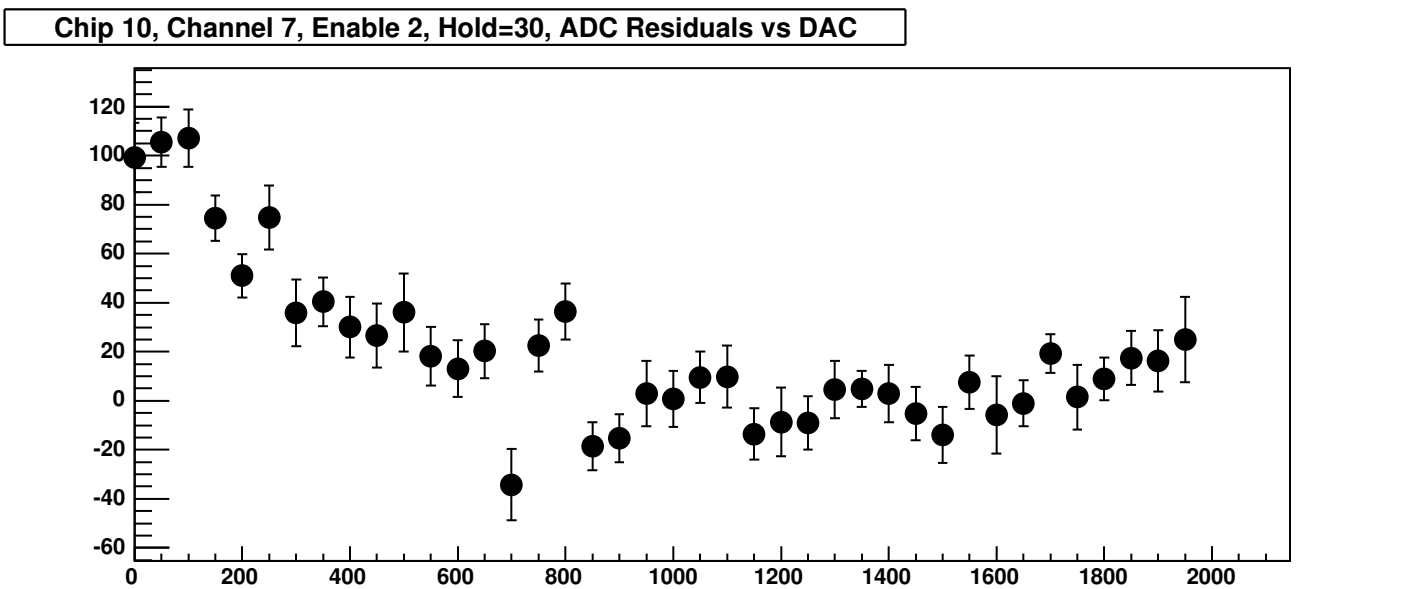

Chip 10, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 12.52 / 11
p0 -1072 ± 19.05
p1 0.07785 ± 0.01725
-900
-920
-940
-960
-980
-1000
-1020
-1040
0
200
400
600
800
1000
1200
1400
1600
1800
2000

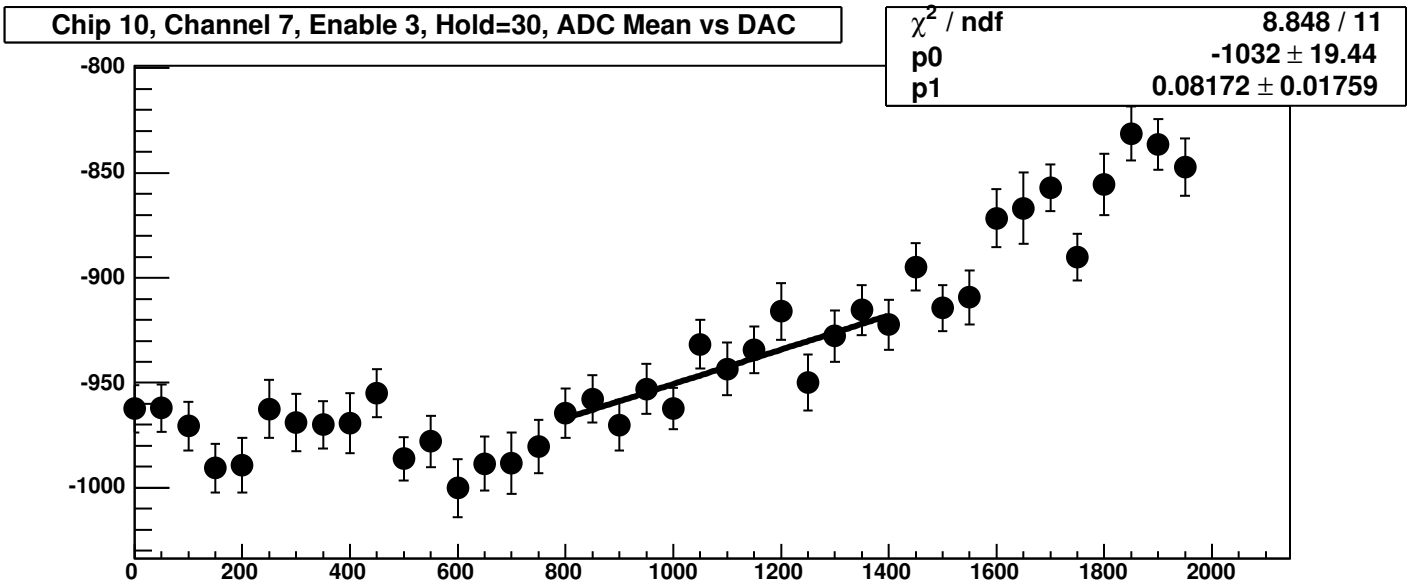

Chip 10, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

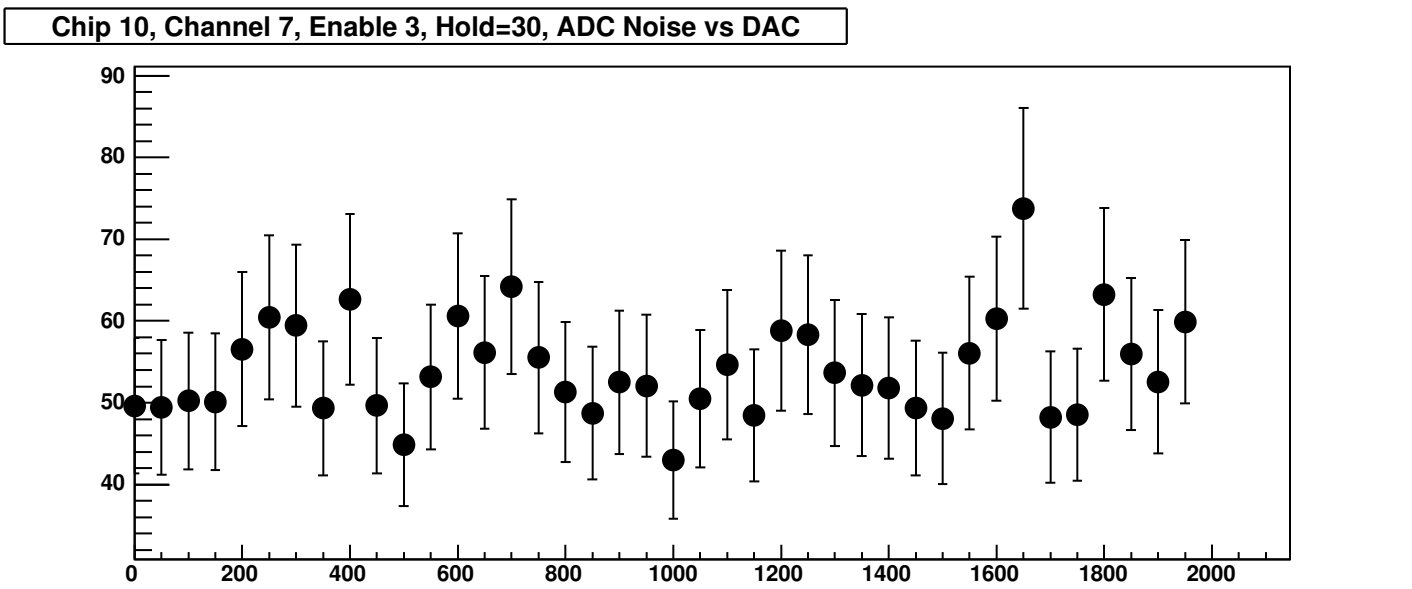

Chip 10, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

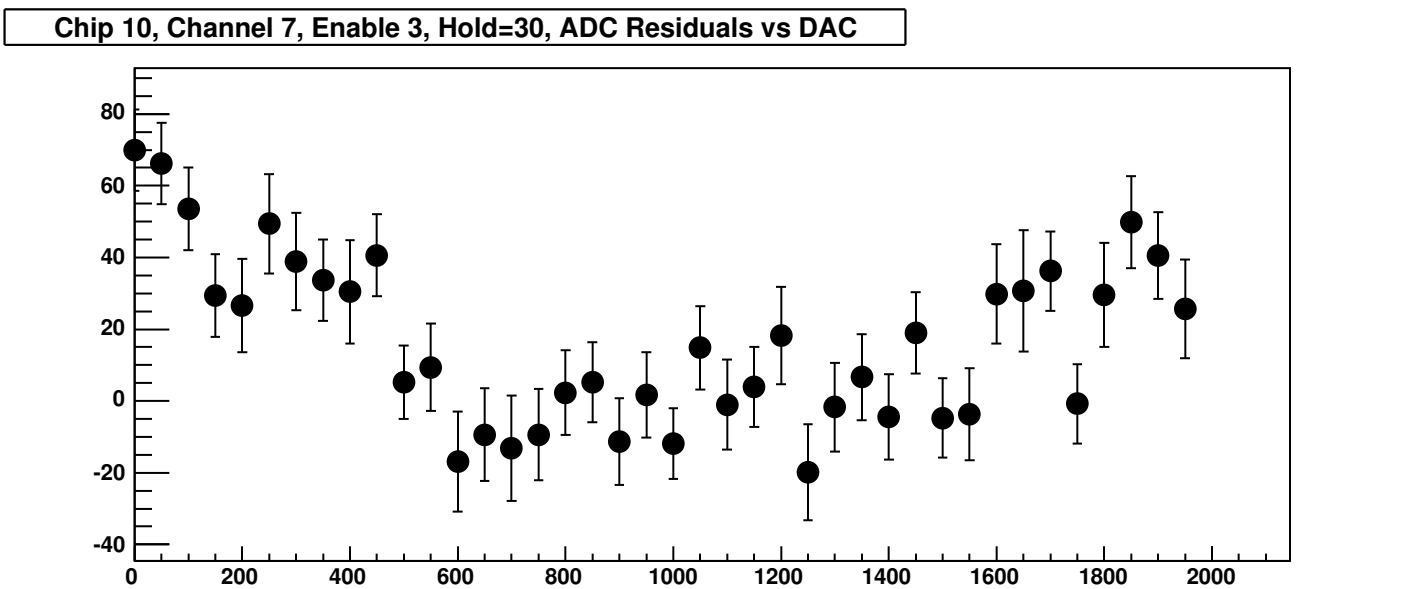

Chip 10, Channel 7, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 21.03 / 11
p0 -1041 ± 17.23
p1 0.1458 ± 0.01525
-700
-750
-800
-850
-900
-950
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

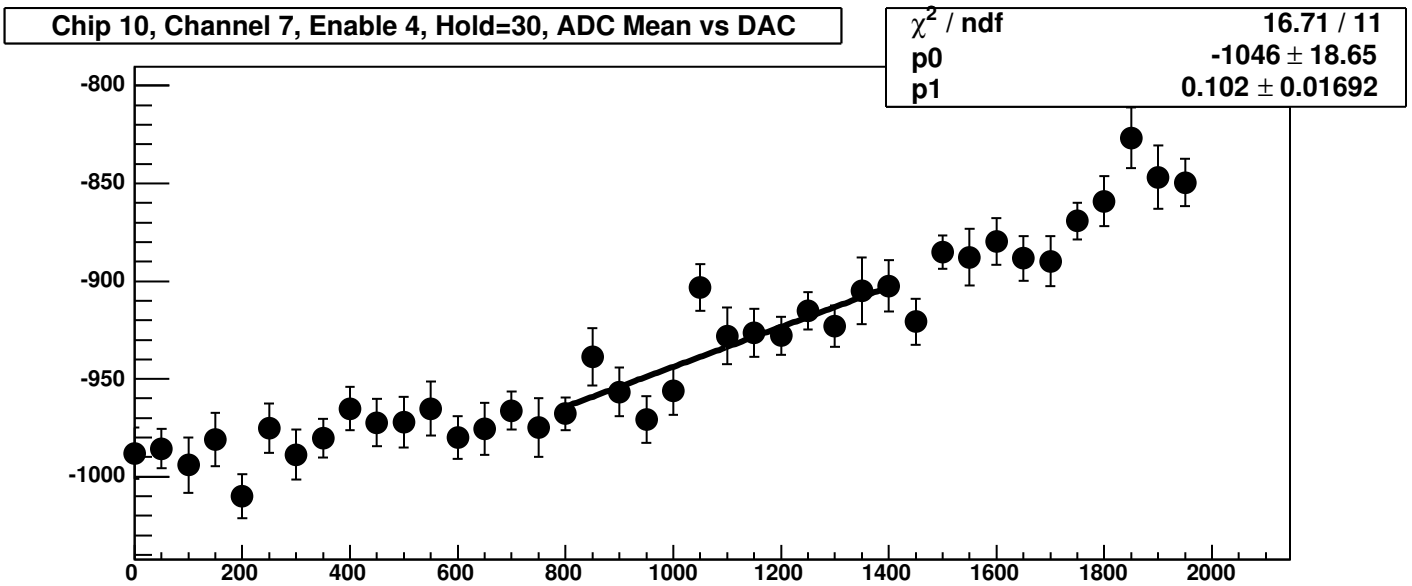

Chip 10, Channel 7, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

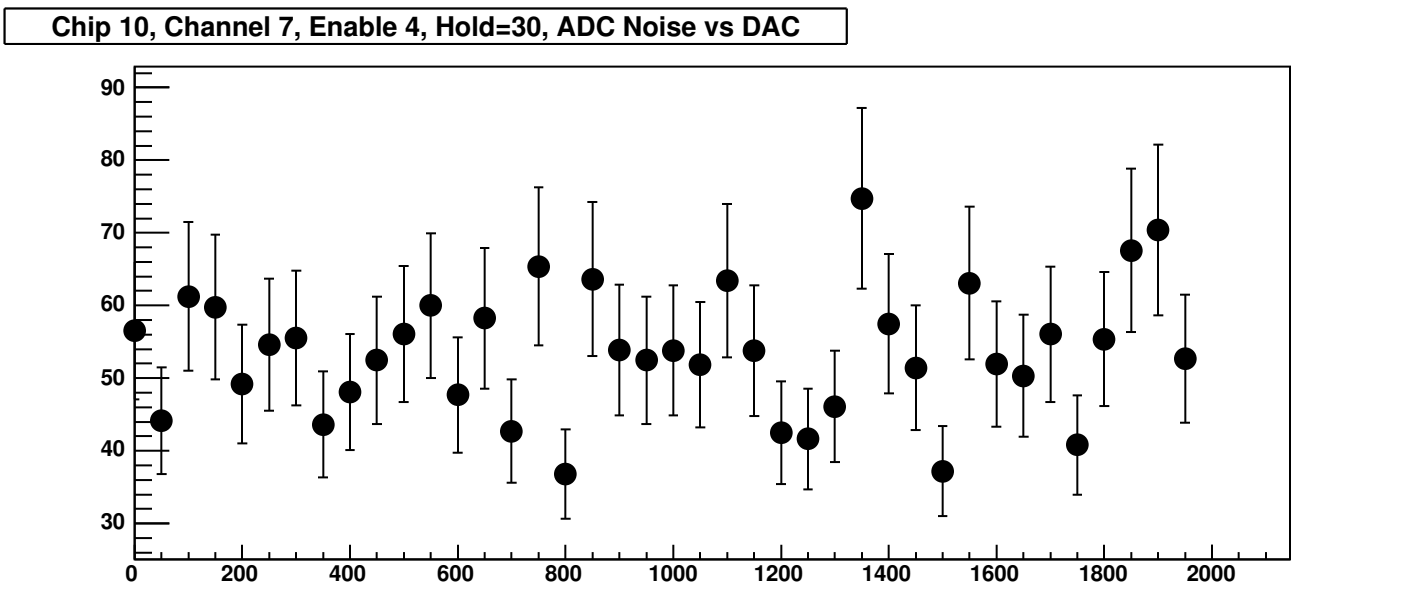

Chip 10, Channel 7, Enable 2, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

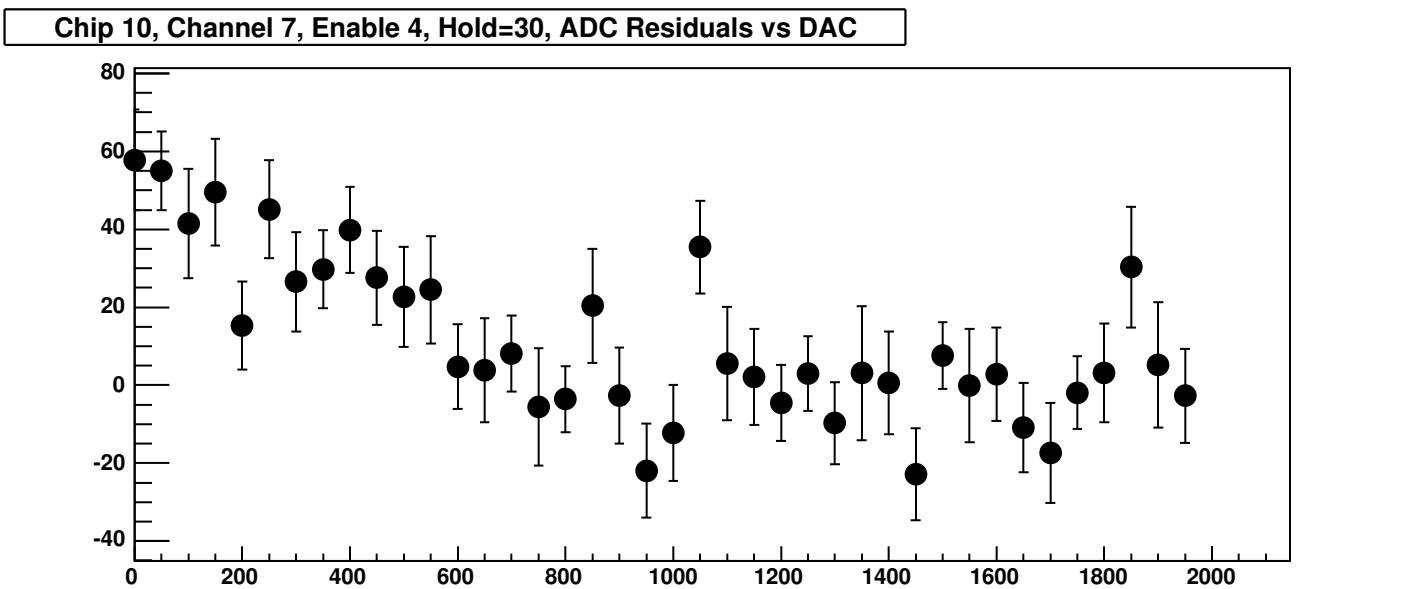

Chip 10, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 8.848 / 11
p0 -1032 ± 19.44
p1 0.08172 ± 0.01759
-800
-850
-900
-950
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

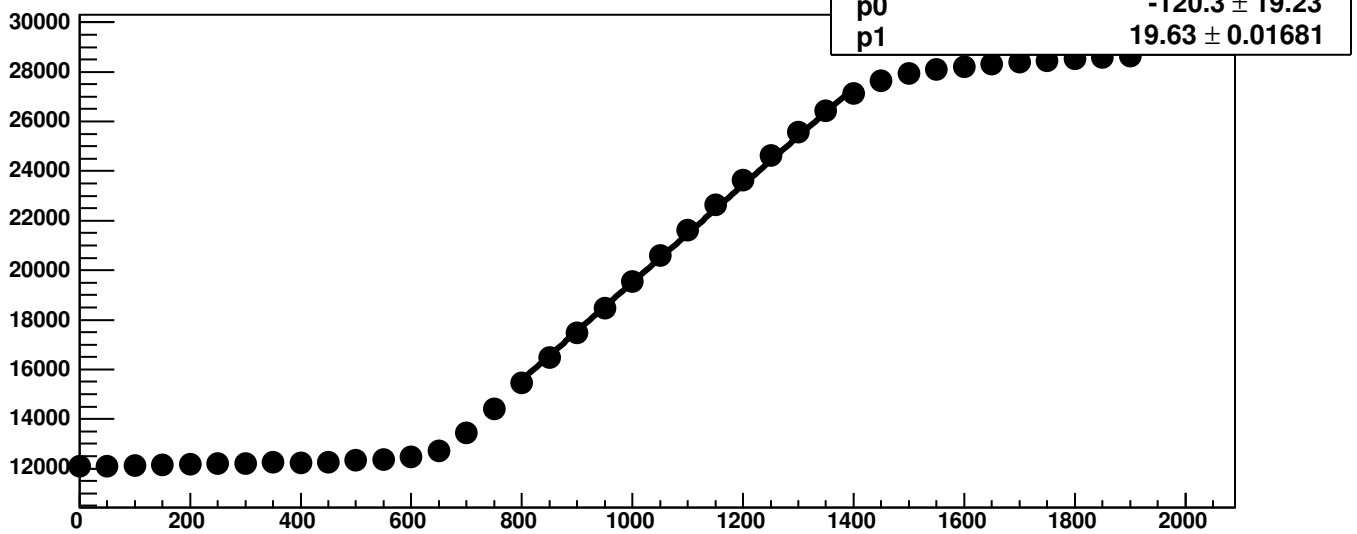

Chip 10, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 16.71 / 11
p0 -1046 ± 18.65
p1 0.102 ± 0.01692
-800
-850
-900
-950
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

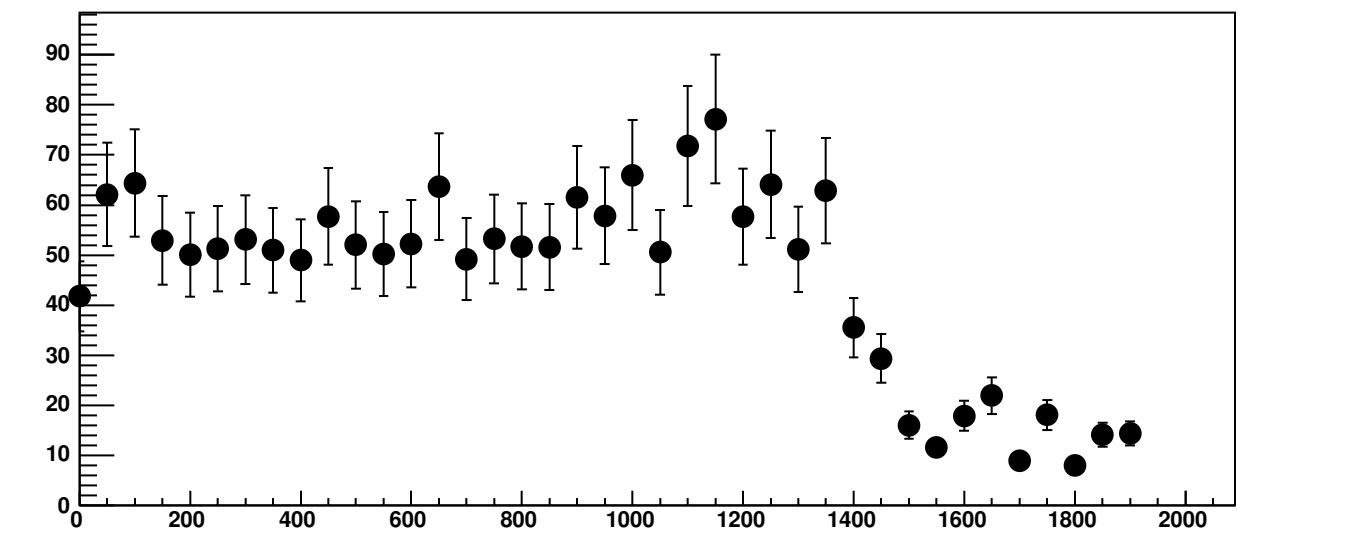

Chip 10, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

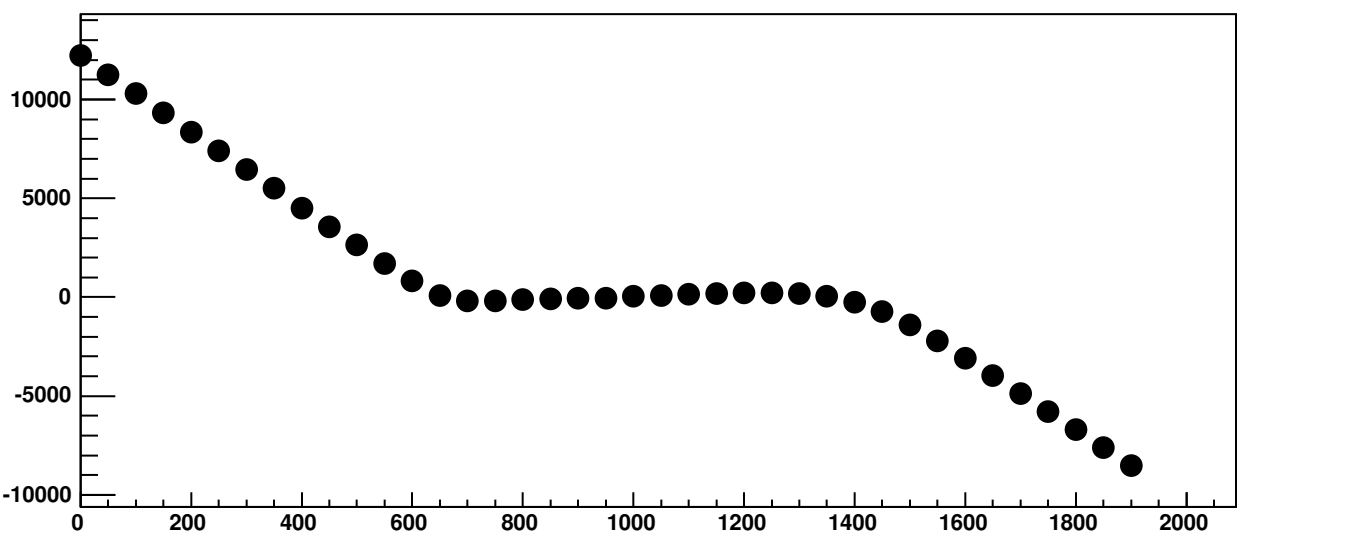
Chip 10, Channel 7, Enable 5!, Hold=30, ADC Mean vs DAC

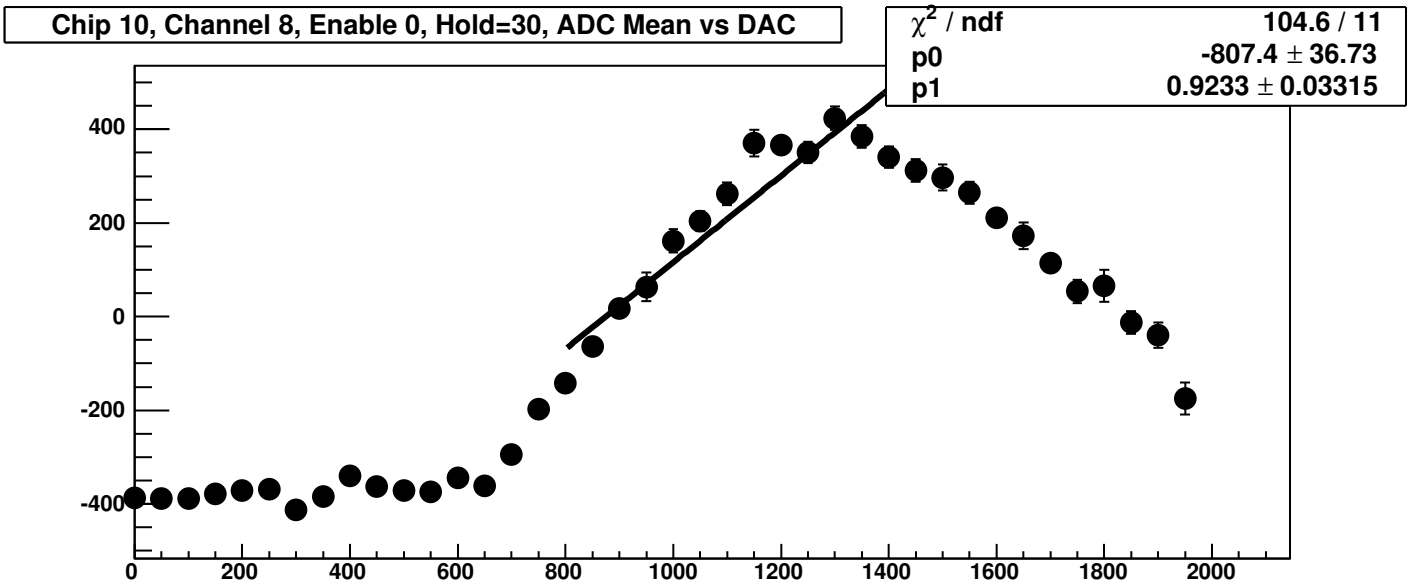


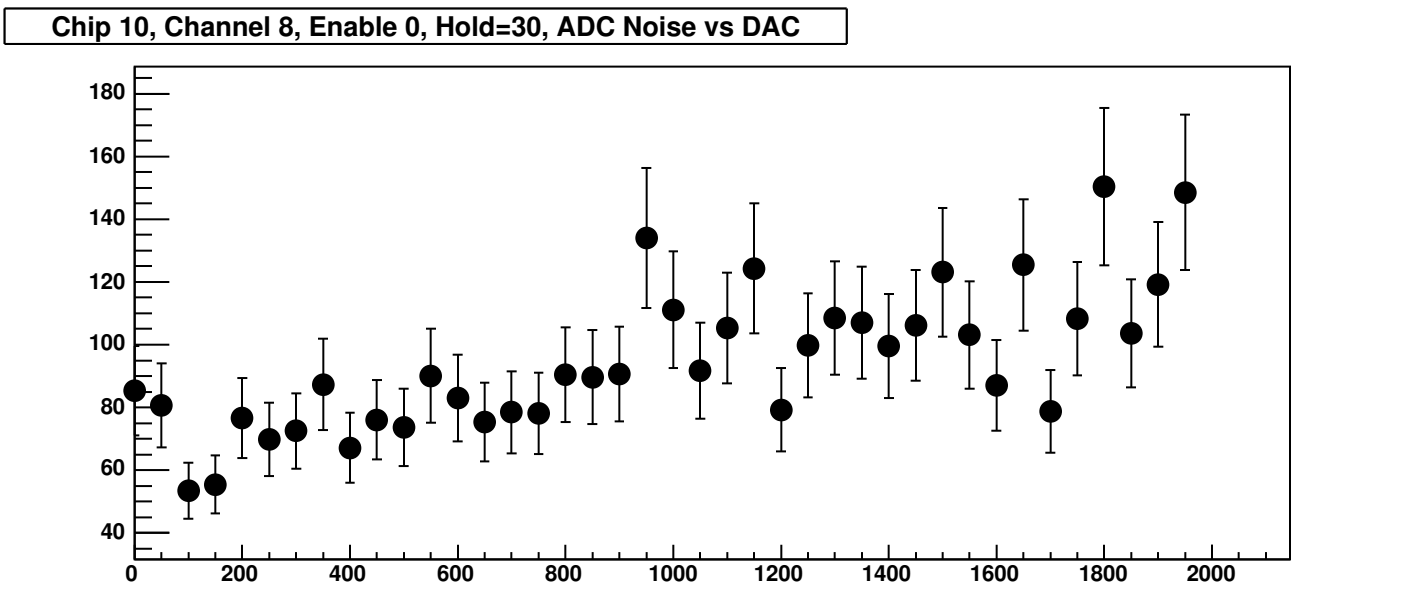
Chip 10, Channel 7, Enable 5!, Hold=30, ADC Noise vs DAC

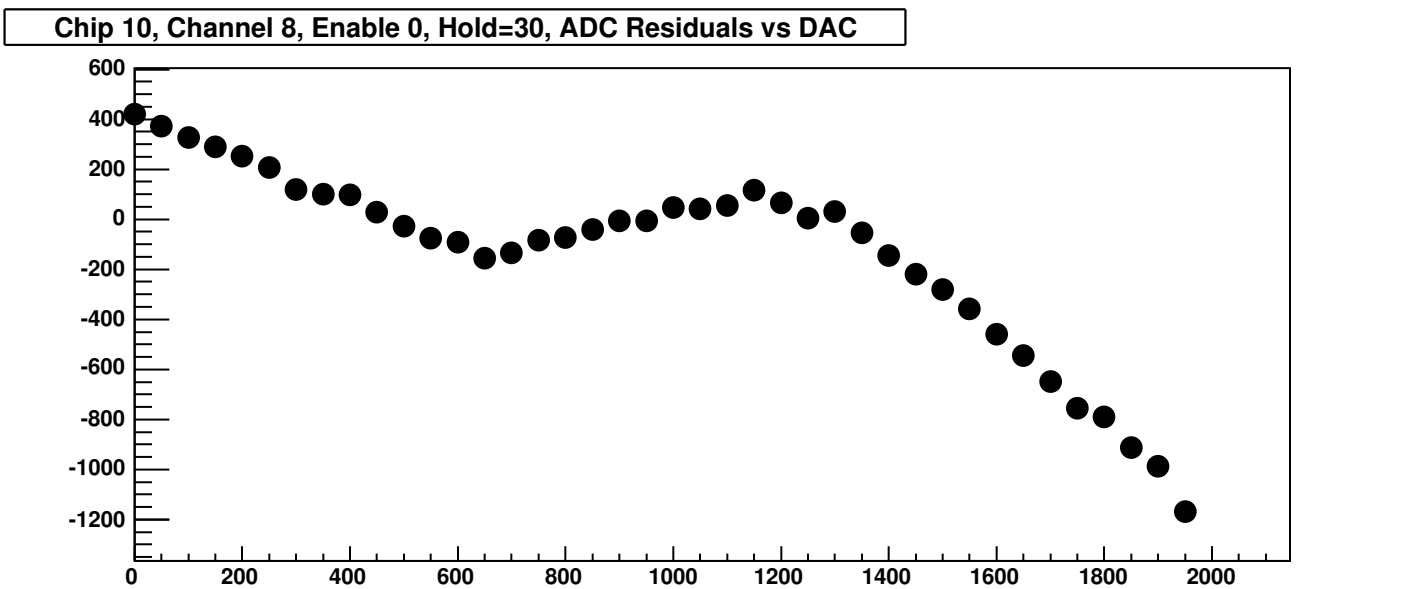


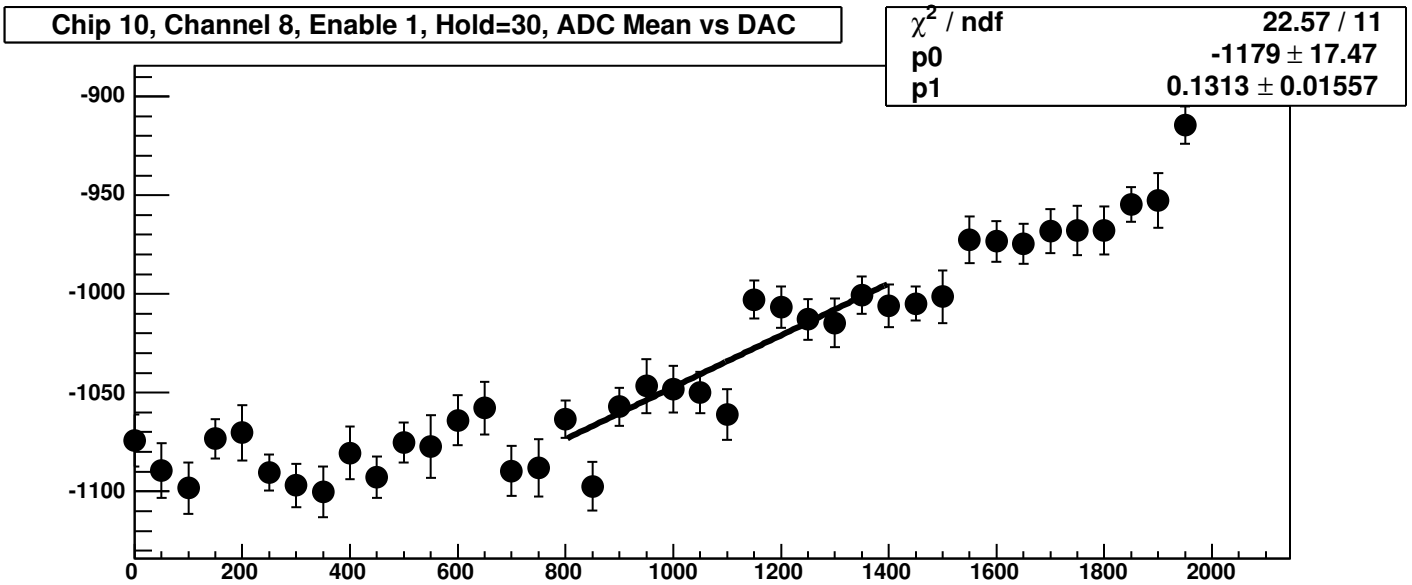
Chip 10, Channel 7, Enable 5!, Hold=30, ADC Residuals vs DAC

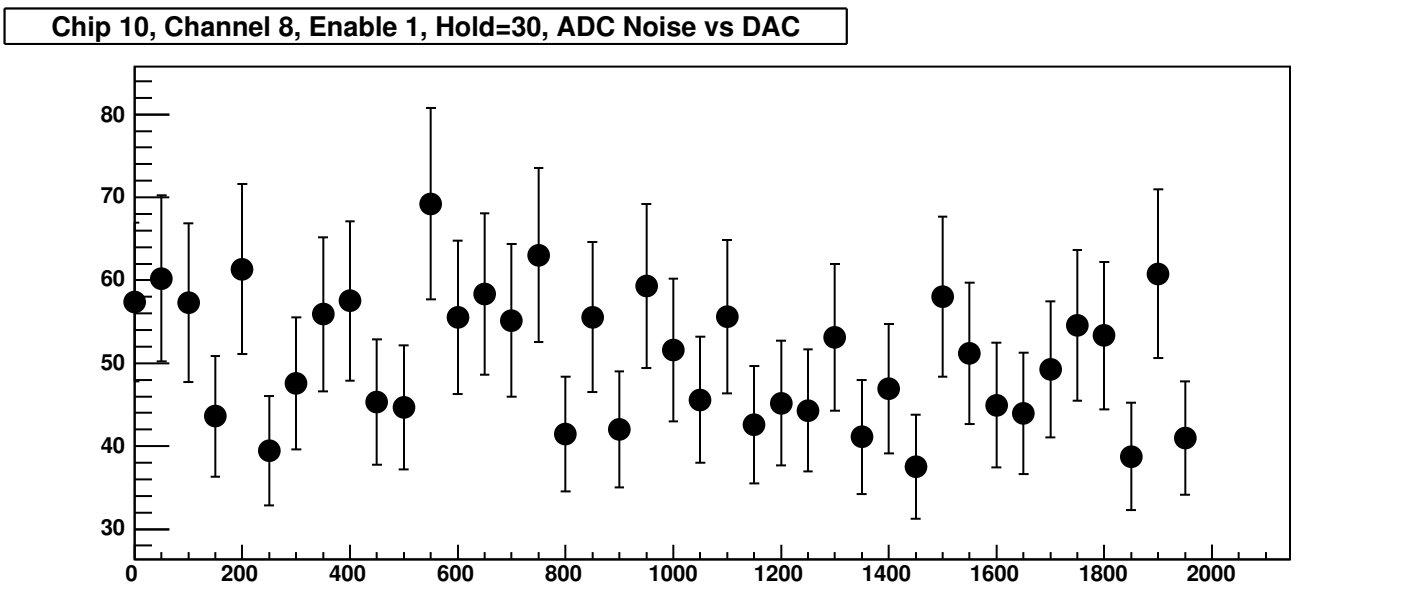


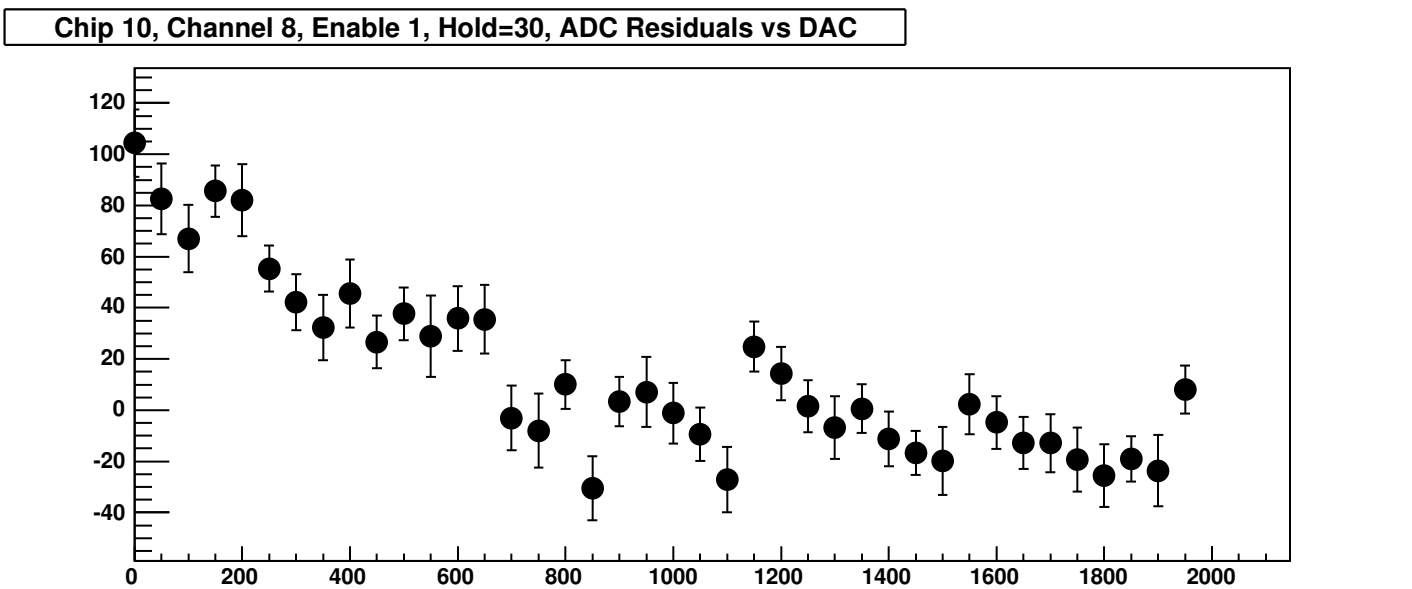

Chip 10, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 104.6 / 11
p0 -807.4 ± 36.73
p1 0.9233 ± 0.03315
400
200
0
-200
-400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

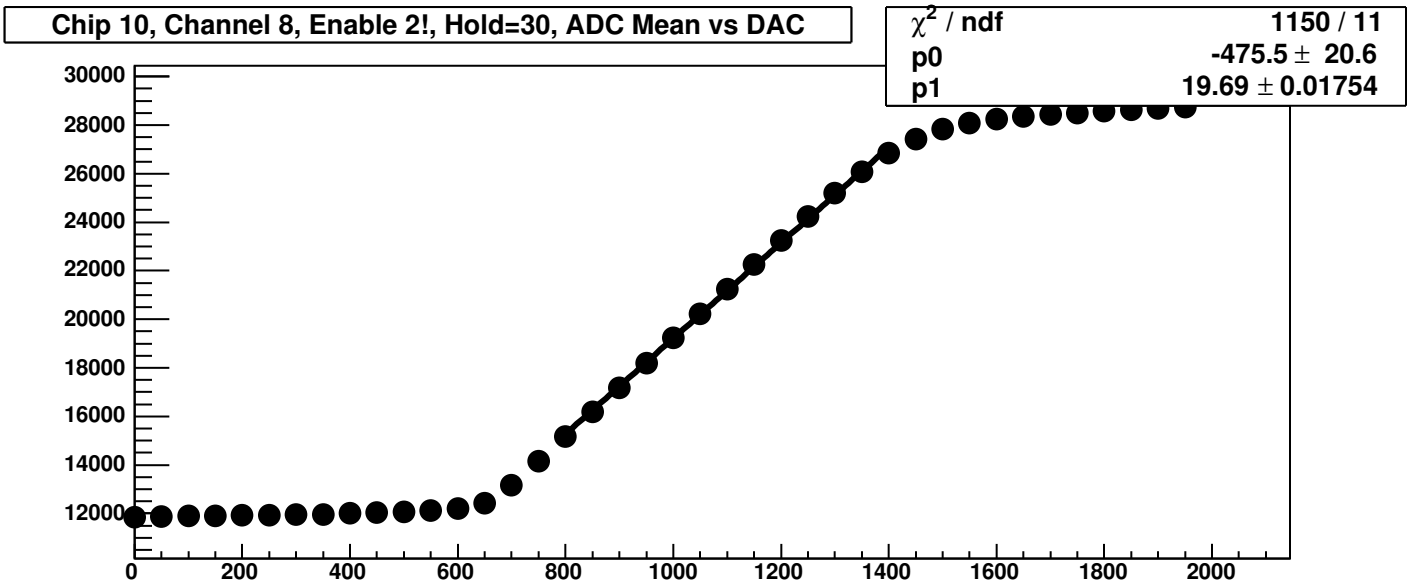

Chip 10, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

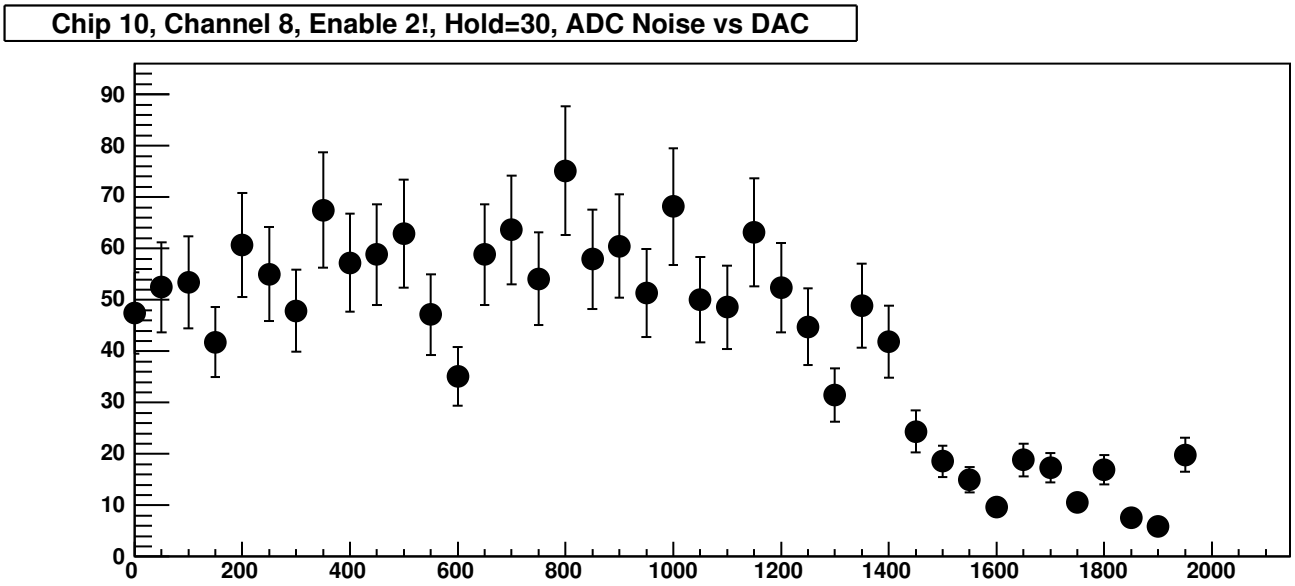

Chip 10, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC
600
400
200
0
-200
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

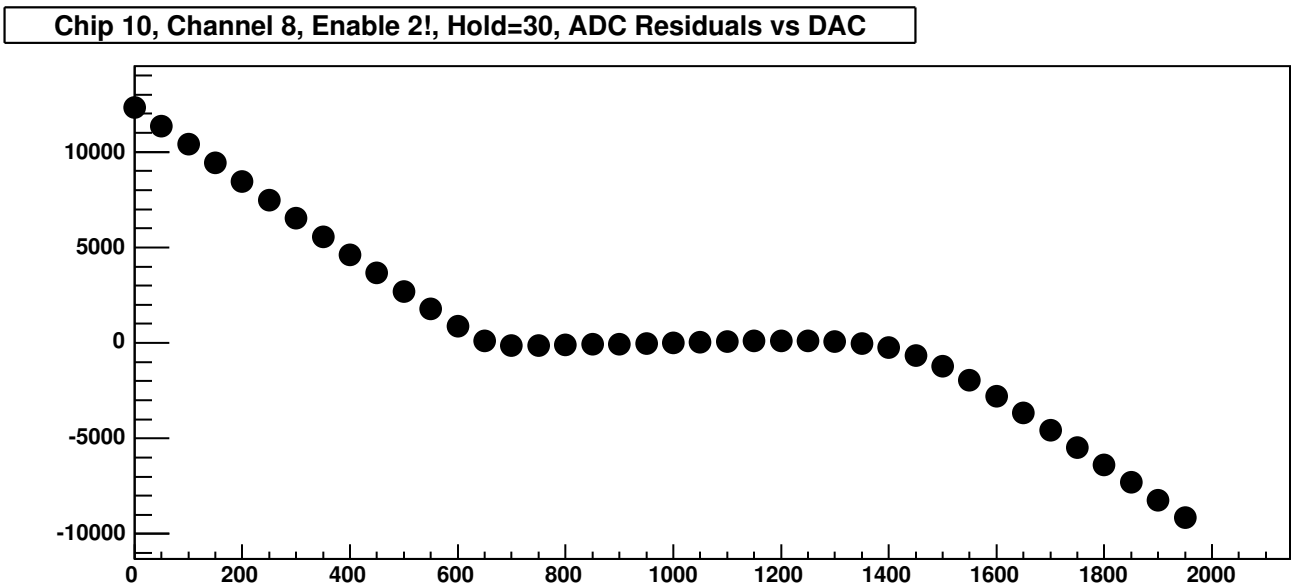

Chip 10, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 22.57 / 11
p0 -1179 ± 17.47
p1 0.1313 ± 0.01557
-900
-950
-1000
-1050
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

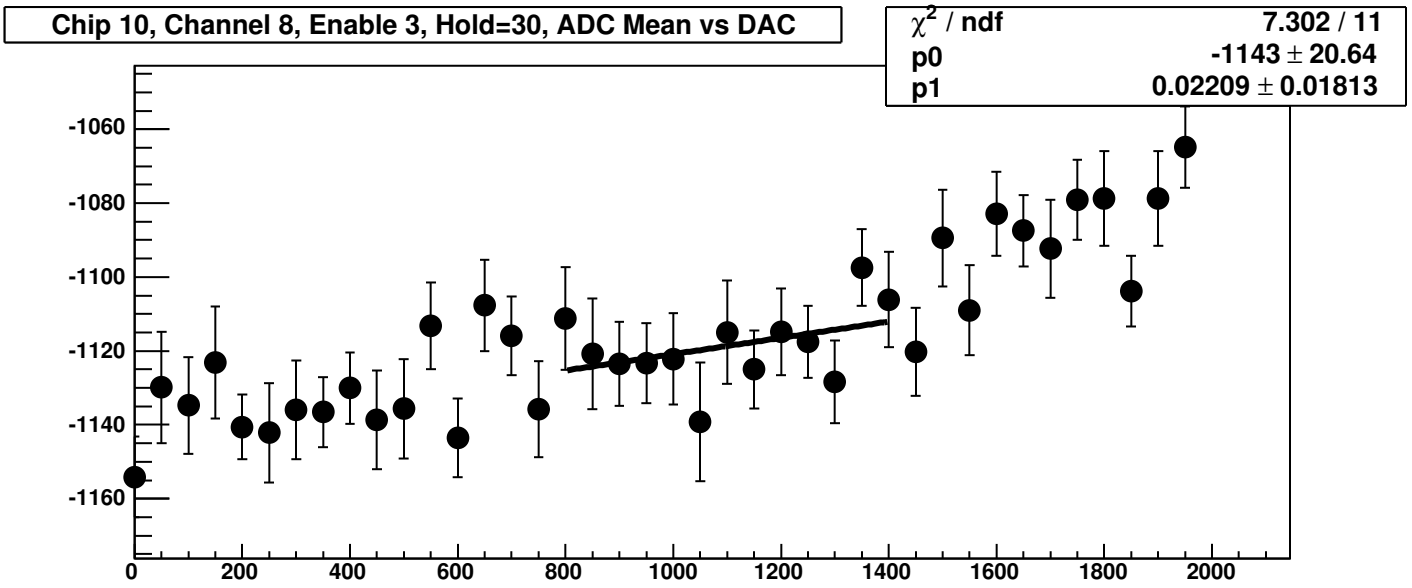

Chip 10, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

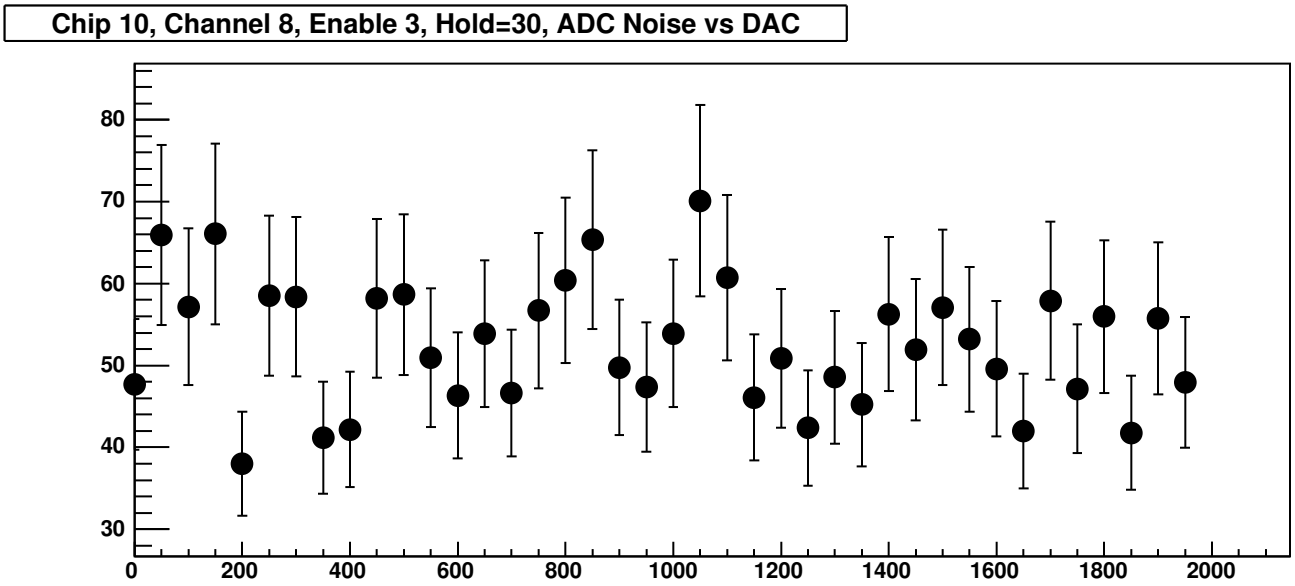

Chip 10, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

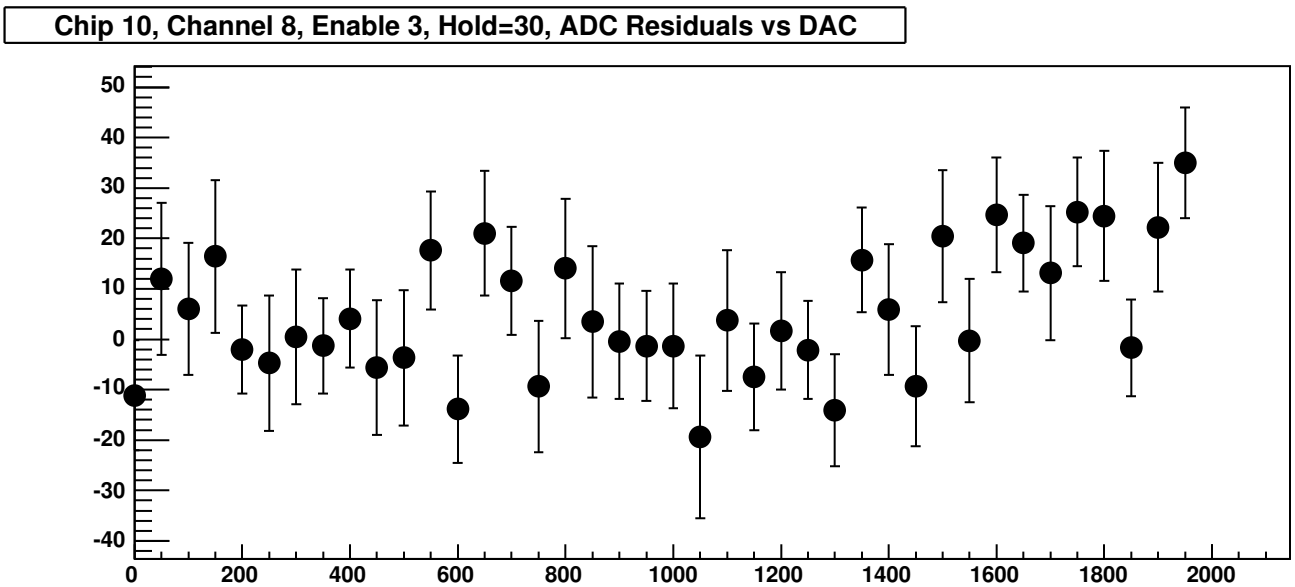

Chip 10, Channel 8, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 1150 / 11
p0 -475.5 ± 20.6
p1 19.69 ± 0.01754
30000
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

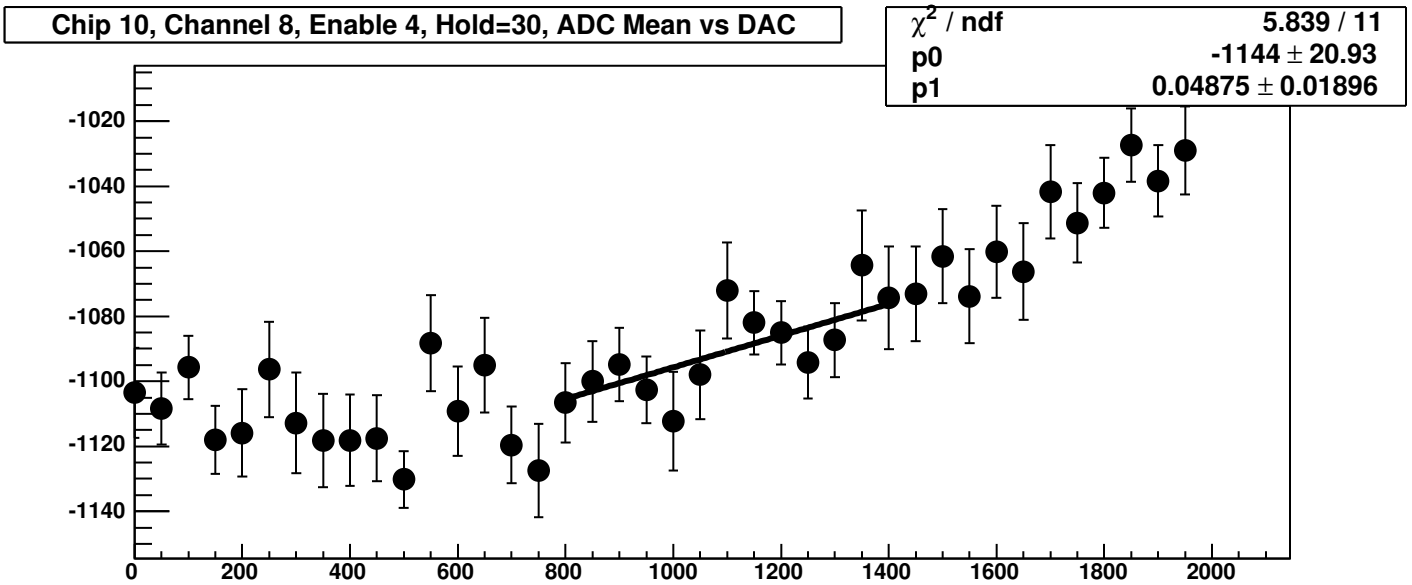

Chip 10, Channel 8, Enable 2!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

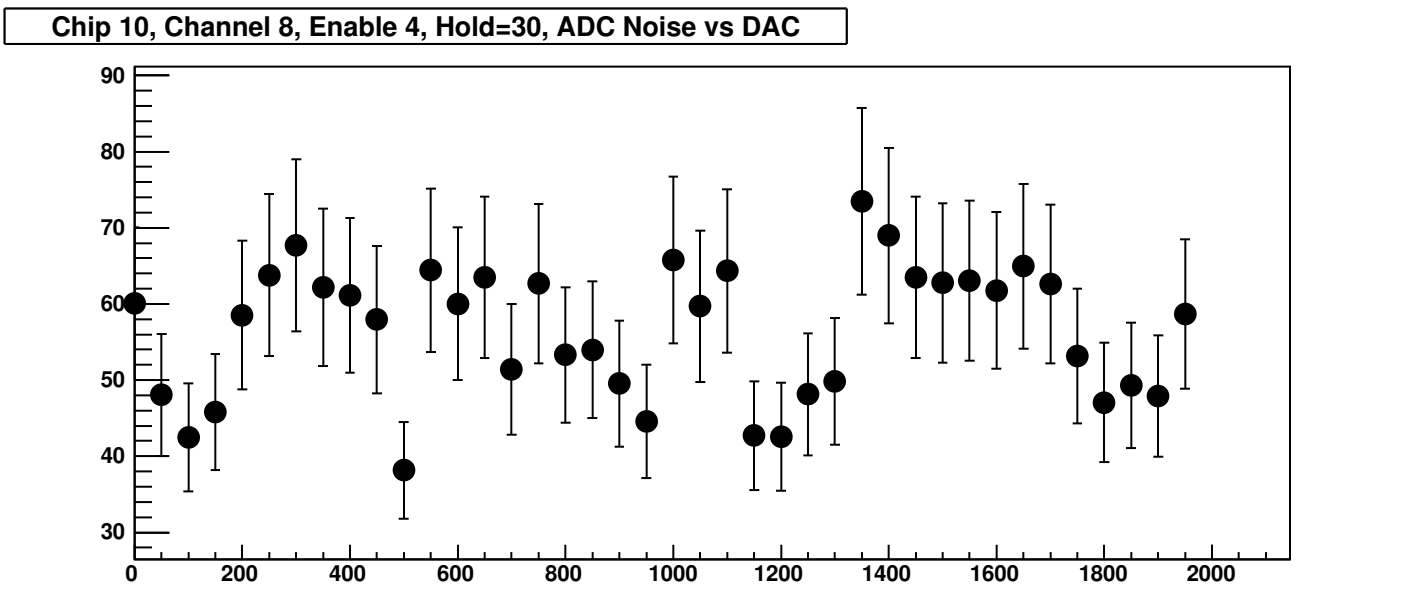

Chip 10, Channel 8, Enable 2!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

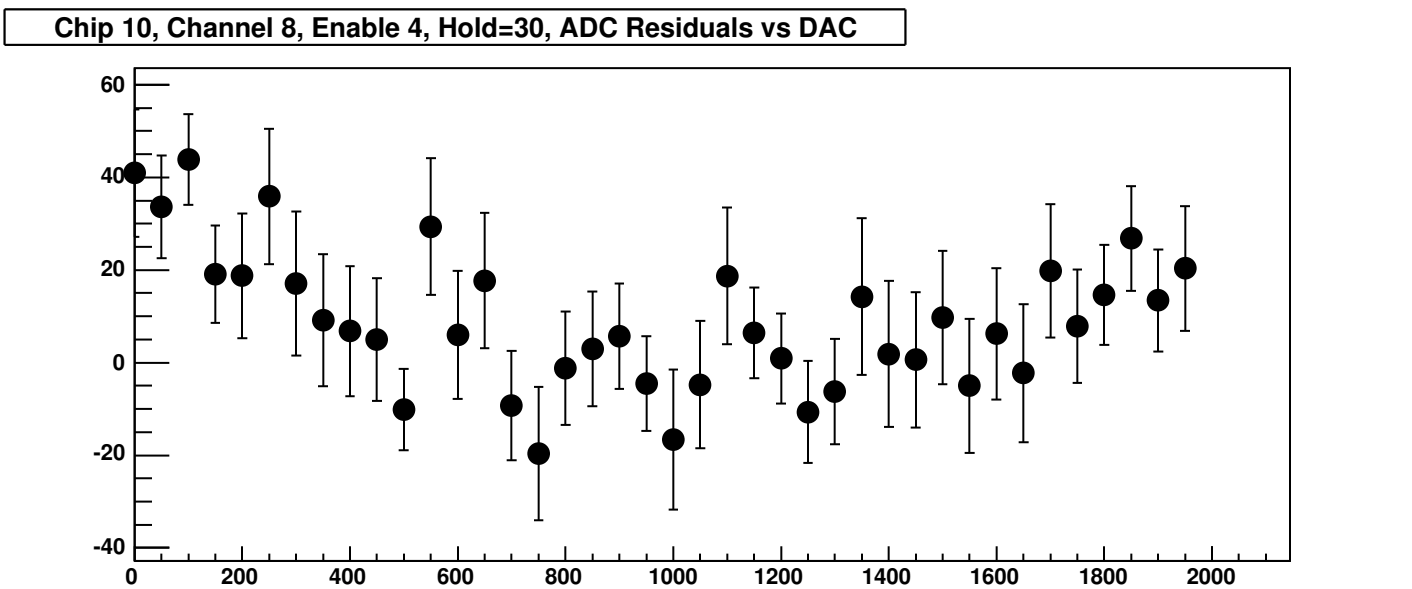

Chip 10, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 7.302 / 11
p0 -1143 ± 20.64
p1 0.02209 ± 0.01813
-1060
-1080
-1100
-1120
-1140
-1160
0
200
400
600
800
1000
1200
1400
1600
1800
2000

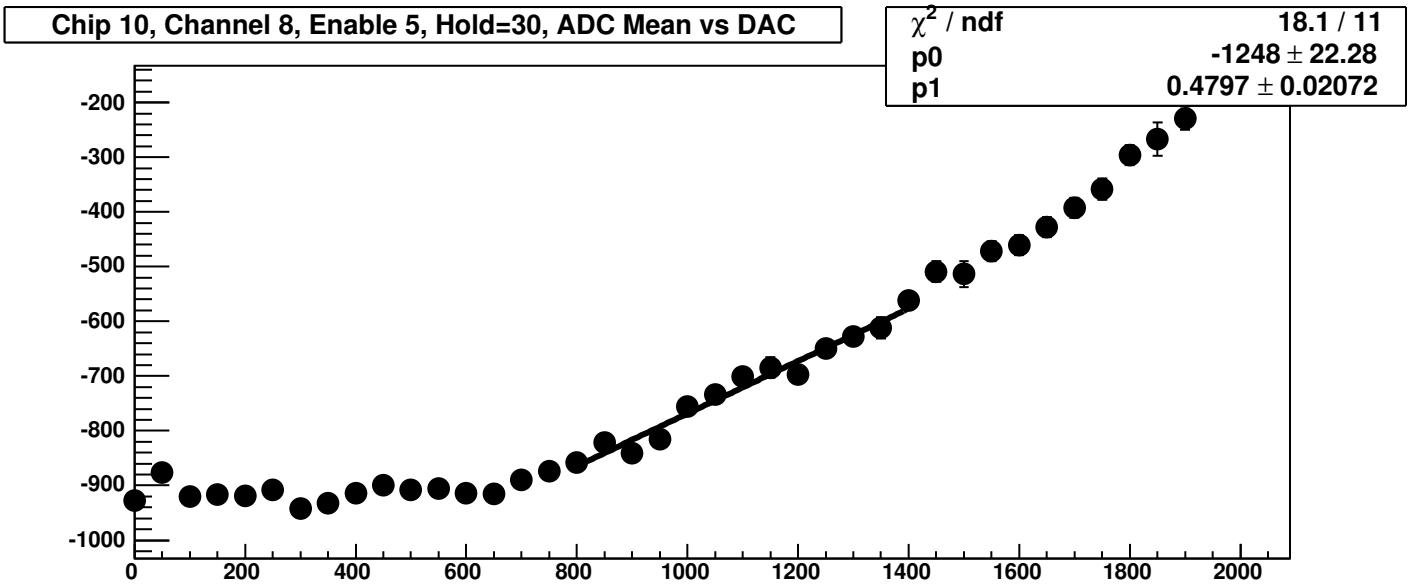

Chip 10, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

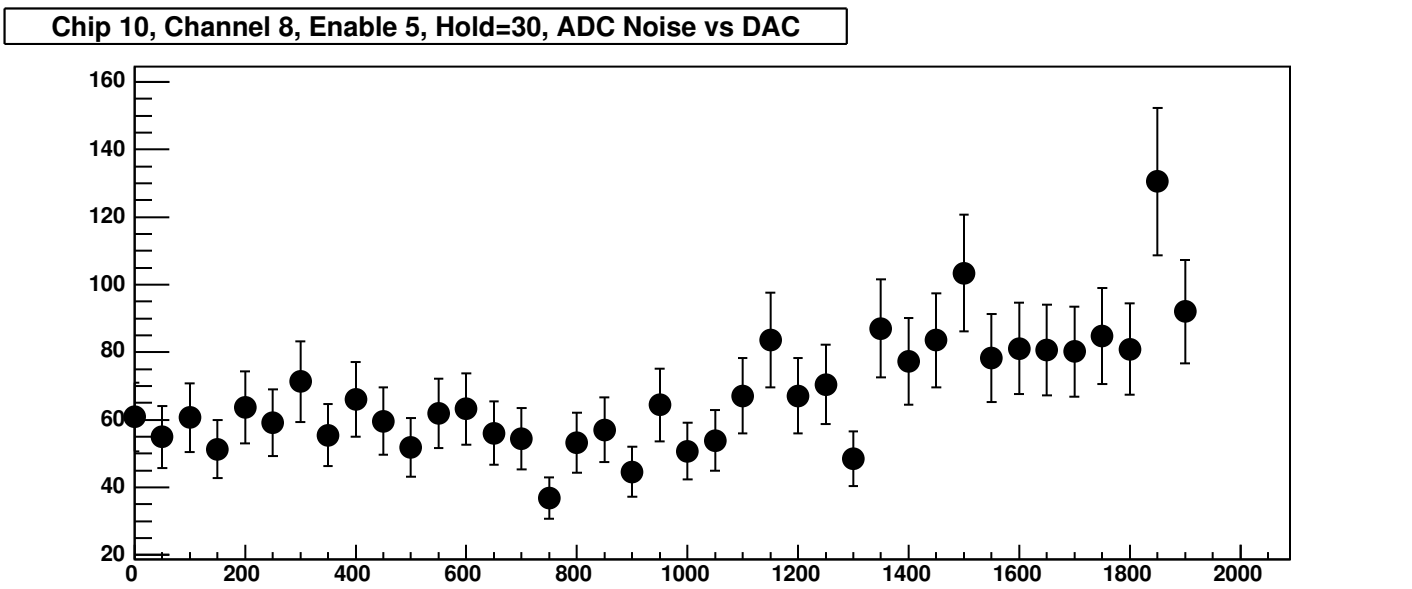

Chip 10, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
50
40
30
20
10
0
-10
-20
-30
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

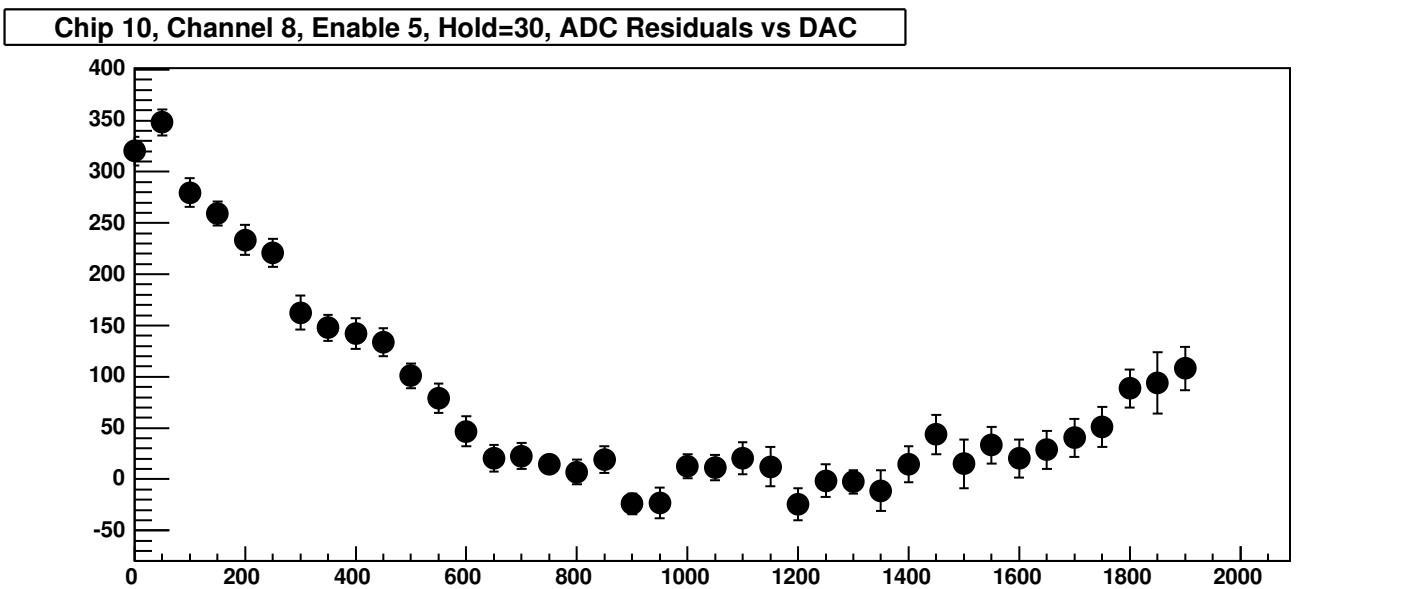

Chip 10, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5.839 / 11
p0 -1144 ± 20.93
p1 0.04875 ± 0.01896
-1020
-1040
-1060
-1080
-1100
-1120
-1140
0
200
400
600
800
1000
1200
1400
1600
1800
2000

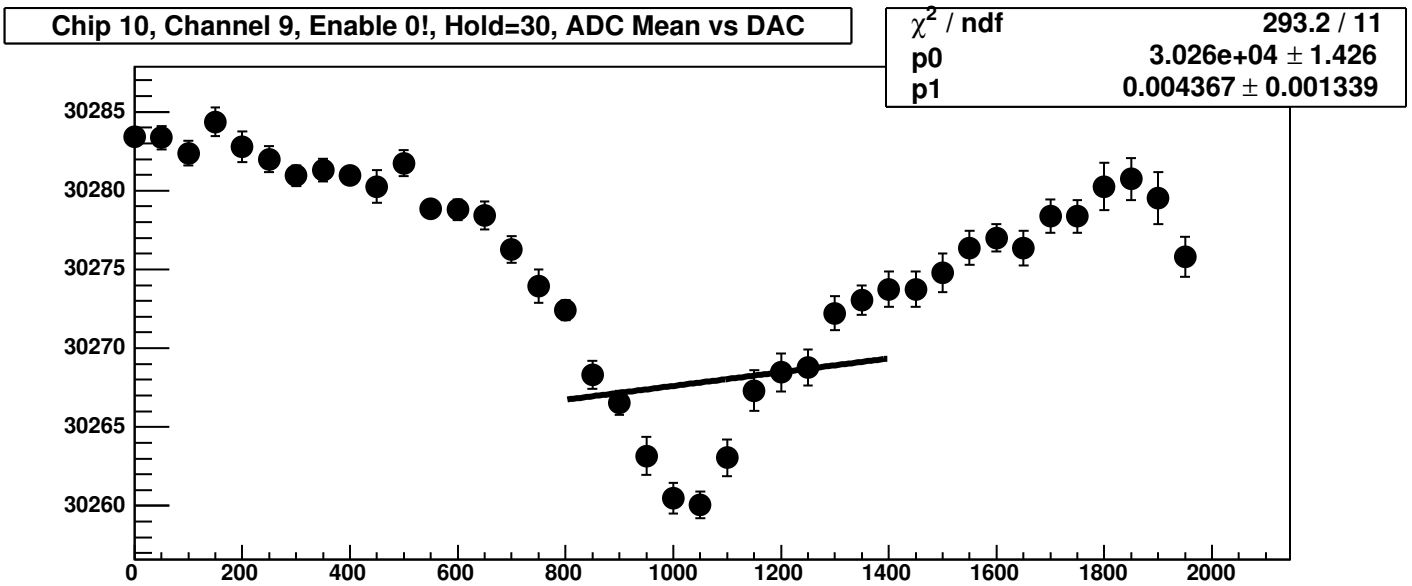

Chip 10, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

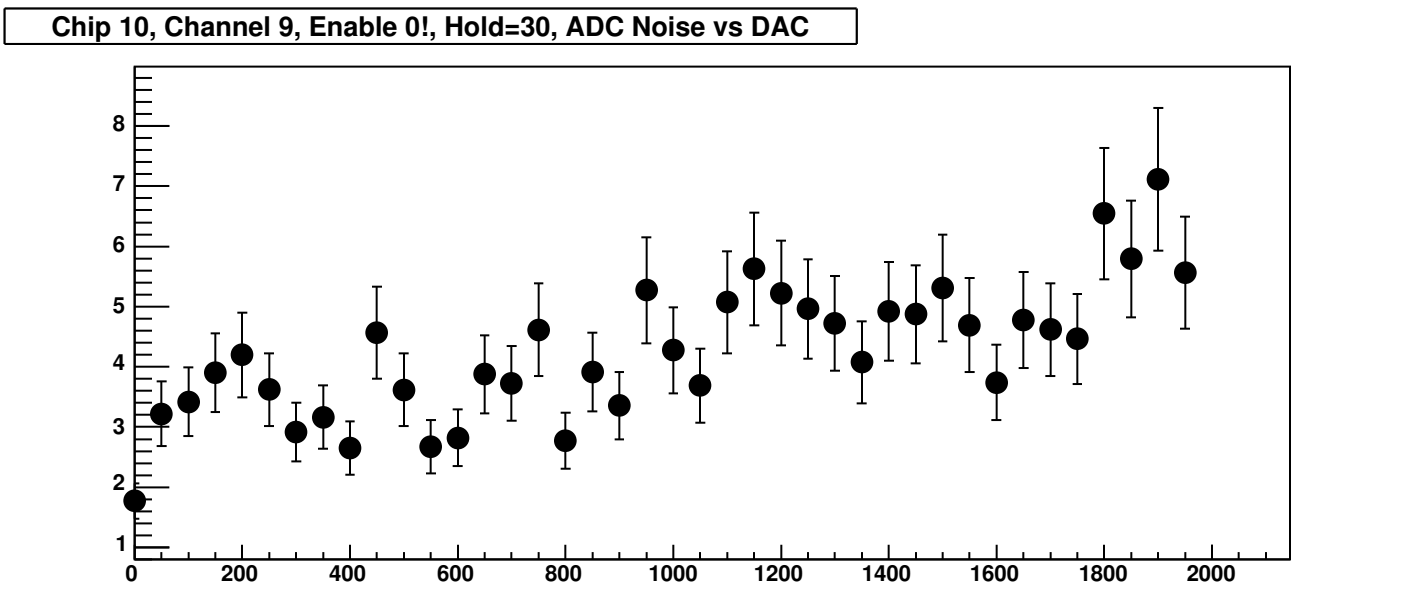

Chip 10, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

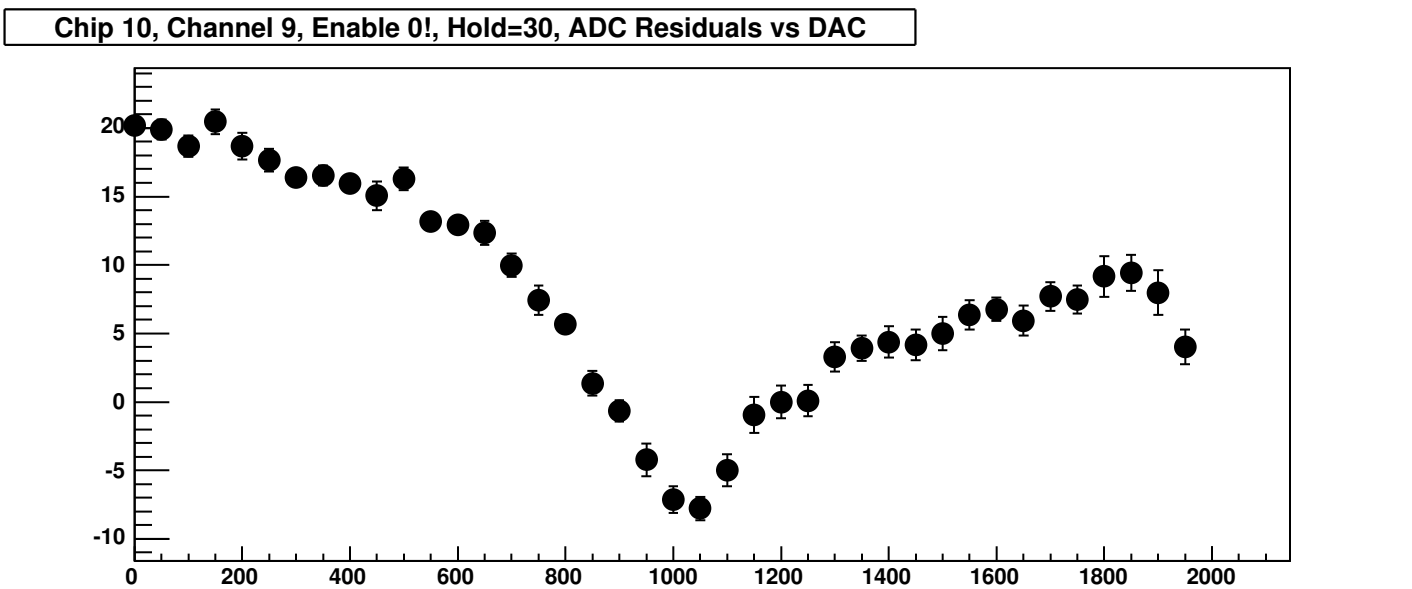

Chip 10, Channel 8, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 18.1 / 11
p0 -1248 ± 22.28
p1 0.4797 ± 0.02072
-200
-300
-400
-500
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

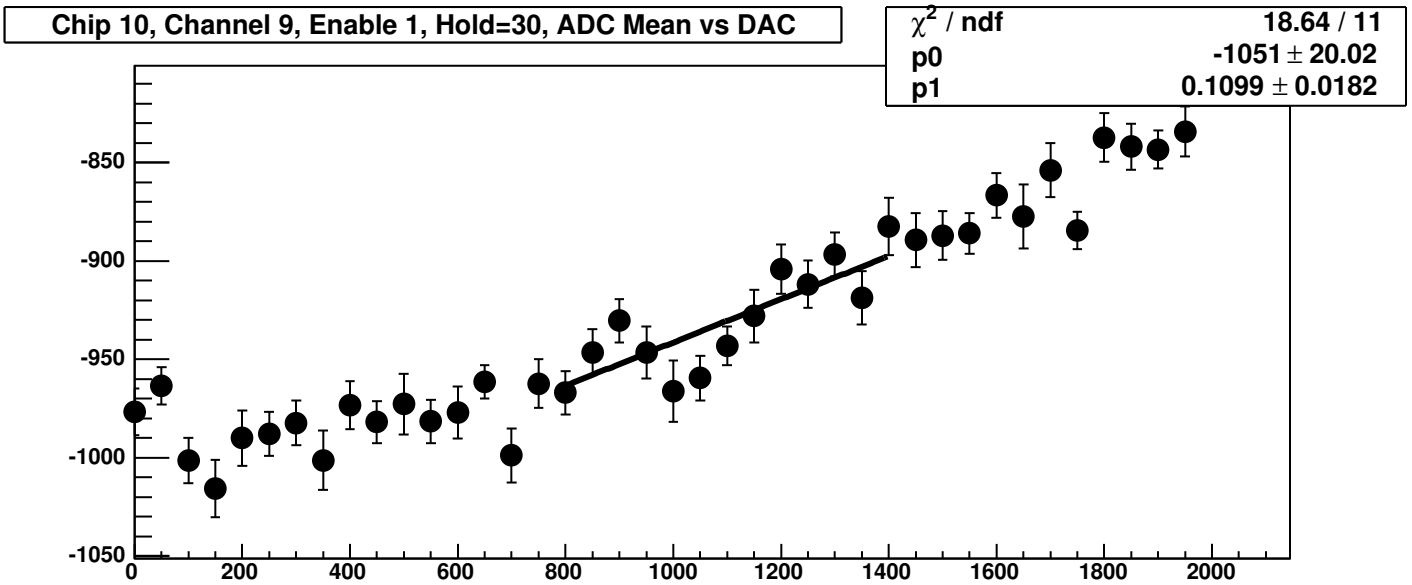

Chip 10, Channel 8, Enable 5, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

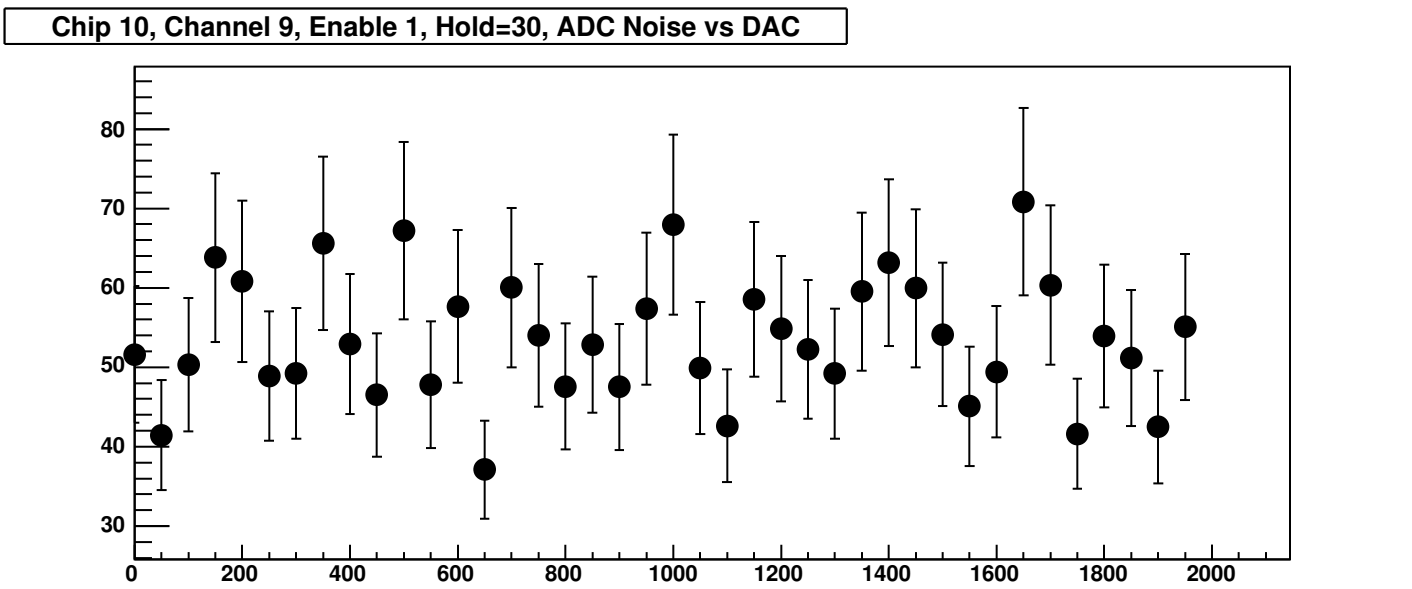

Chip 10, Channel 8, Enable 5, Hold=30, ADC Residuals vs DAC
400
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

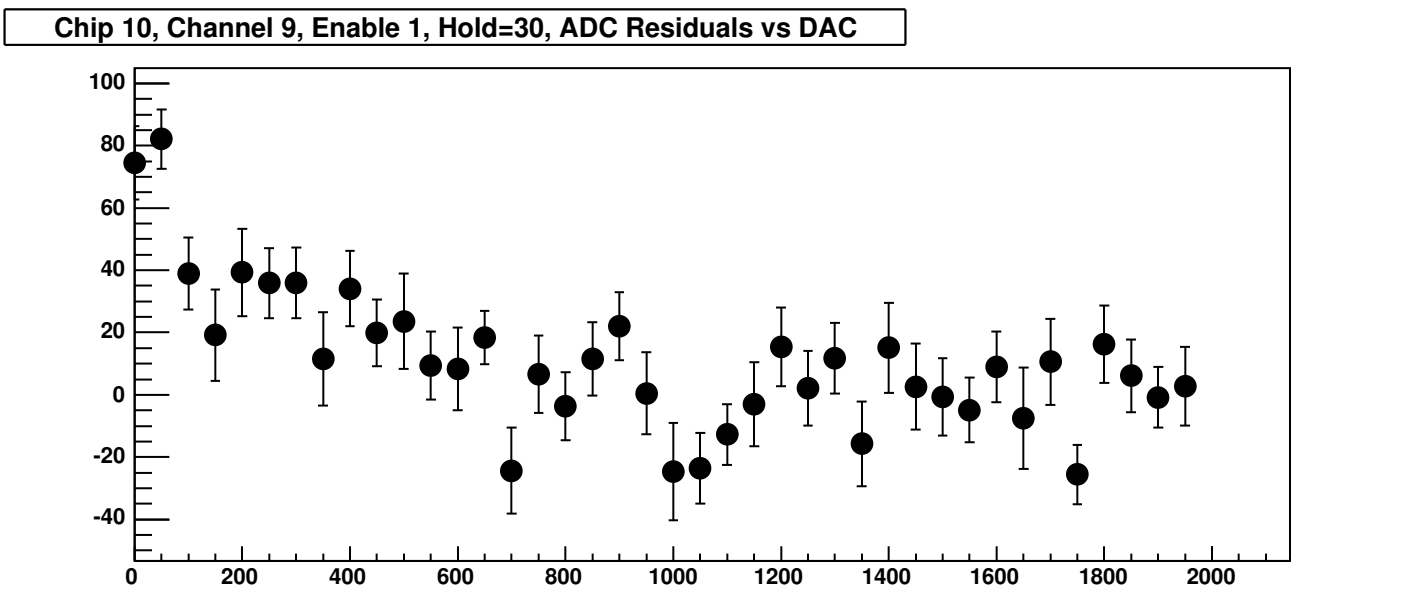

Chip 10, Channel 9, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 293.2 / 11
p0 3.026e+04 ± 1.426
p1 0.004367 ± 0.001339
30285
30280
30275
30270
30265
30260
0
200
400
600
800
1000
1200
1400
1600
1800
2000

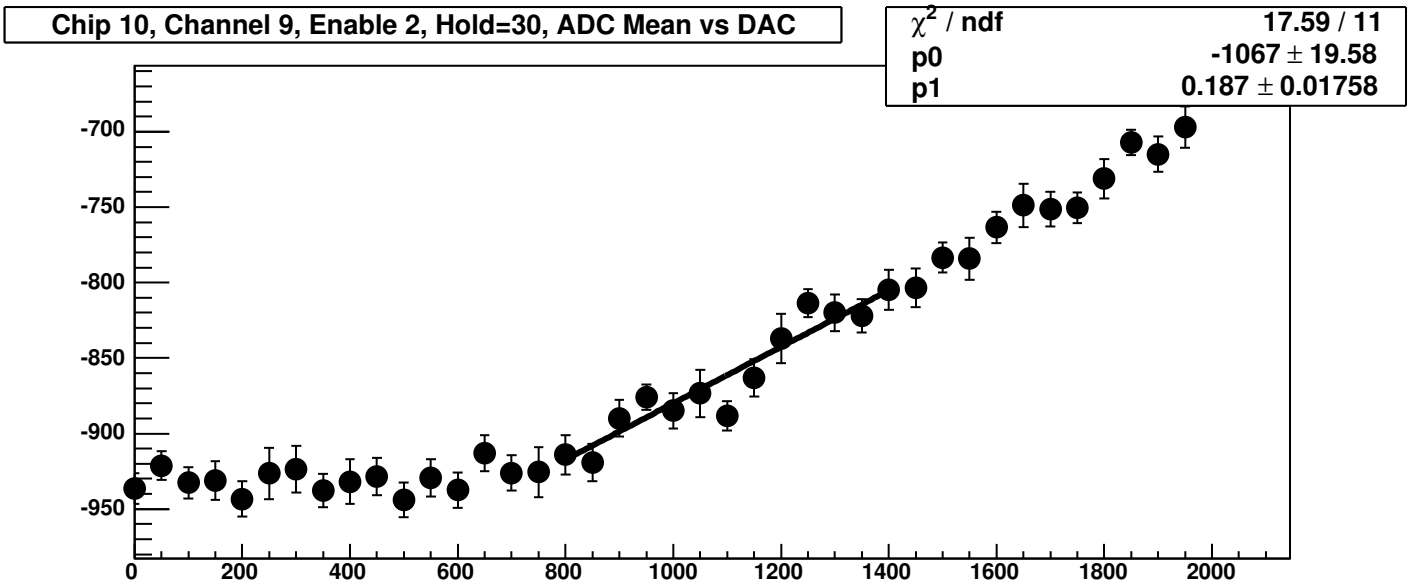

Chip 10, Channel 9, Enable 0!, Hold=30, ADC Noise vs DAC
8
7
6
5
4
3
2
1
0
200
400
600
800
1000
1200
1400
1600
1800
2000

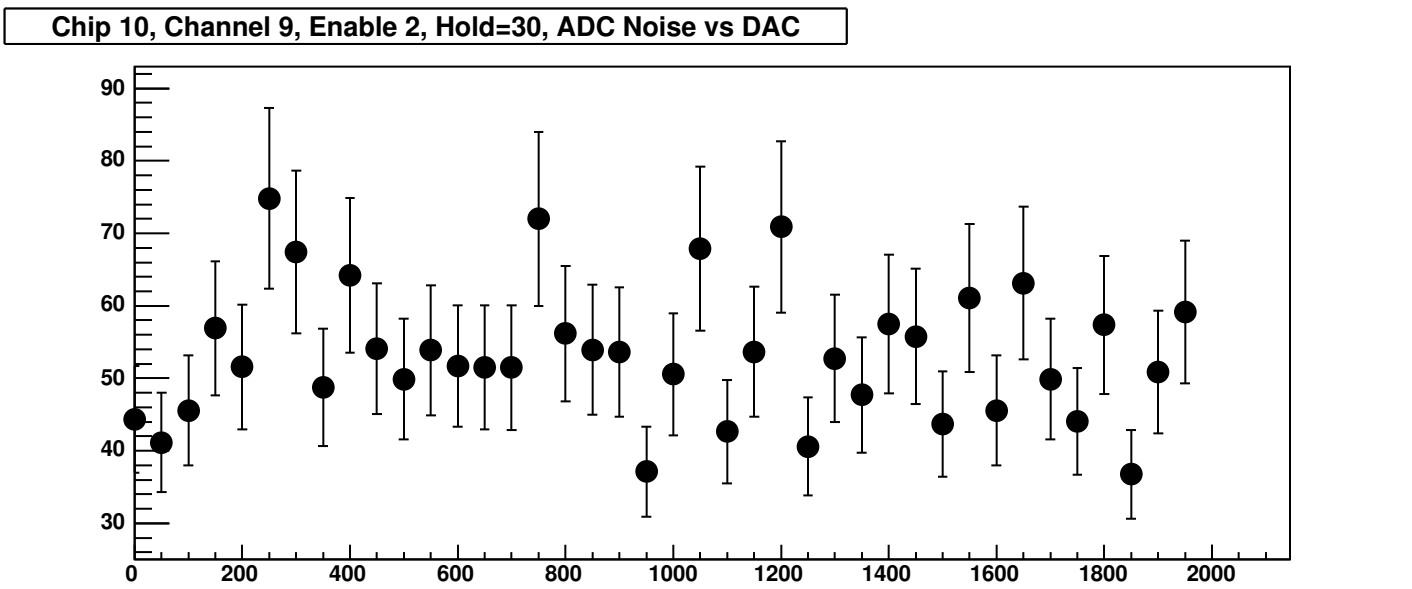

Chip 10, Channel 9, Enable 0!, Hold=30, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

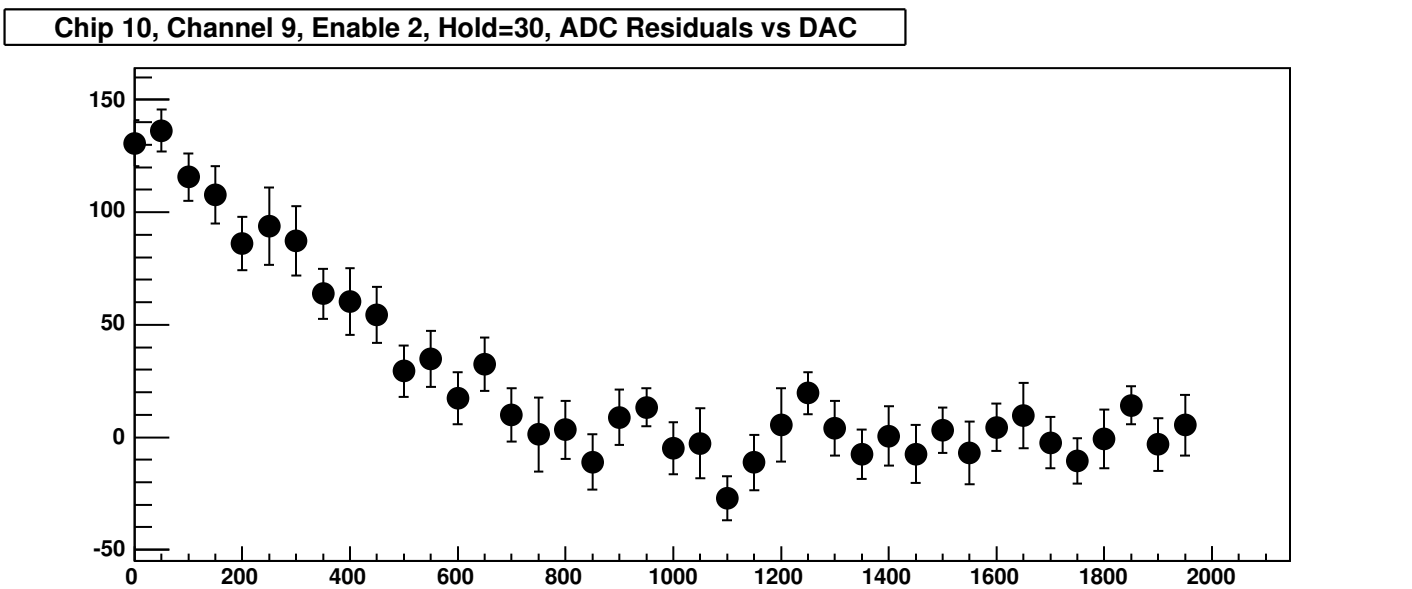

Chip 10, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 18.64 / 11
p0 -1051 ± 20.02
p1 0.1099 ± 0.0182
-850
-900
-950
-1000
-1050
0
200
400
600
800
1000
1200
1400
1600
1800
2000

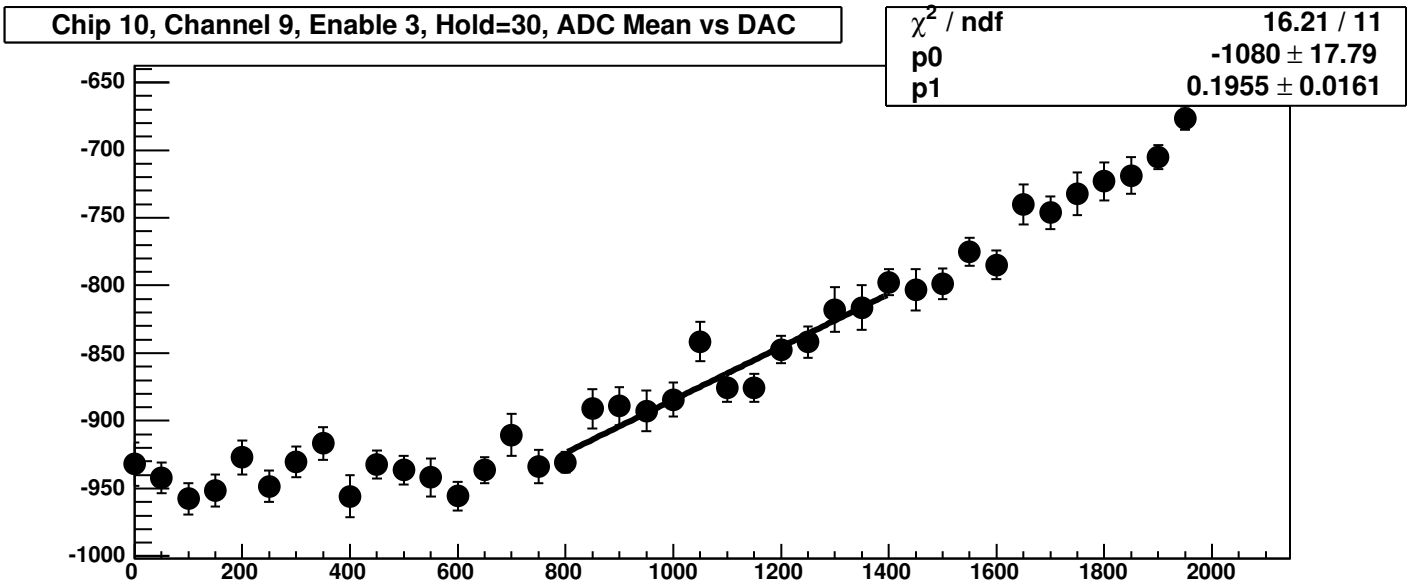

Chip 10, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

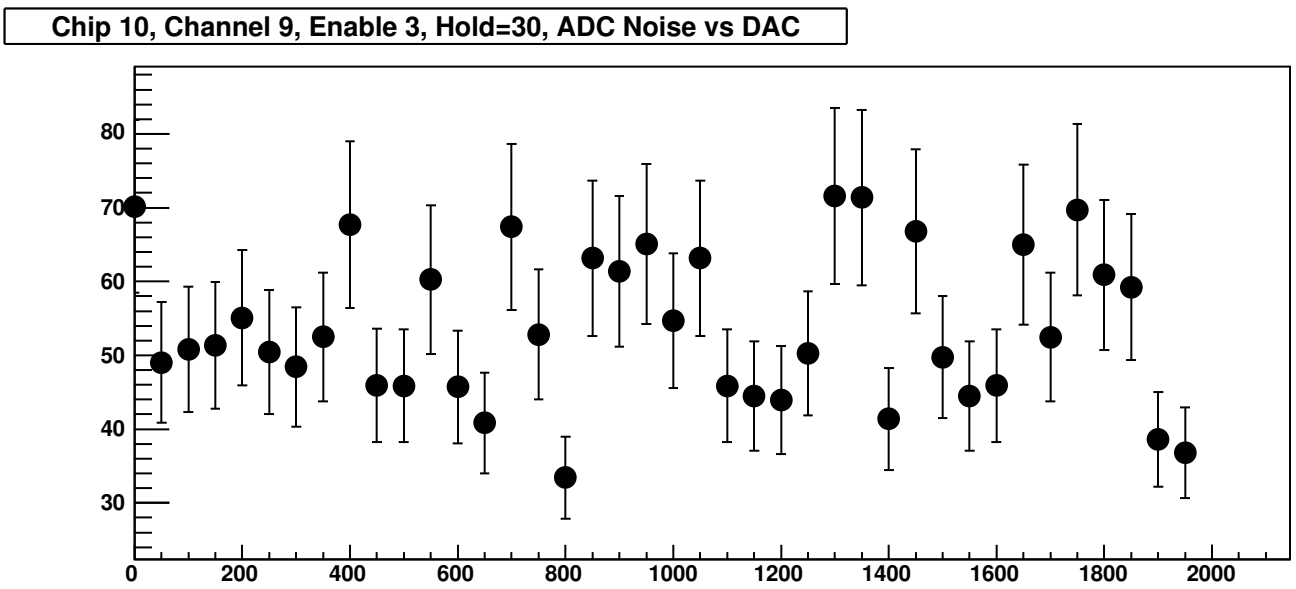

Chip 10, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

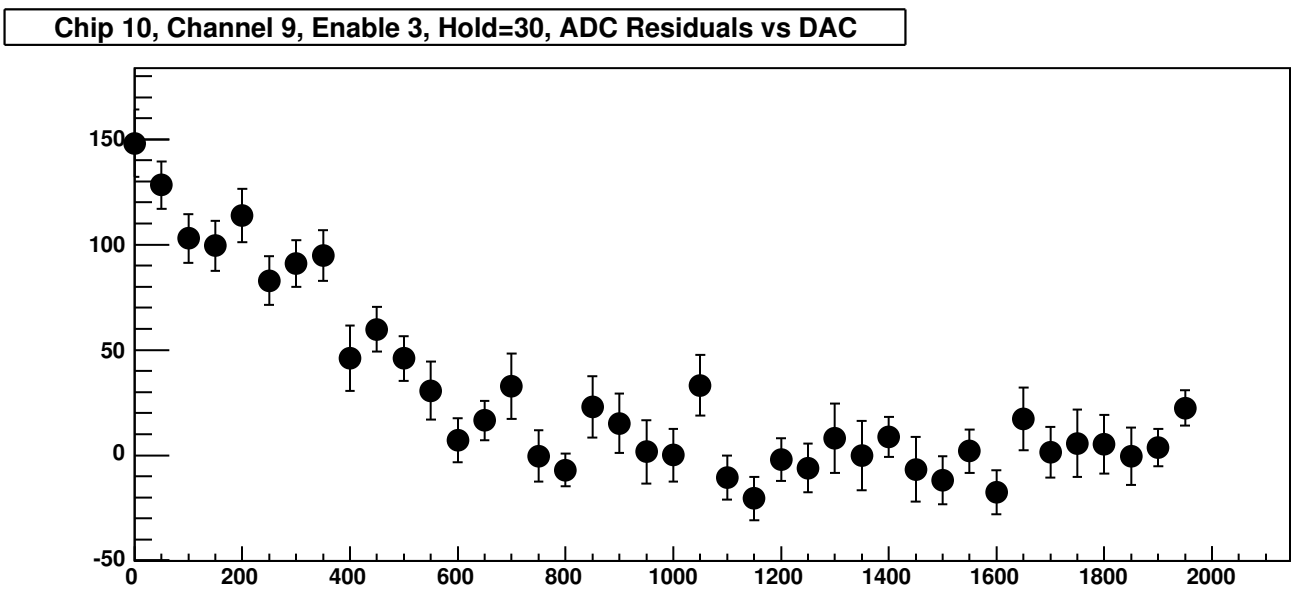

Chip 10, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 17.59 / 11
p0 -1067 ± 19.58
p1 0.187 ± 0.01758
-700
-750
-800
-850
-900
-950
0
200
400
600
800
1000
1200
1400
1600
1800
2000

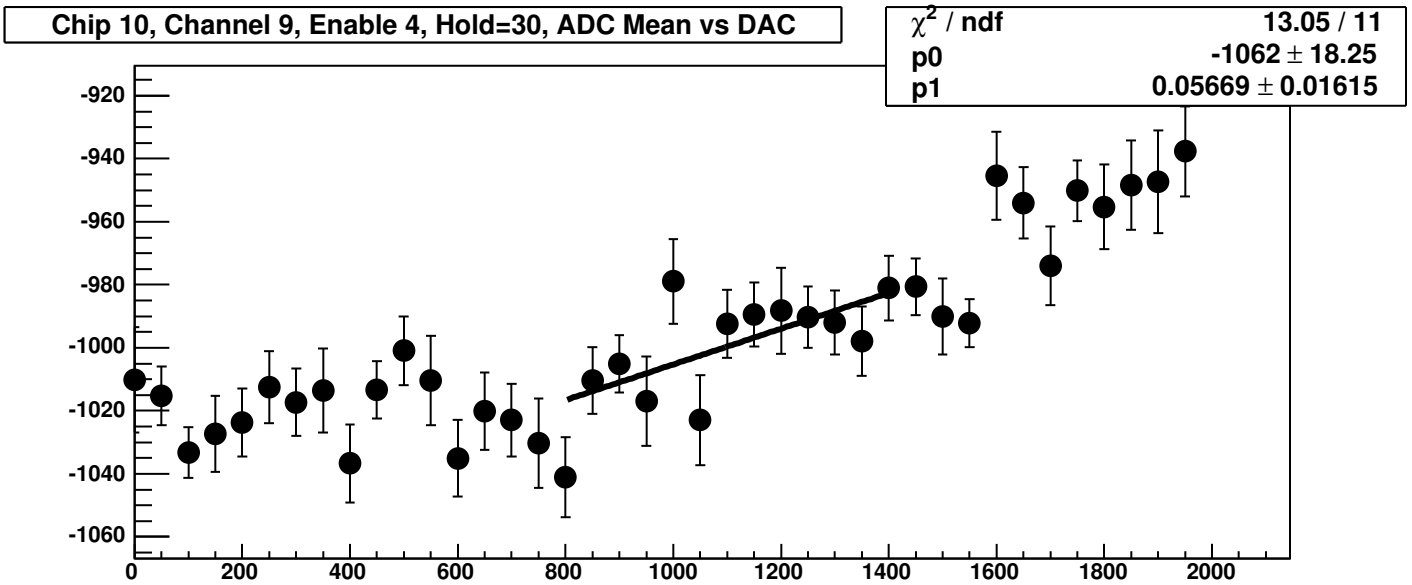

Chip 10, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

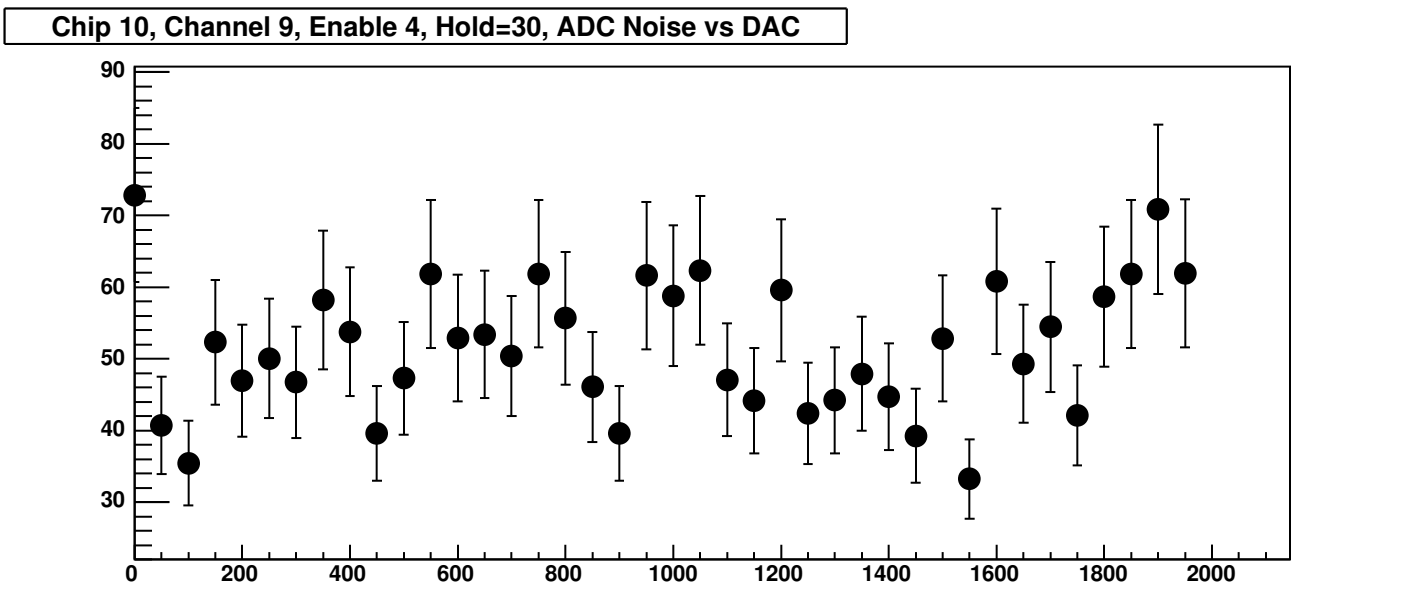

Chip 10, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

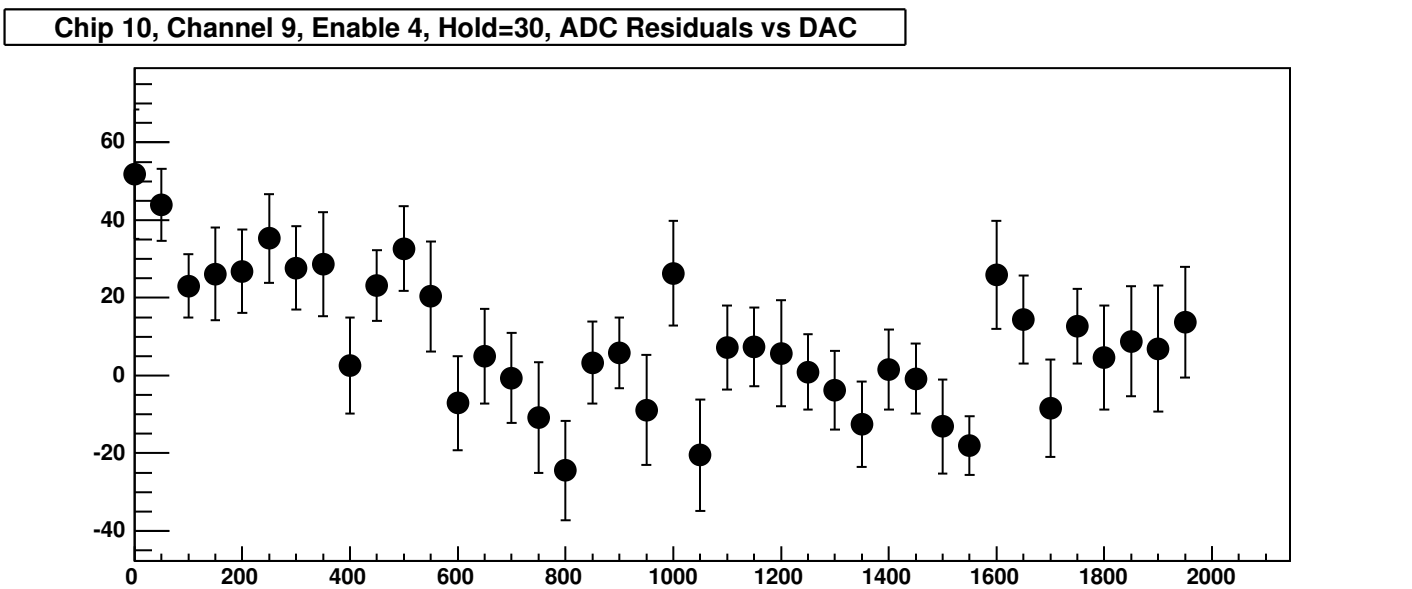

Chip 10, Channel 9, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 16.21 / 11
p0 -1080 ± 17.79
p1 0.1955 ± 0.0161
-650
-700
-750
-800
-850
-900
-950
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

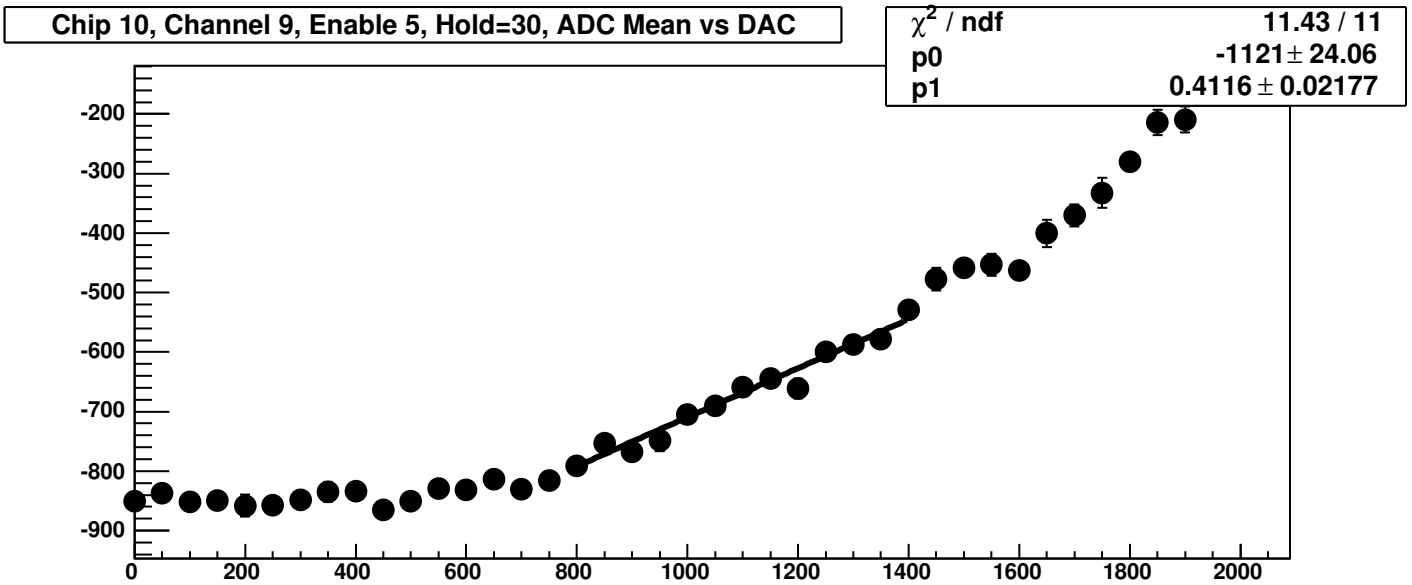

Chip 10, Channel 9, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

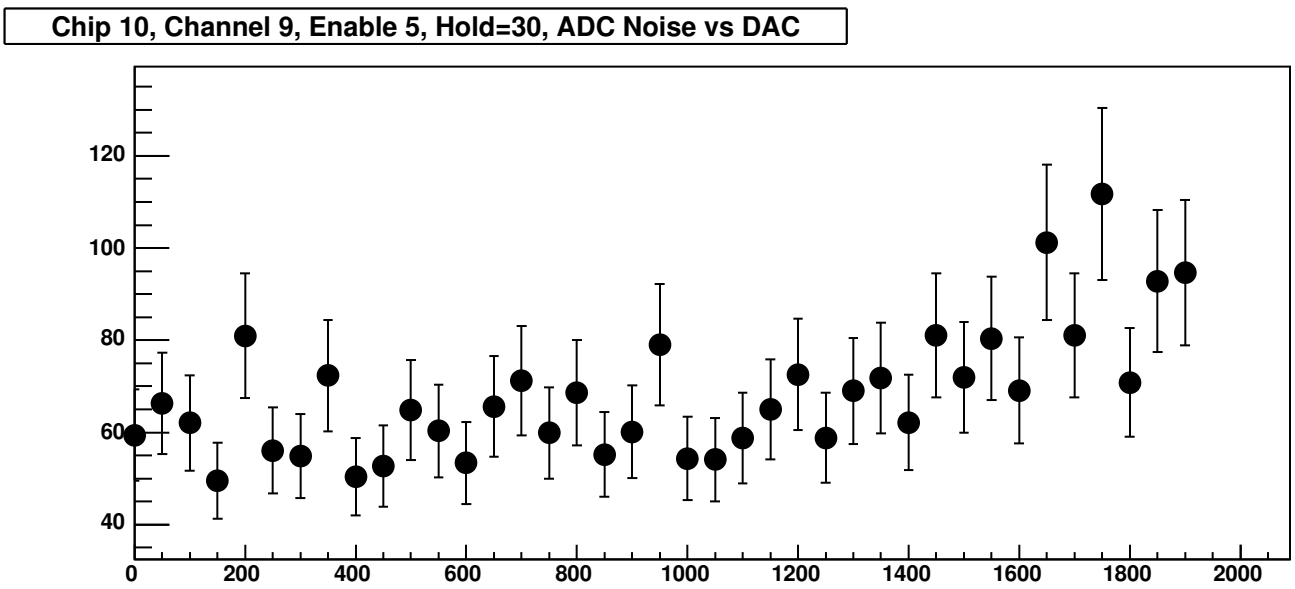

Chip 10, Channel 9, Enable 3, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

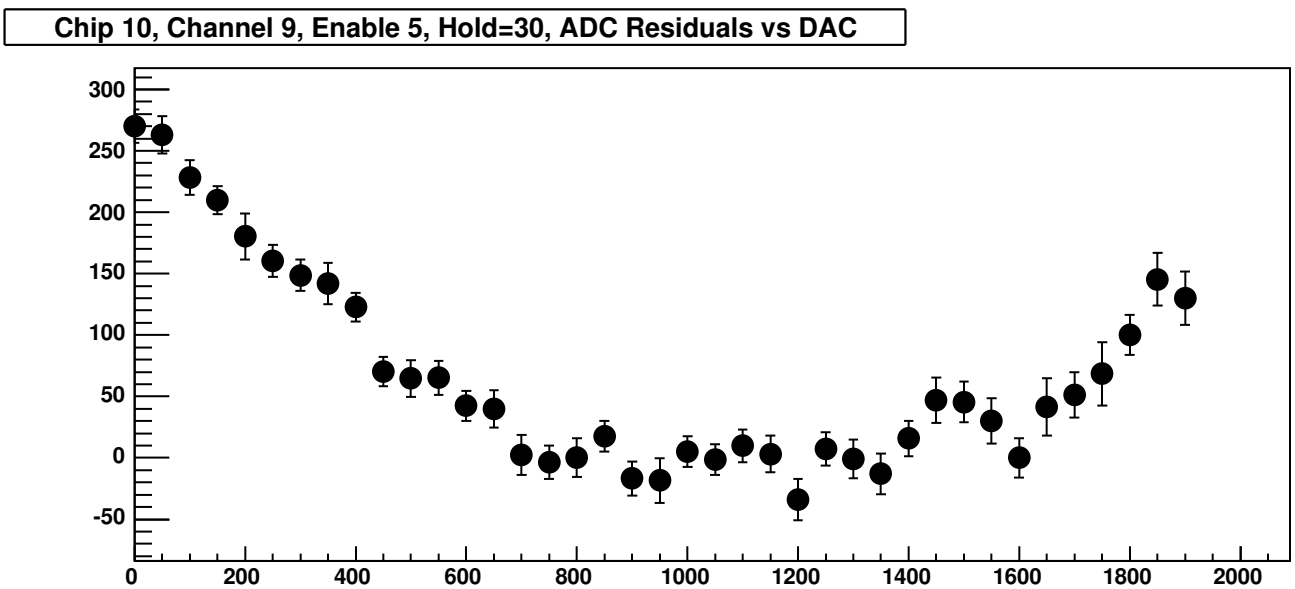

Chip 10, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 13.05 / 11
p0 -1062 ± 18.25
p1 0.05669 ± 0.01615
-920
-940
-960
-980
-1000
-1020
-1040
-1060
0
200
400
600
800
1000
1200
1400
1600
1800
2000

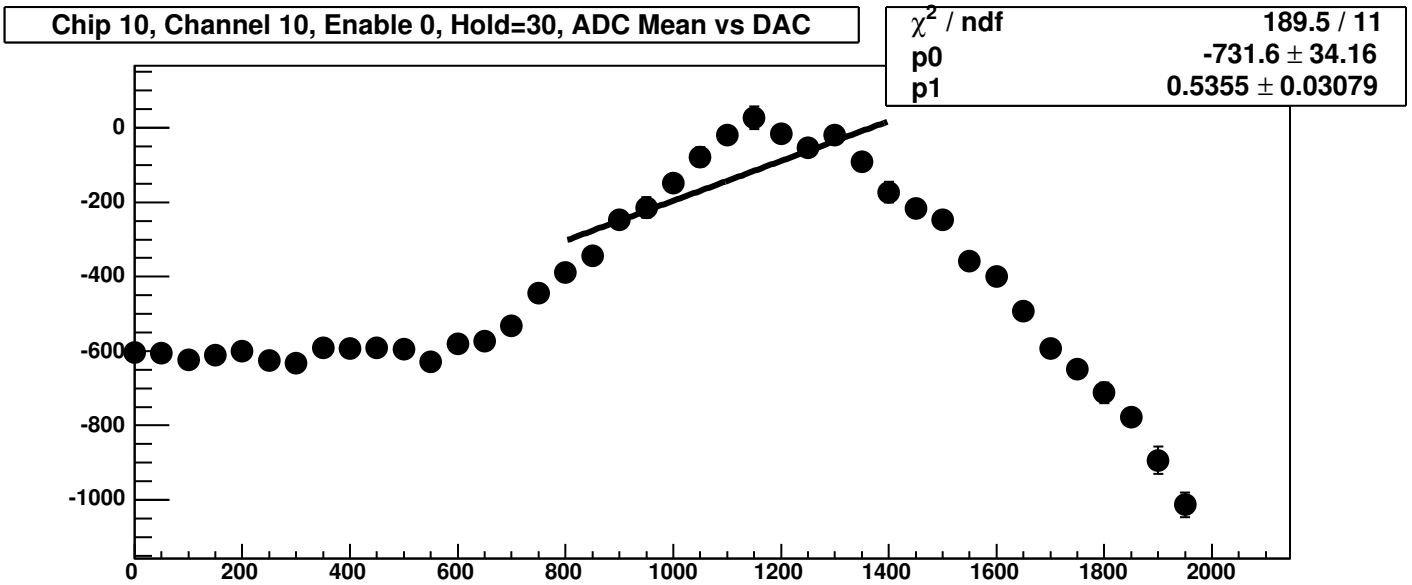

Chip 10, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

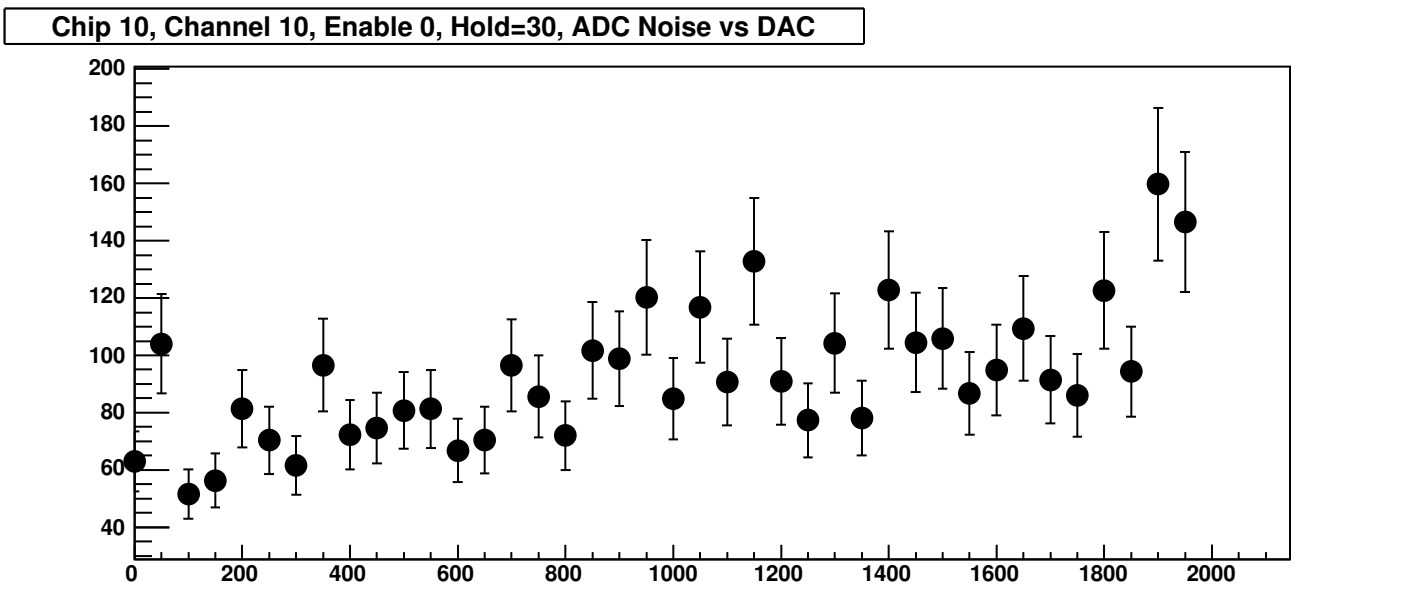

Chip 10, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

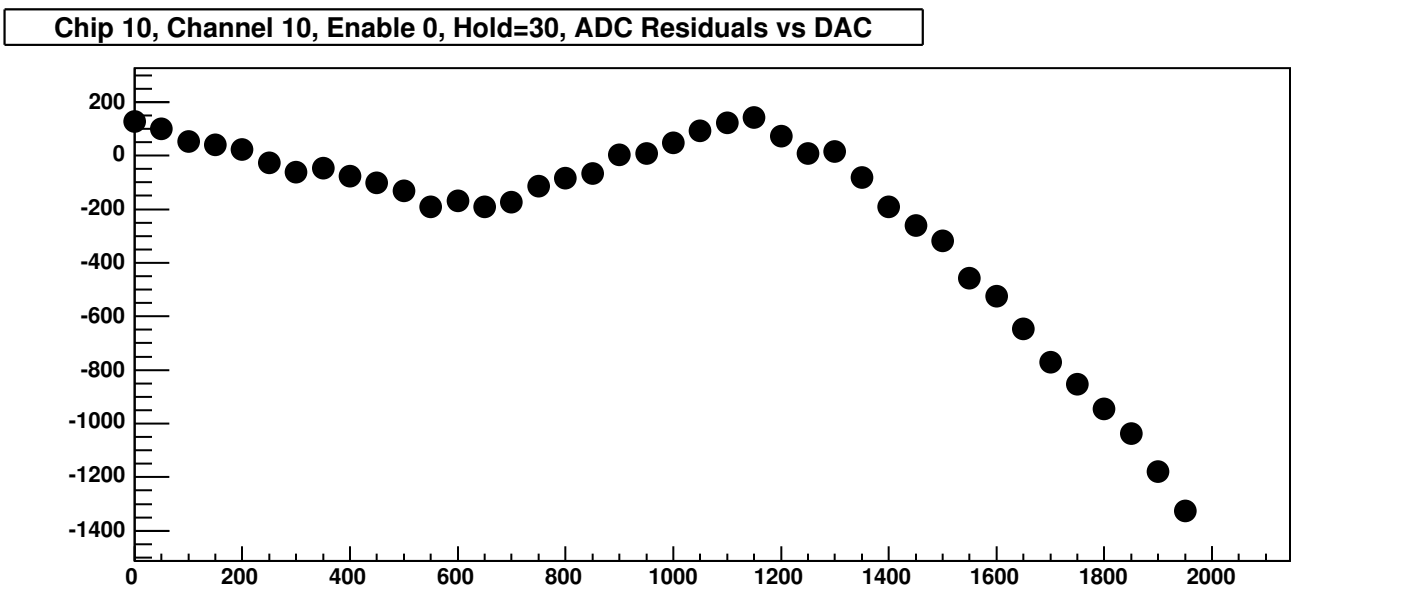

Chip 10, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 11.43 / 11
p0 -1121 ± 24.06
p1 0.4116 ± 0.02177
-200
-300
-400
-500
-600
-700
-800
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

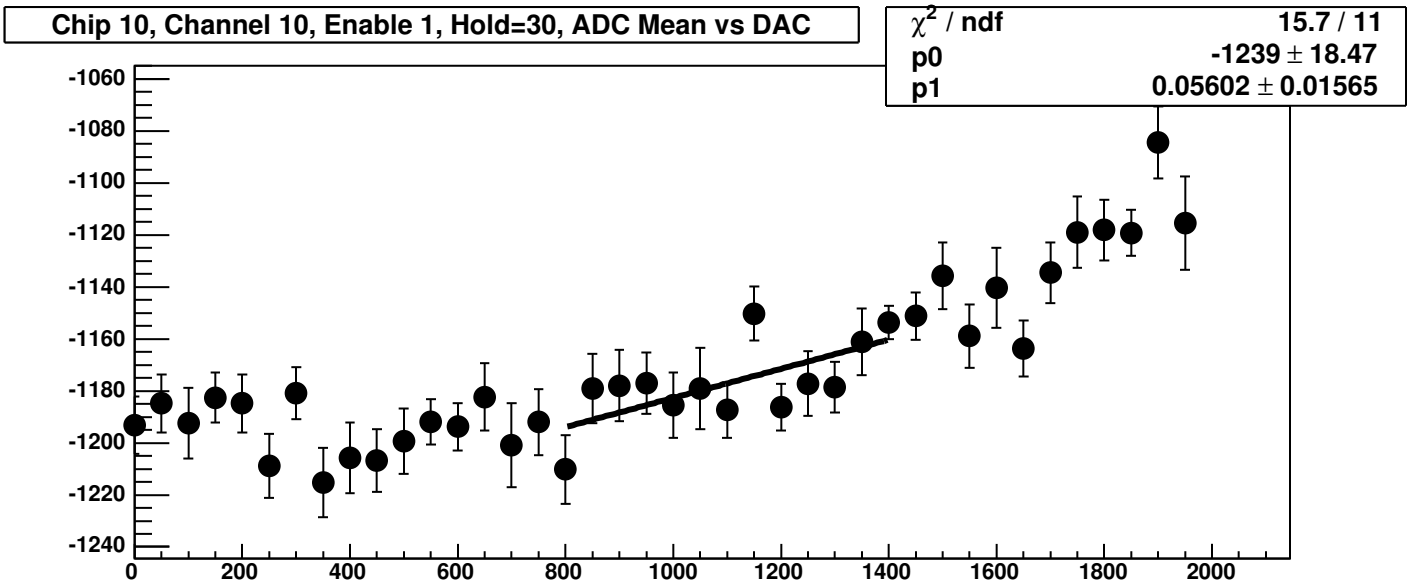

Chip 10, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

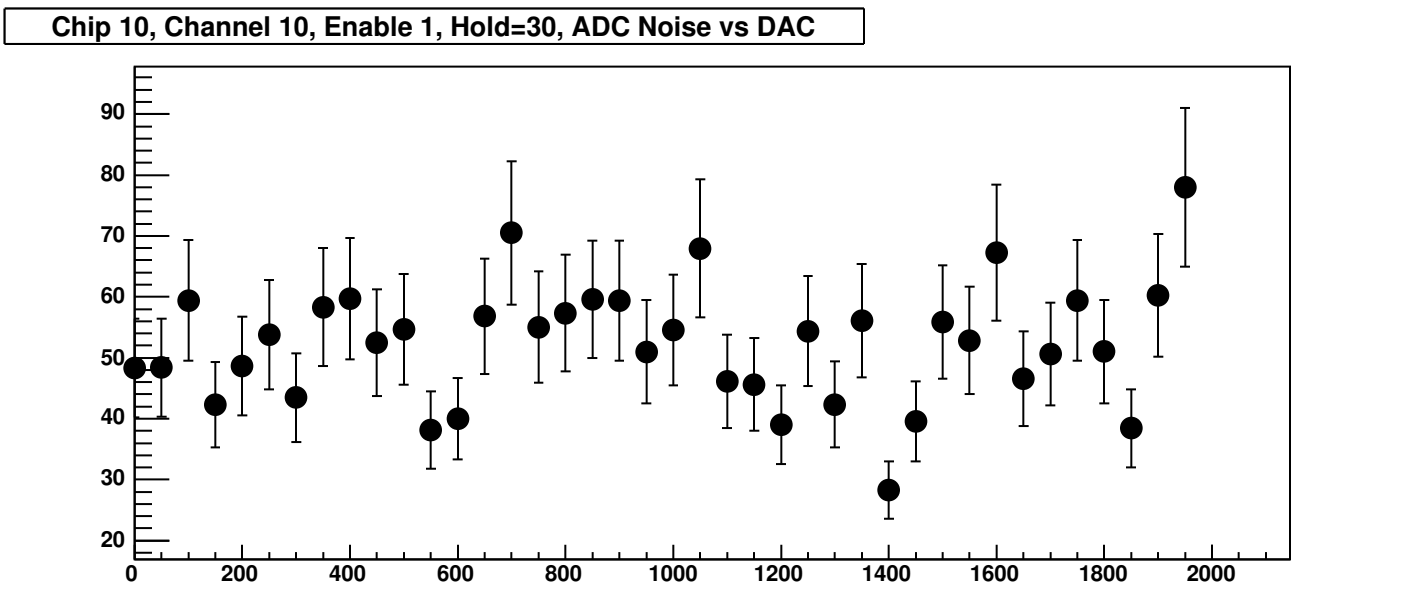

Chip 10, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

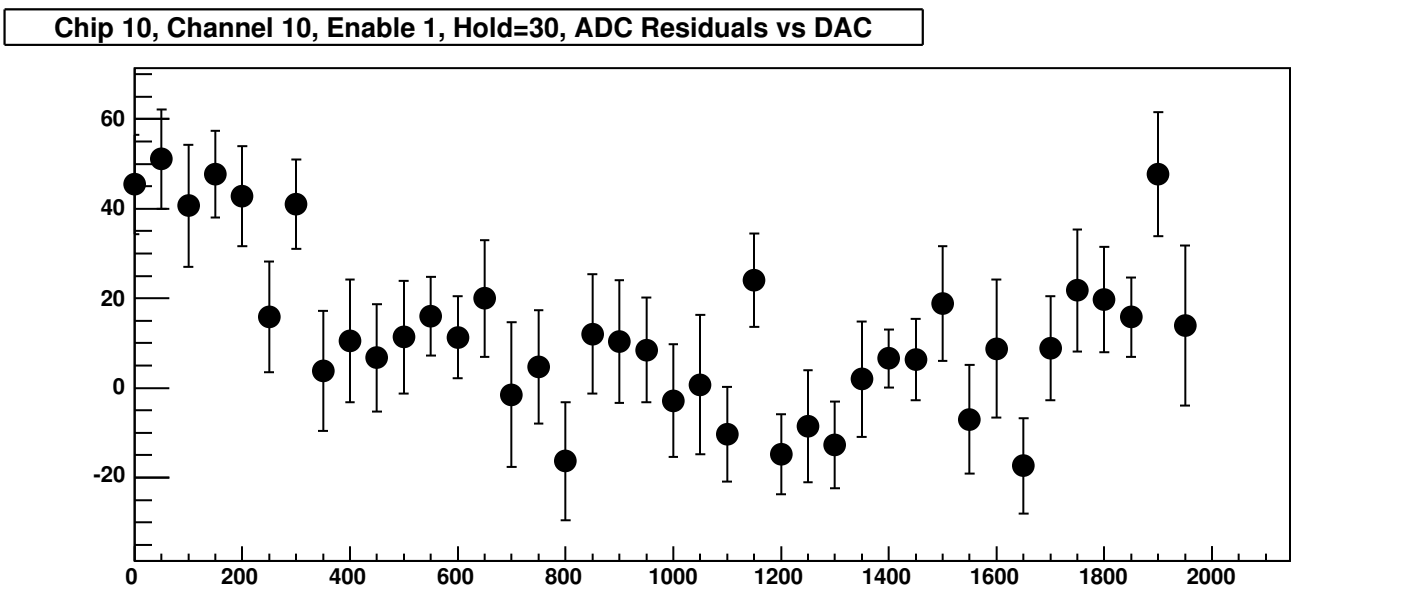

Chip 10, Channel 10, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 189.5 / 11
p0 -731.6 ± 34.16
p1 0.5355 ± 0.03079
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

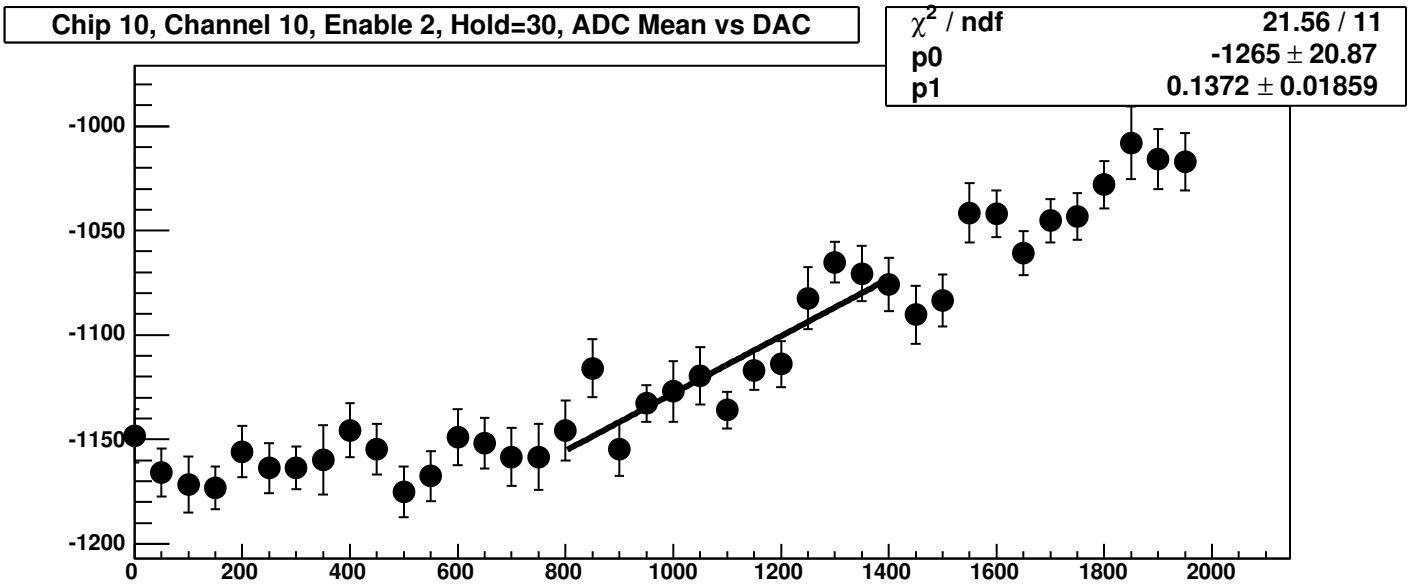

Chip 10, Channel 10, Enable 0, Hold=30, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

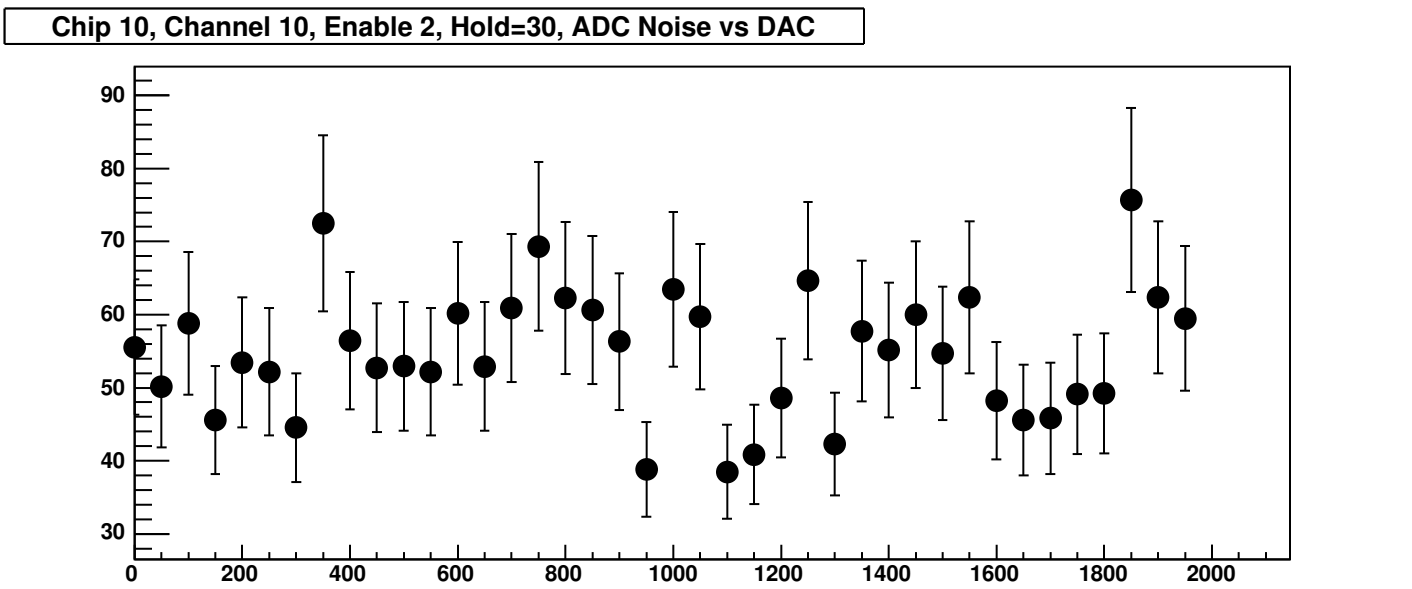

Chip 10, Channel 10, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

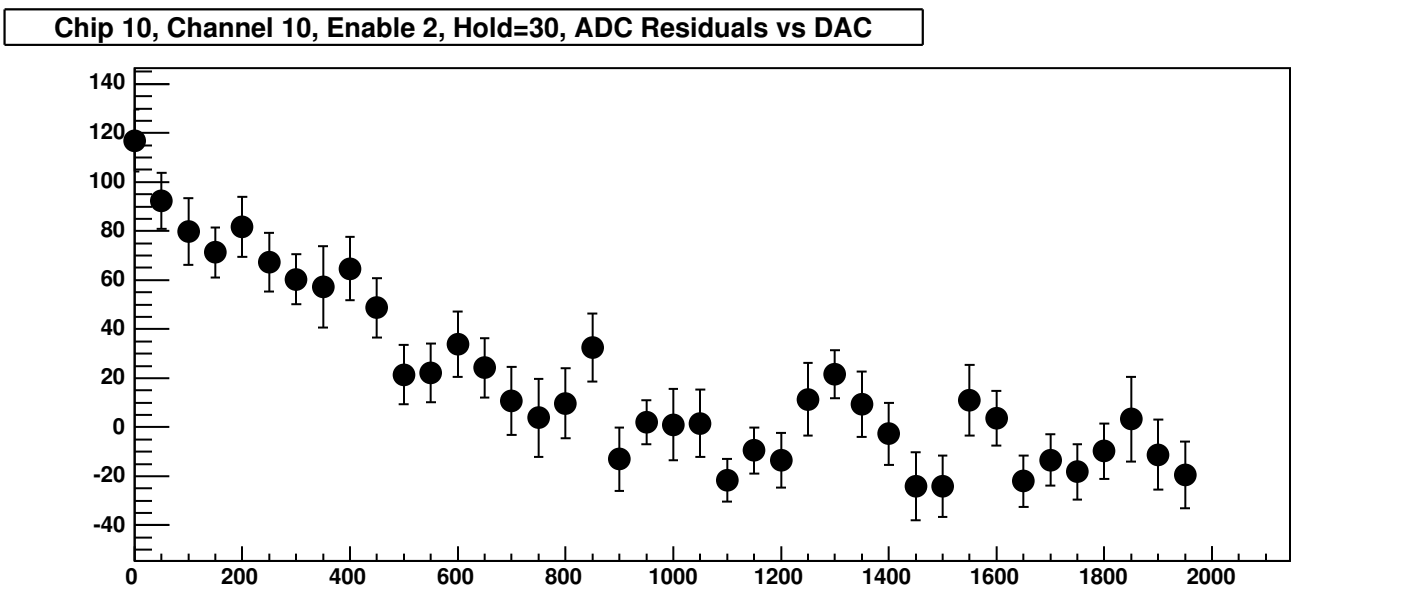

Chip 10, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 15.7 / 11
p0 -1239 ± 18.47
p1 0.05602 ± 0.01565
-1060
-1080
-1100
-1120
-1140
-1160
-1180
-1200
-1220
-1240
0
200
400
600
800
1000
1200
1400
1600
1800
2000

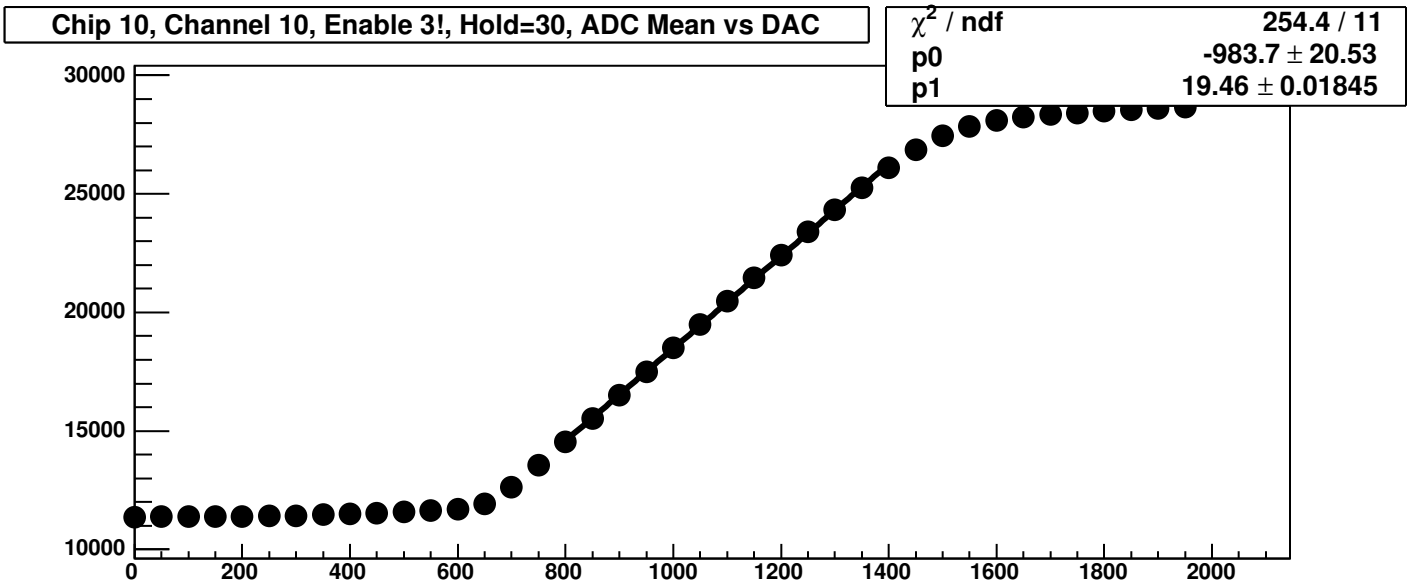

Chip 10, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

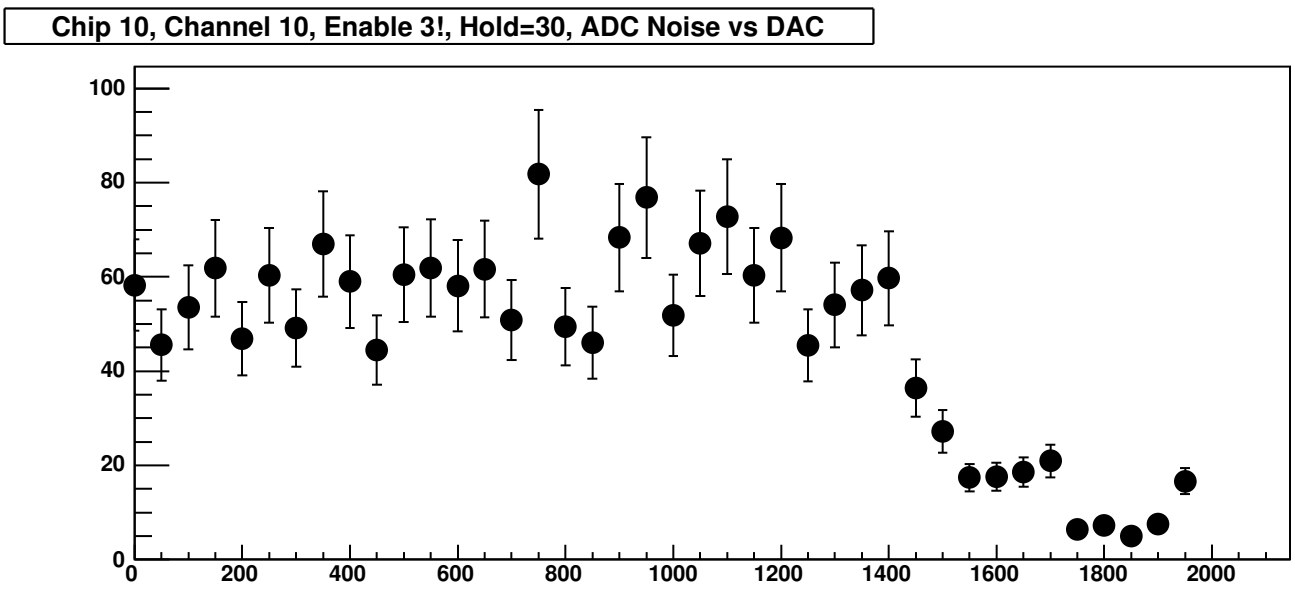

Chip 10, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

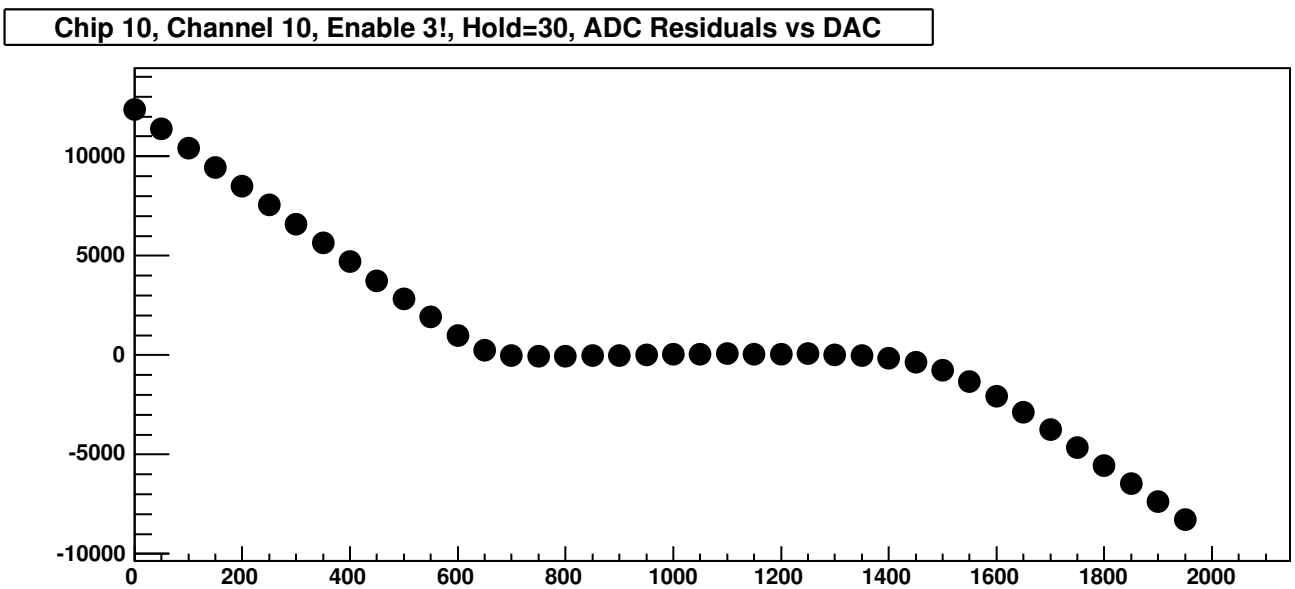

Chip 10, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 21.56 / 11
p0 -1265 ± 20.87
p1 0.1372 ± 0.01859
-1000
-1050
-1100
-1150
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

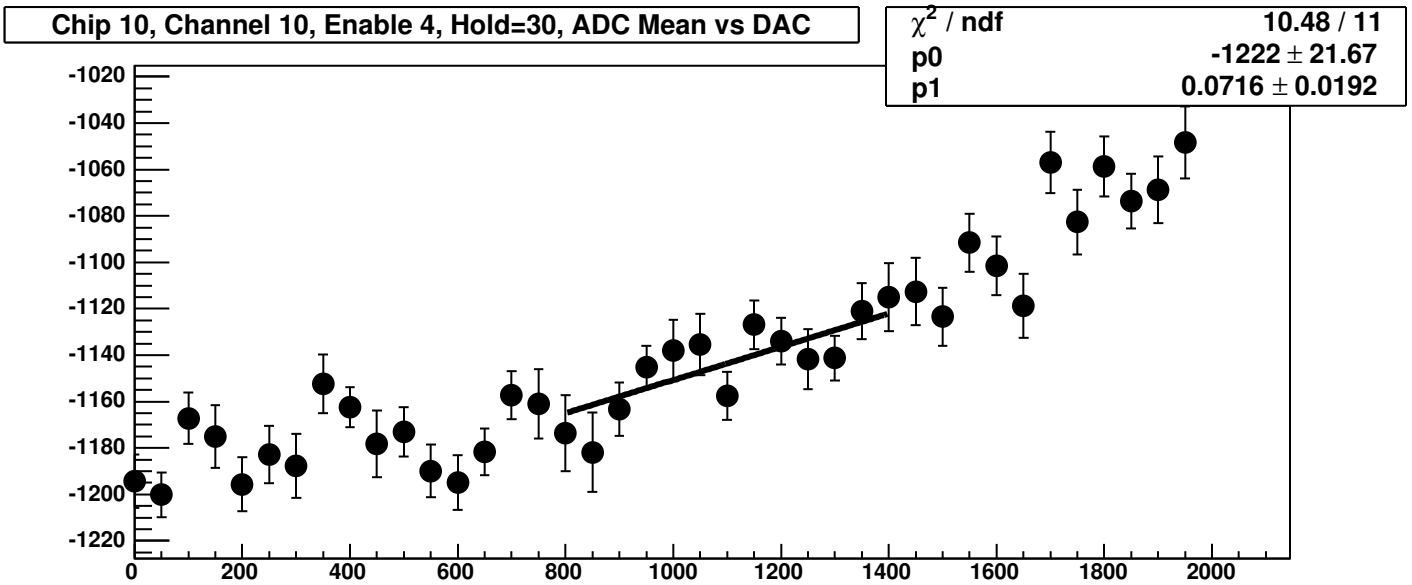

Chip 10, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

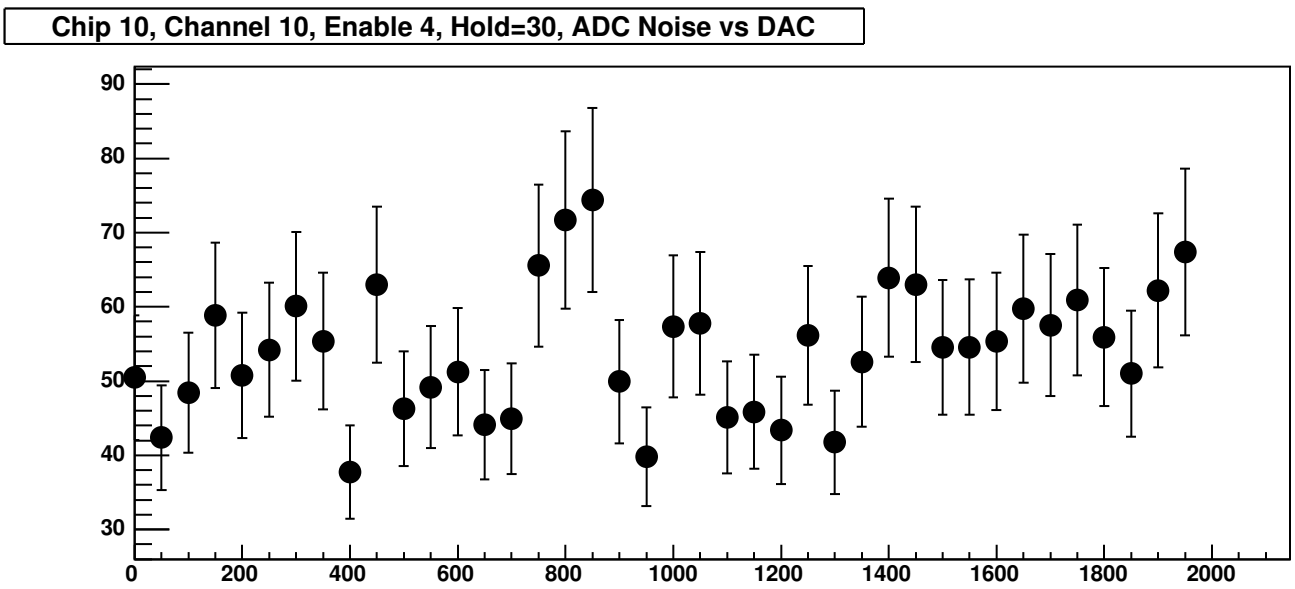

Chip 10, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

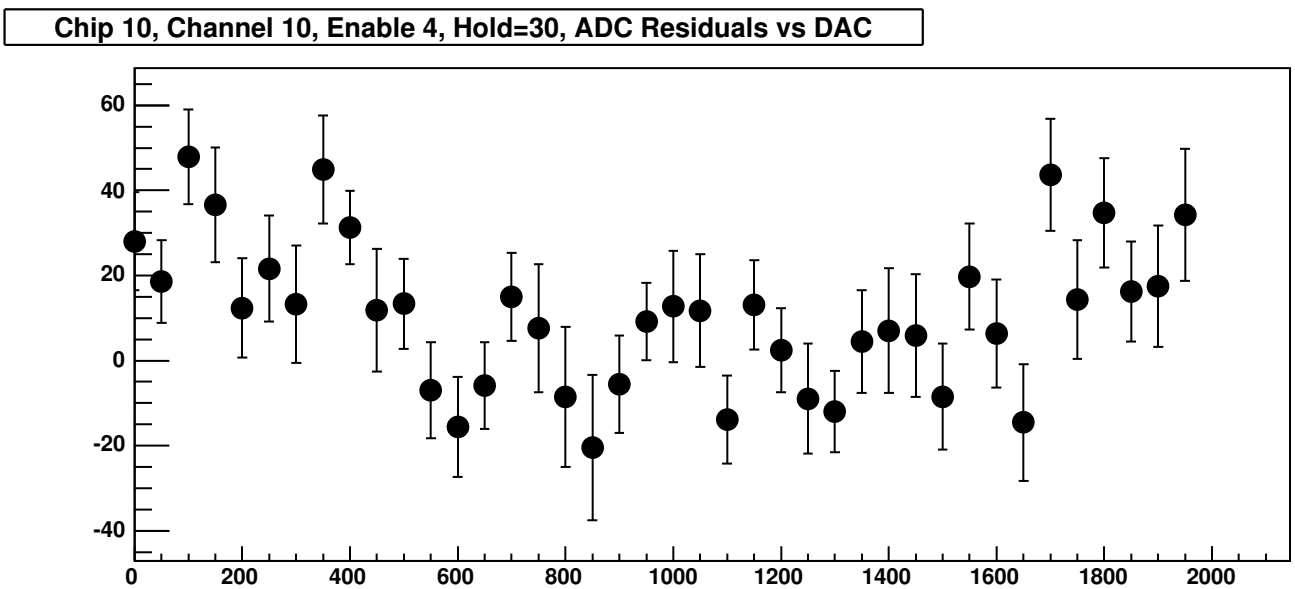

Chip 10, Channel 10, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 254.4 / 11
p0 -983.7 ± 20.53
p1 19.46 ± 0.01845
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

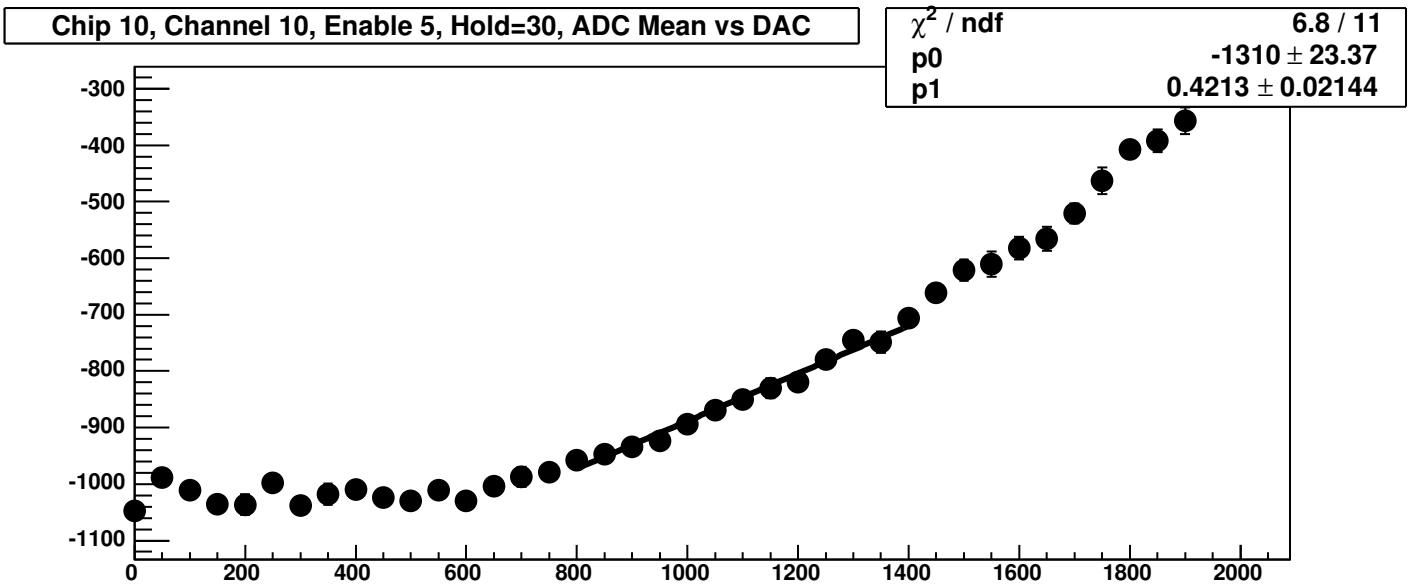

Chip 10, Channel 10, Enable 3!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

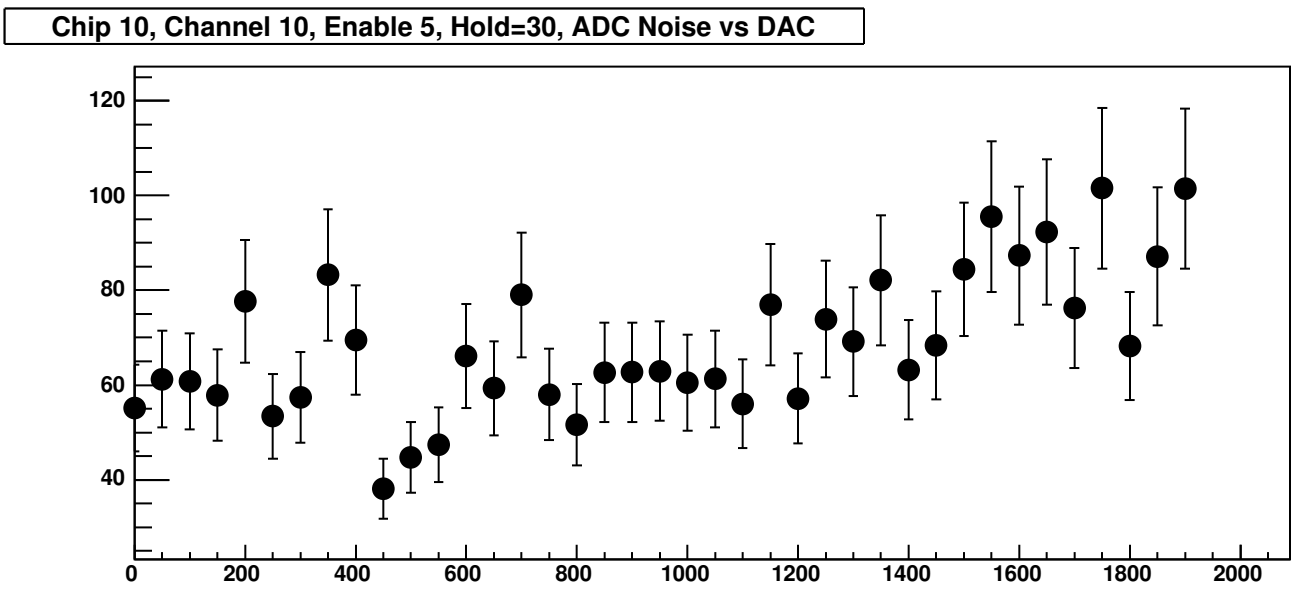

Chip 10, Channel 10, Enable 3!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

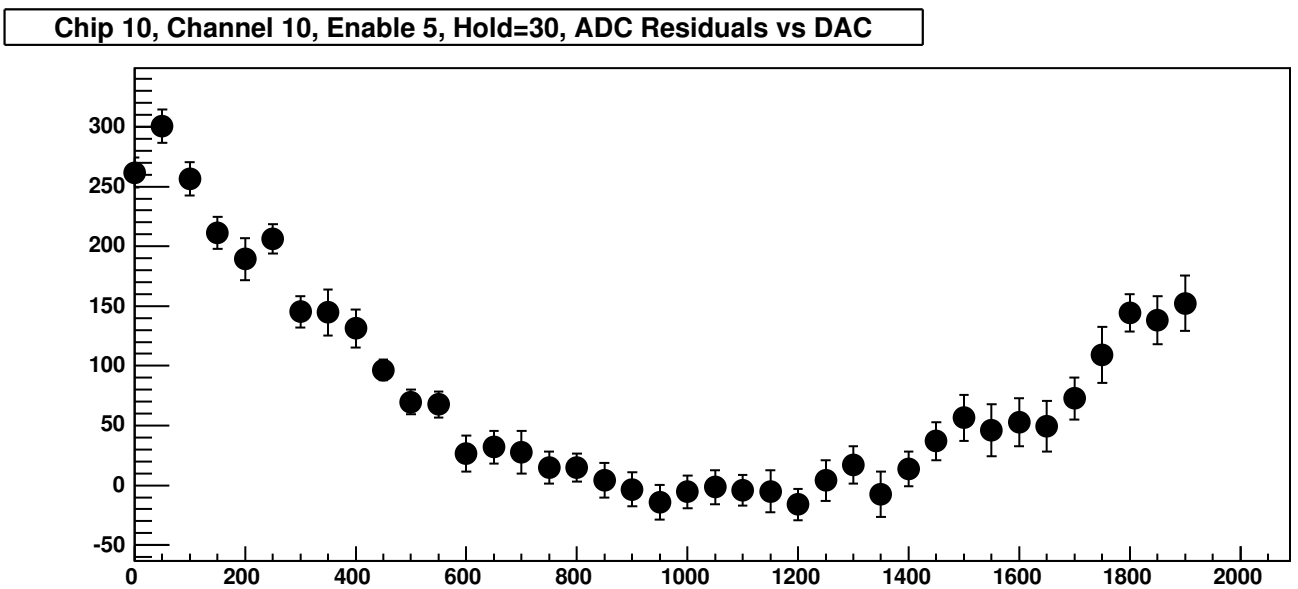

Chip 10, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 10.48 / 11
p0 -1222 ± 21.67
p1 0.0716 ± 0.0192
-1020
-1040
-1060
-1080
-1100
-1120
-1140
-1160
-1180
-1200
-1220
0
200
400
600
800
1000
1200
1400
1600
1800
2000

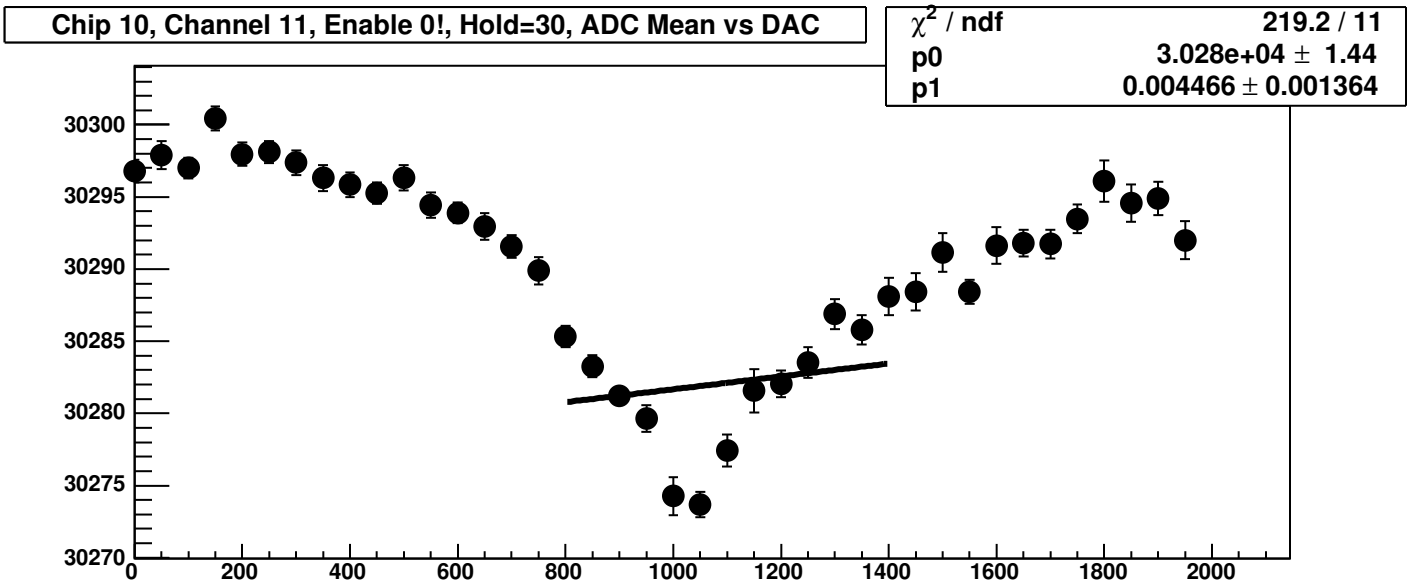

Chip 10, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

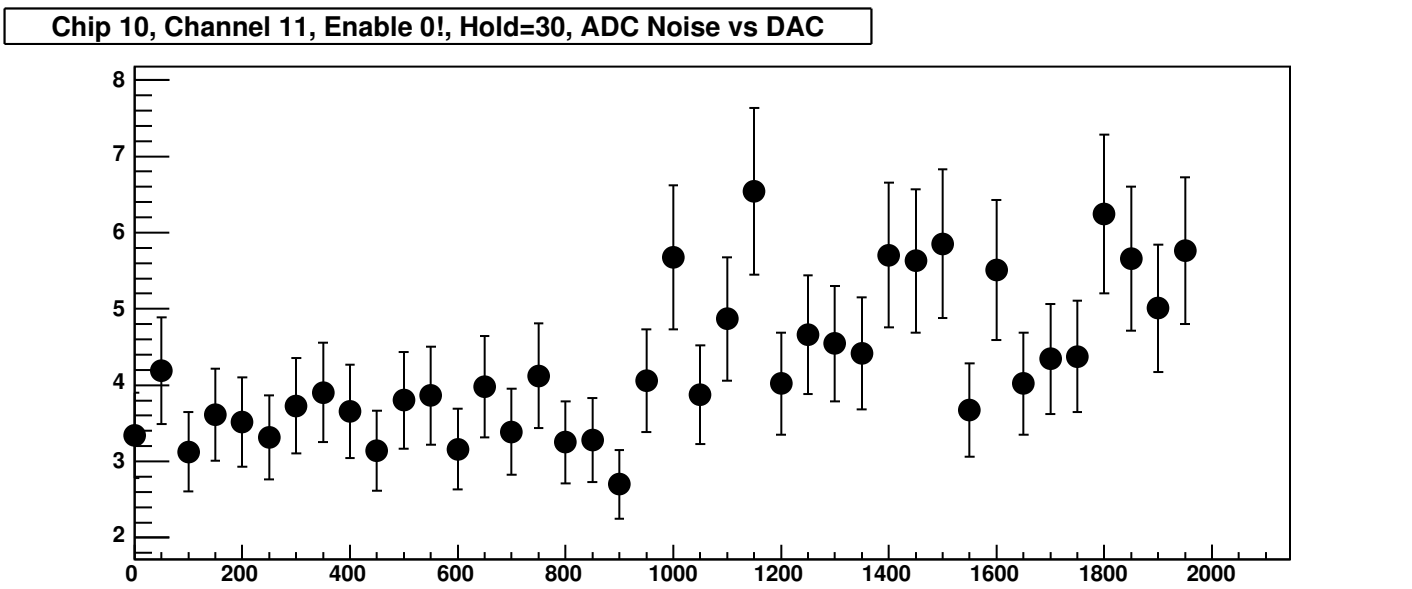

Chip 10, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

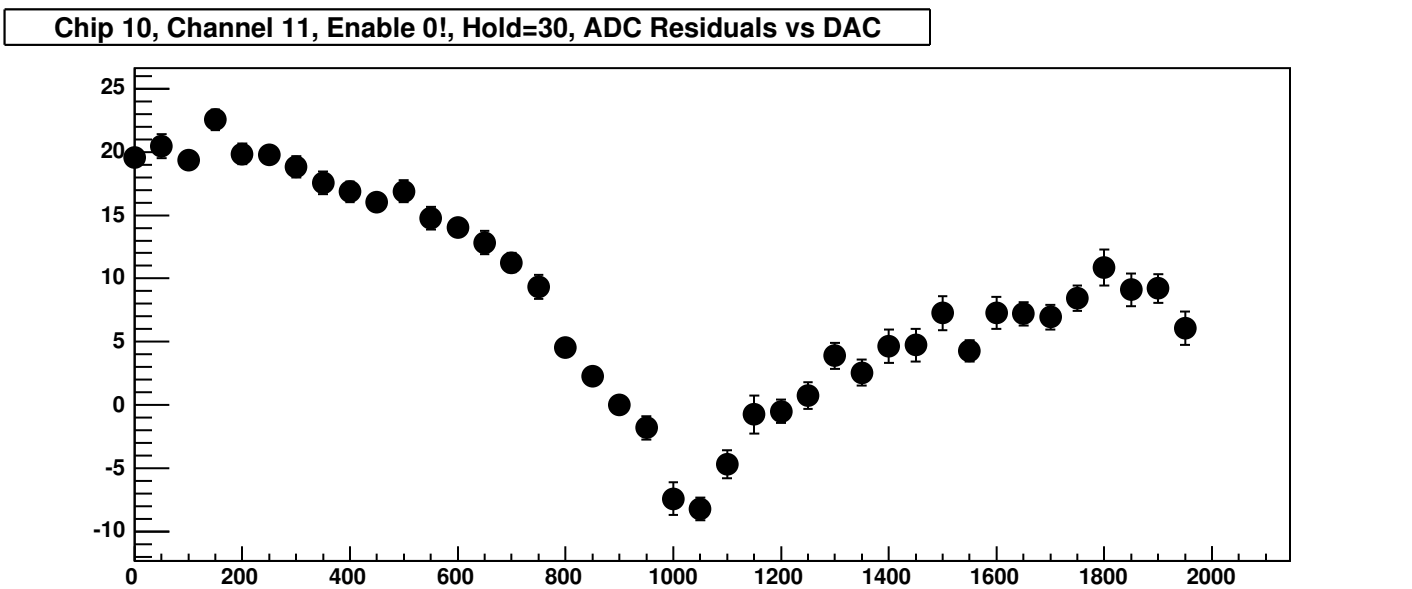

Chip 10, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 6.8 / 11
p0 -1310 ± 23.37
p1 0.4213 ± 0.02144
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

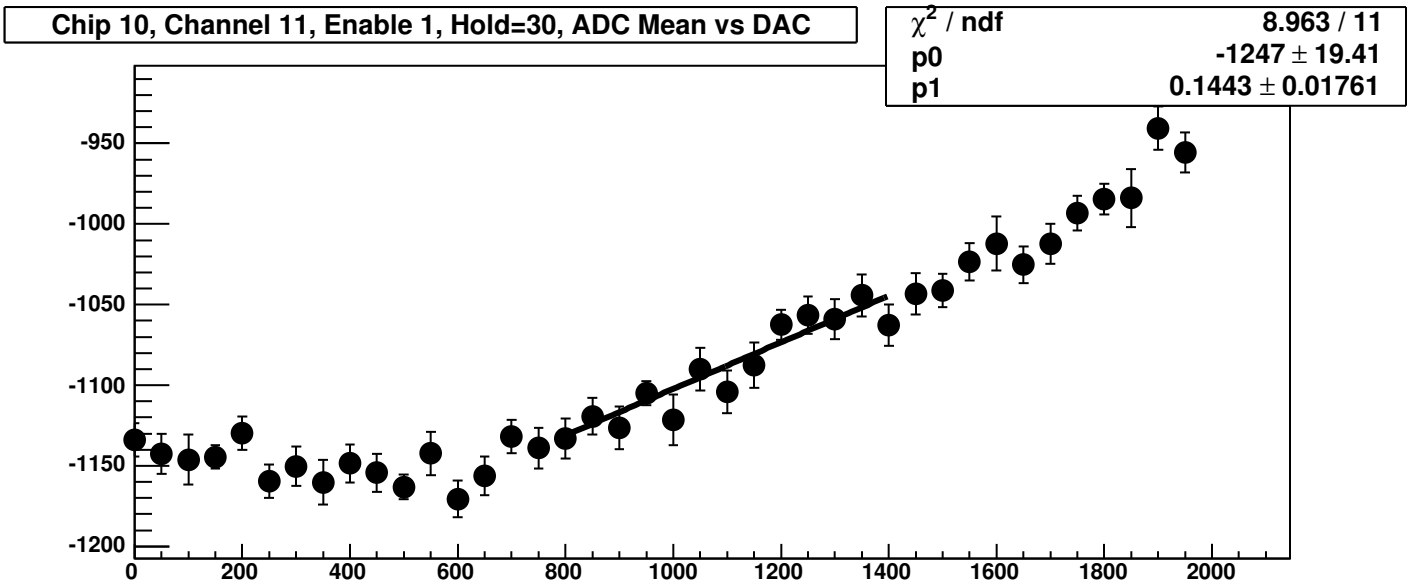

Chip 10, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

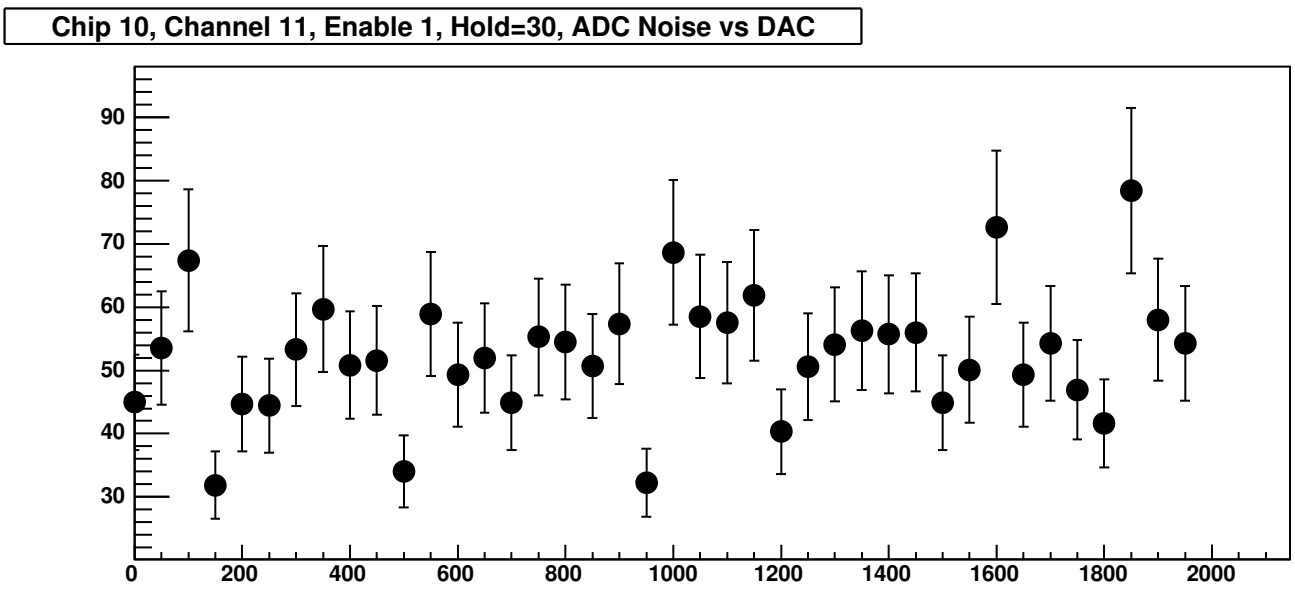

Chip 10, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

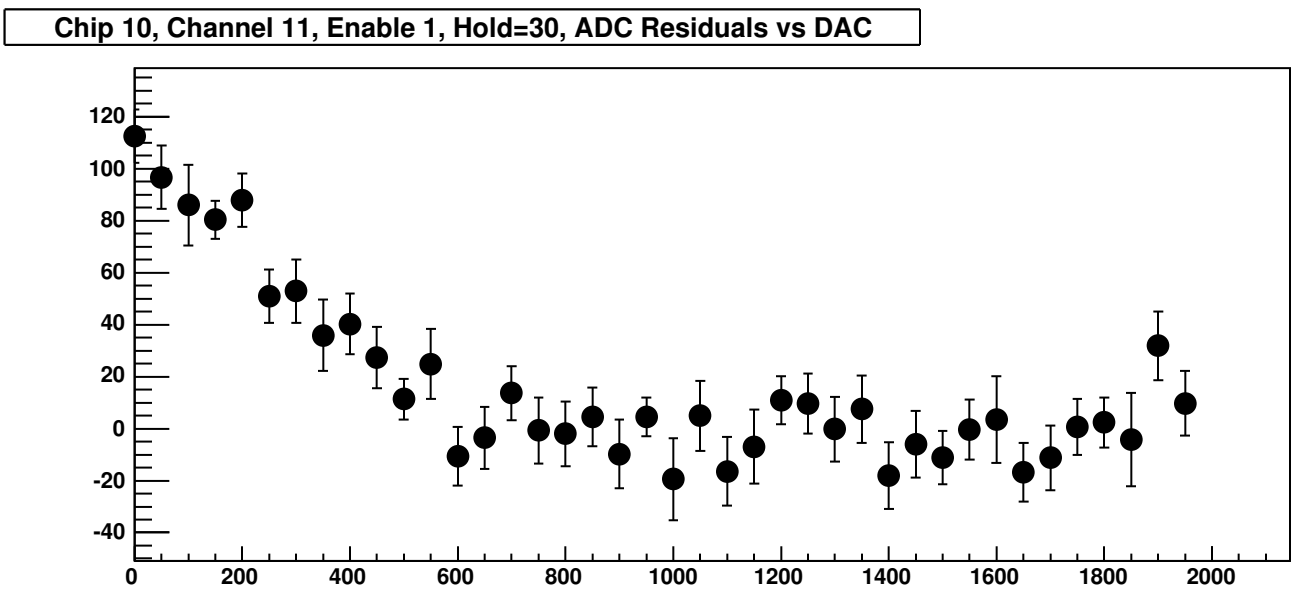

Chip 10, Channel 11, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 219.2 / 11
p0 3.028e+04 ± 1.44
p1 0.004466 ± 0.001364
30300
30295
30290
30285
30280
30275
30270
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 11, Enable 0!, Hold=30, ADC Noise vs DAC
8
7
6
5
4
3
2
0
200
400
600
800
1000
1200
1400
1600
1800
2000

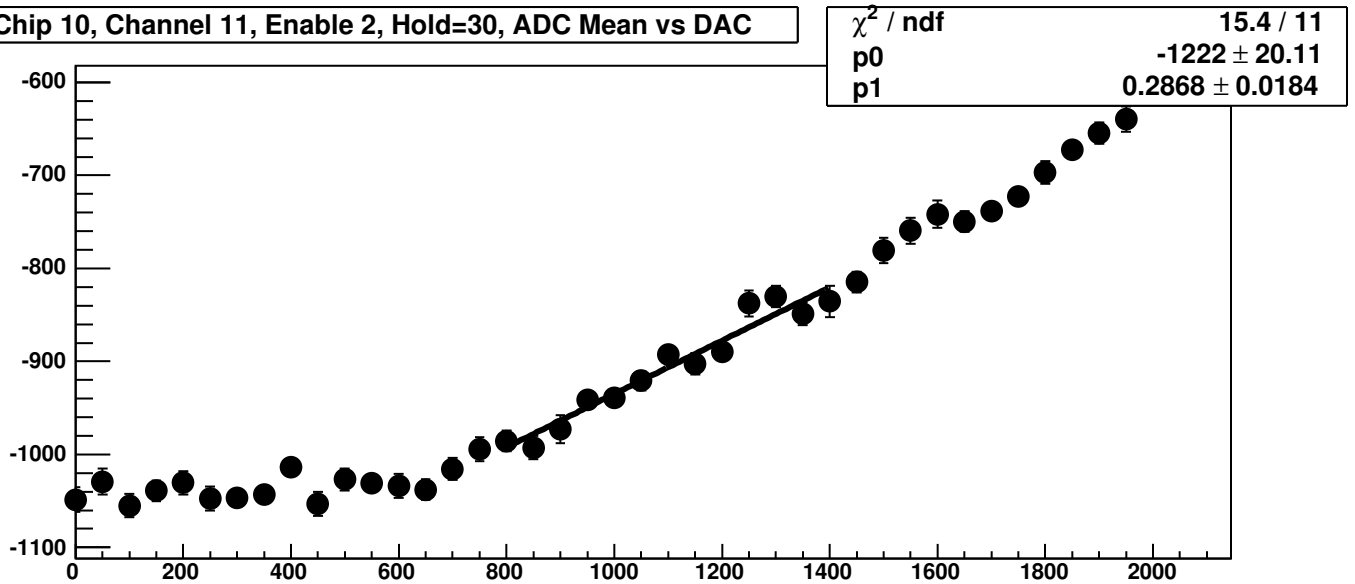

Chip 10, Channel 11, Enable 0!, Hold=30, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 8.963 / 11
p0 -1247 ± 19.41
p1 0.1443 ± 0.01761
-950
-1000
-1050
-1100
-1150
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

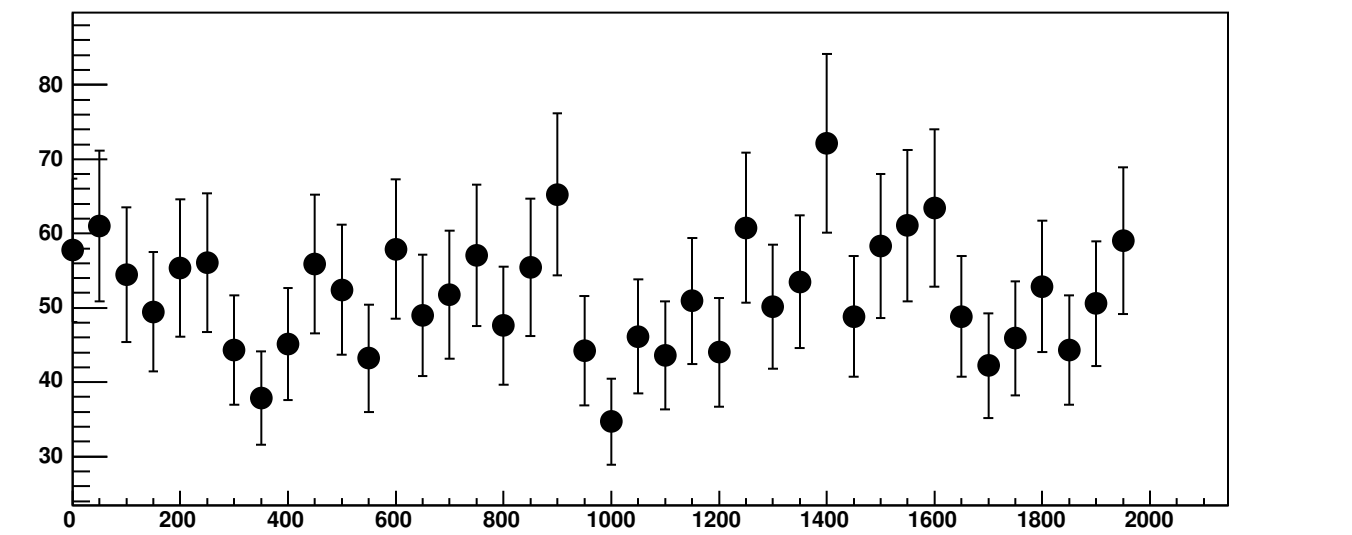

Chip 10, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

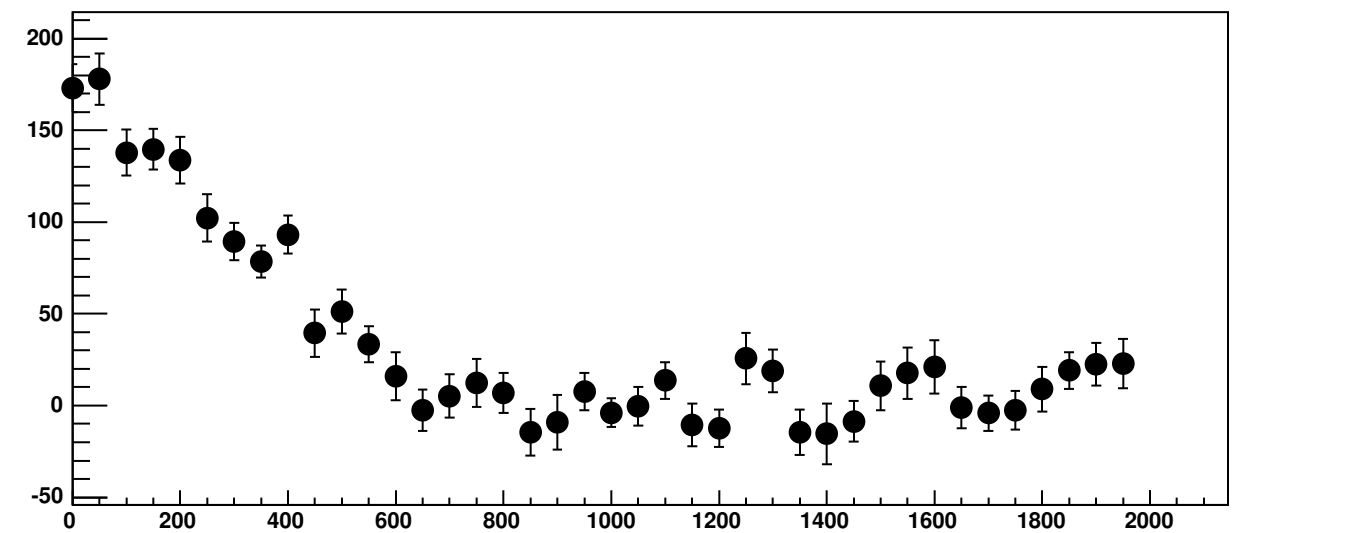
Chip 10, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC

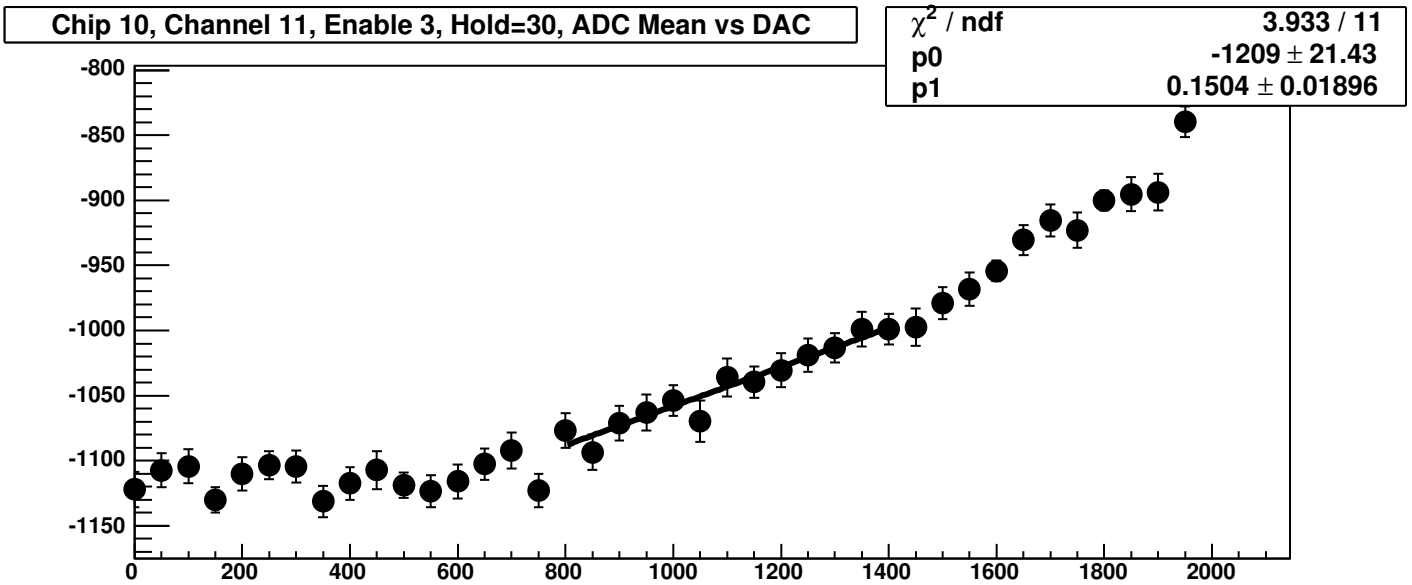


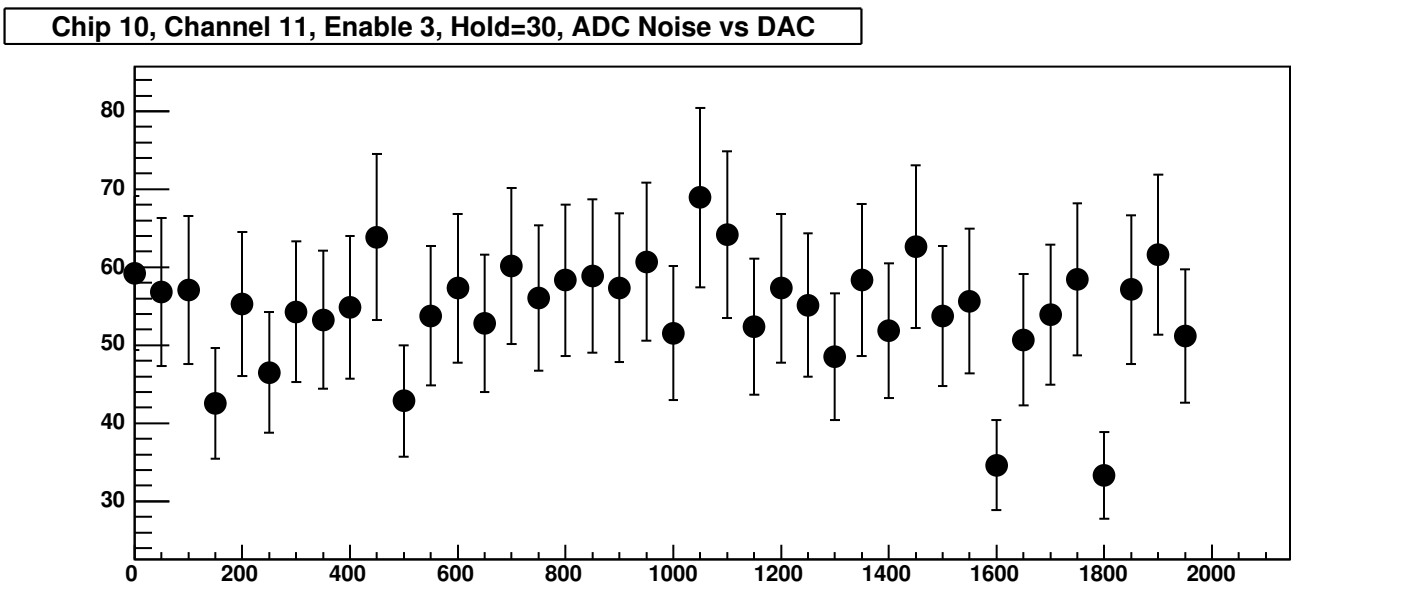
Chip 10, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC

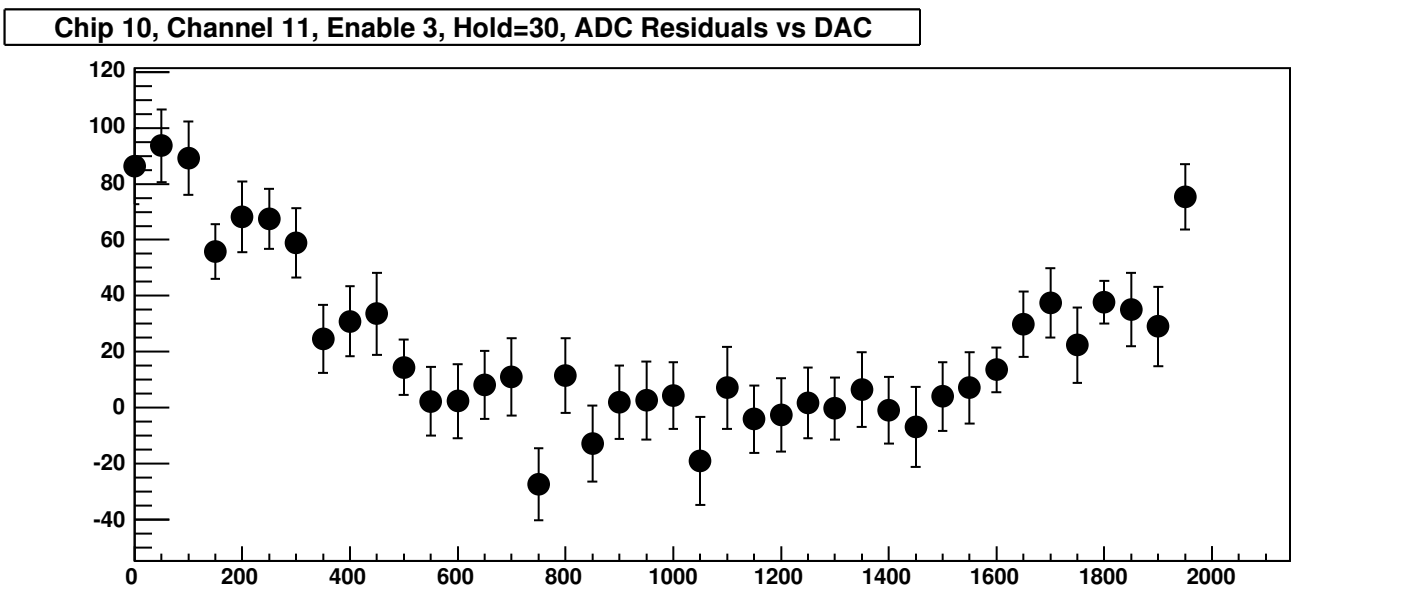


Chip 10, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC

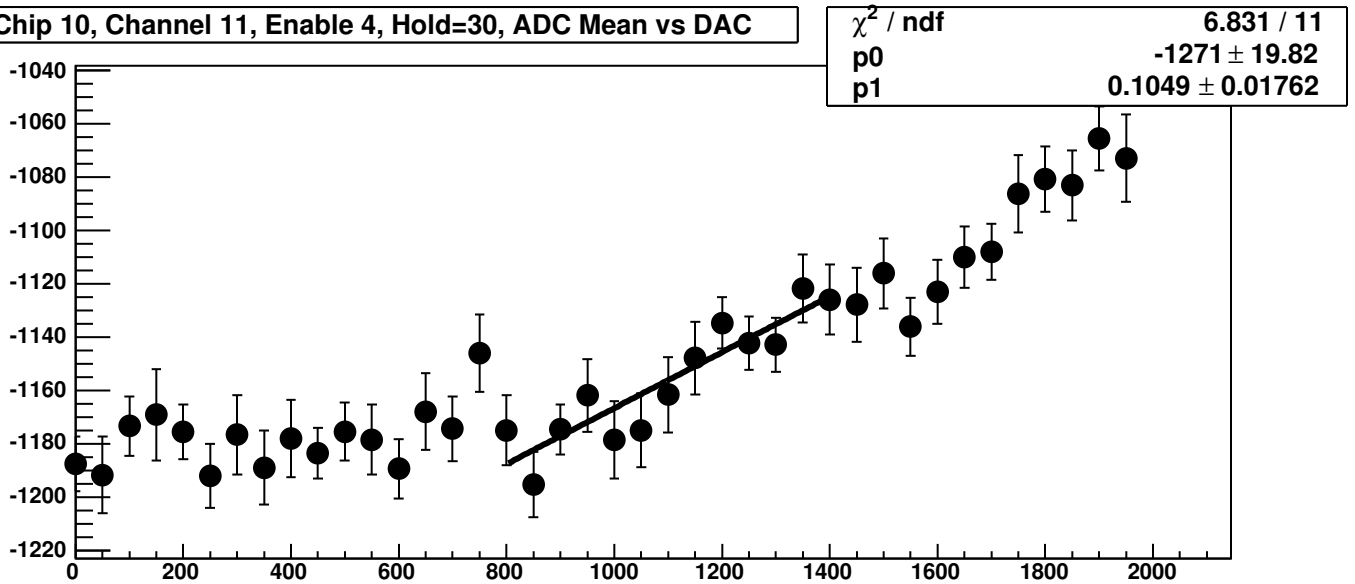



Chip 10, Channel 11, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 3.933 / 11
p0 -1209 ± 21.43
p1 0.1504 ± 0.01896
-800
-850
-900
-950
-1000
-1050
-1100
-1150
0
200
400
600
800
1000
1200
1400
1600
1800
2000

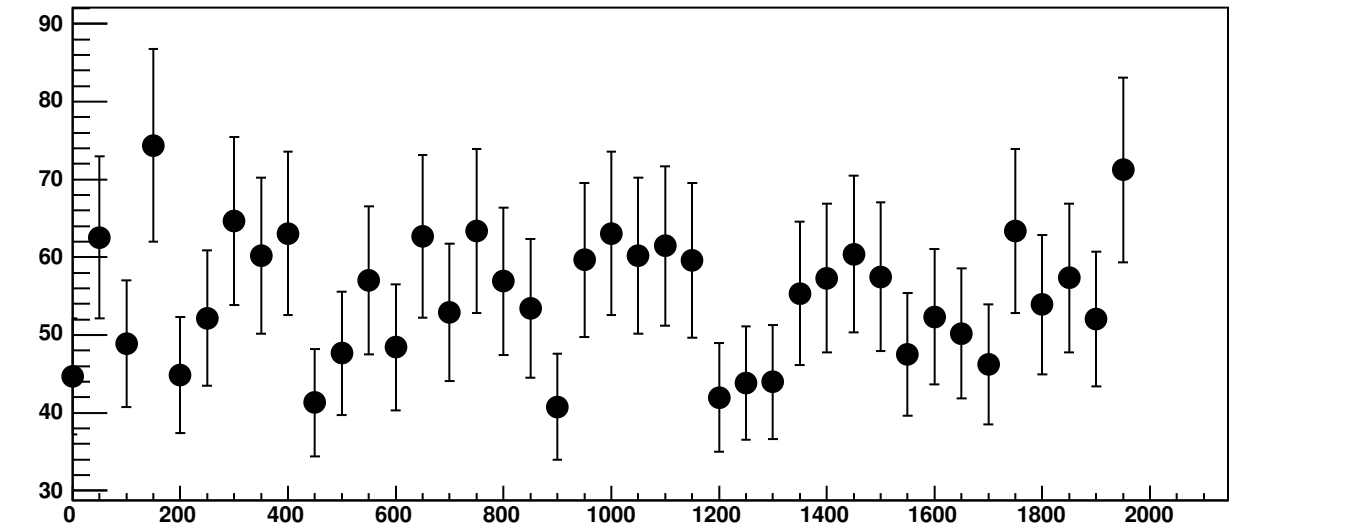

Chip 10, Channel 11, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 11, Enable 3, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

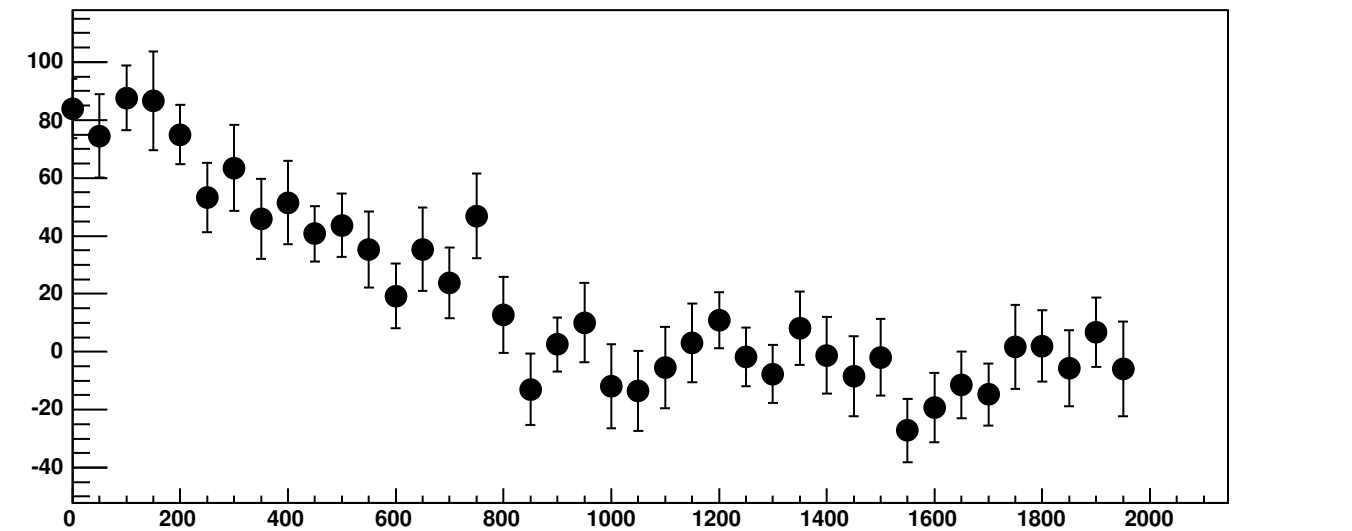
Chip 10, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC

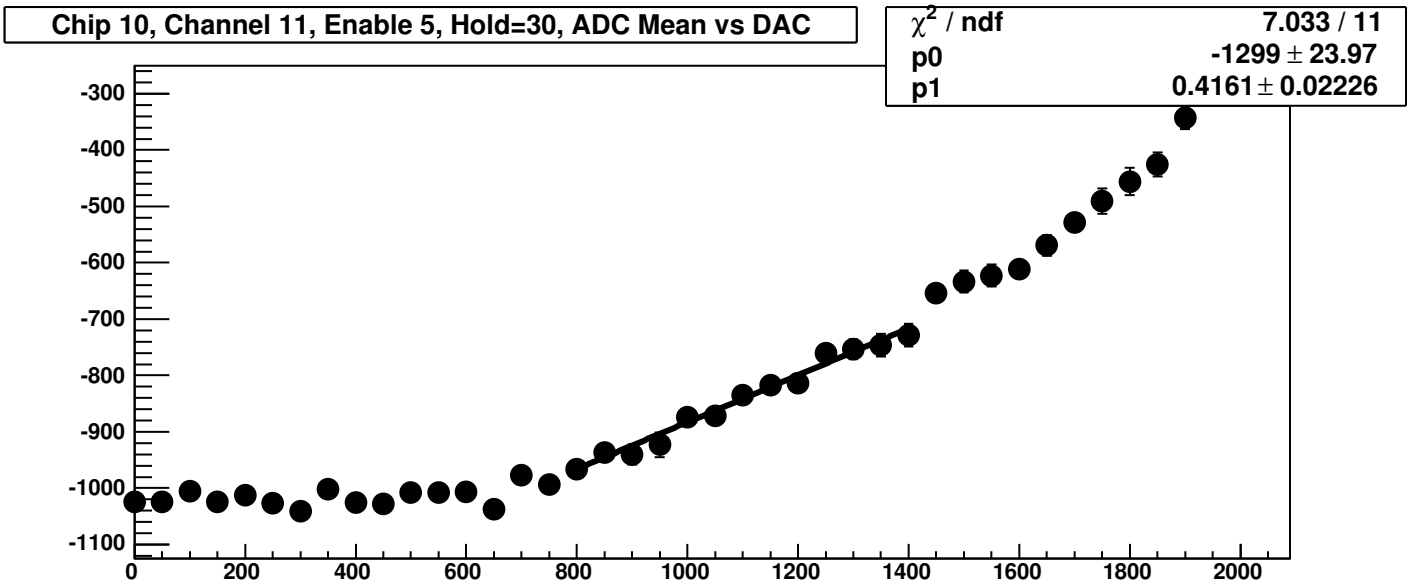


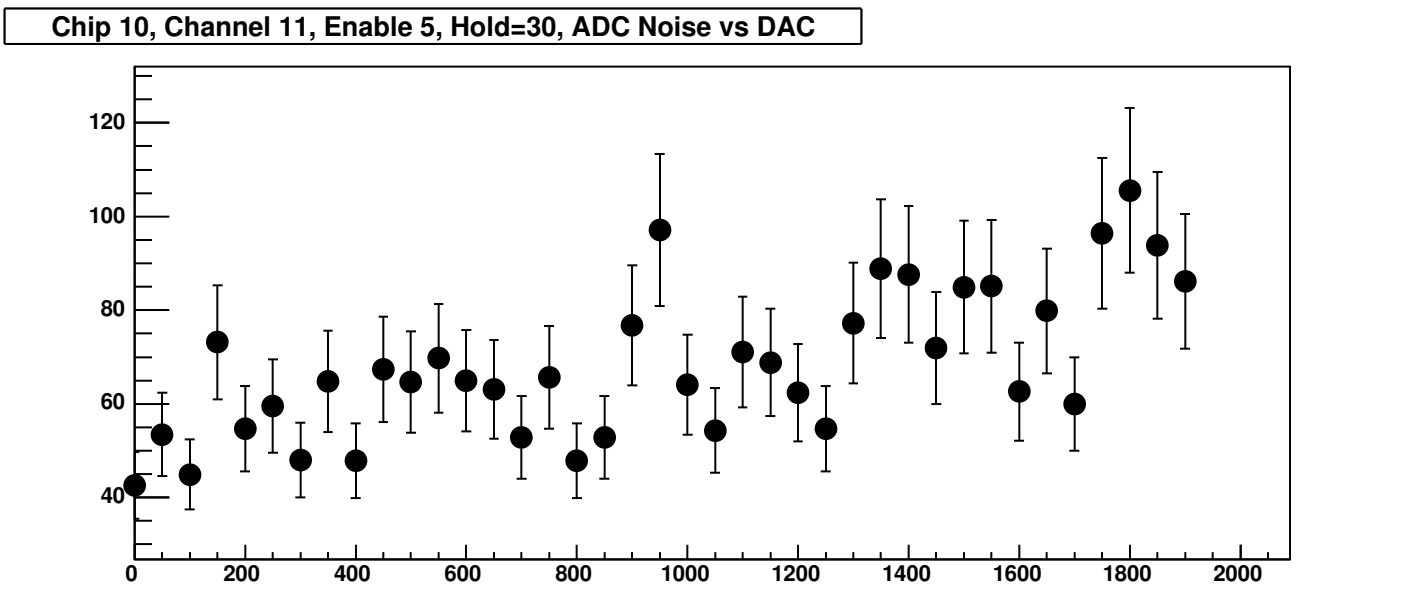
Chip 10, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC

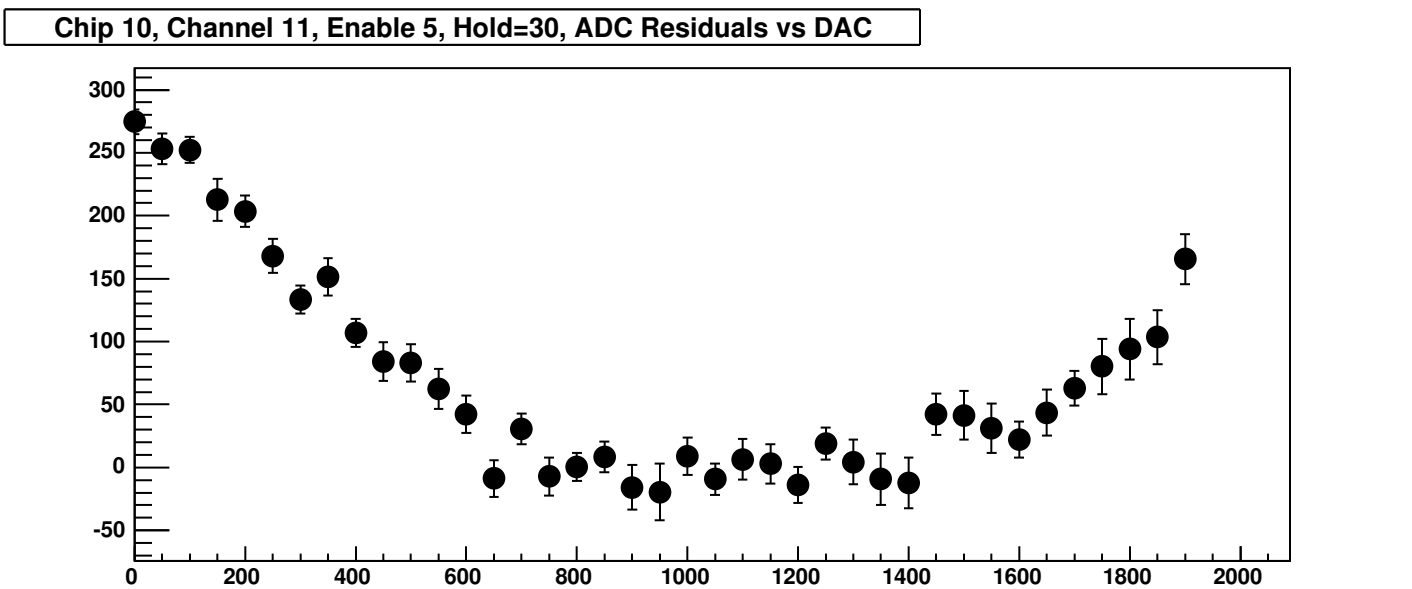


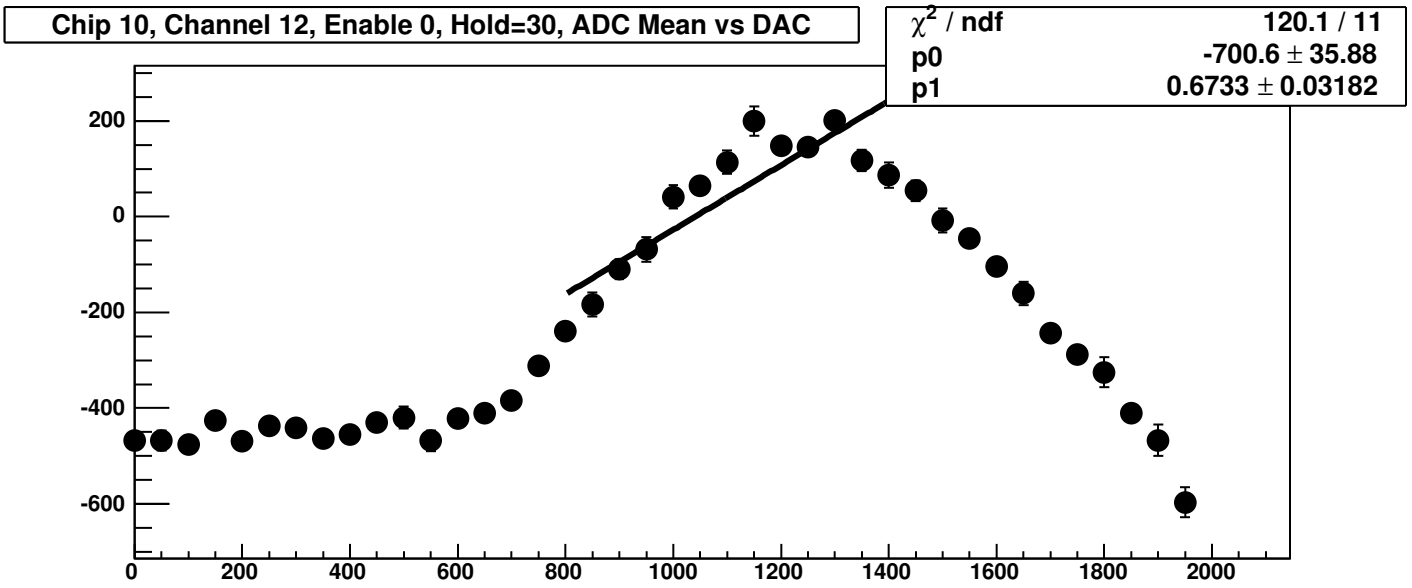
Chip 10, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC

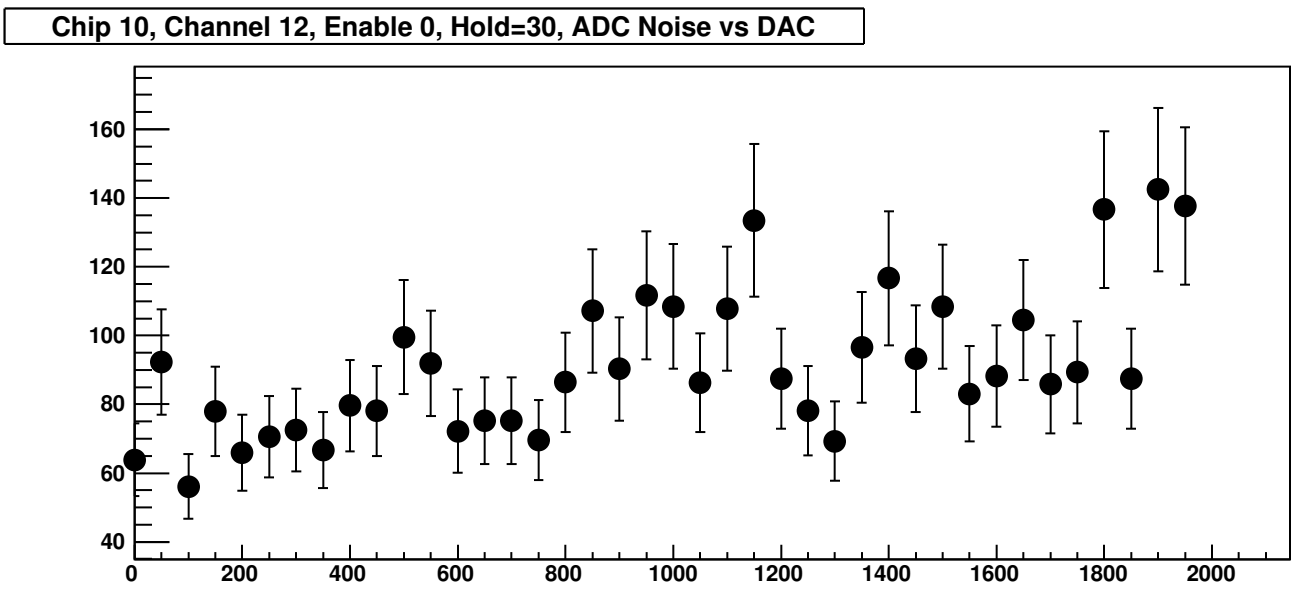


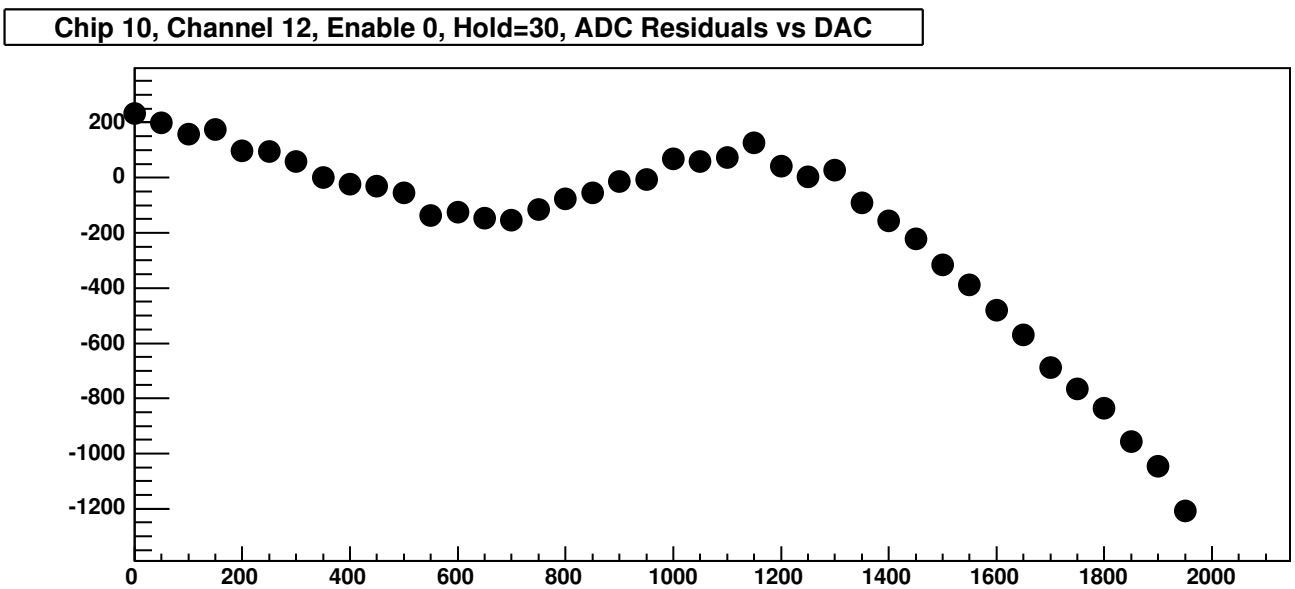

Chip 10, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7.033 / 11
p0 -1299 ± 23.97
p1 0.4161 ± 0.02226
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

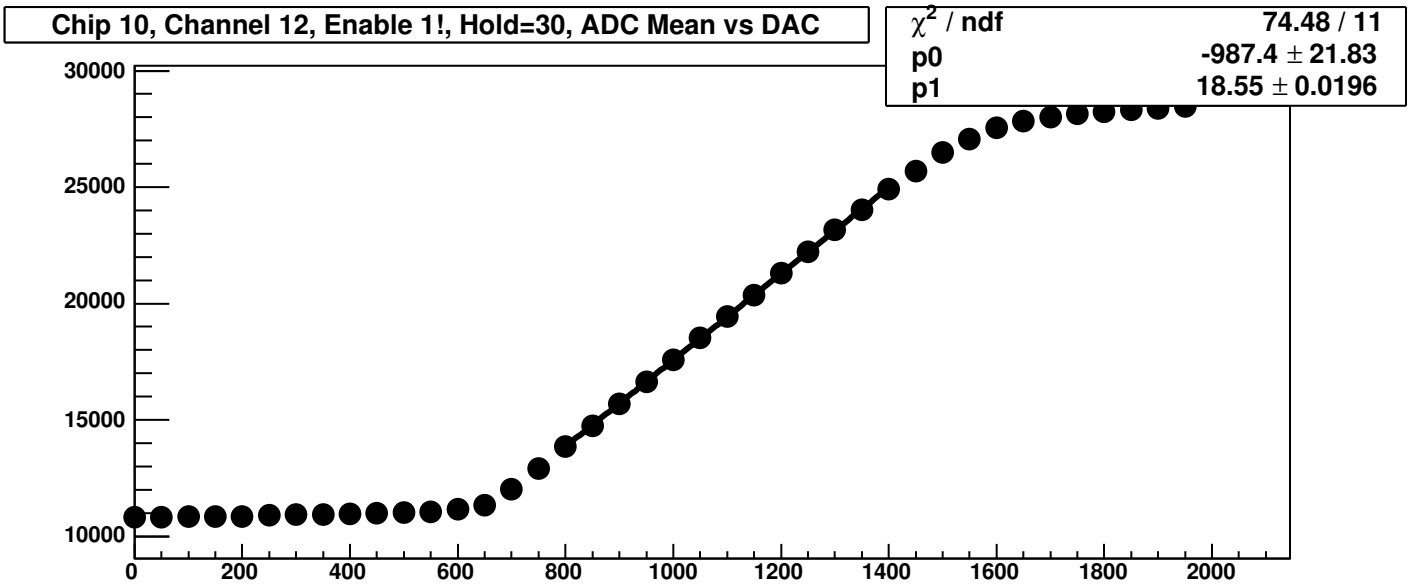

Chip 10, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

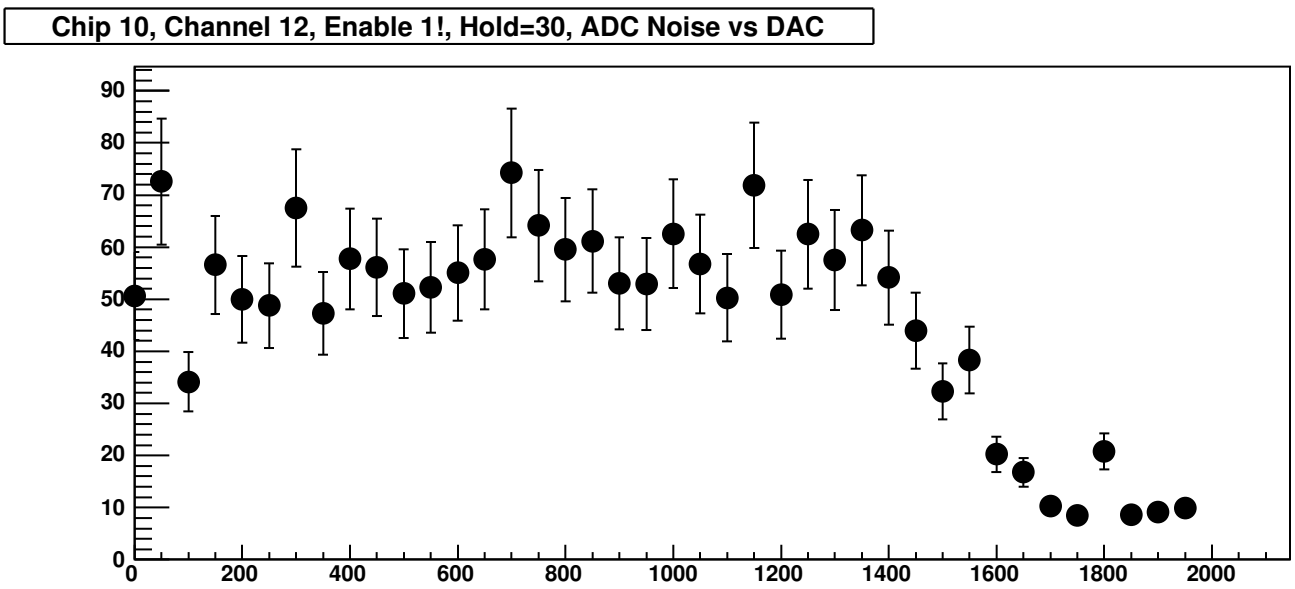

Chip 10, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

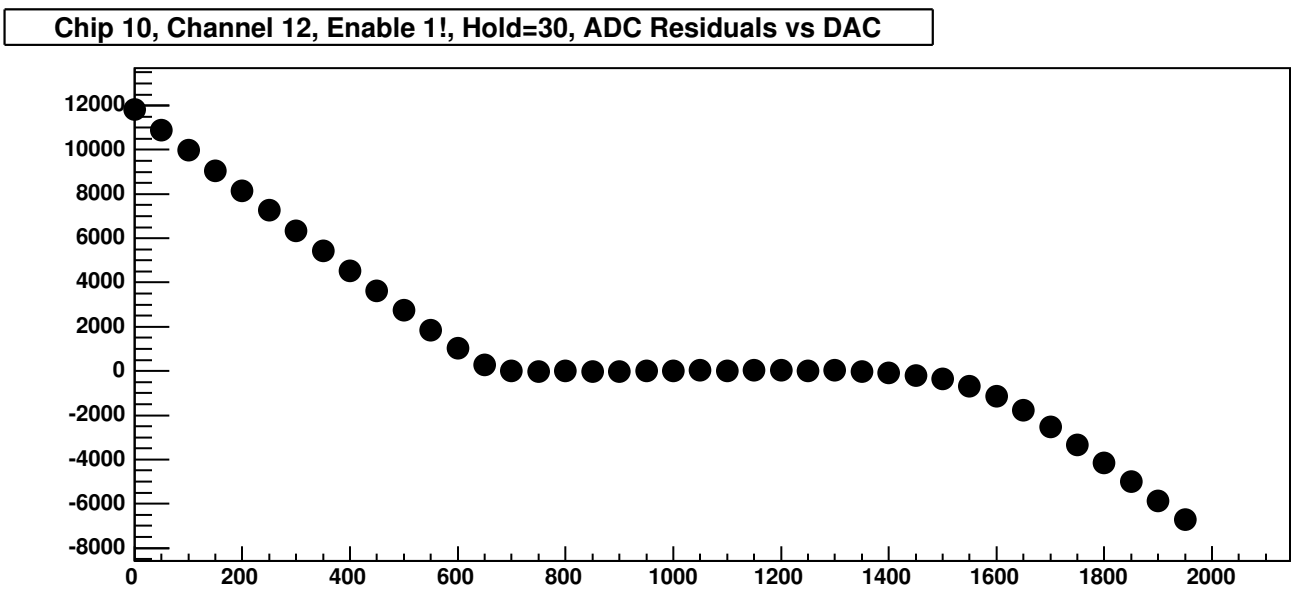

Chip 10, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 120.1 / 11
p0 -700.6 ± 35.88
p1 0.6733 ± 0.03182
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

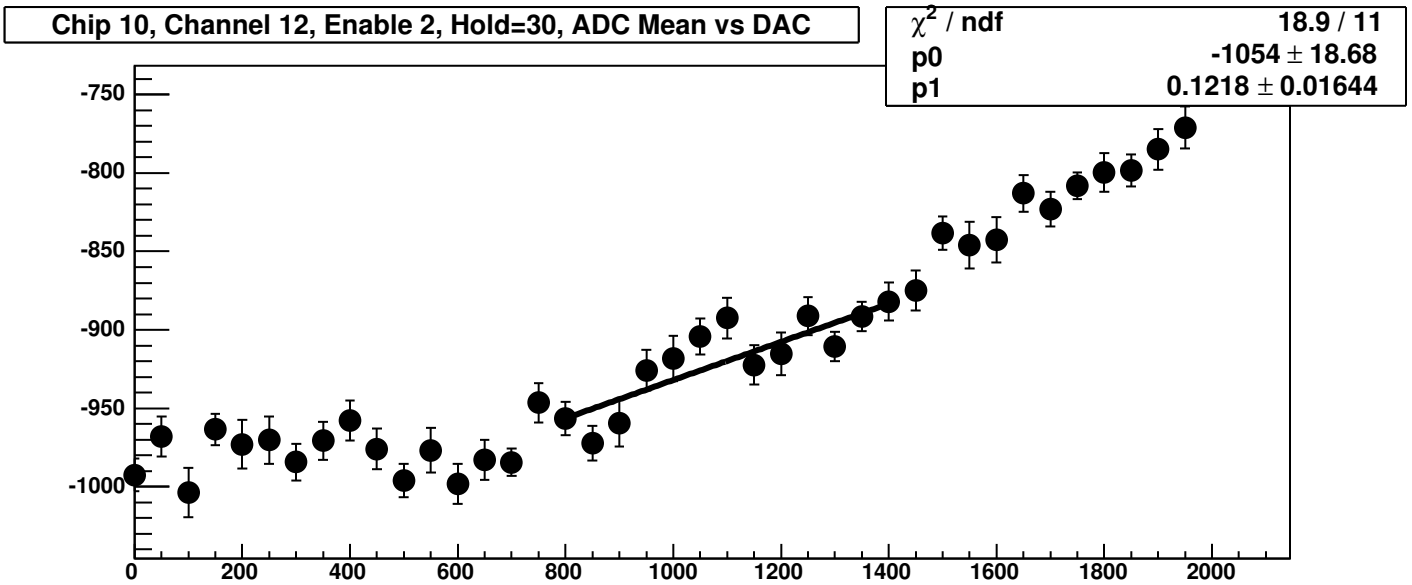

Chip 10, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

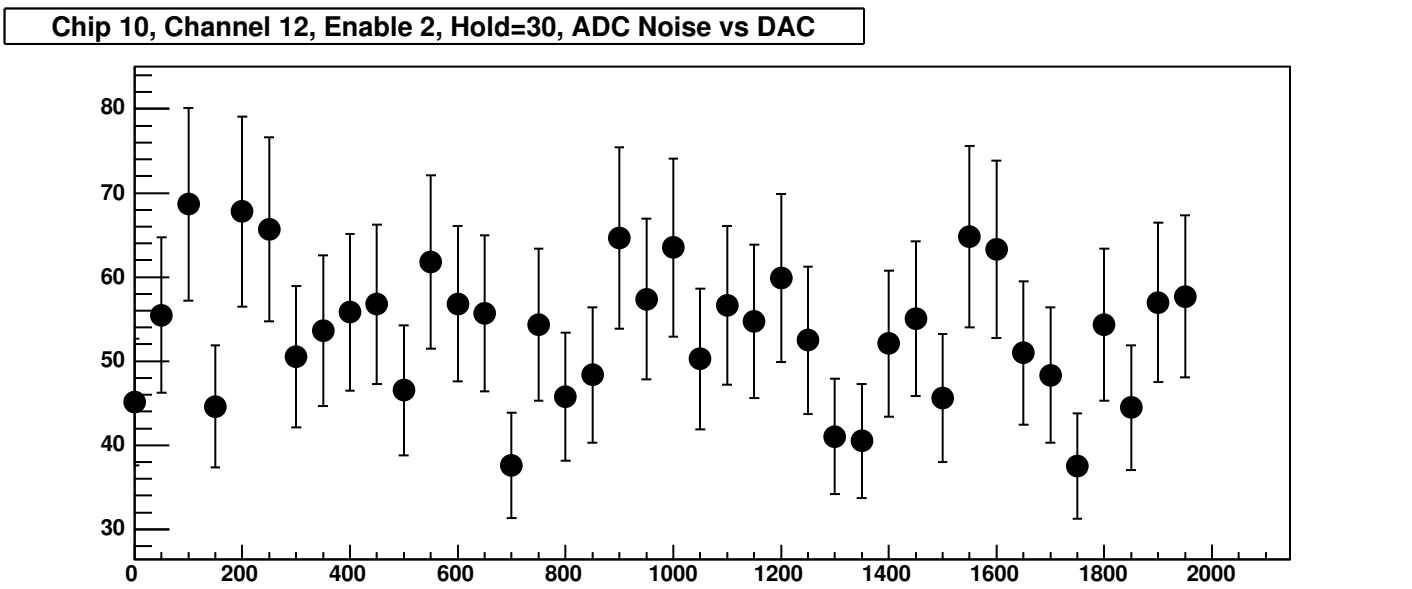

Chip 10, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

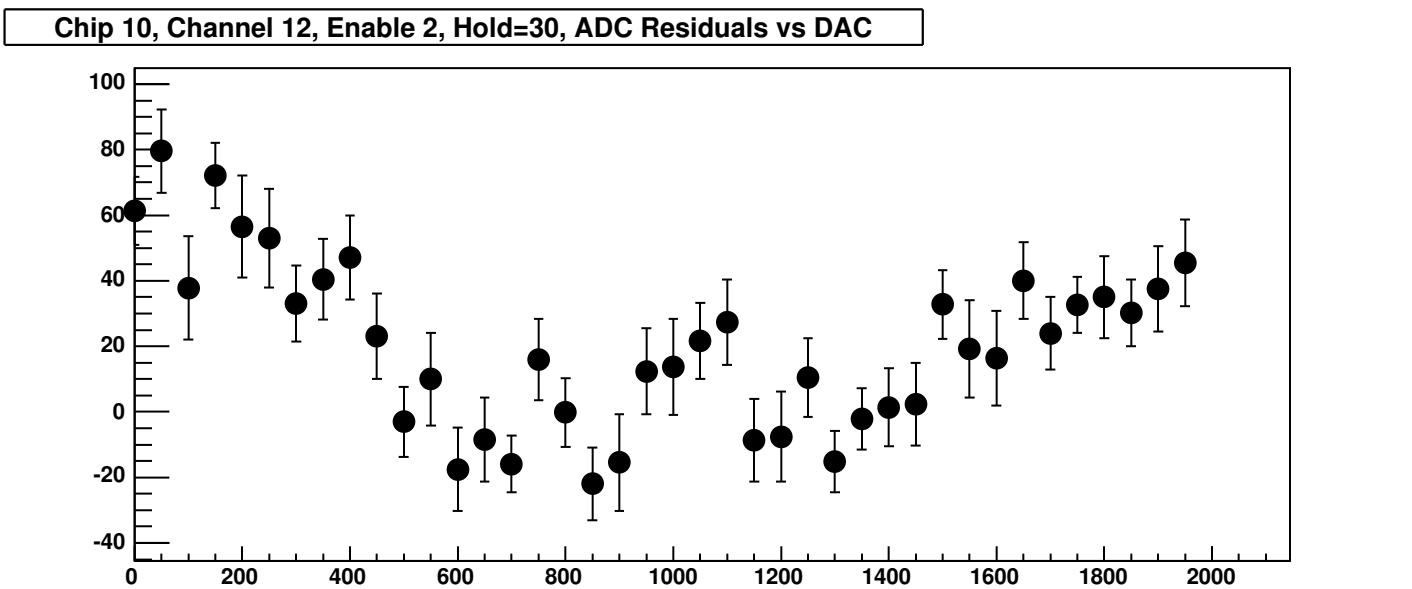

Chip 10, Channel 12, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 74.48 / 11
p0 -987.4 ± 21.83
p1 18.55 ± 0.0196
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 12, Enable 1!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

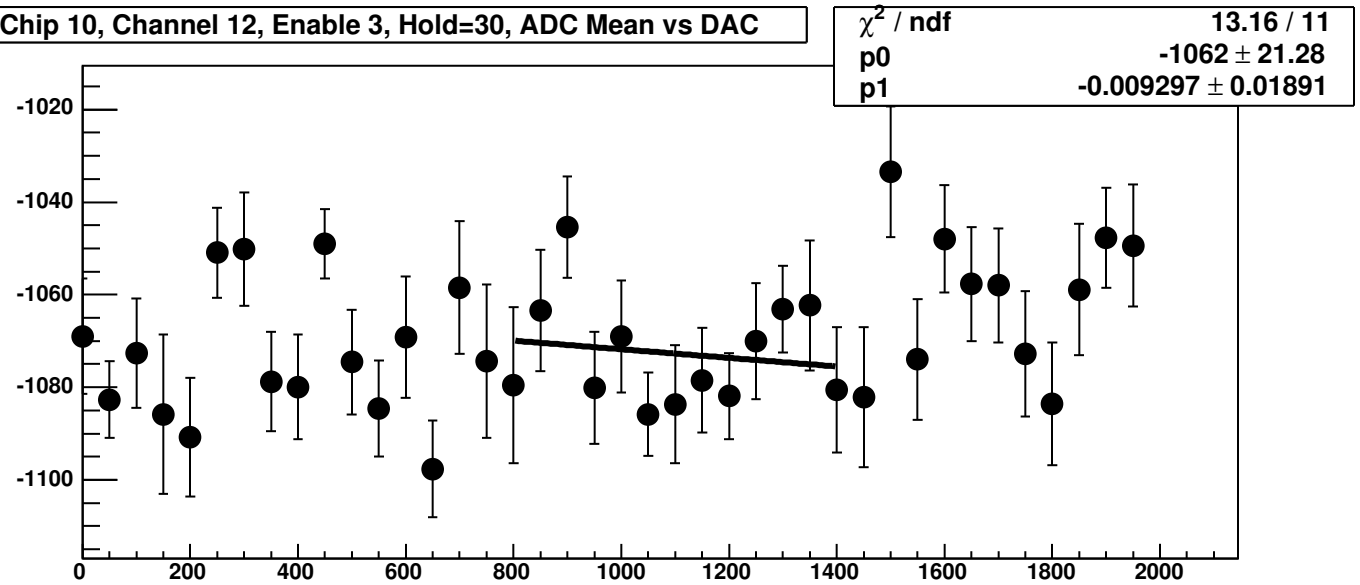

Chip 10, Channel 12, Enable 1!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 18.9 / 11
p0 -1054 ± 18.68
p1 0.1218 ± 0.01644
-750
-800
-850
-900
-950
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

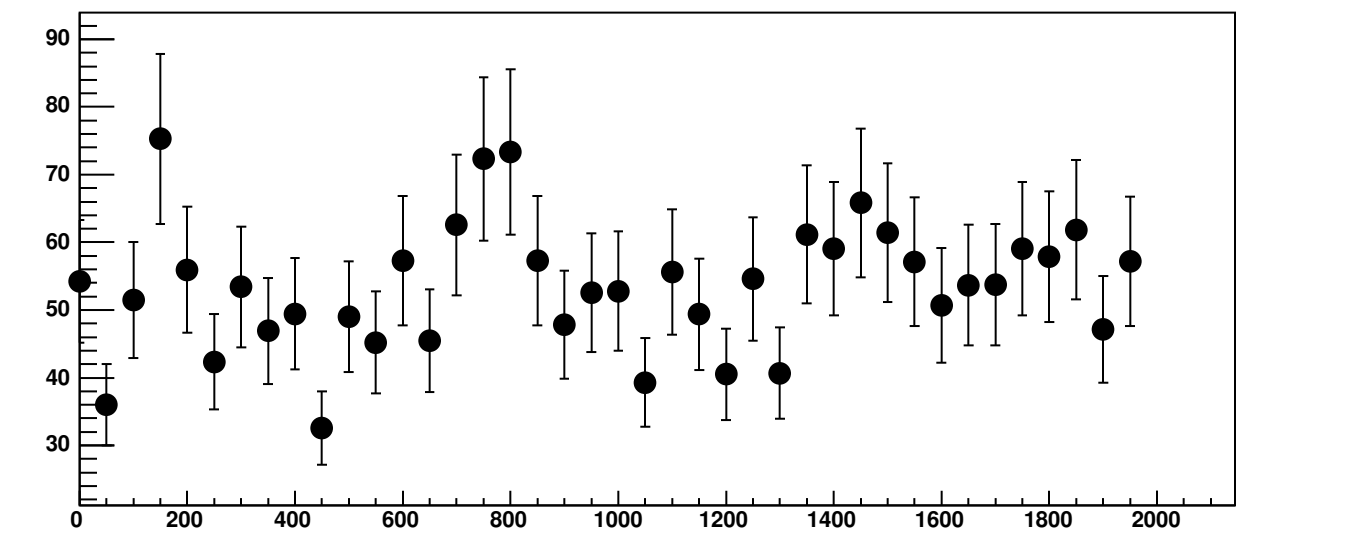

Chip 10, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

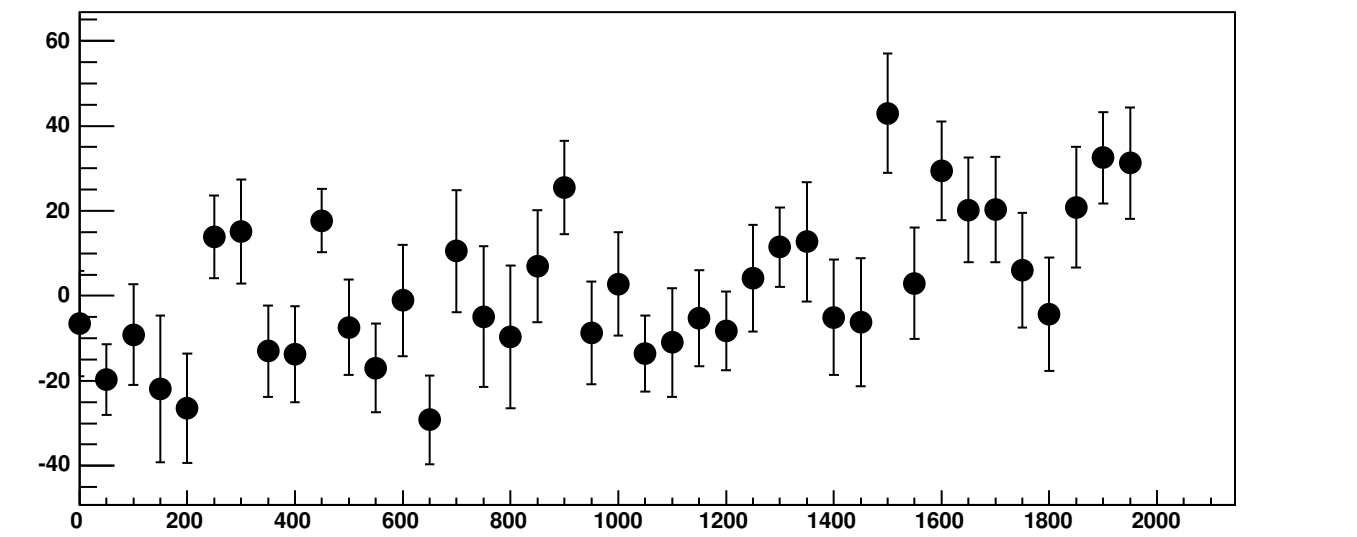
Chip 10, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC

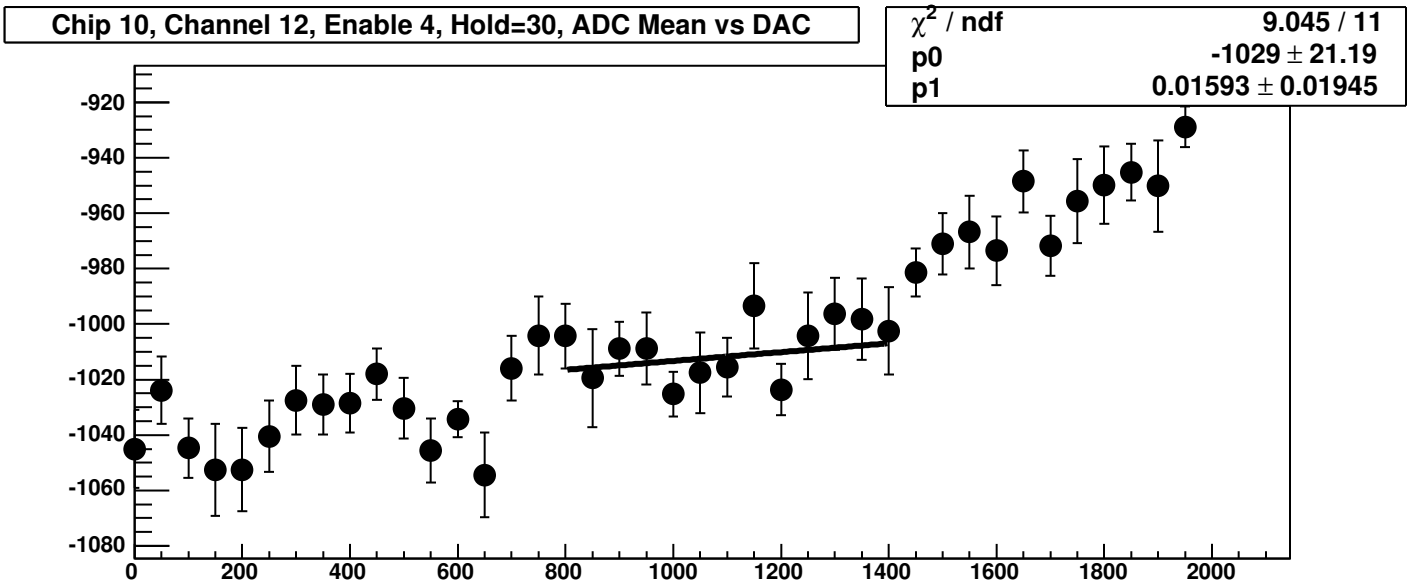


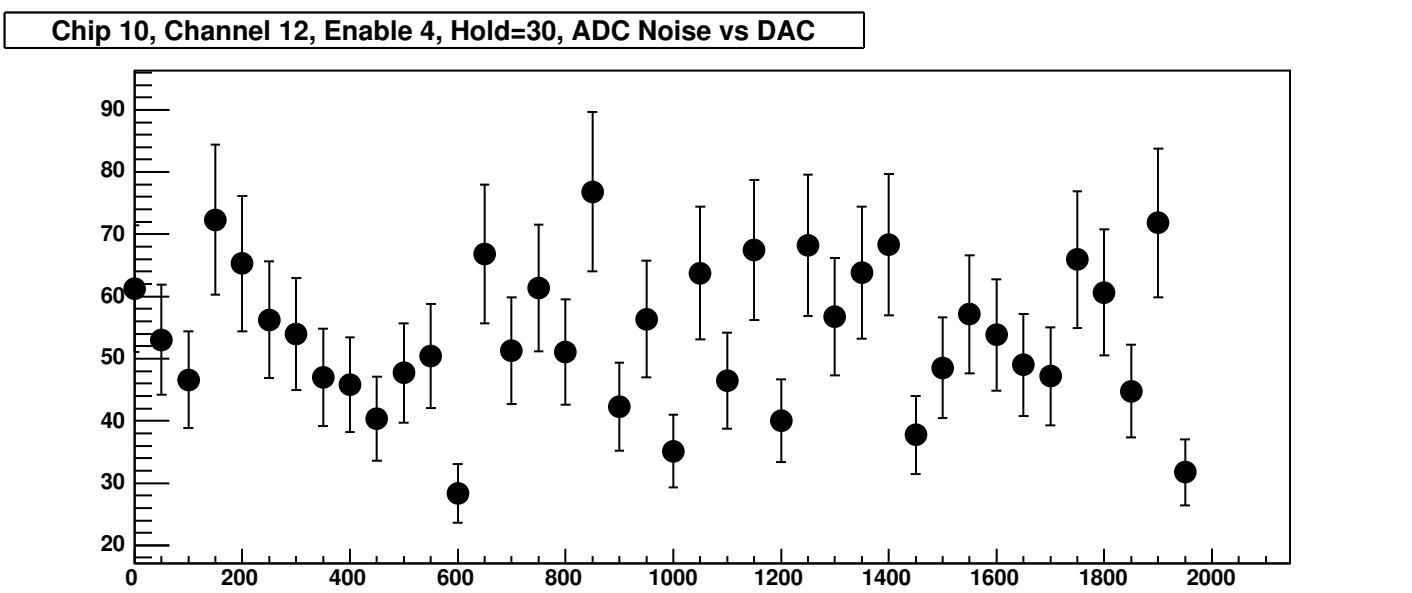
Chip 10, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC

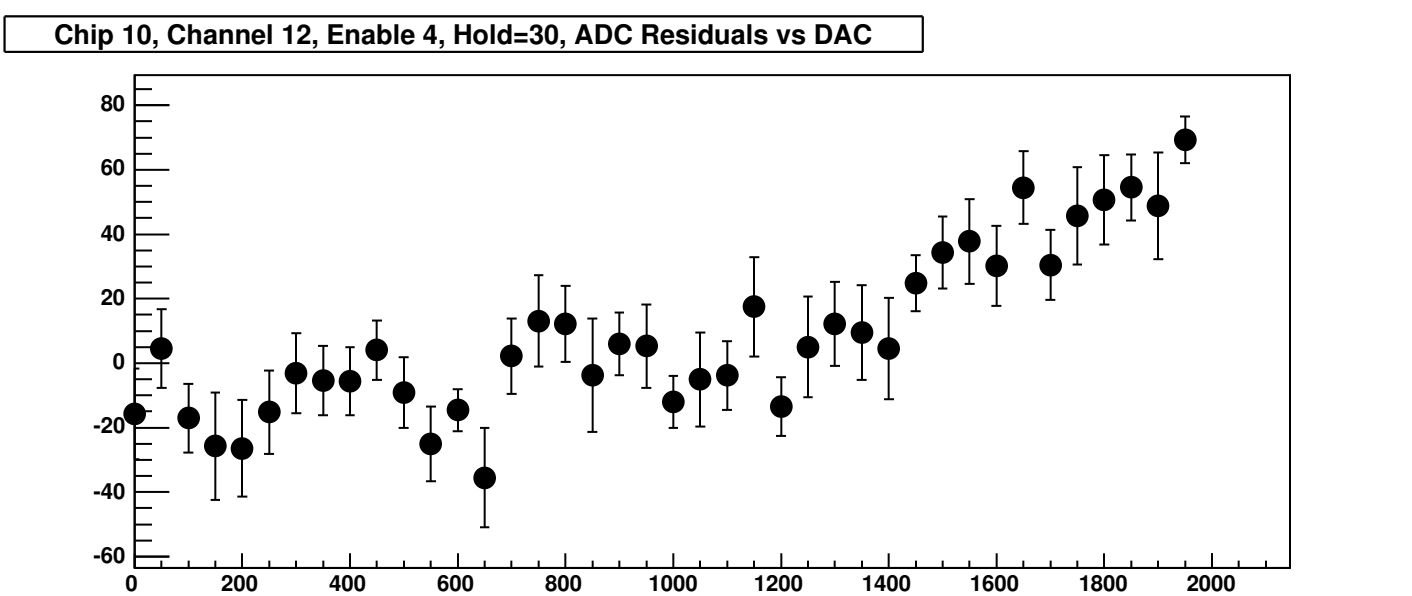


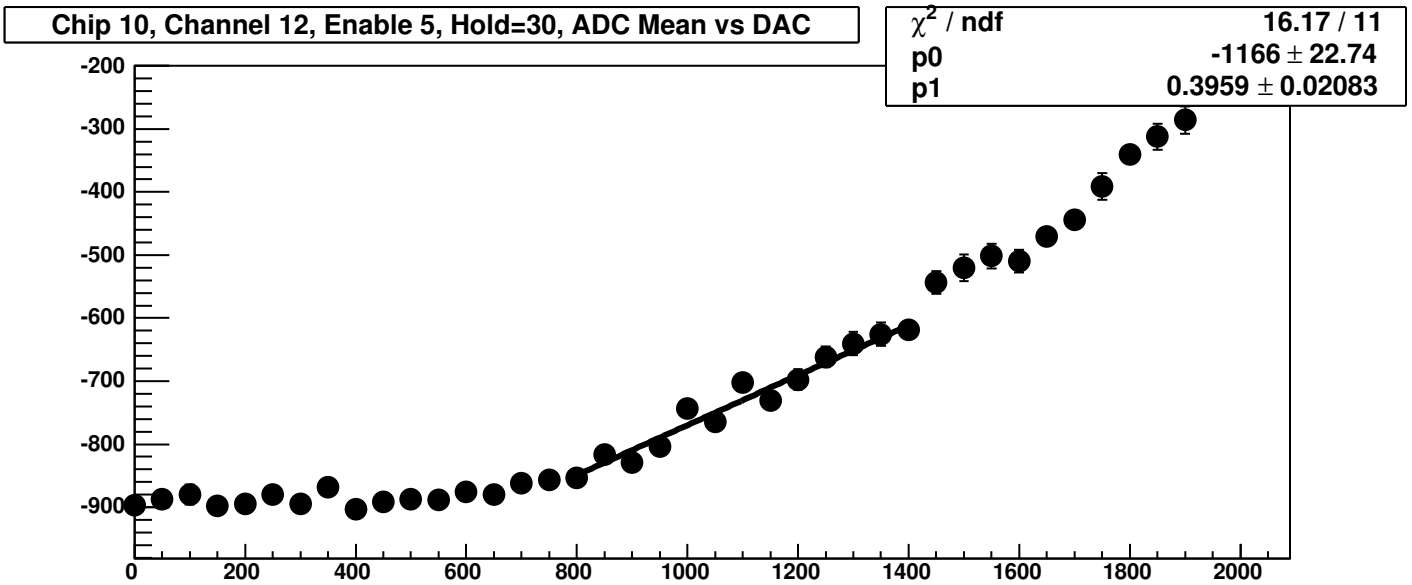
Chip 10, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC

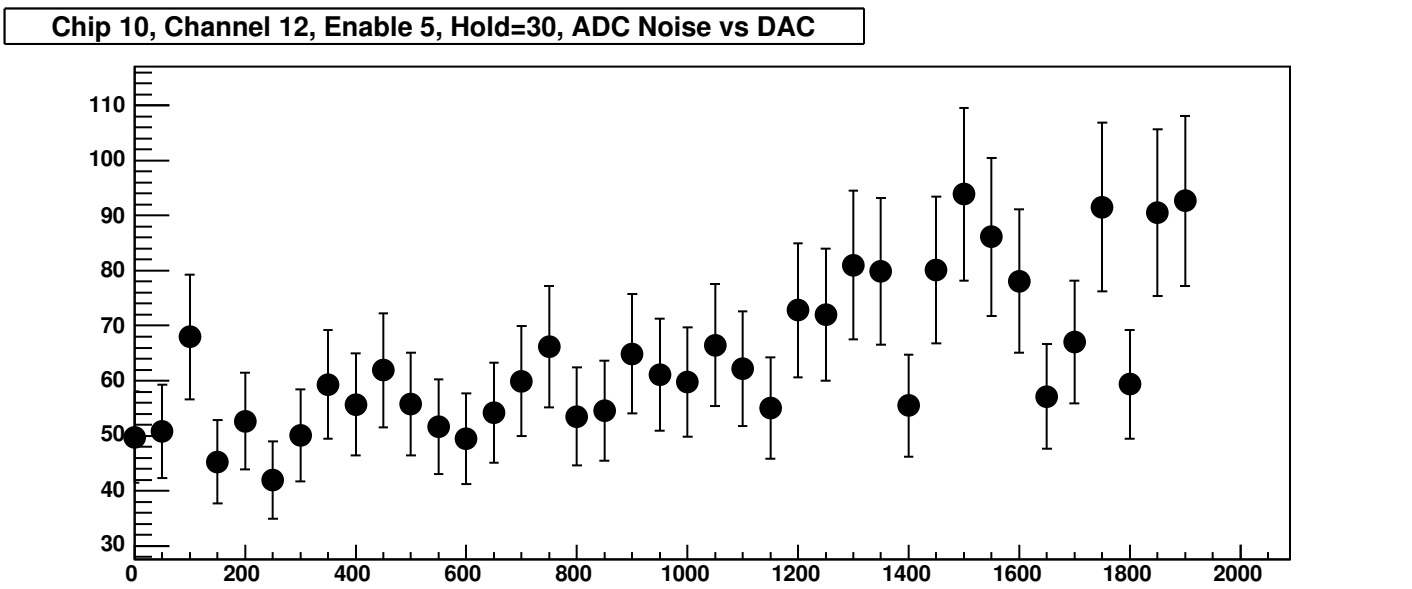


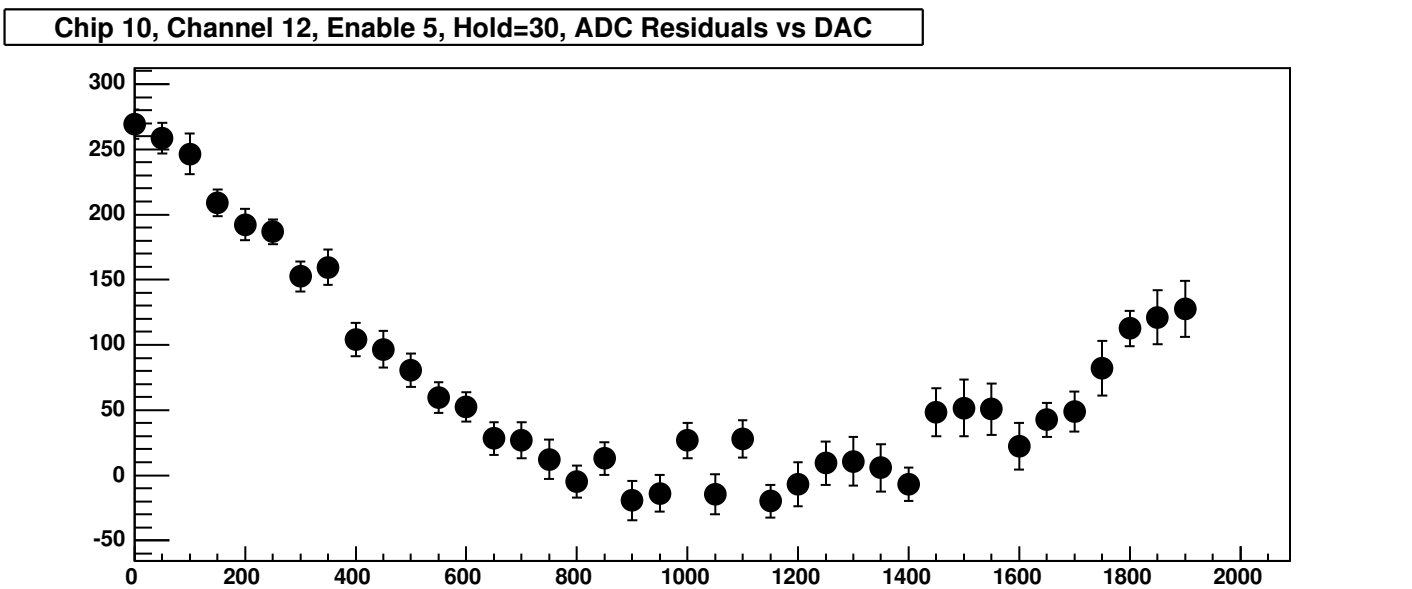

Chip 10, Channel 12, Enable 4, Hold=30, ADC Mean vs DAC
χ^2 / ndf 9.045 / 11
p0 -1029 ± 21.19
p1 0.01593 ± 0.01945
-920
-940
-960
-980
-1000
-1020
-1040
-1060
-1080
0
200
400
600
800
1000
1200
1400
1600
1800
2000

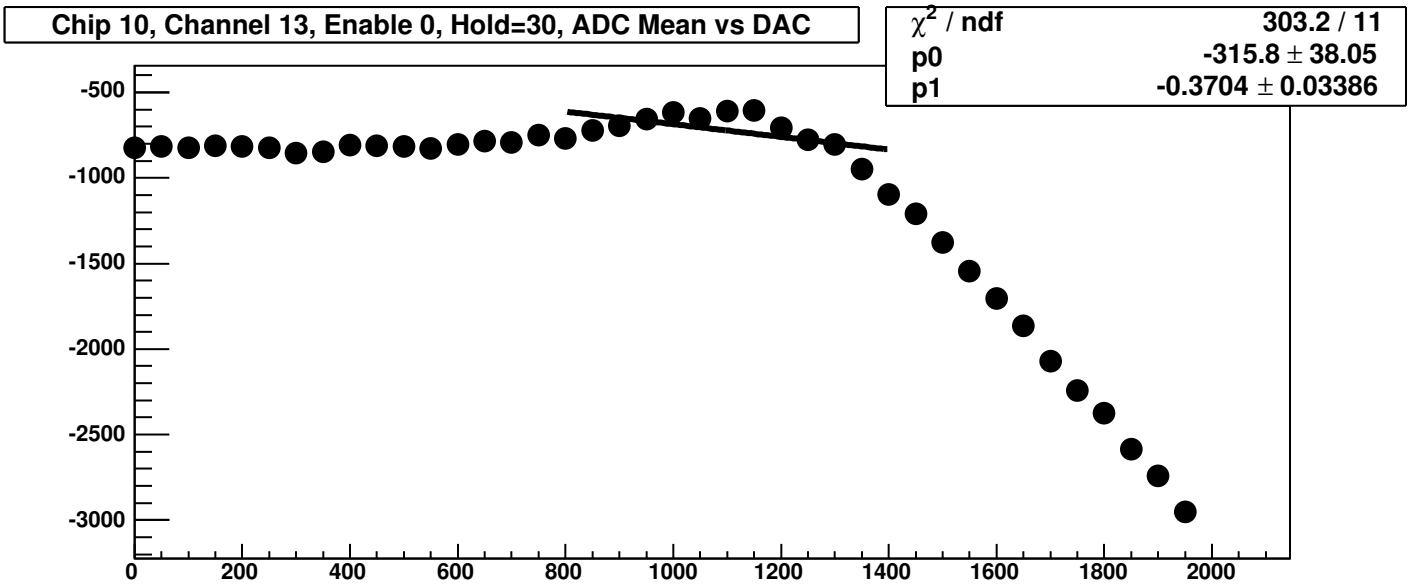

Chip 10, Channel 12, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

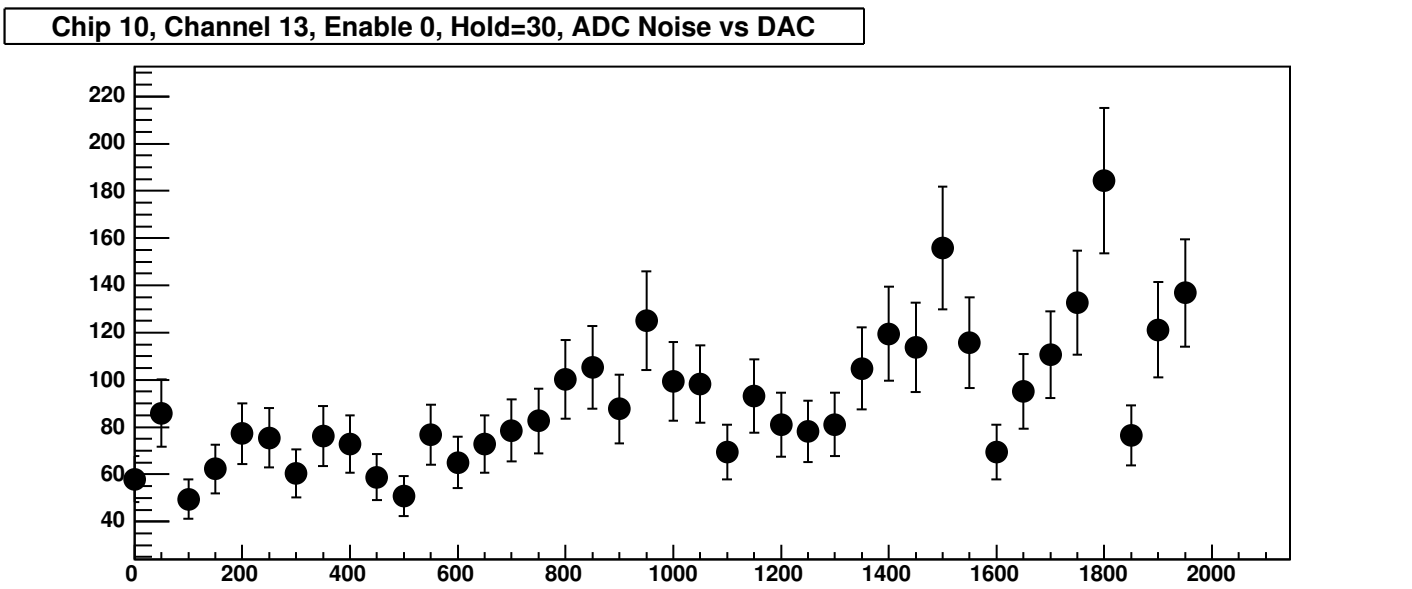

Chip 10, Channel 12, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

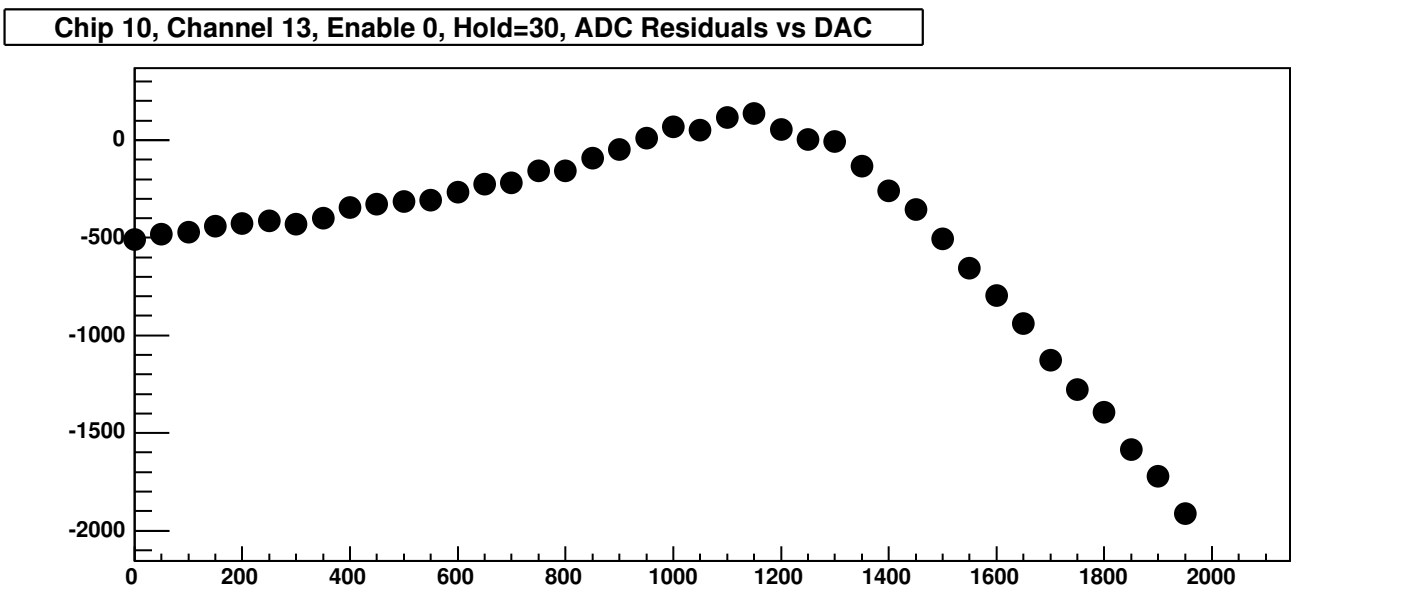

Chip 10, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 16.17 / 11
p0 -1166 ± 22.74
p1 0.3959 ± 0.02083
-200
-300
-400
-500
-600
-700
-800
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

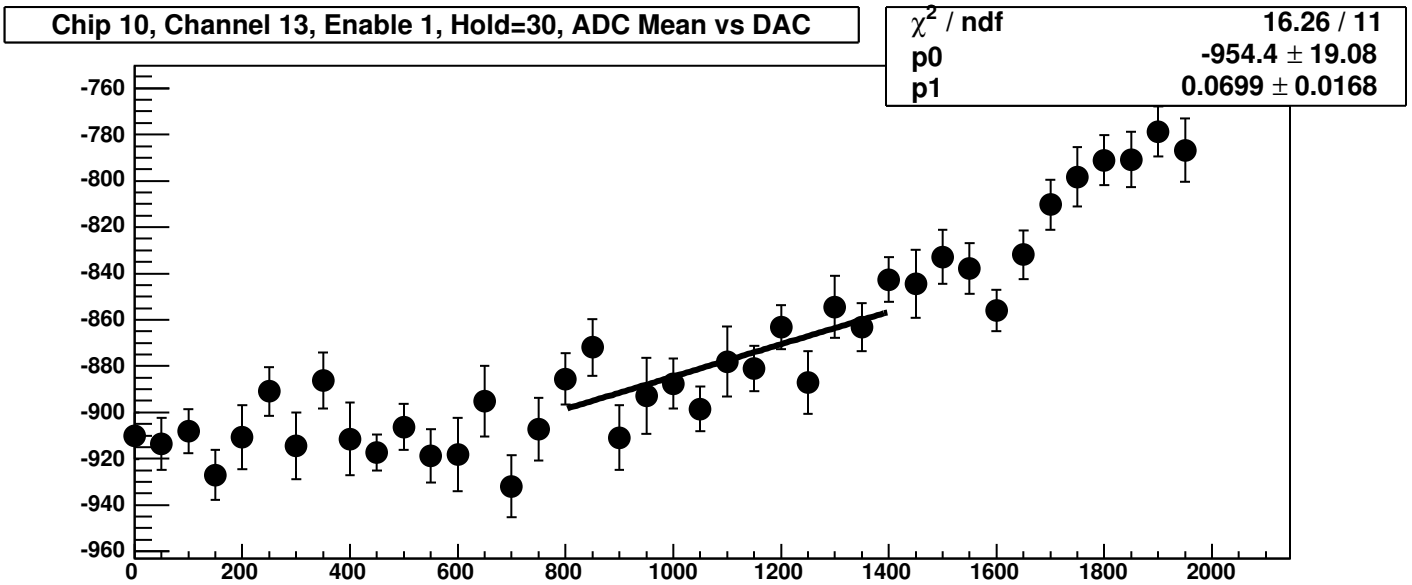

Chip 10, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

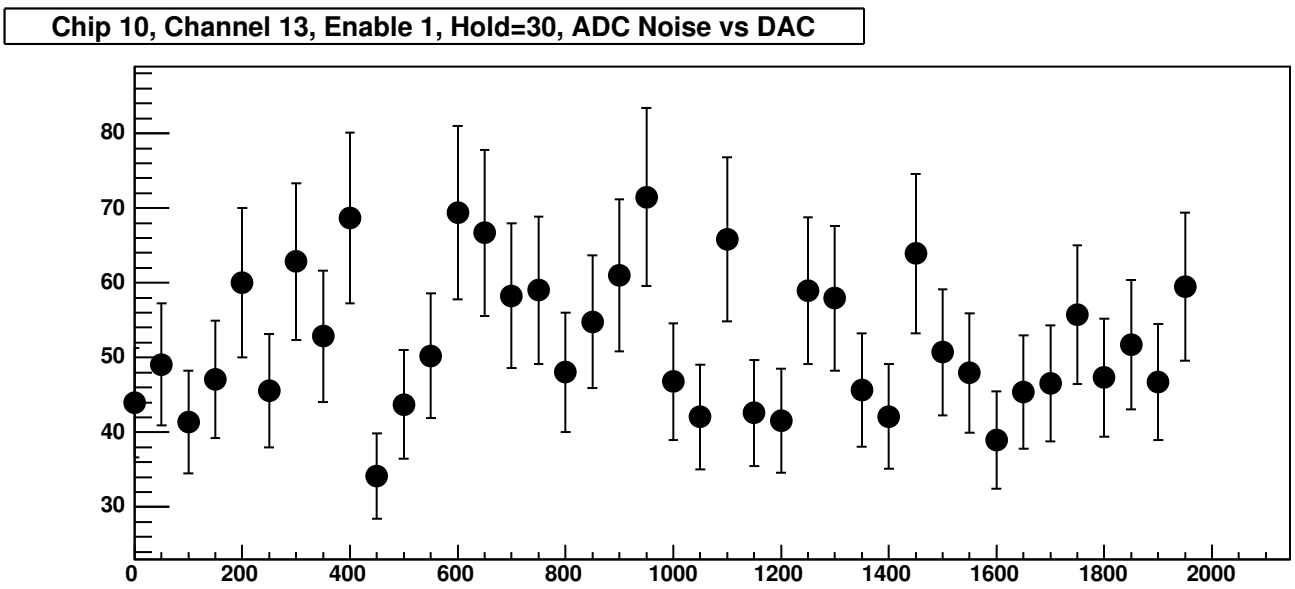

Chip 10, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

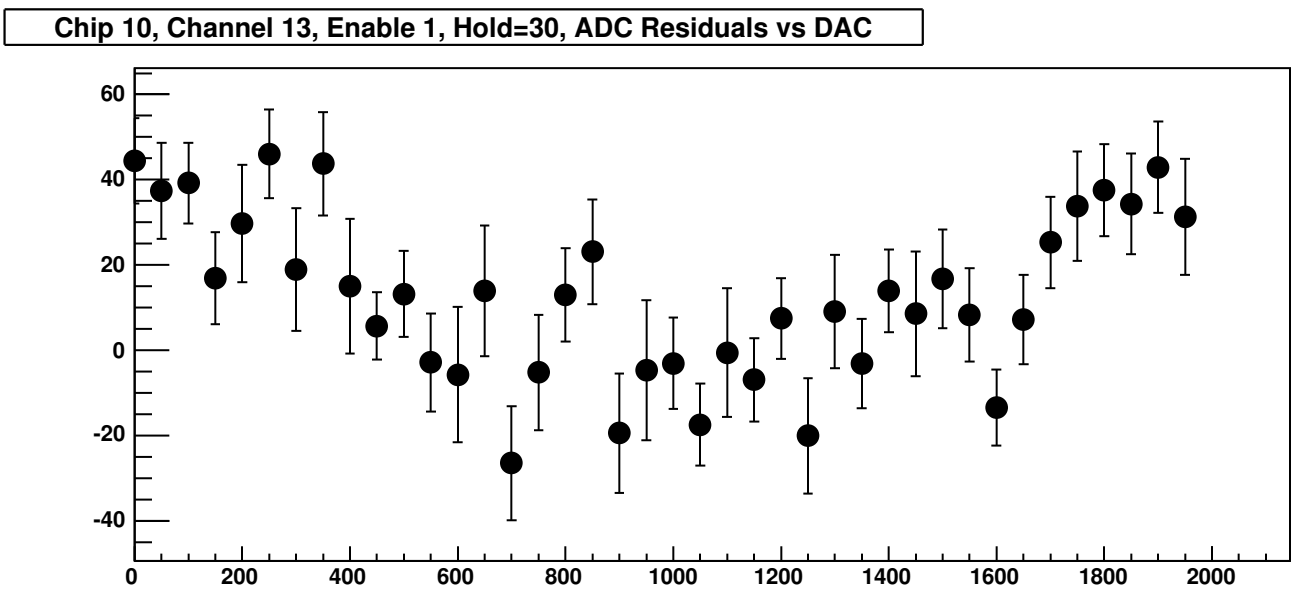

Chip 10, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 303.2 / 11
p0 -315.8 ± 38.05
p1 -0.3704 ± 0.03386
-500
-1000
-1500
-2000
-2500
-3000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

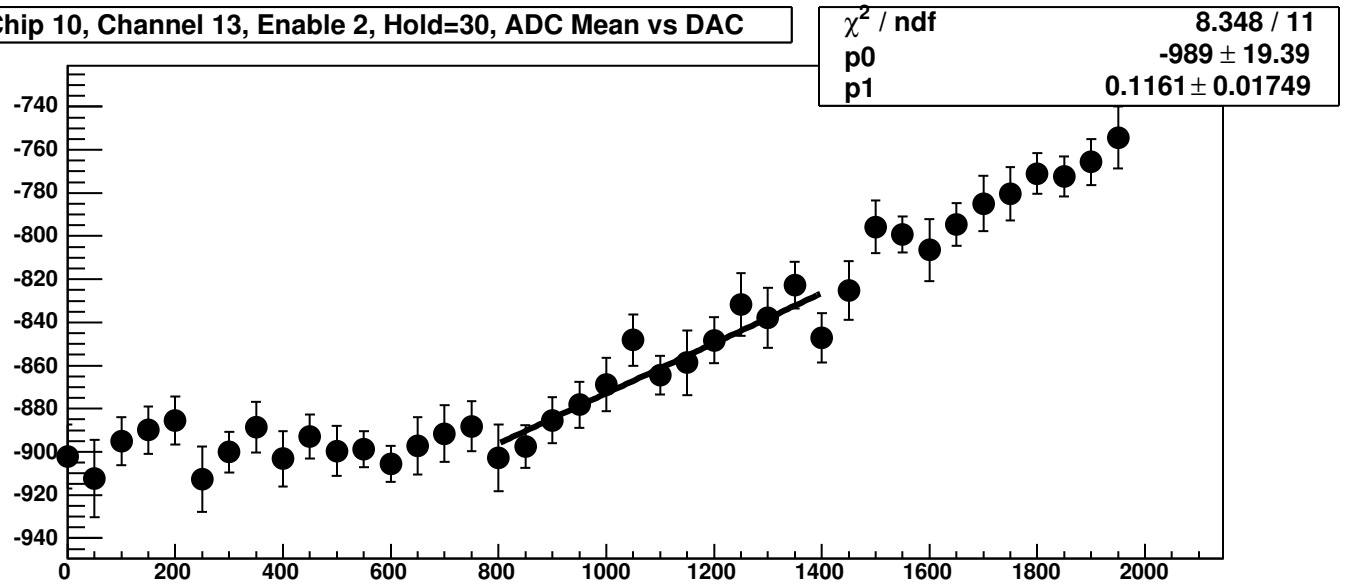

Chip 10, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 13, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 16.26 / 11
p0 -954.4 ± 19.08
p1 0.0699 ± 0.0168
-760
-780
-800
-820
-840
-860
-880
-900
-920
-940
-960
0
200
400
600
800
1000
1200
1400
1600
1800
2000

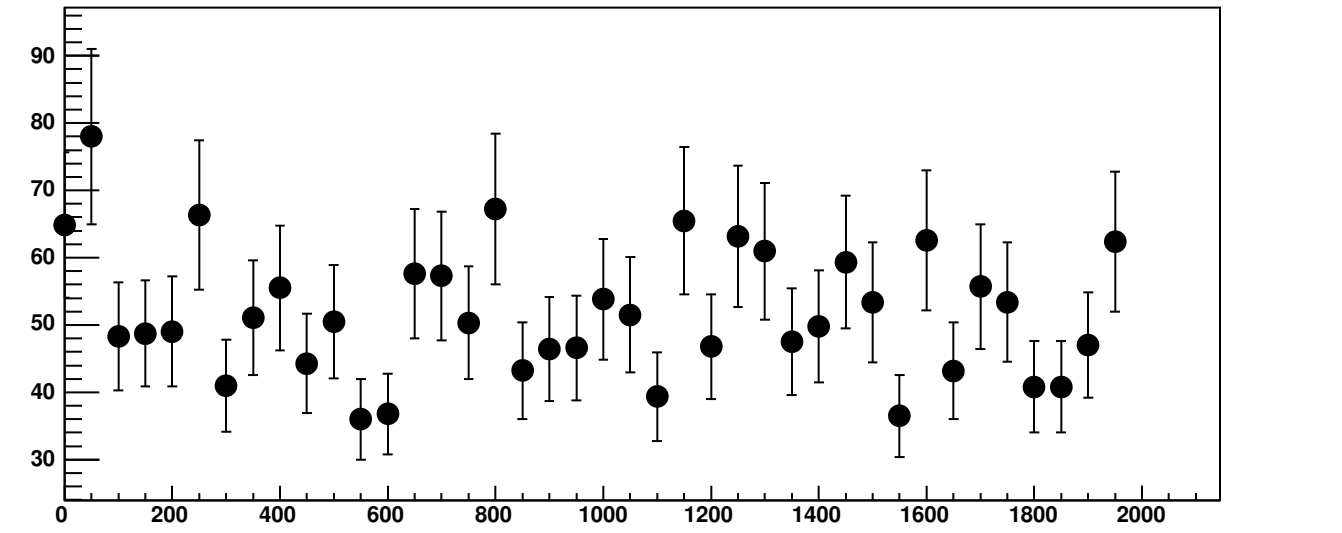

Chip 10, Channel 13, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 13, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

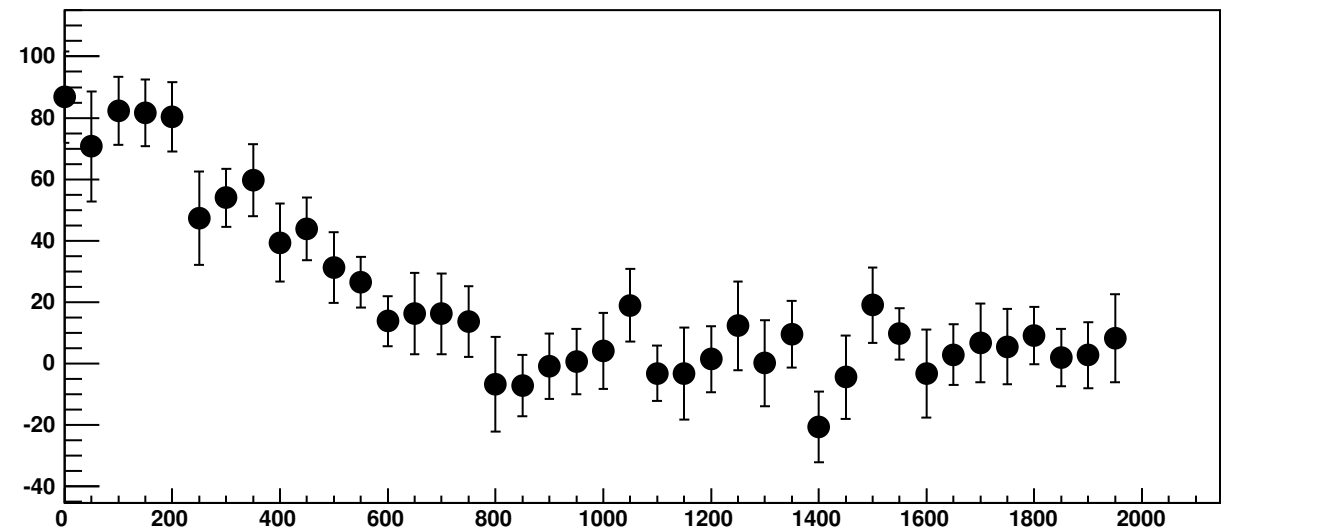
Chip 10, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC

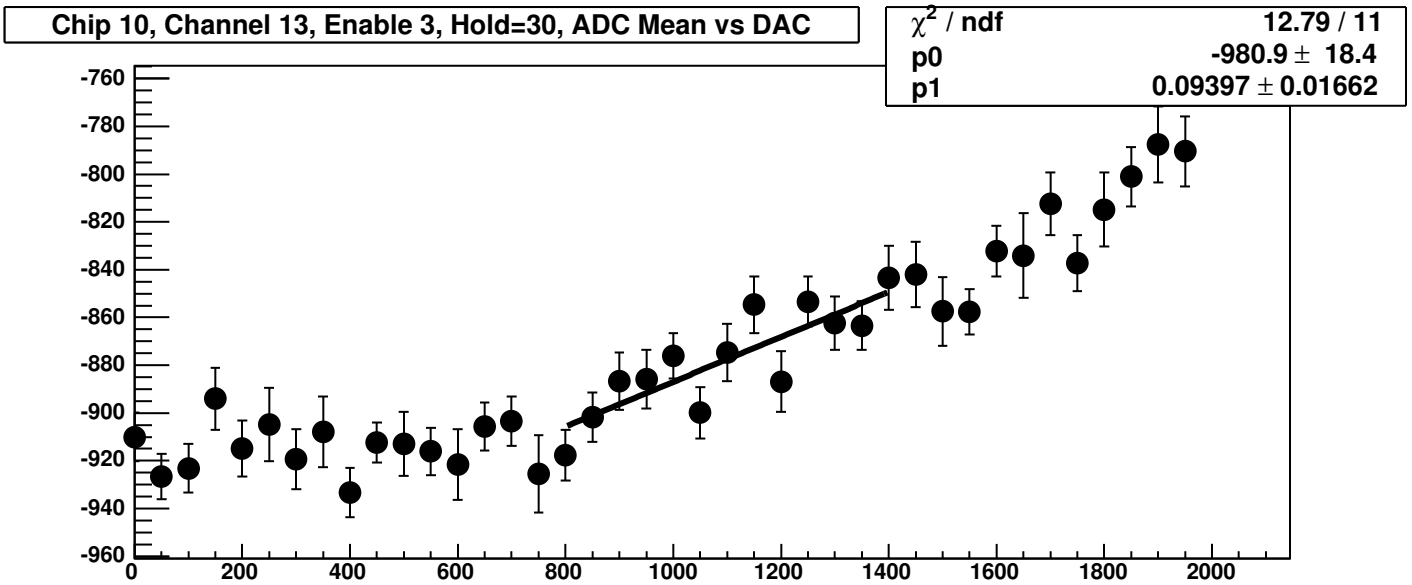


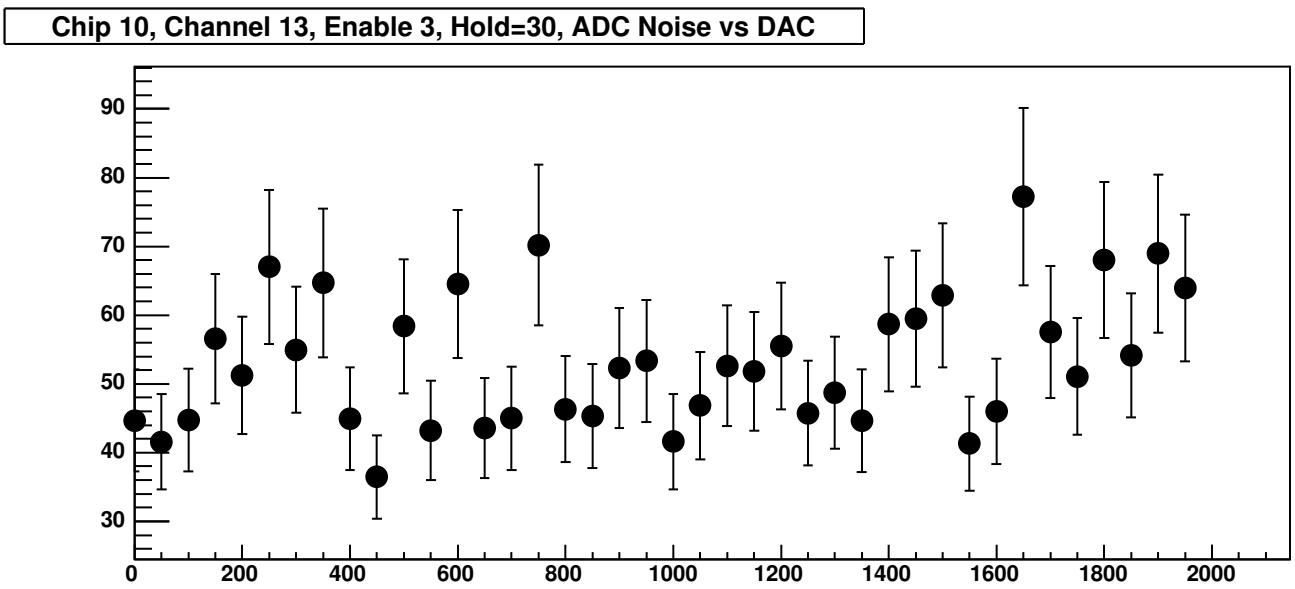
Chip 10, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC

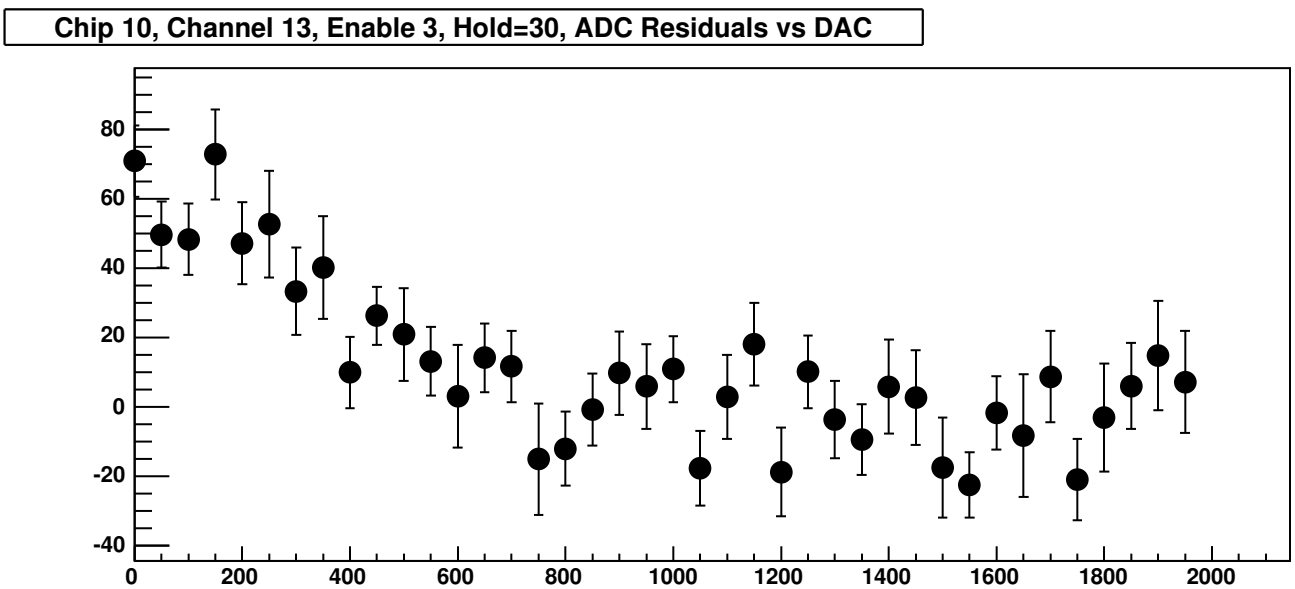


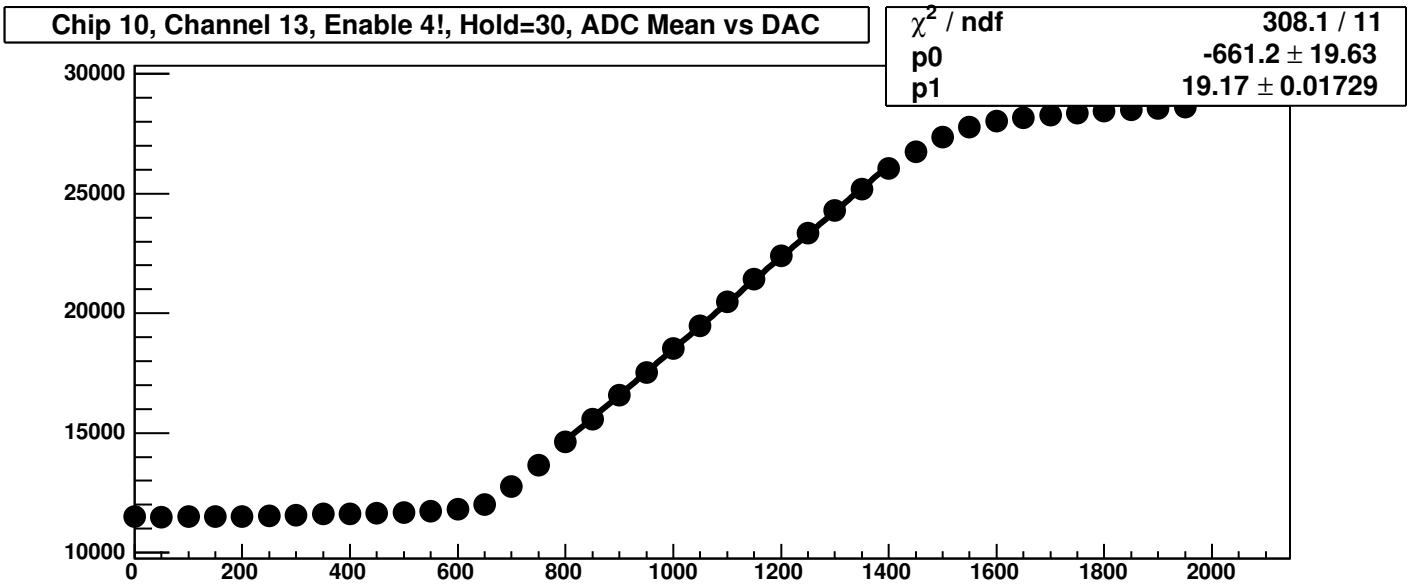
Chip 10, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC

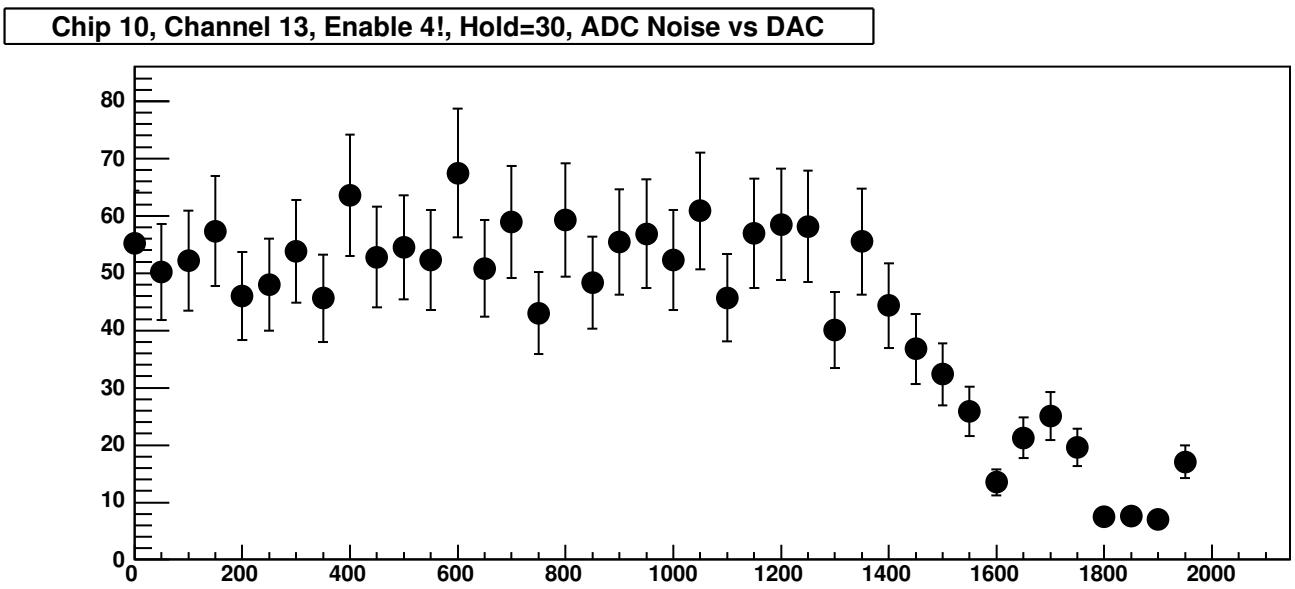


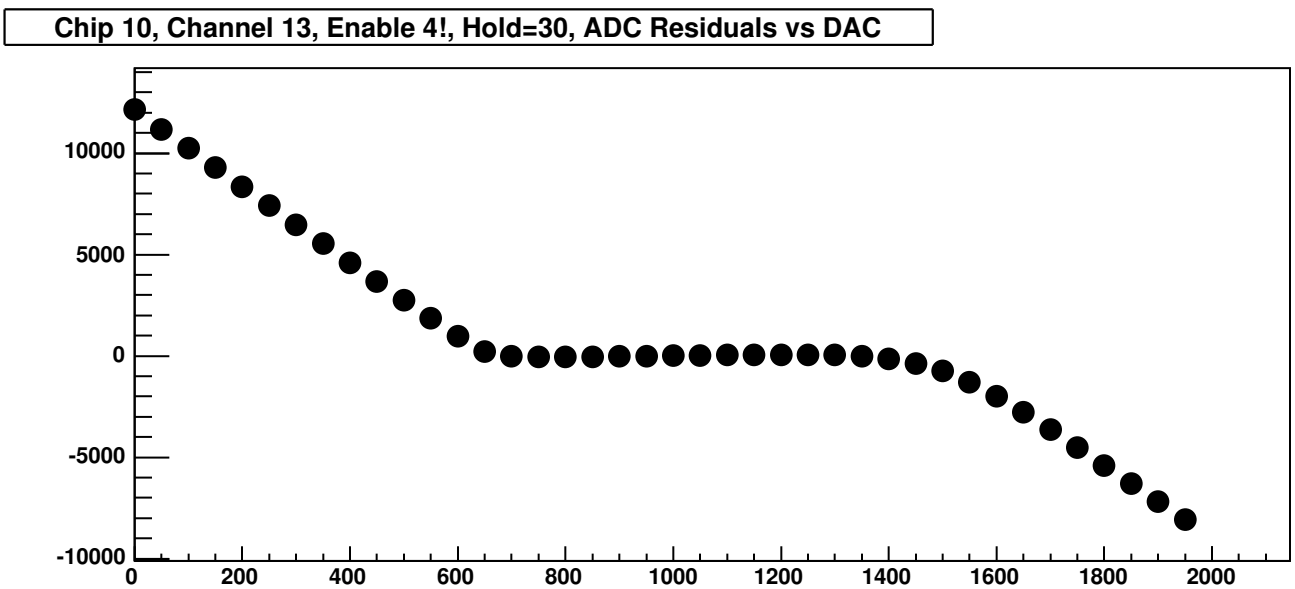

Chip 10, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 12.79 / 11
p0 -980.9 ± 18.4
p1 0.09397 ± 0.01662
-760
-780
-800
-820
-840
-860
-880
-900
-920
-940
-960
0
200
400
600
800
1000
1200
1400
1600
1800
2000

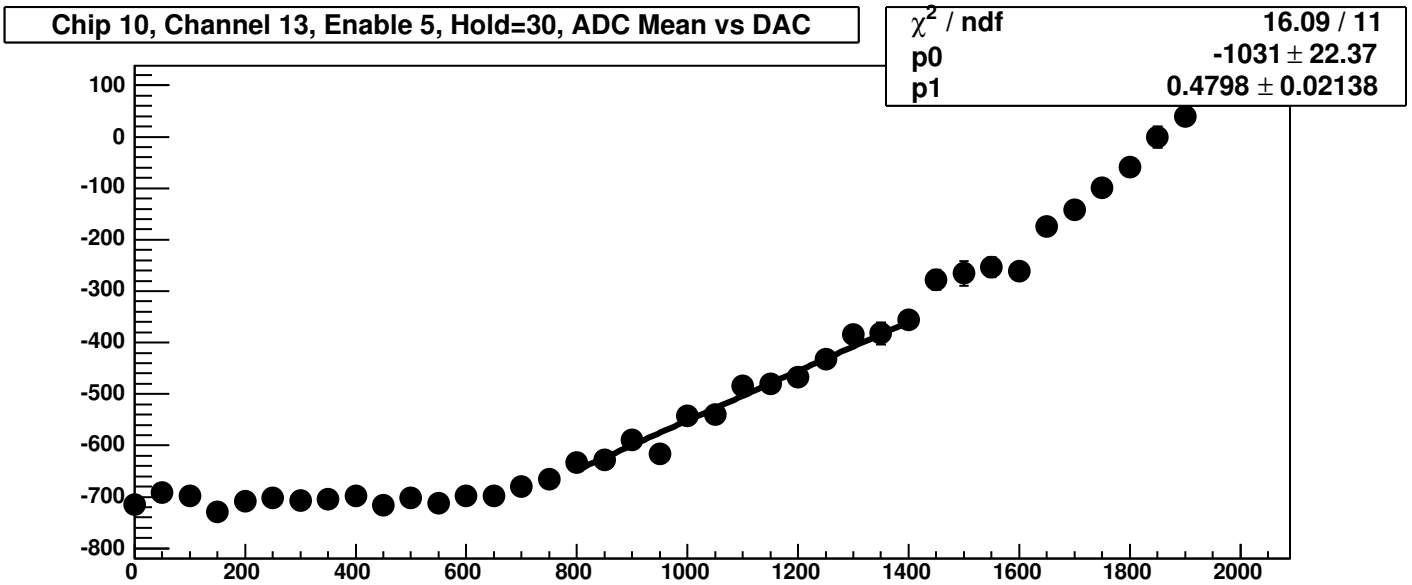

Chip 10, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

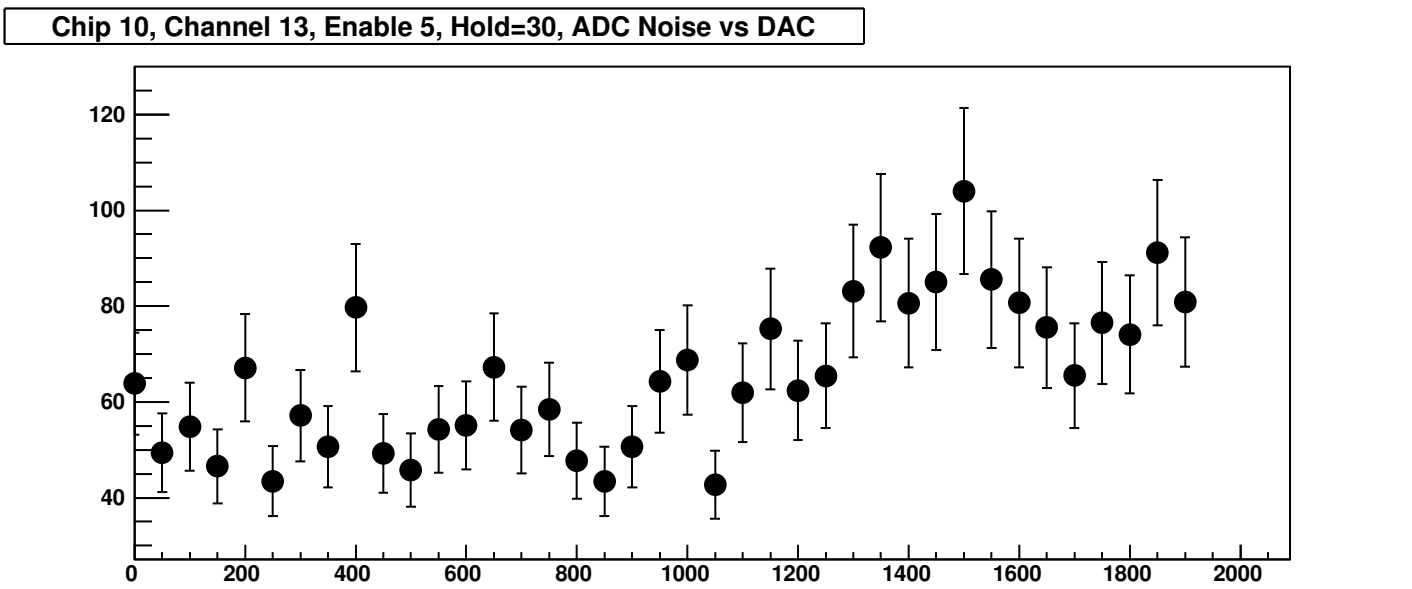

Chip 10, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

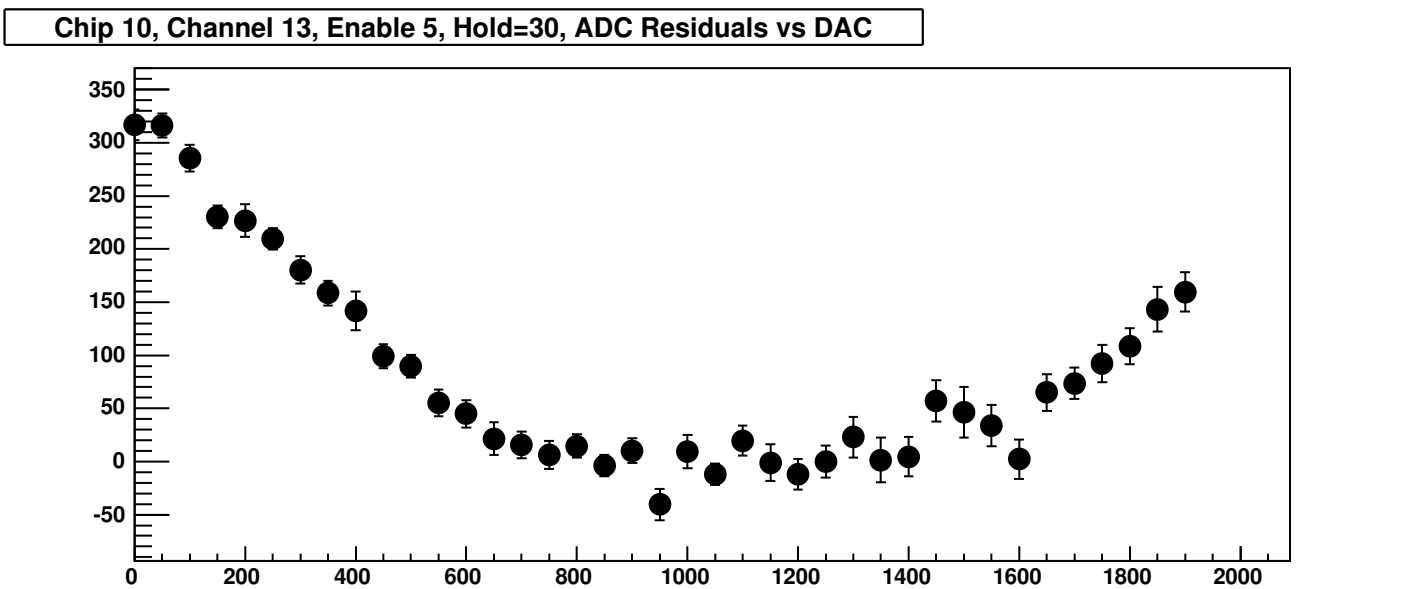

Chip 10, Channel 13, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 308.1 / 11
p0 -661.2 ± 19.63
p1 19.17 ± 0.01729
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 13, Enable 4!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

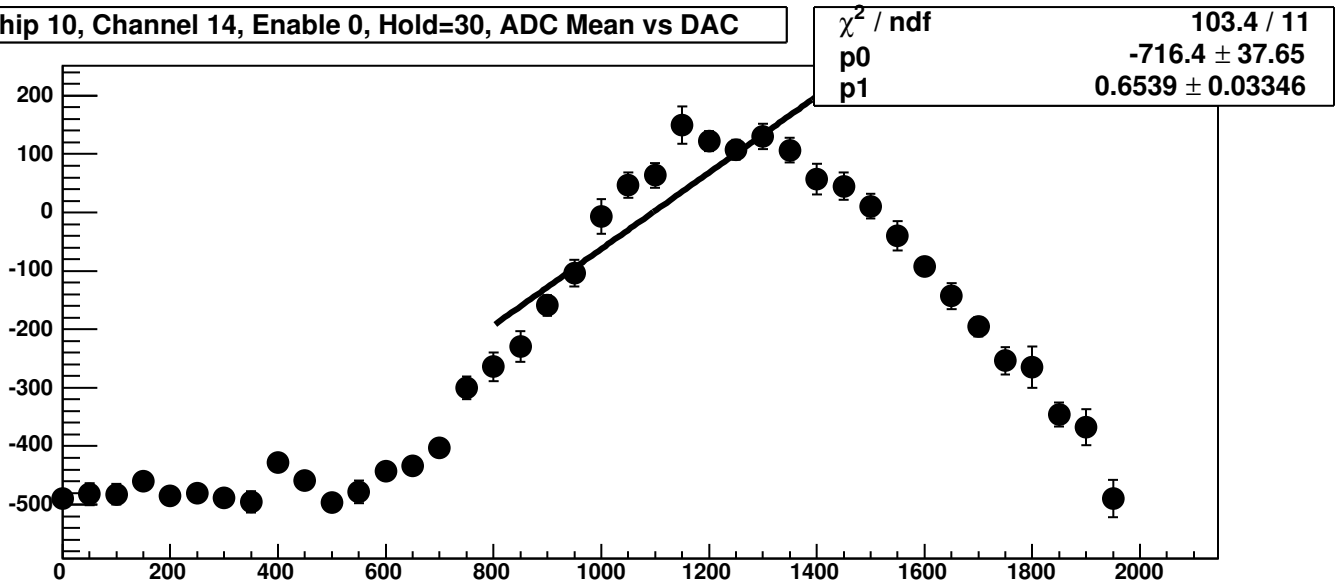

Chip 10, Channel 13, Enable 4!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 16.09 / 11
p0 -1031 ± 22.37
p1 0.4798 ± 0.02138
100
0
-100
-200
-300
-400
-500
-600
-700
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

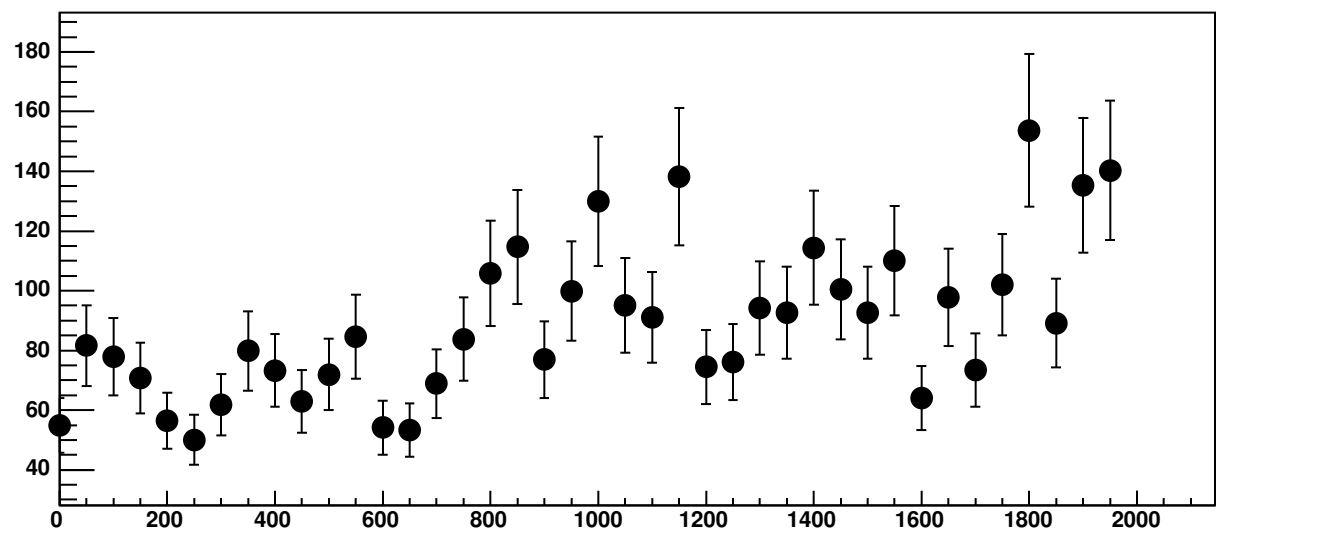

Chip 10, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

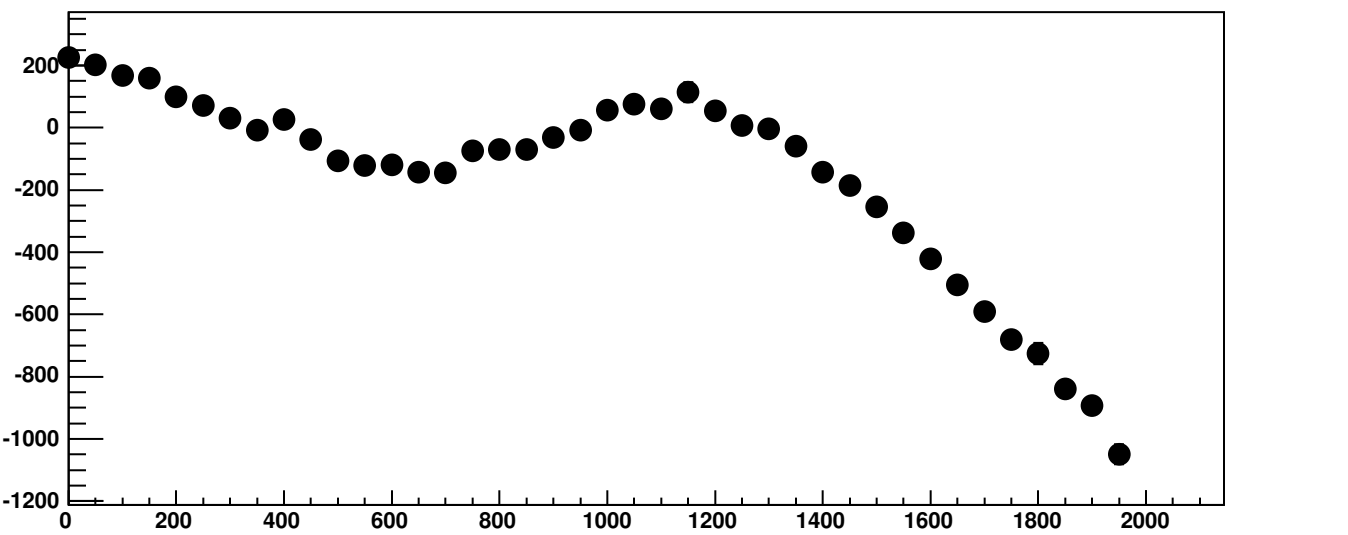
Chip 10, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC

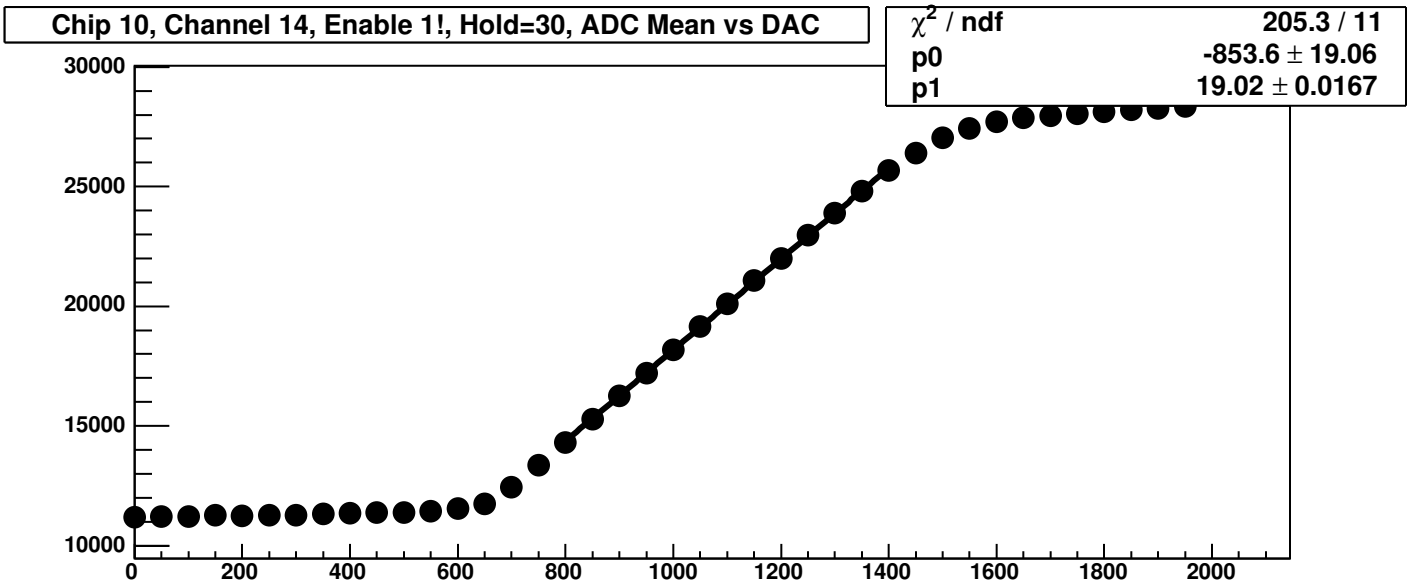


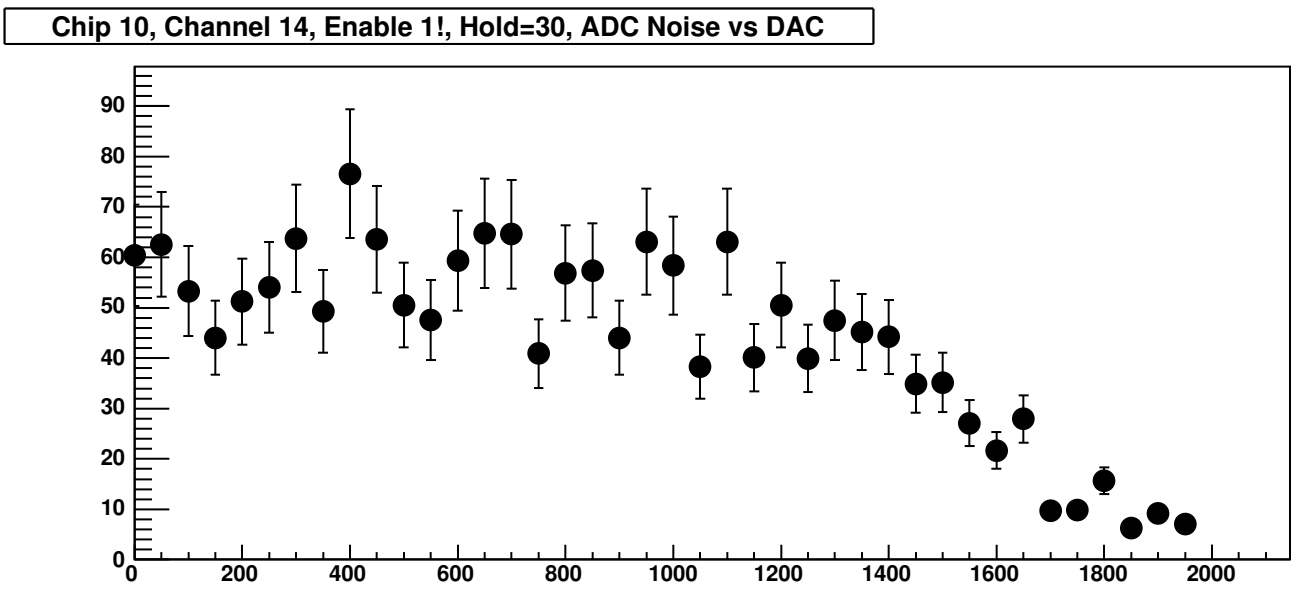
Chip 10, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC

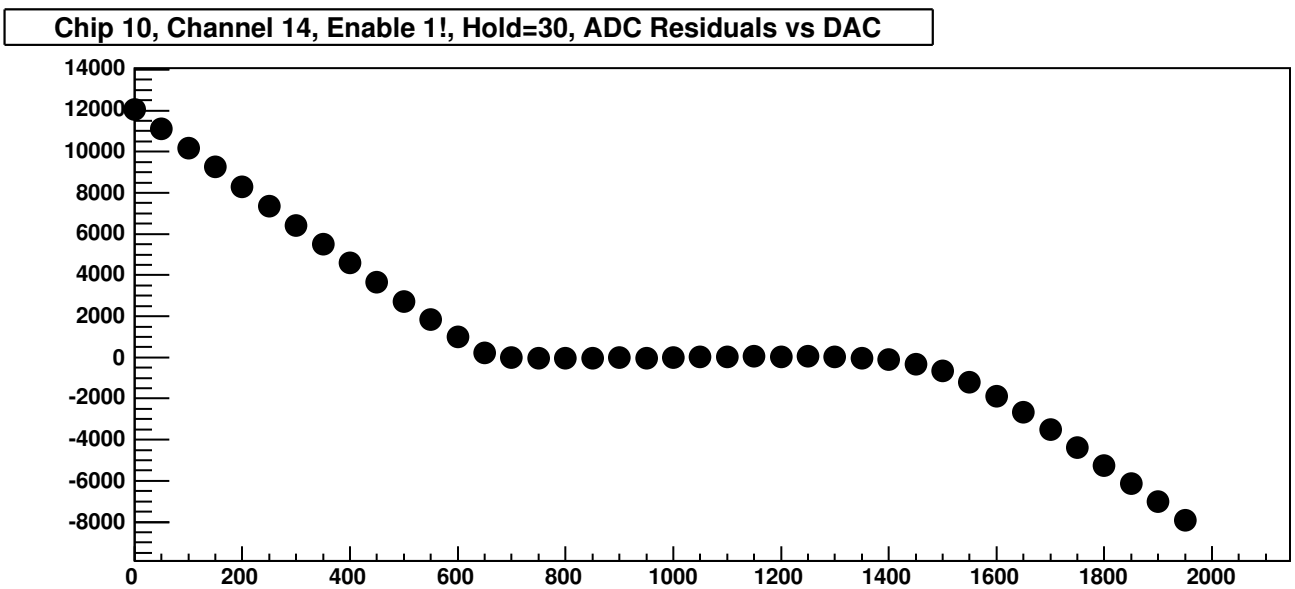


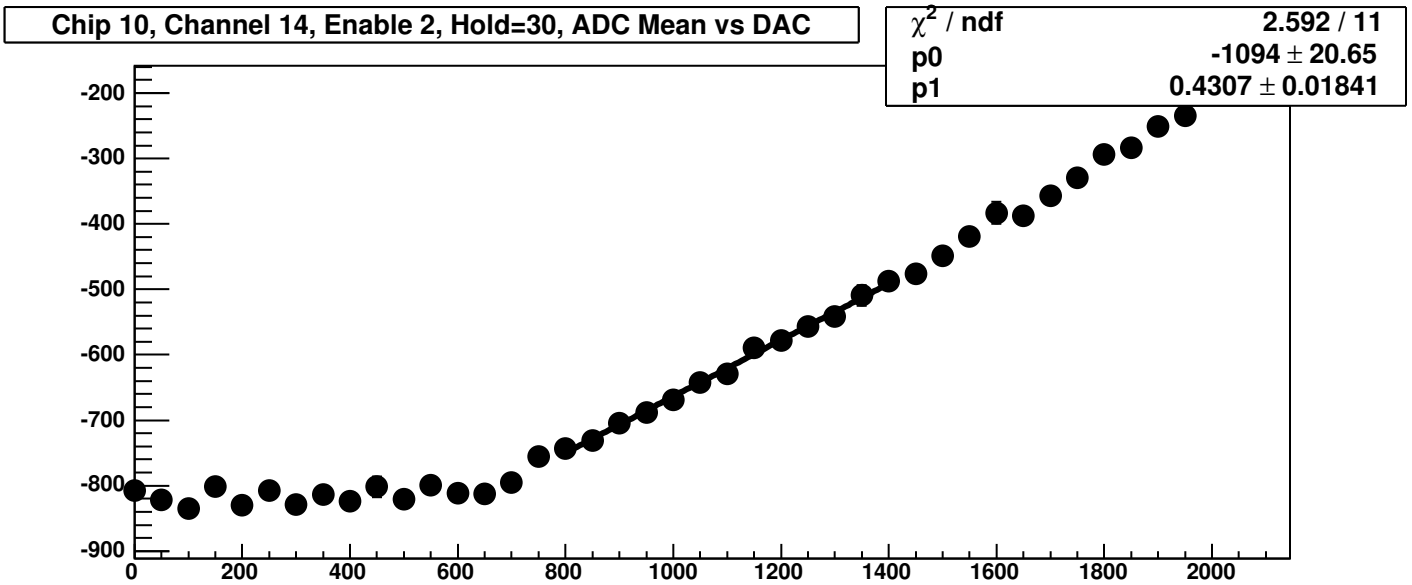
Chip 10, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC

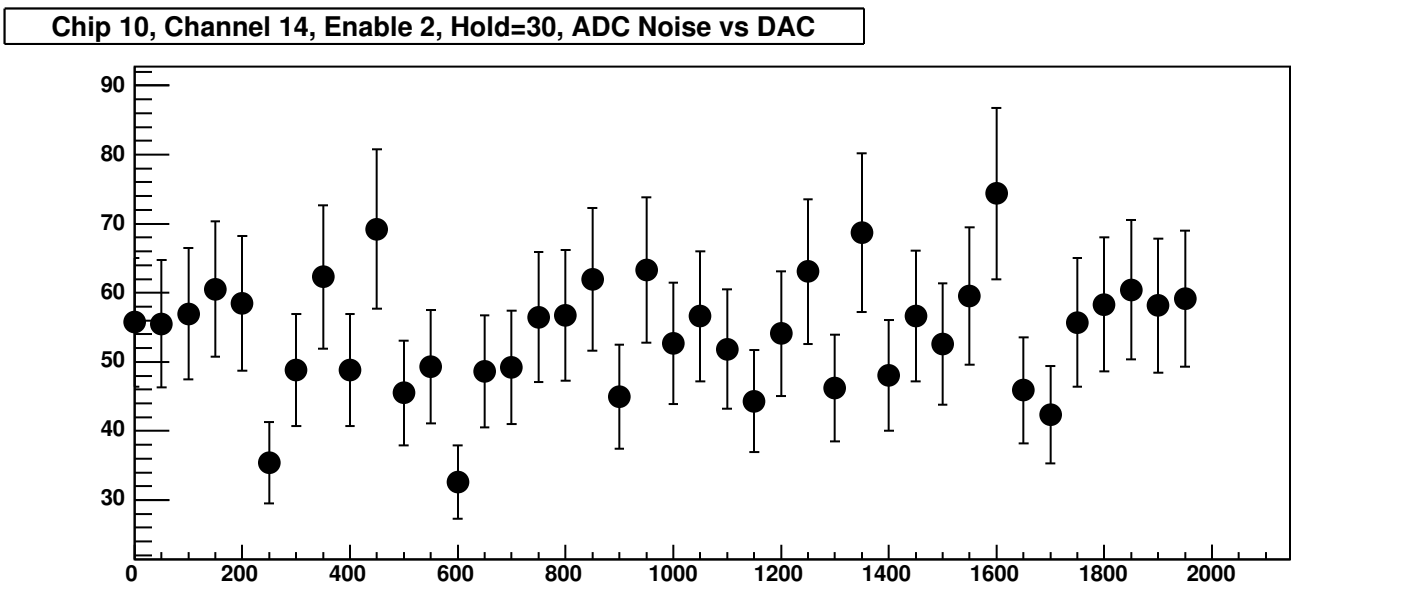


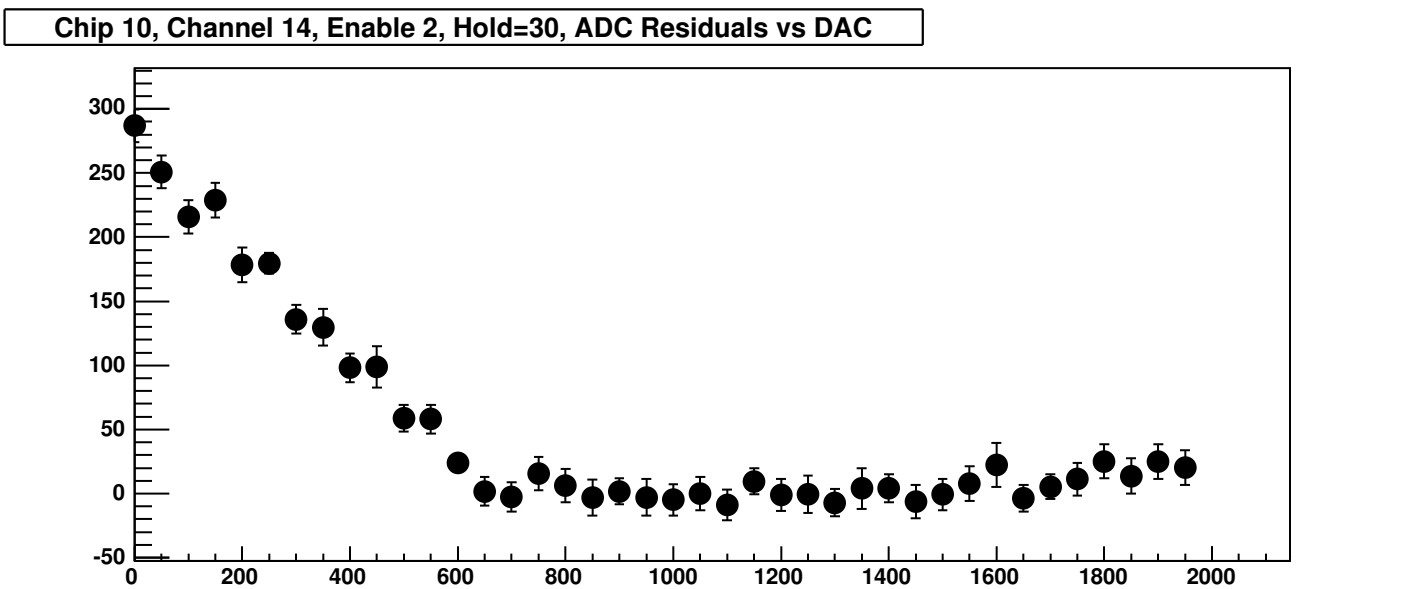

Chip 10, Channel 14, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 205.3 / 11
p0 -853.6 ± 19.06
p1 19.02 ± 0.0167
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

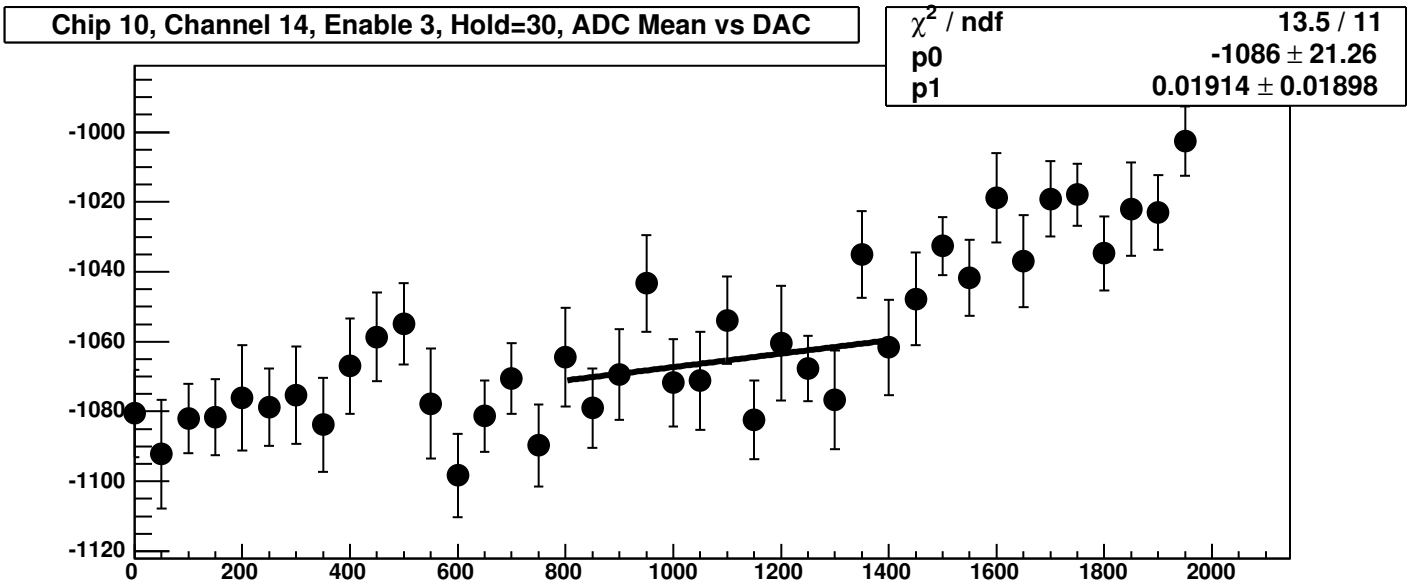

Chip 10, Channel 14, Enable 1!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

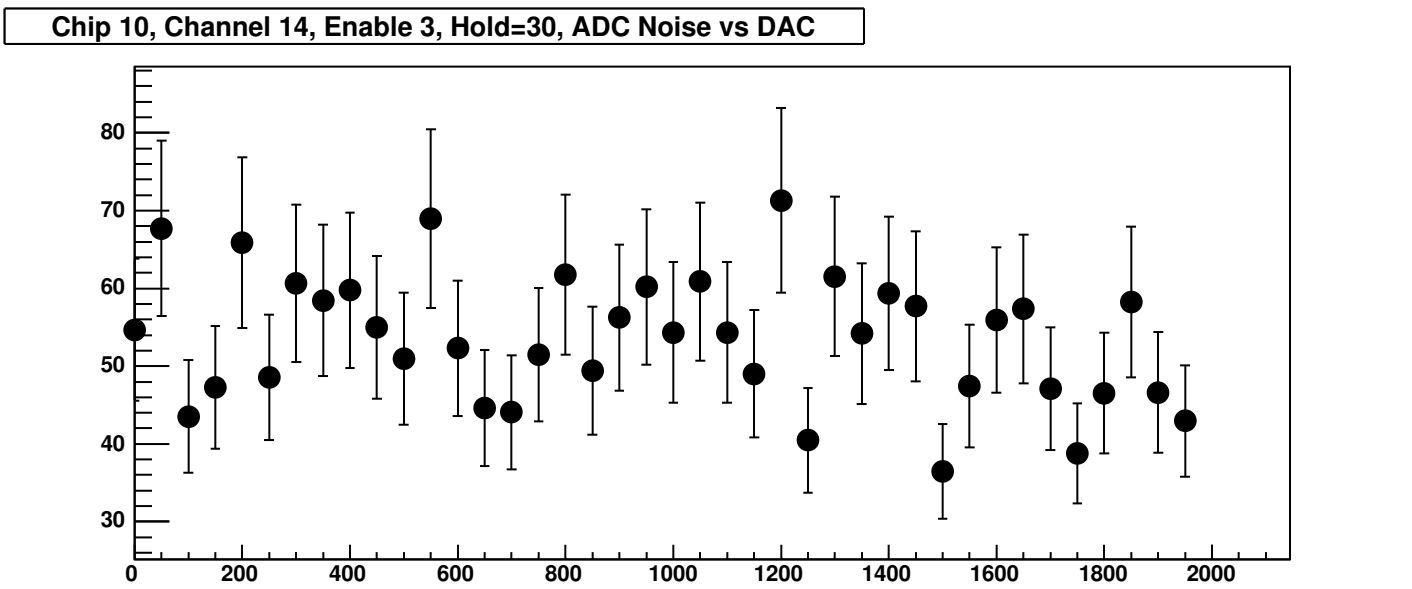

Chip 10, Channel 14, Enable 1!, Hold=30, ADC Residuals vs DAC
14000
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

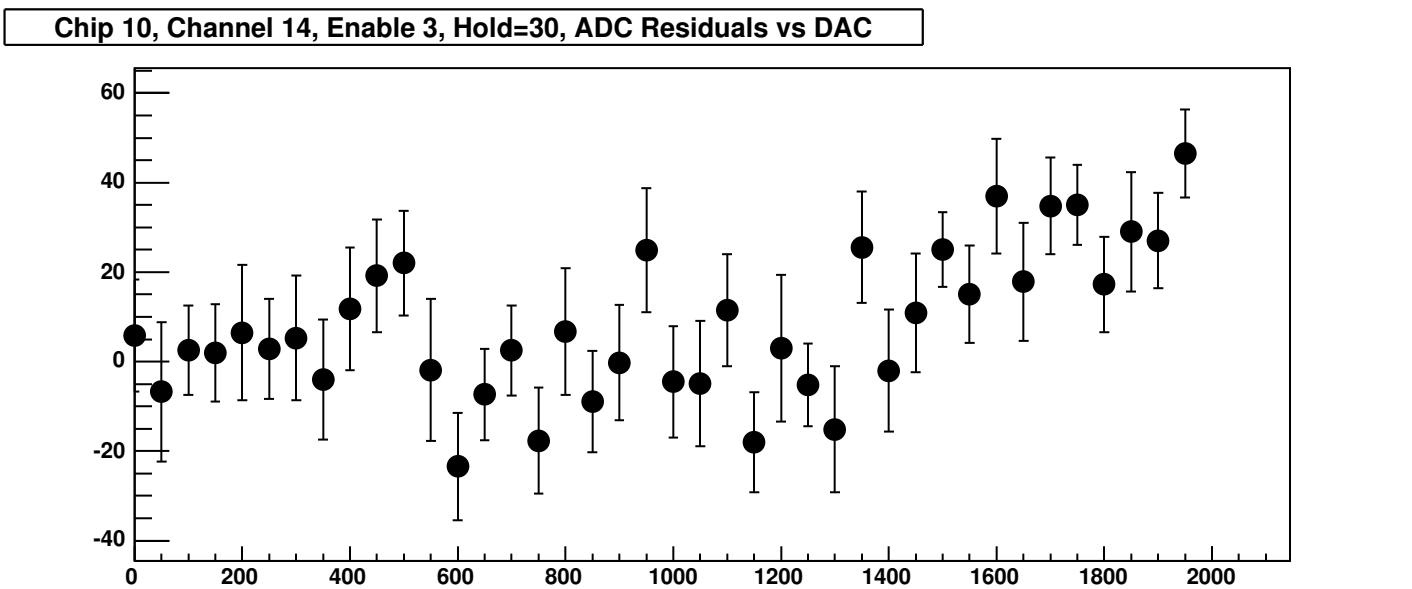

Chip 10, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2.592 / 11
p0 -1094 ± 20.65
p1 0.4307 ± 0.01841
-200
-300
-400
-500
-600
-700
-800
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

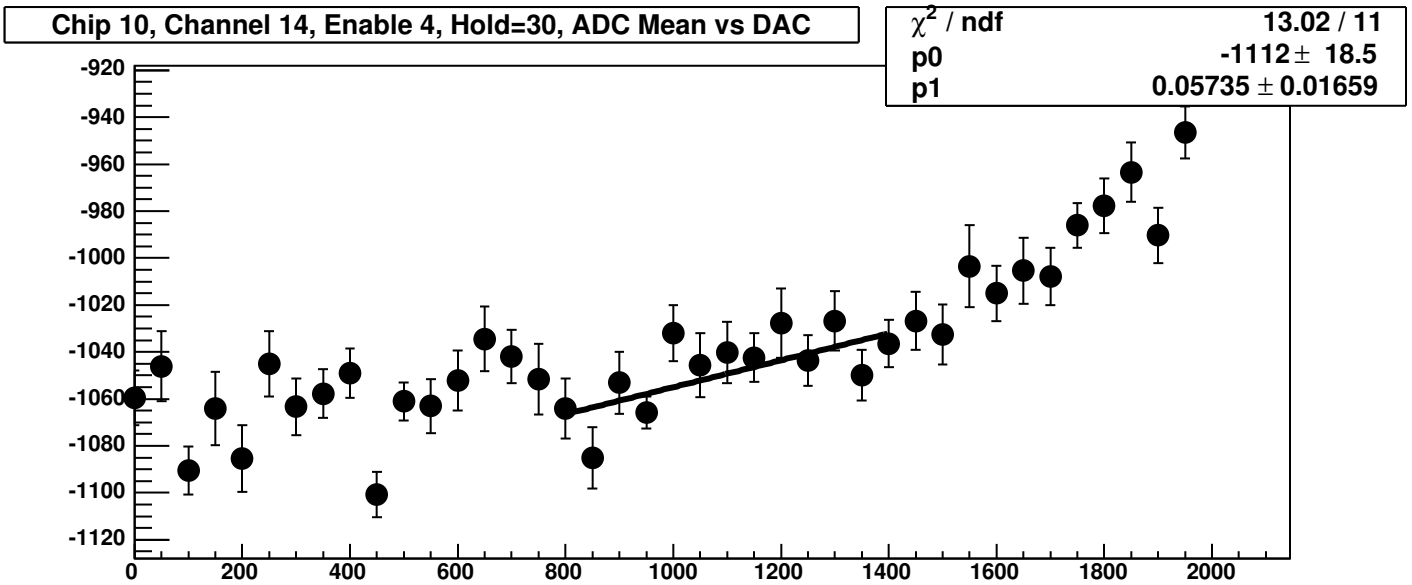

Chip 10, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

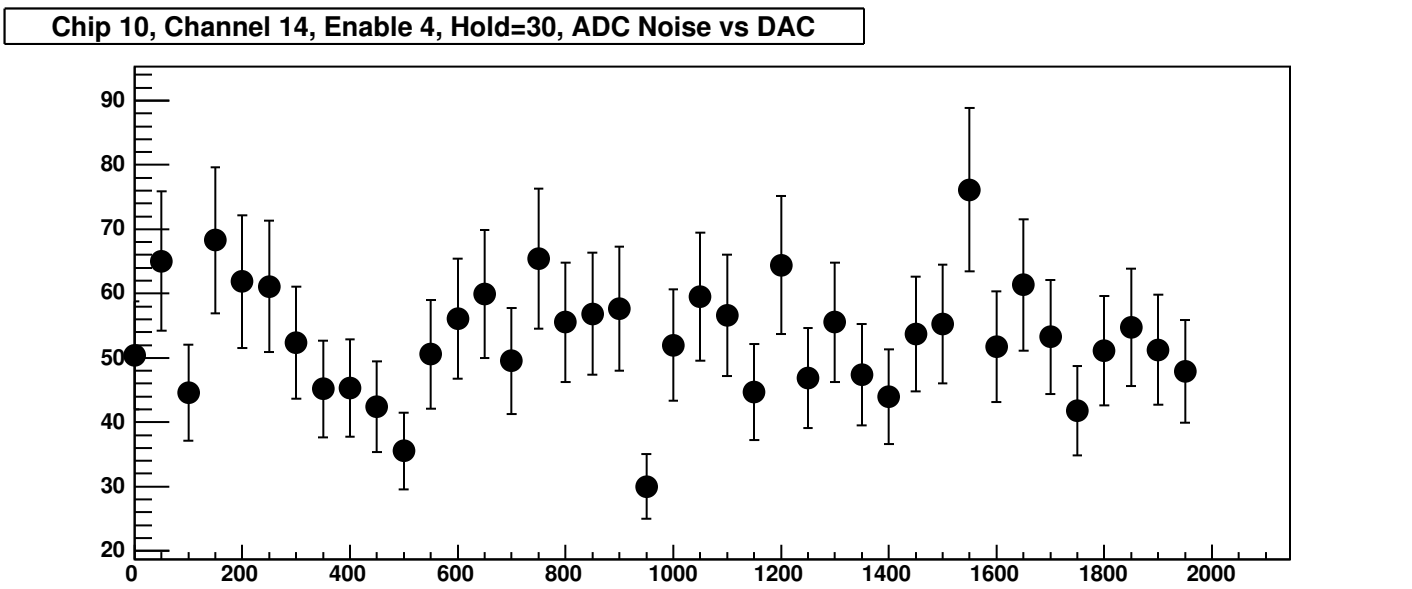

Chip 10, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

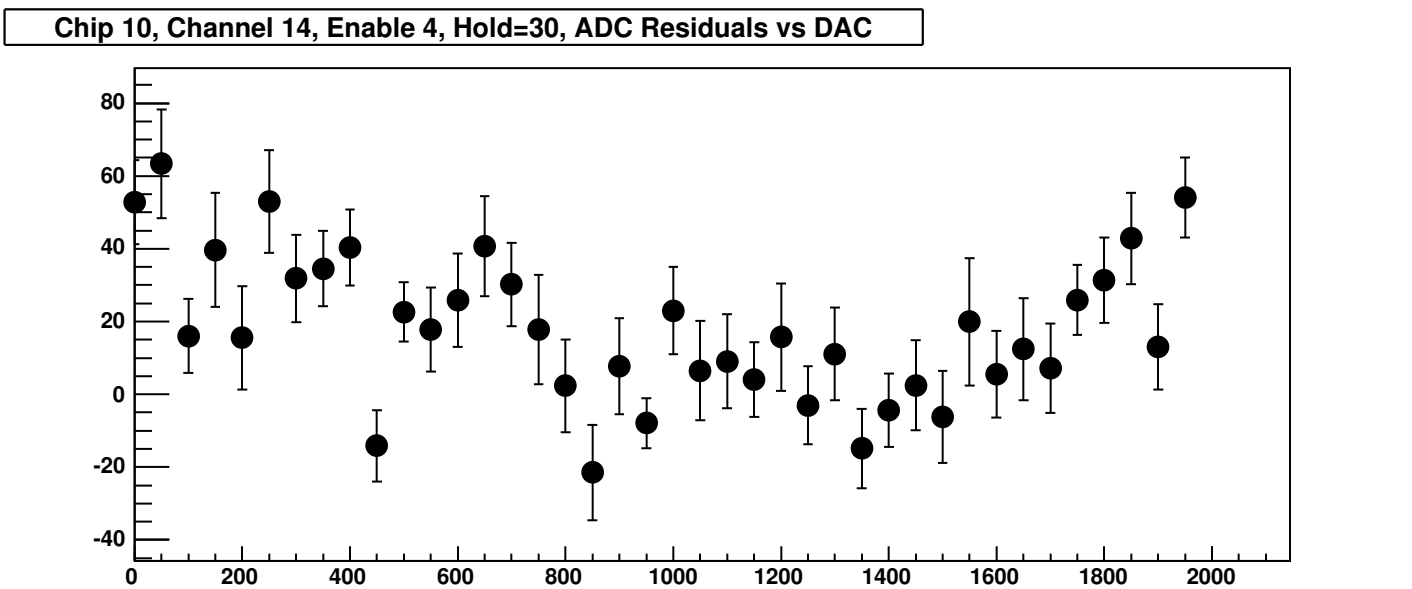

Chip 10, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 13.5 / 11
p0 -1086 ± 21.26
p1 0.01914 ± 0.01898
-1000
-1020
-1040
-1060
-1080
-1100
-1120
0
200
400
600
800
1000
1200
1400
1600
1800
2000

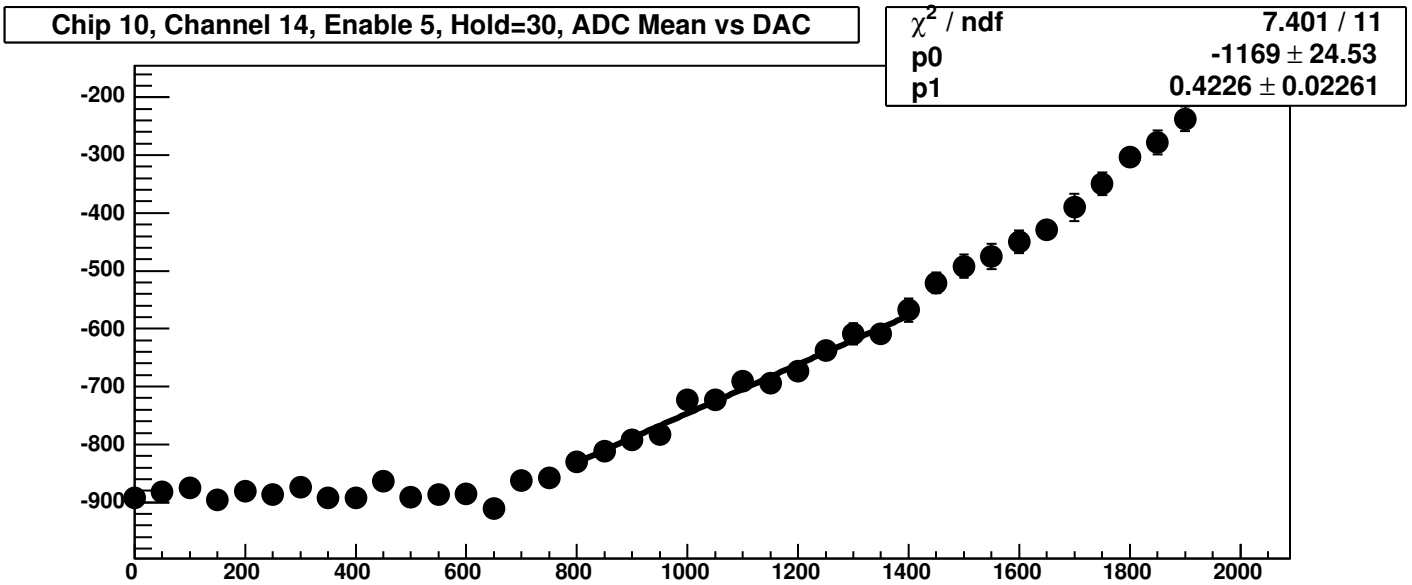

Chip 10, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

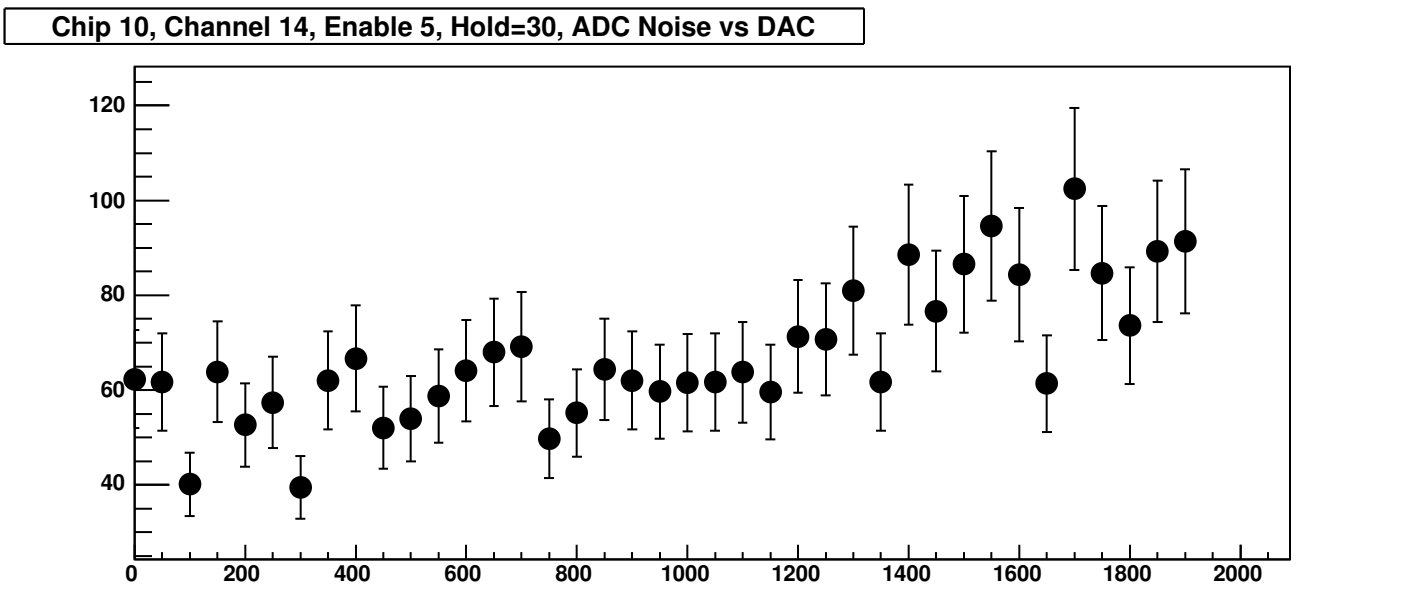

Chip 10, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

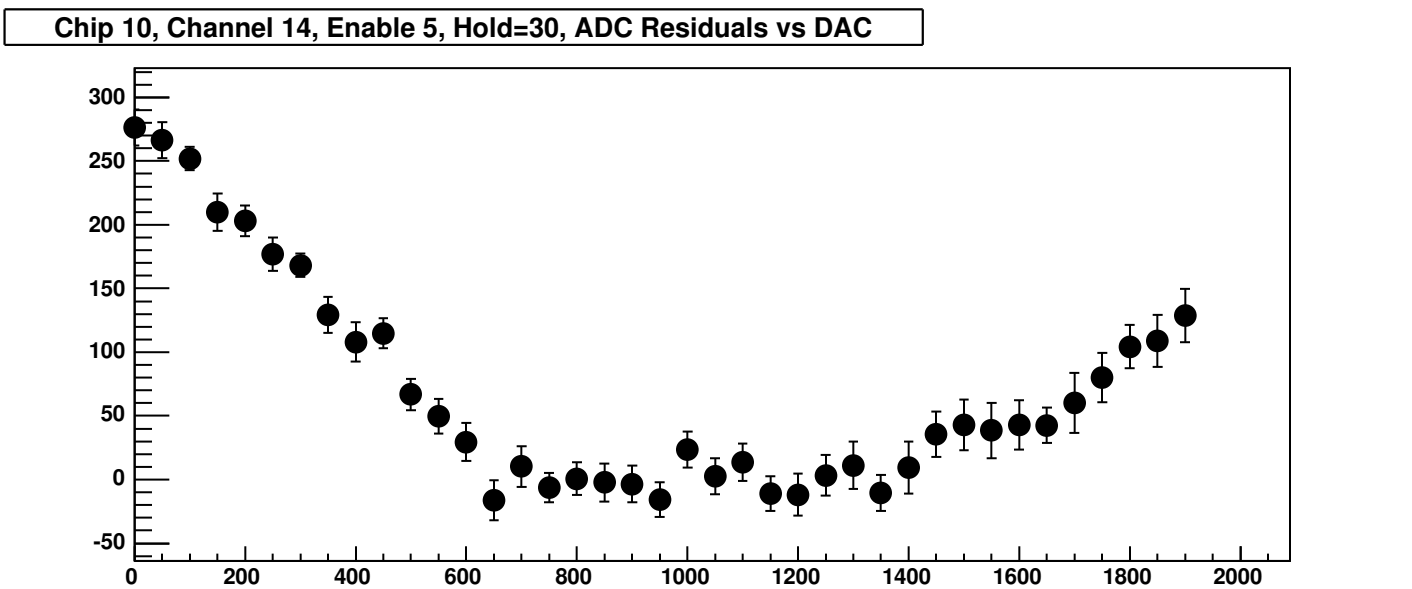

Chip 10, Channel 14, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 13.02 / 11
p0 -1112 ± 18.5
p1 0.05735 ± 0.01659
-920
-940
-960
-980
-1000
-1020
-1040
-1060
-1080
-1100
-1120
0
200
400
600
800
1000
1200
1400
1600
1800
2000

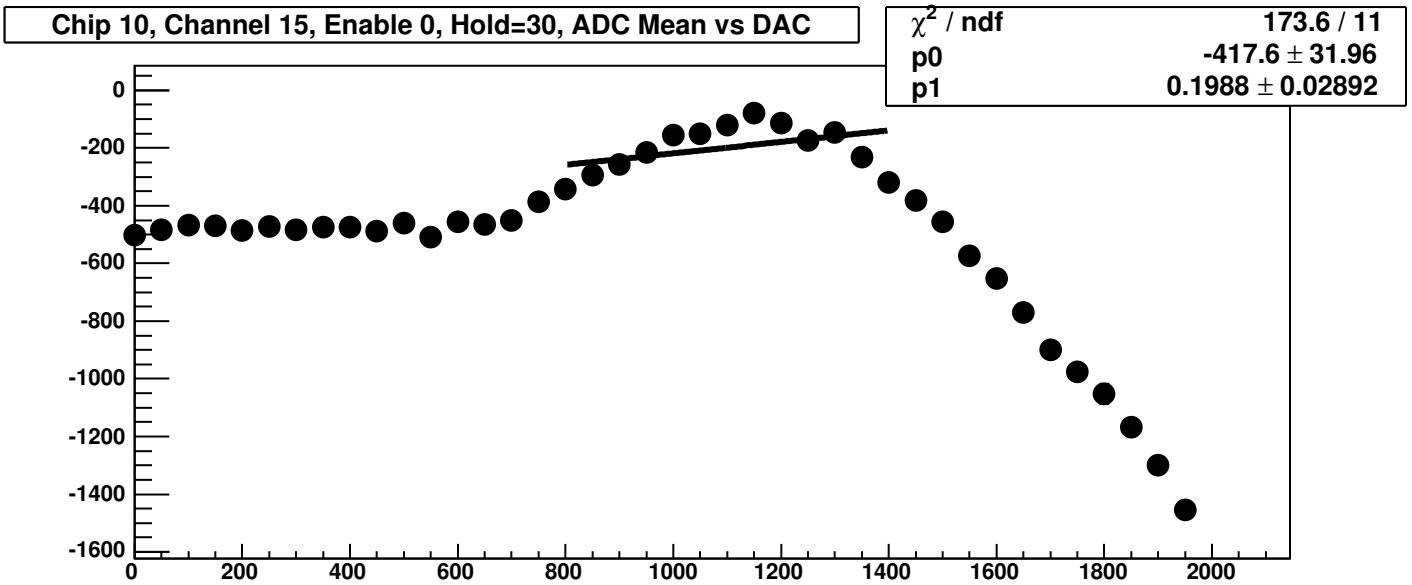

Chip 10, Channel 14, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

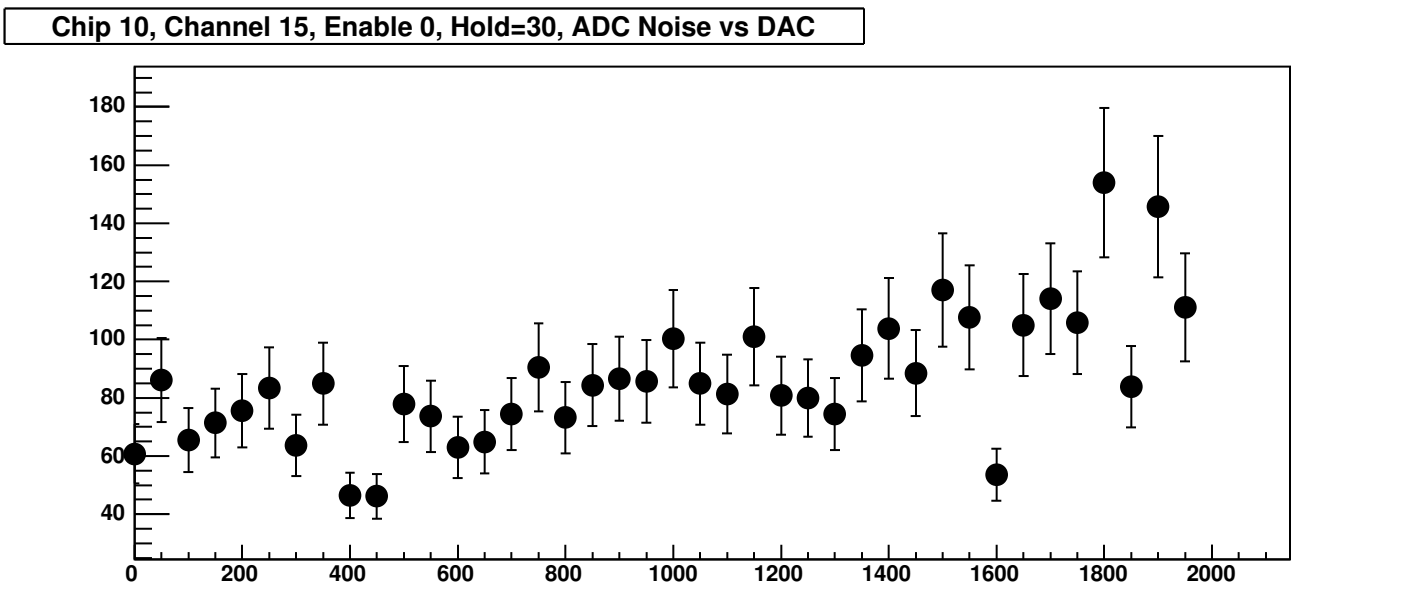

Chip 10, Channel 14, Enable 4, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

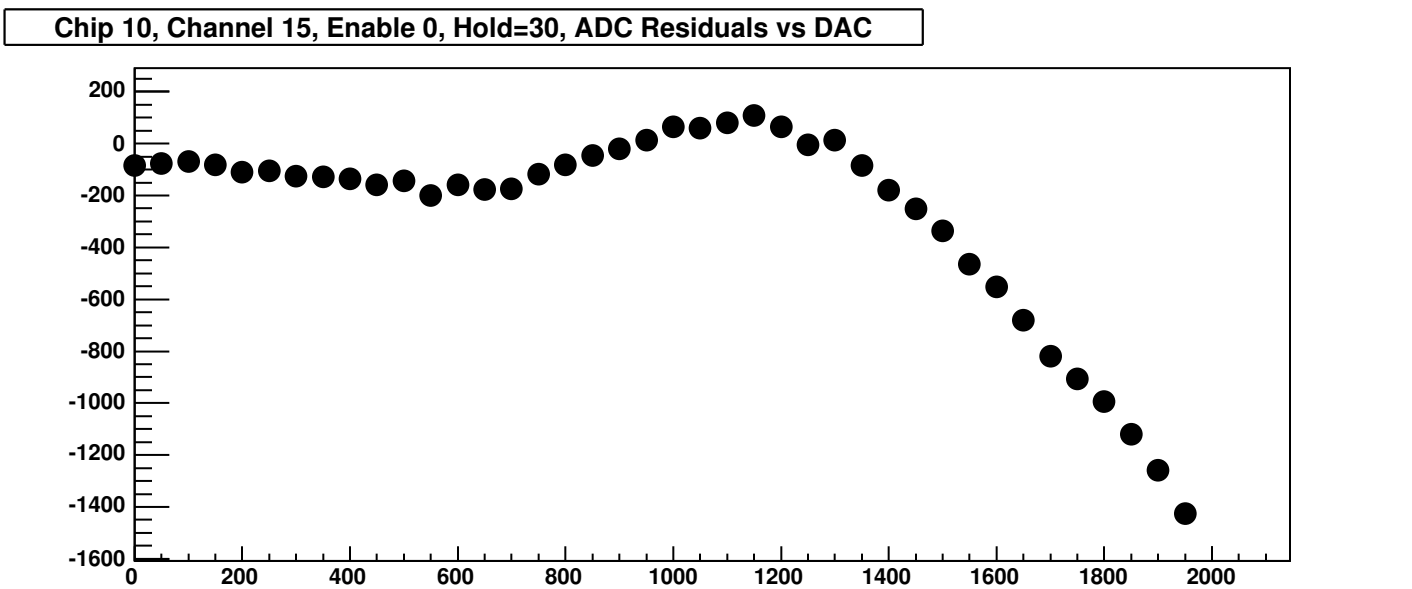

Chip 10, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7.401 / 11
p0 -1169 ± 24.53
p1 0.4226 ± 0.02261
-200
-300
-400
-500
-600
-700
-800
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

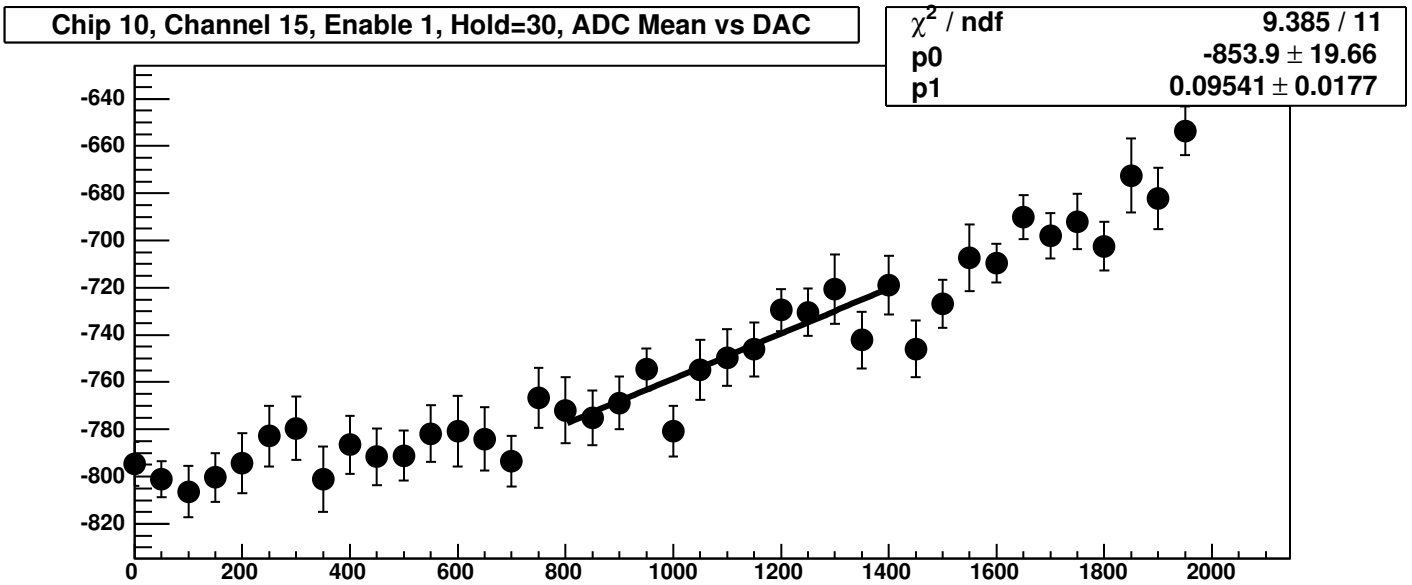

Chip 10, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

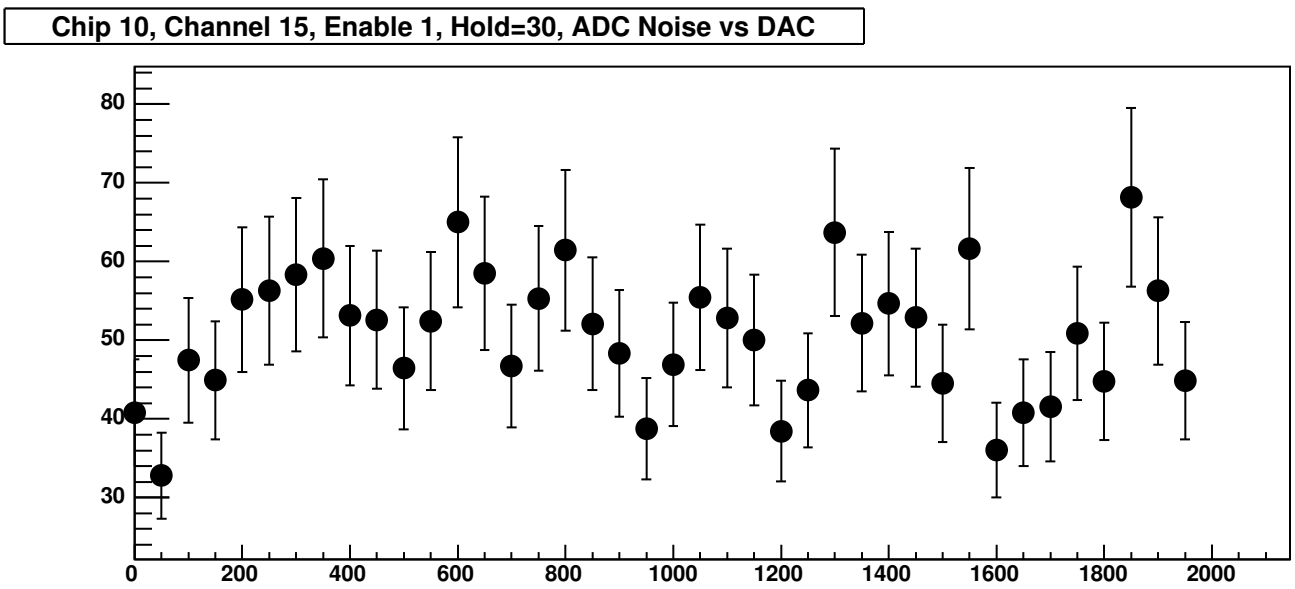

Chip 10, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

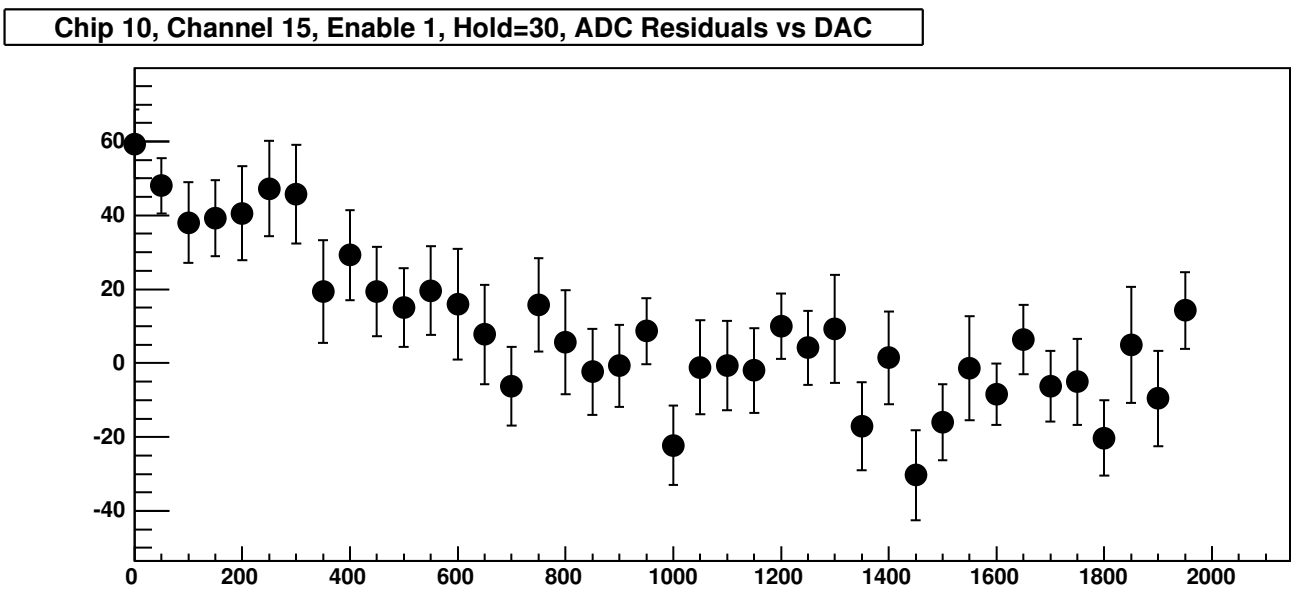

Chip 10, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 173.6 / 11
p0 -417.6 ± 31.96
p1 0.1988 ± 0.02892
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

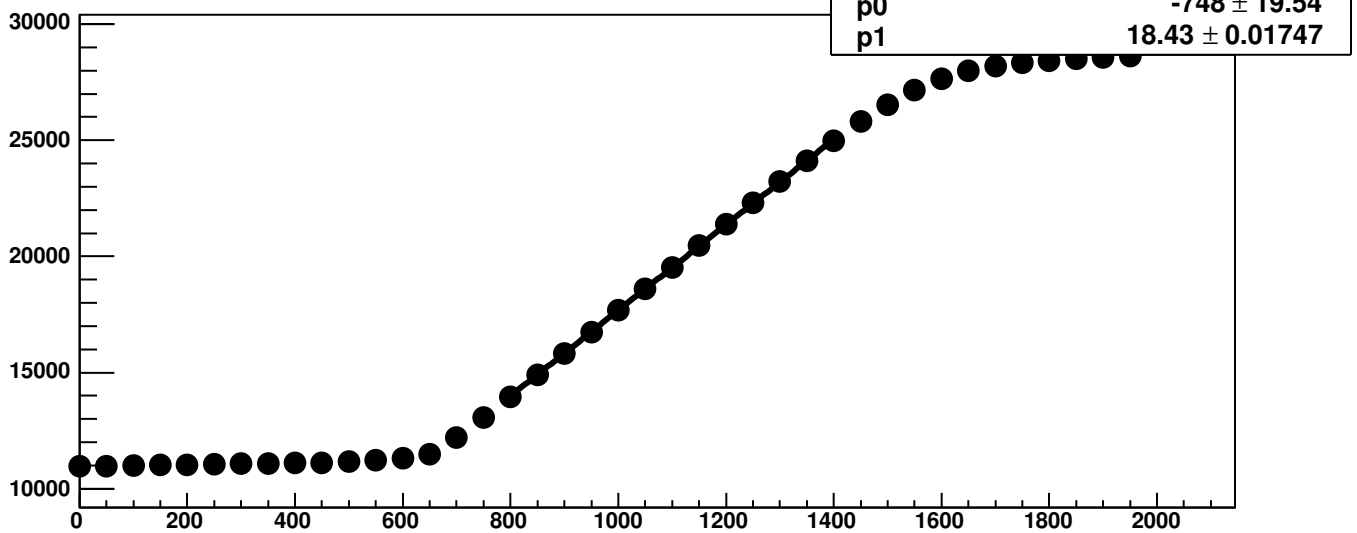

Chip 10, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 9.385 / 11
p0 -853.9 ± 19.66
p1 0.09541 ± 0.0177
-640
-660
-680
-700
-720
-740
-760
-780
-800
-820
0
200
400
600
800
1000
1200
1400
1600
1800
2000

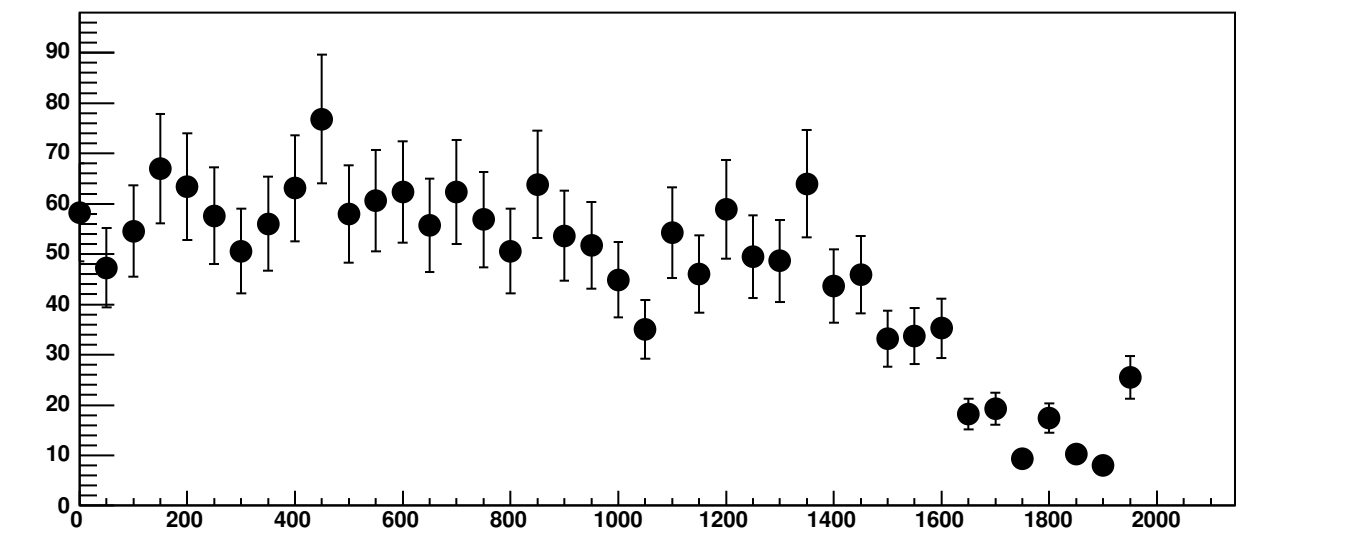

Chip 10, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

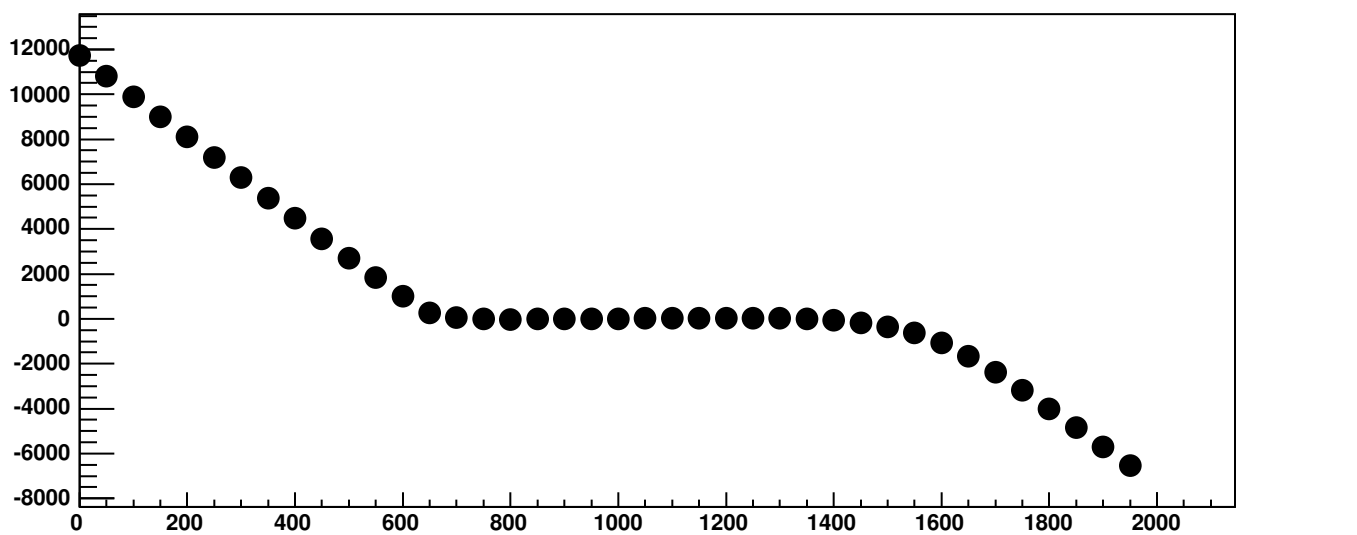
Chip 10, Channel 15, Enable 2!, Hold=30, ADC Mean vs DAC

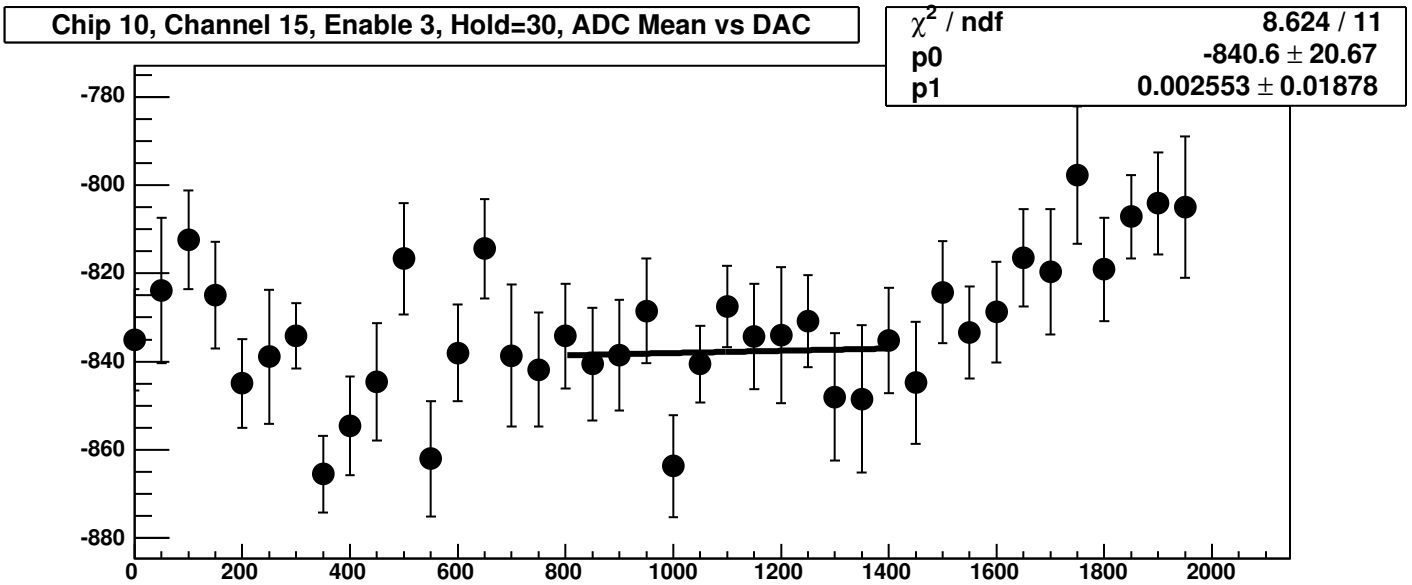


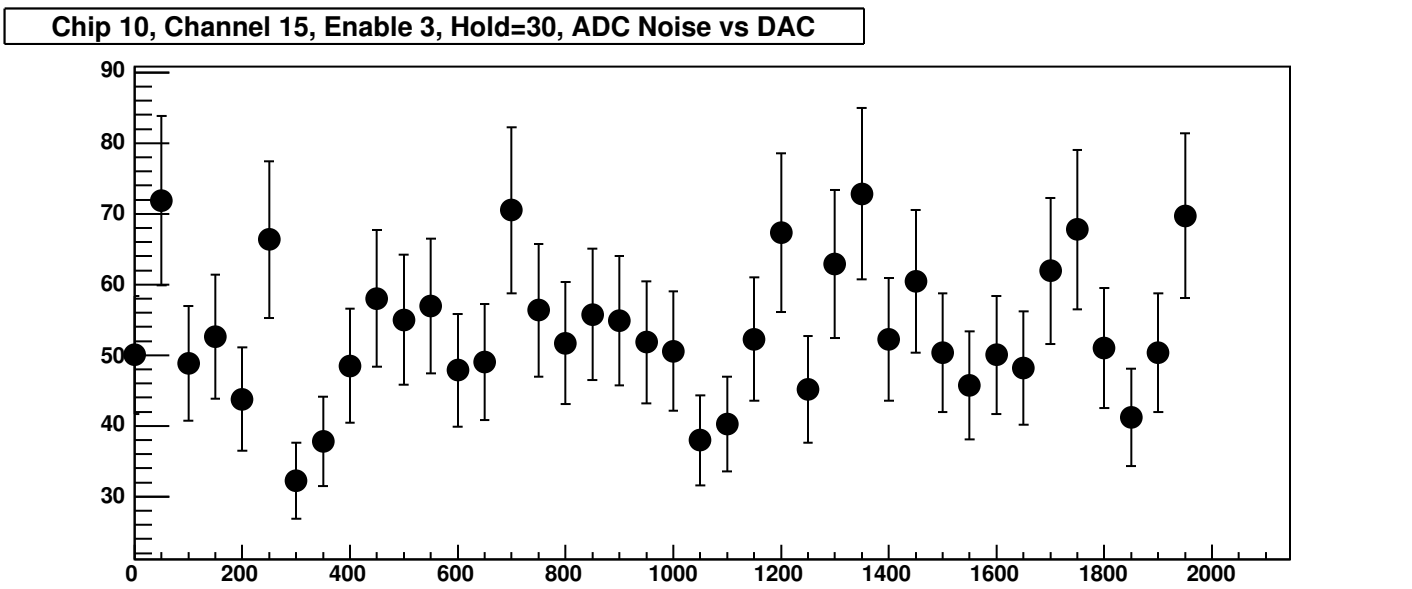
Chip 10, Channel 15, Enable 2!, Hold=30, ADC Noise vs DAC

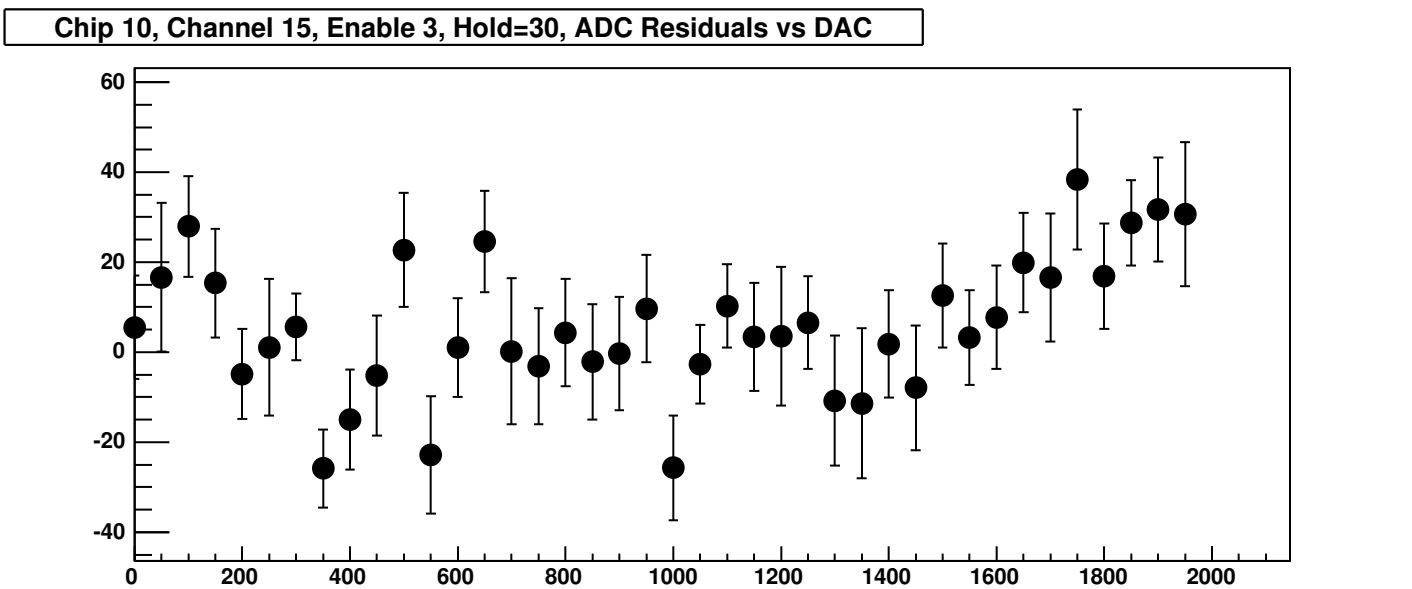


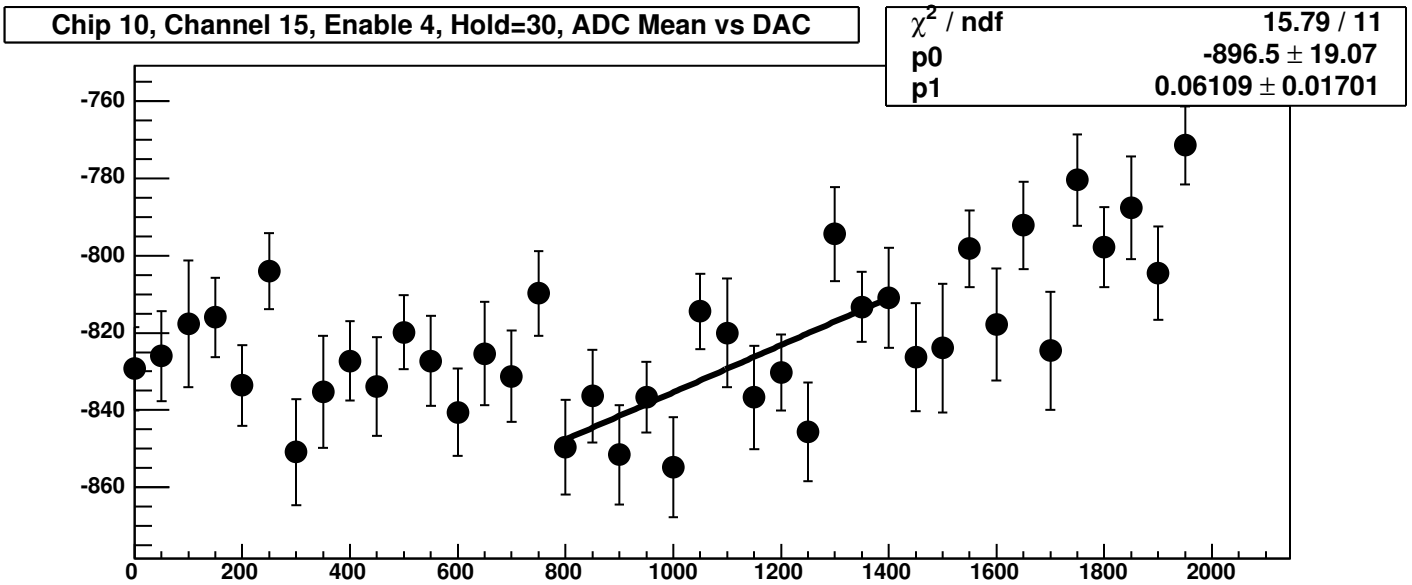
Chip 10, Channel 15, Enable 2!, Hold=30, ADC Residuals vs DAC

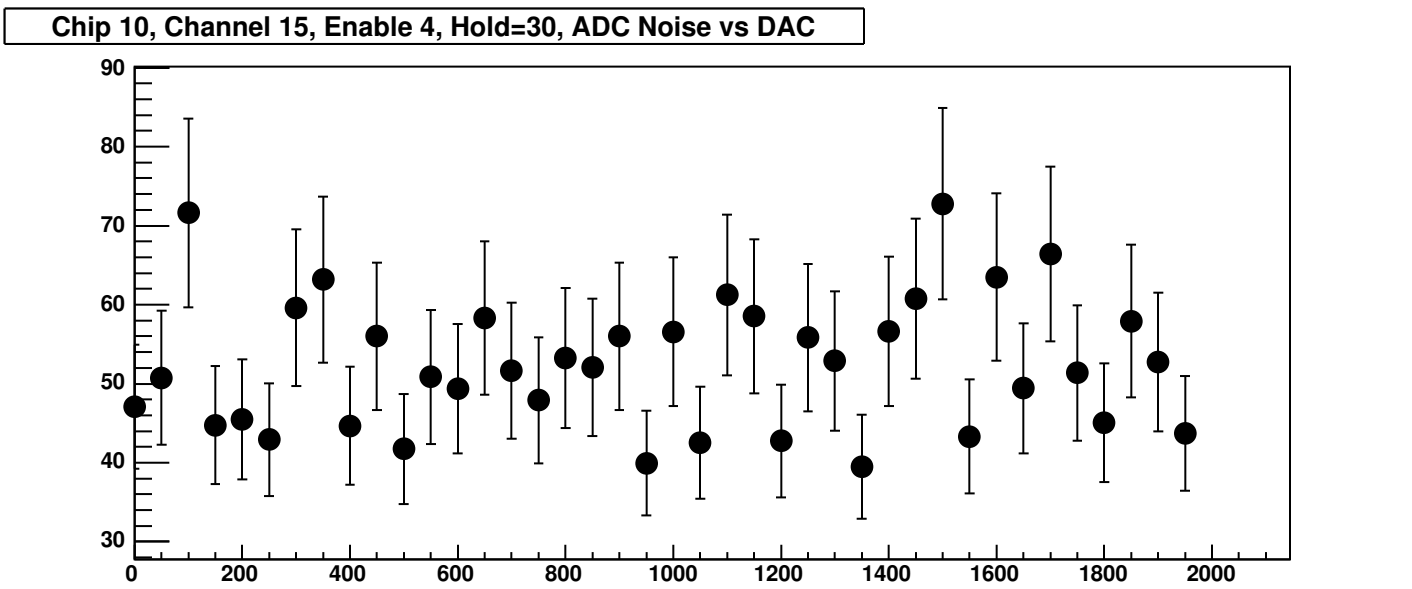


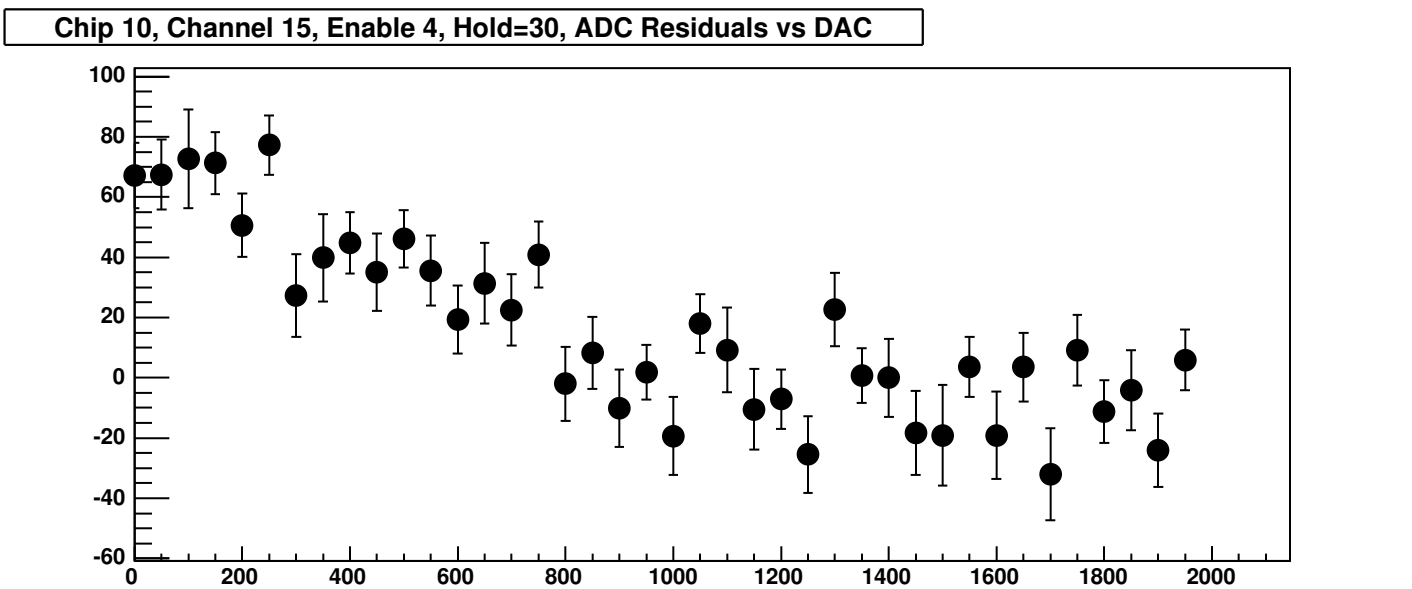

Chip 10, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 8.624 / 11
p0 -840.6 ± 20.67
p1 0.002553 ± 0.01878
-780
-800
-820
-840
-860
-880
0
200
400
600
800
1000
1200
1400
1600
1800
2000

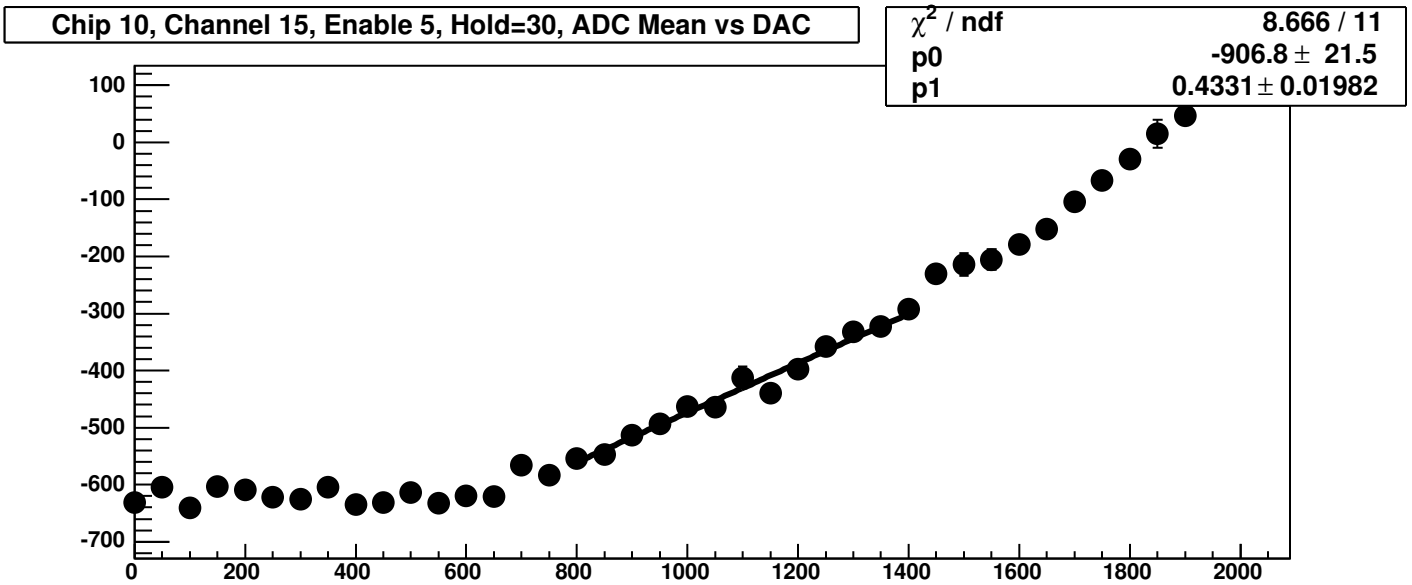

Chip 10, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

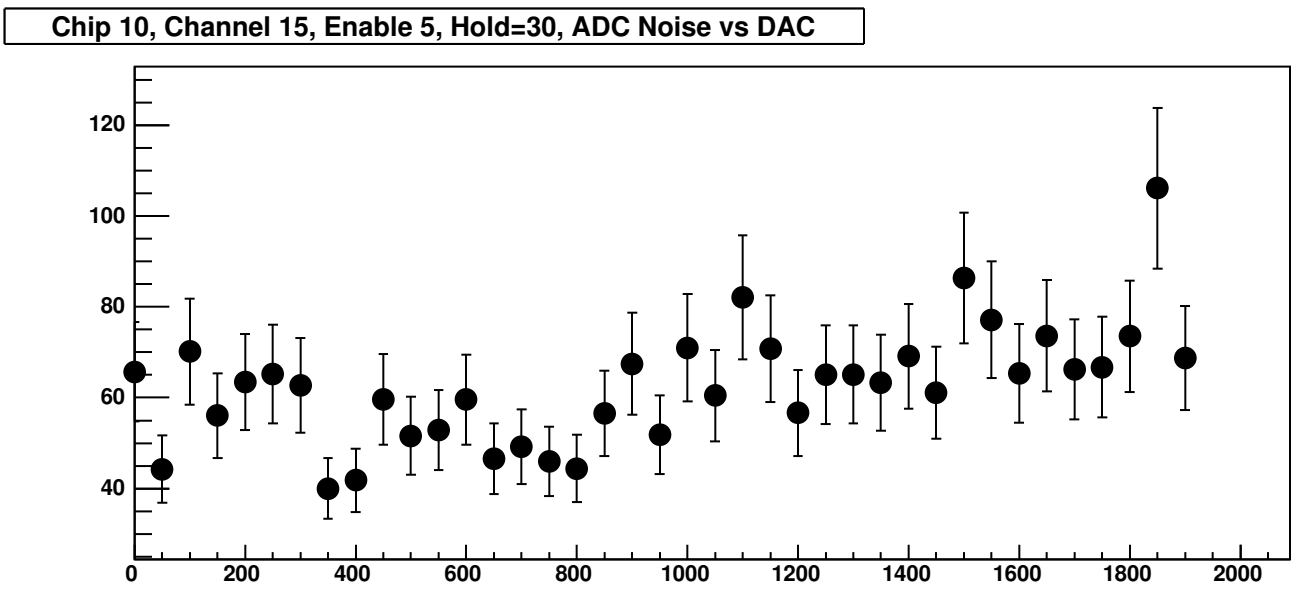

Chip 10, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

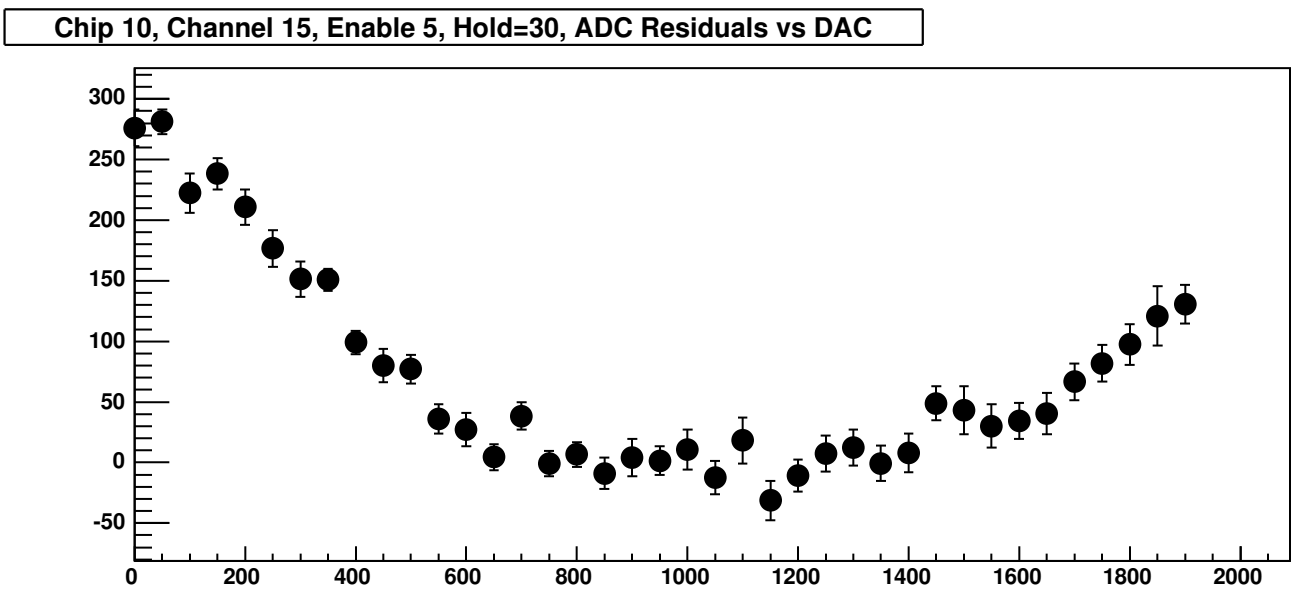

Chip 10, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 15.79 / 11
p0 -896.5 ± 19.07
p1 0.06109 ± 0.01701
-760
-780
-800
-820
-840
-860
0
200
400
600
800
1000
1200
1400
1600
1800
2000

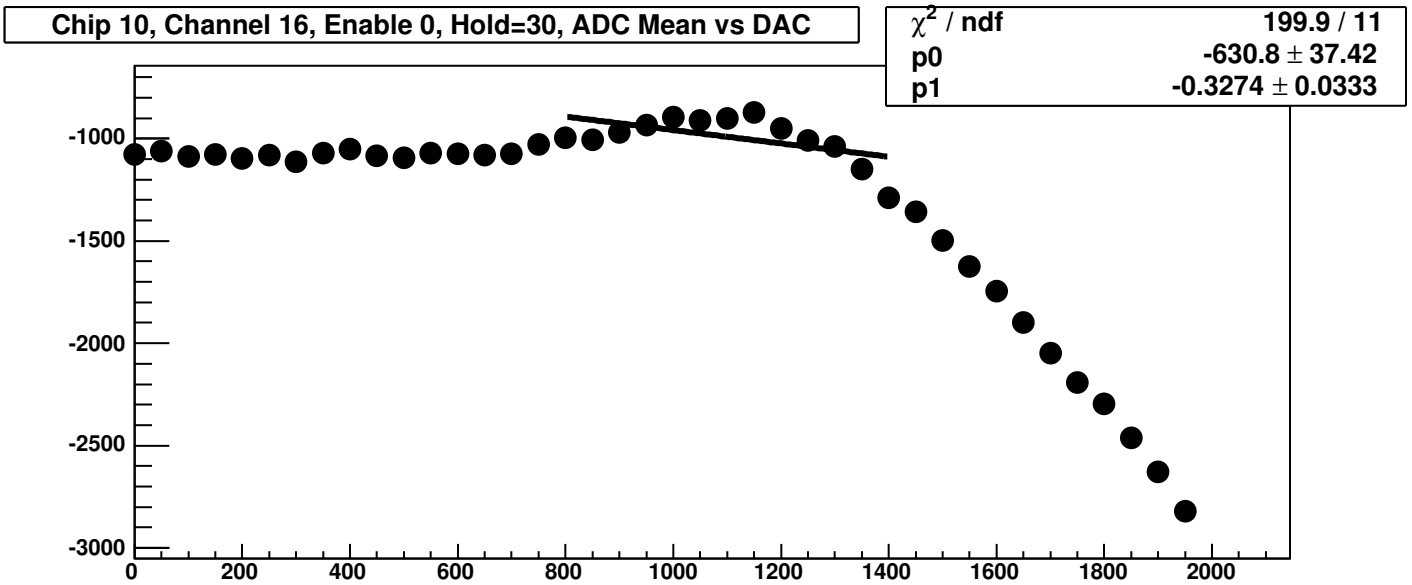

Chip 10, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

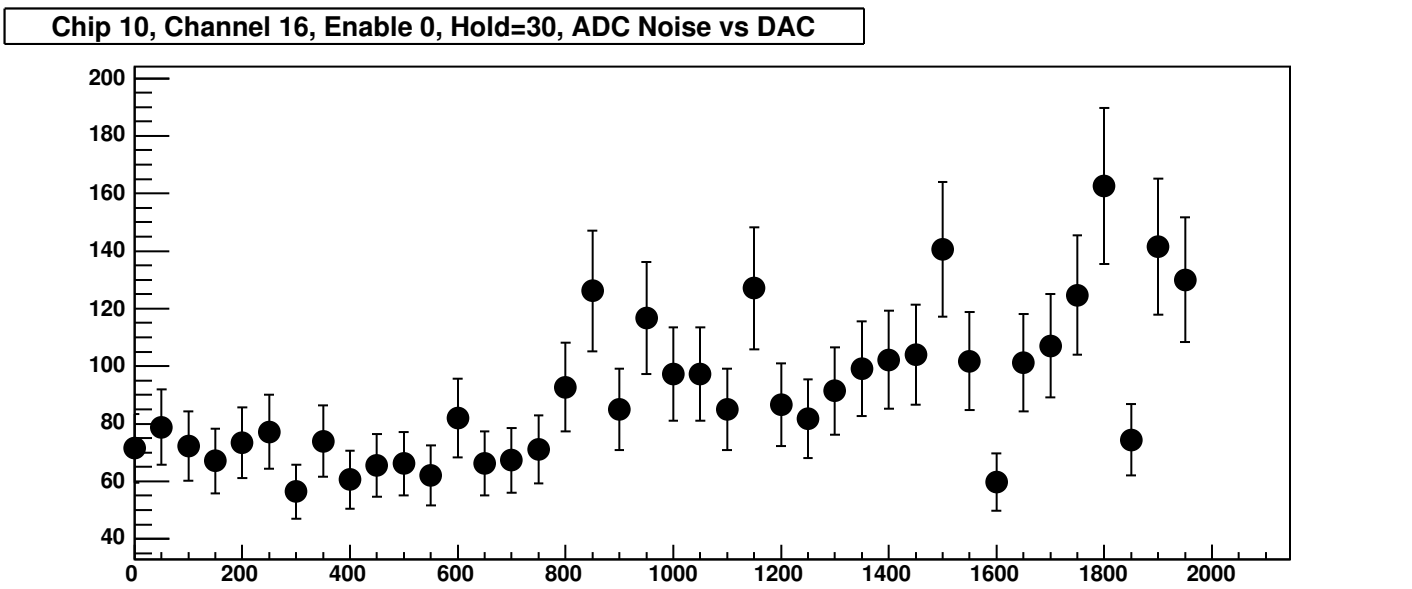

Chip 10, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

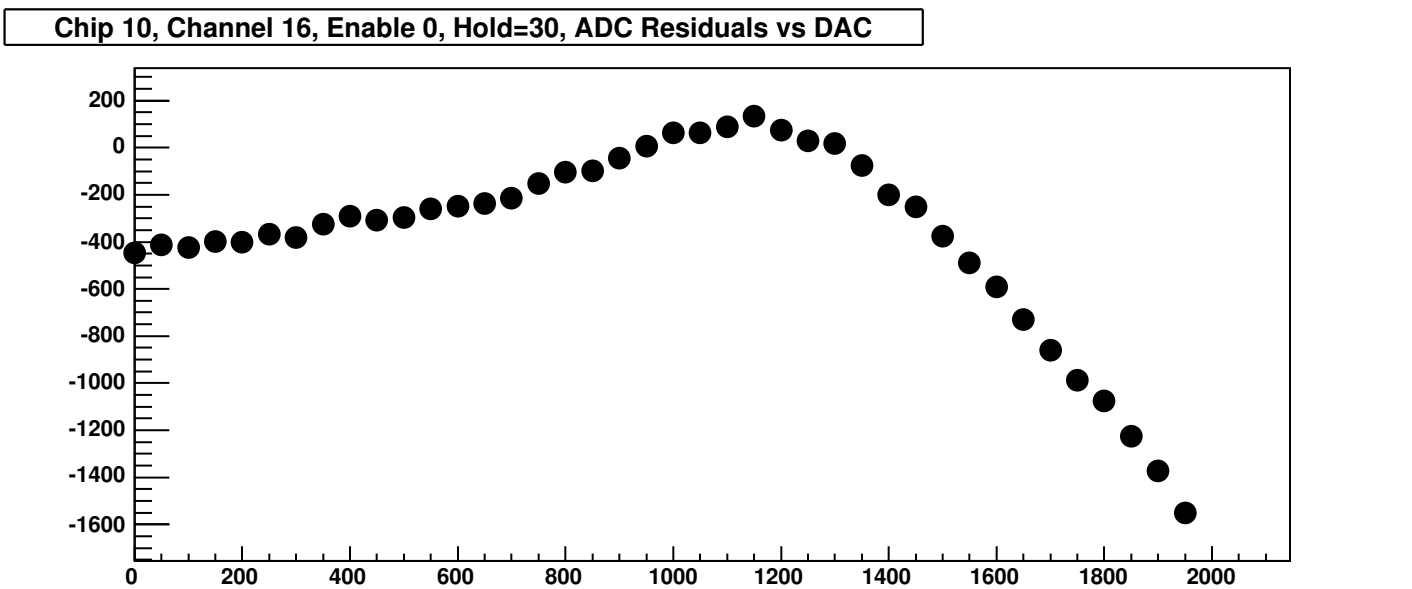

Chip 10, Channel 15, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8.666 / 11
p0 -906.8 ± 21.5
p1 0.4331 ± 0.01982
100
0
-100
-200
-300
-400
-500
-600
-700
0
200
400
600
800
1000
1200
1400
1600
1800
2000

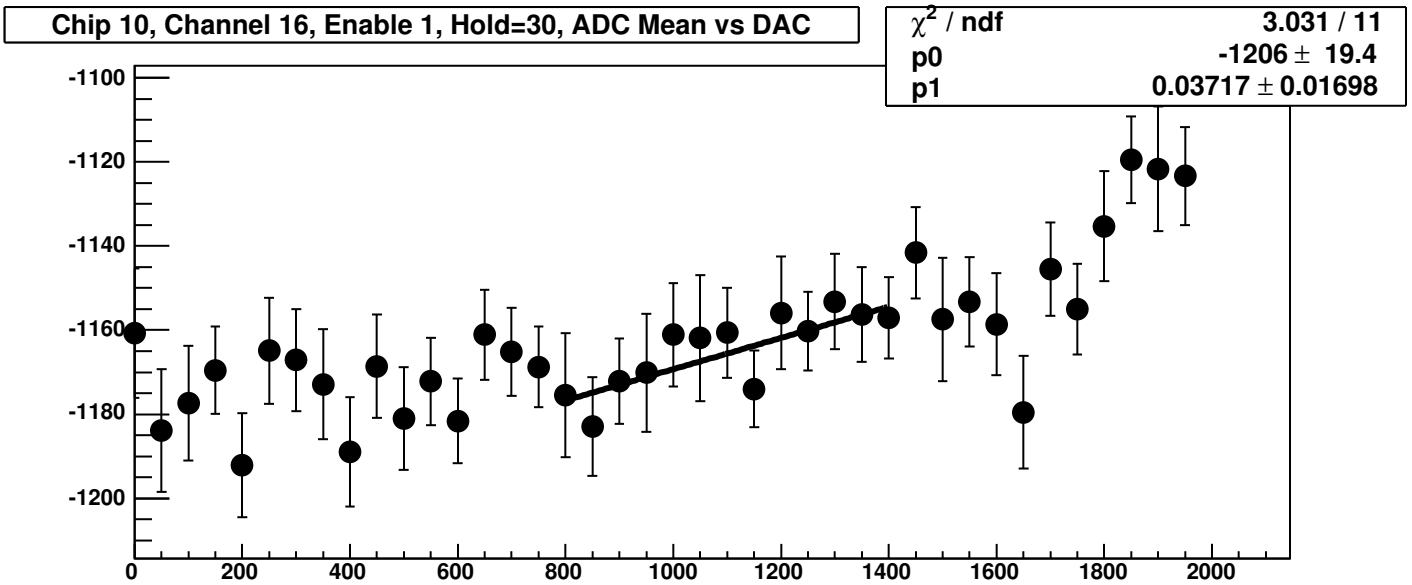

Chip 10, Channel 15, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

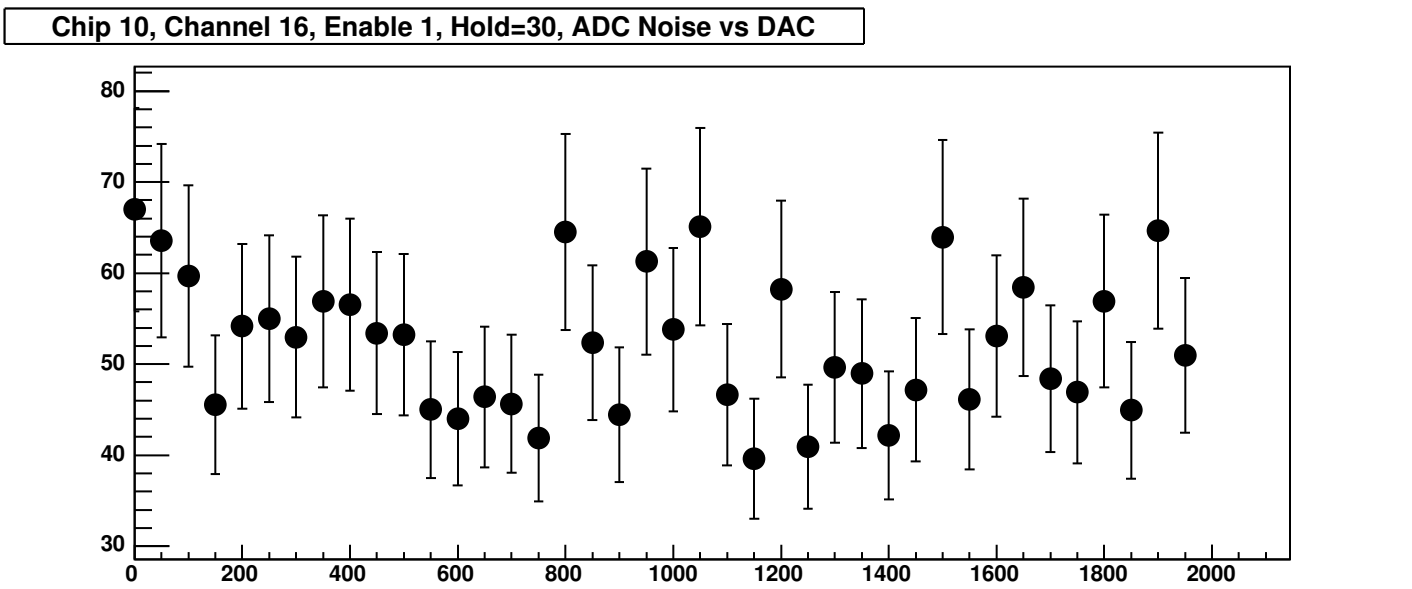

Chip 10, Channel 15, Enable 5, Hold=30, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

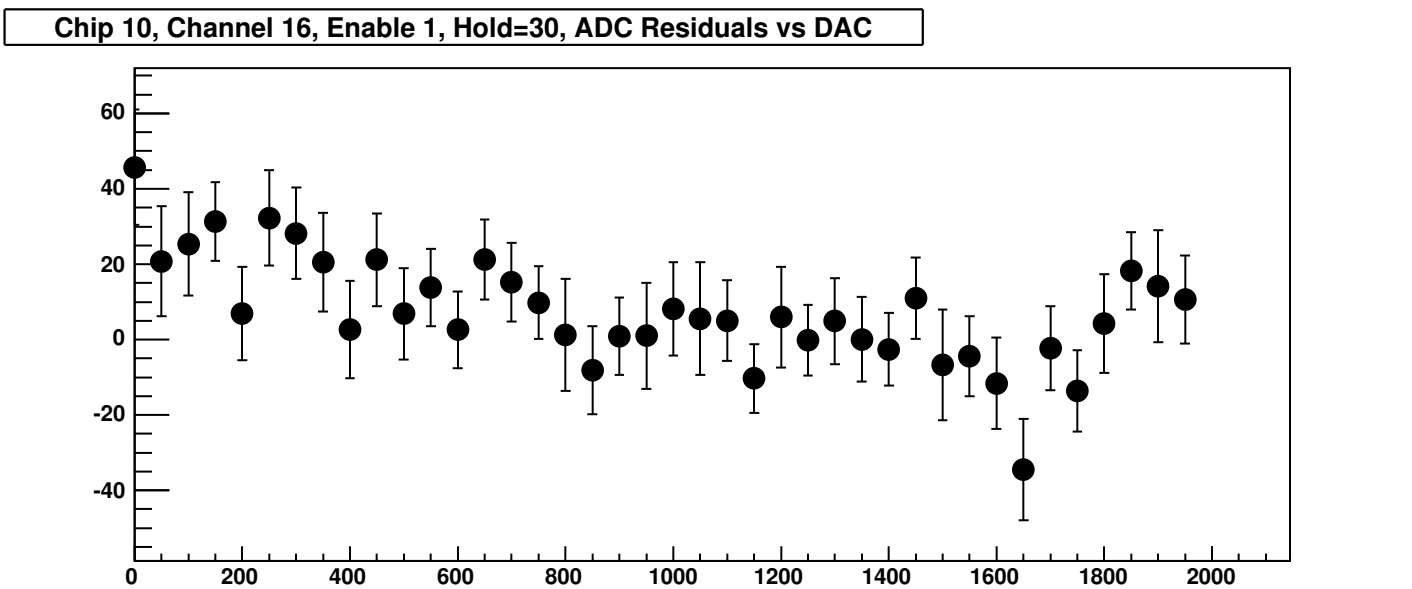

Chip 10, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 199.9 / 11
p0 -630.8 ± 37.42
p1 -0.3274 ± 0.0333
-1000
-1500
-2000
-2500
-3000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

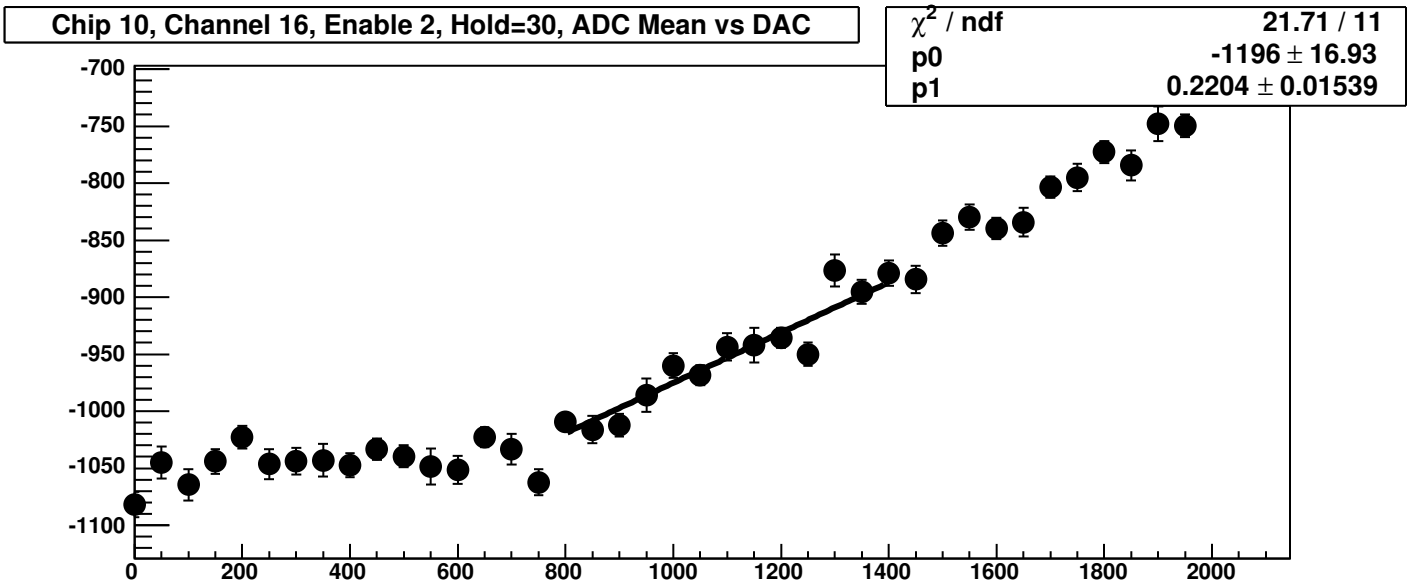

Chip 10, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

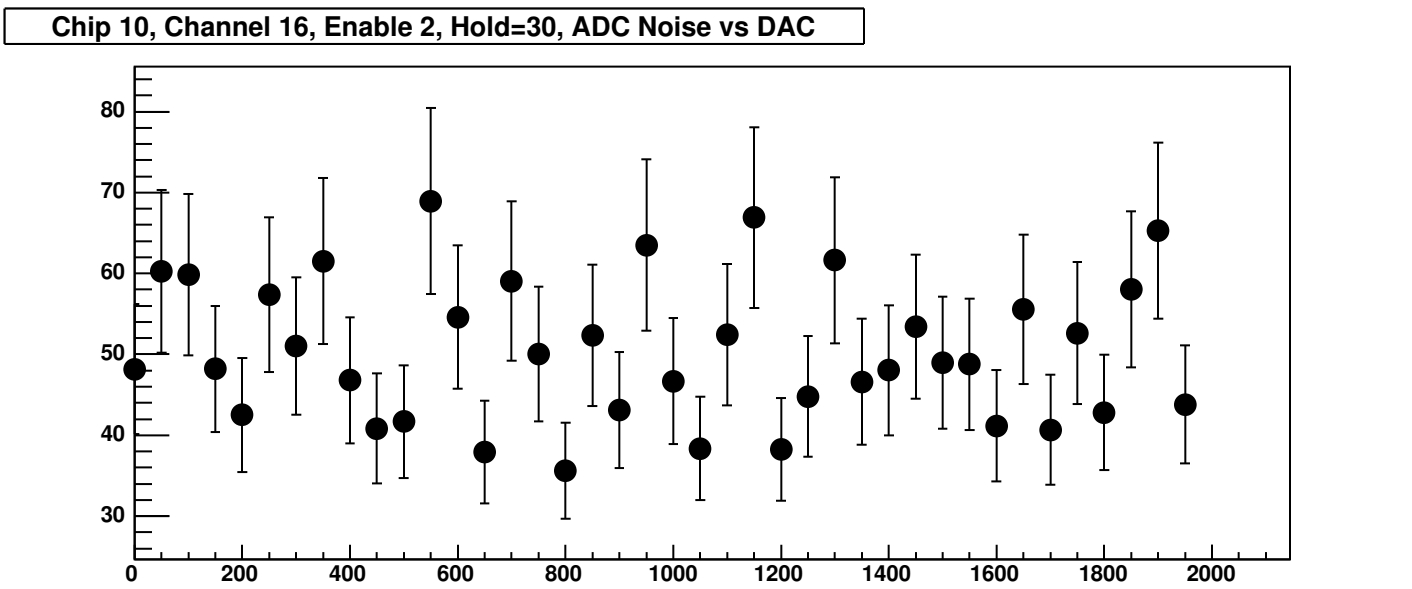

Chip 10, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

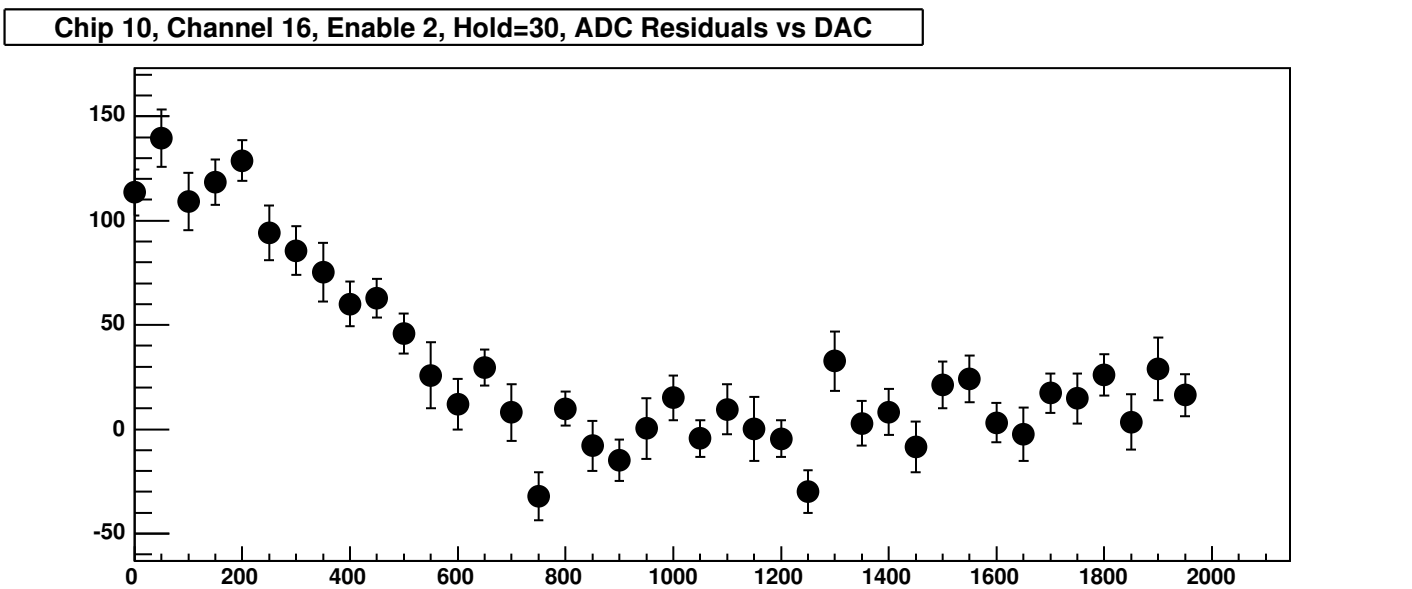

Chip 10, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 3.031 / 11
p0 -1206 ± 19.4
p1 0.03717 ± 0.01698
-1100
-1120
-1140
-1160
-1180
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

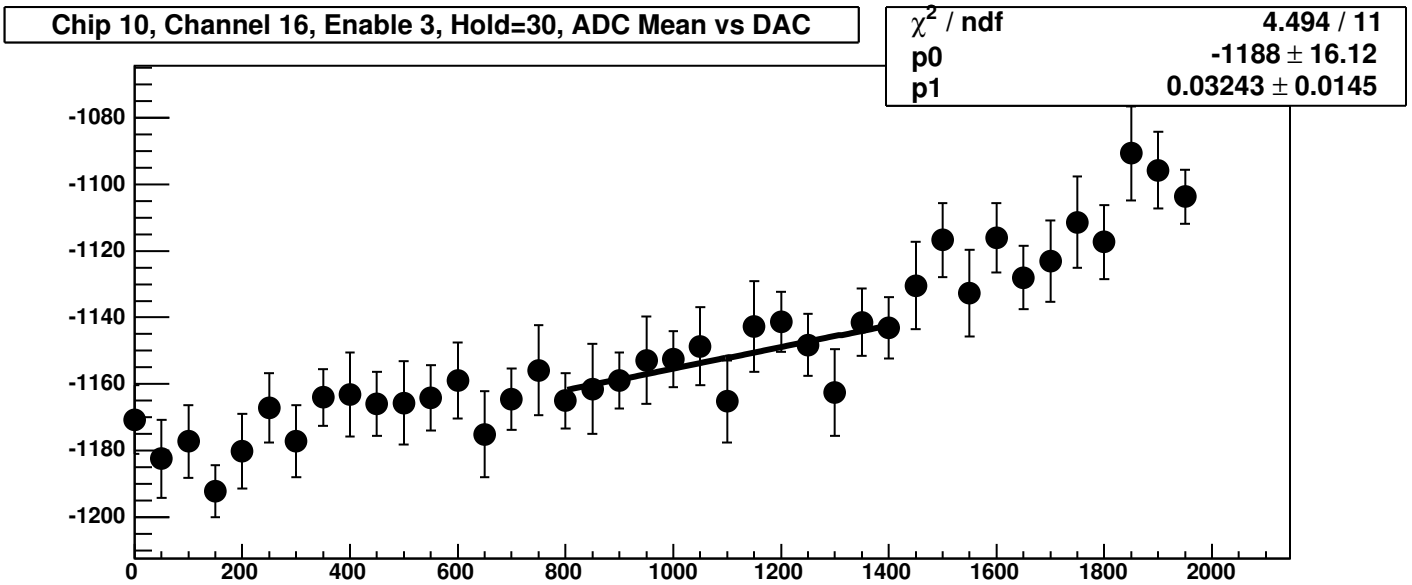

Chip 10, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

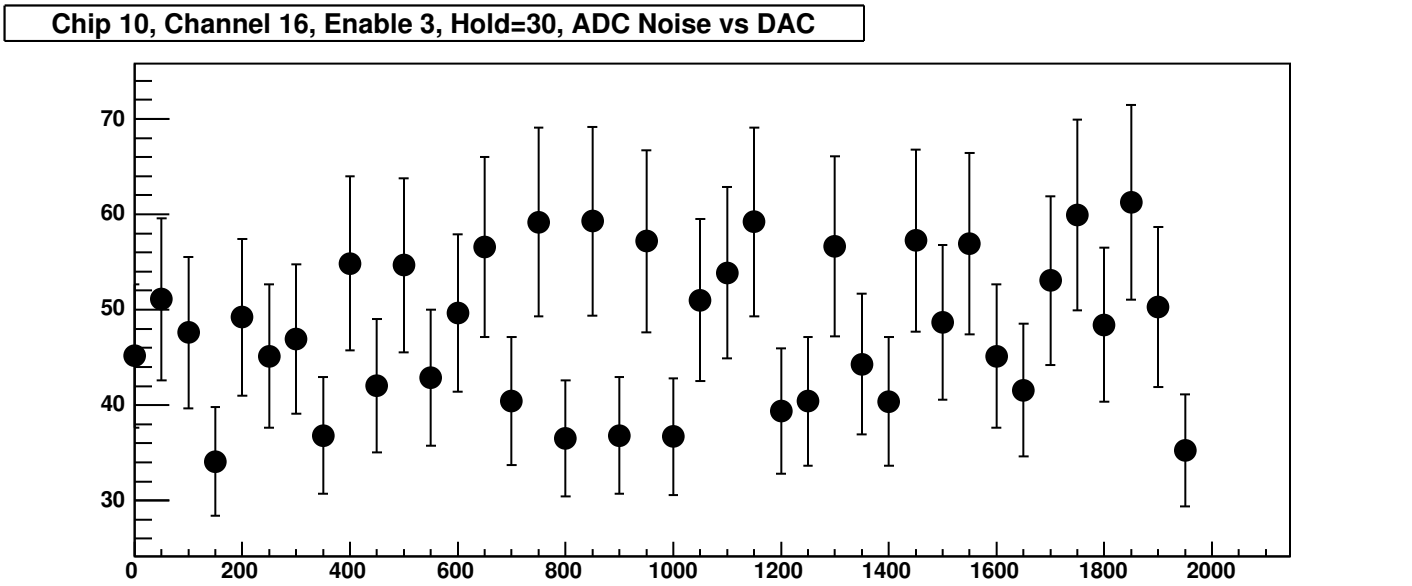

Chip 10, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

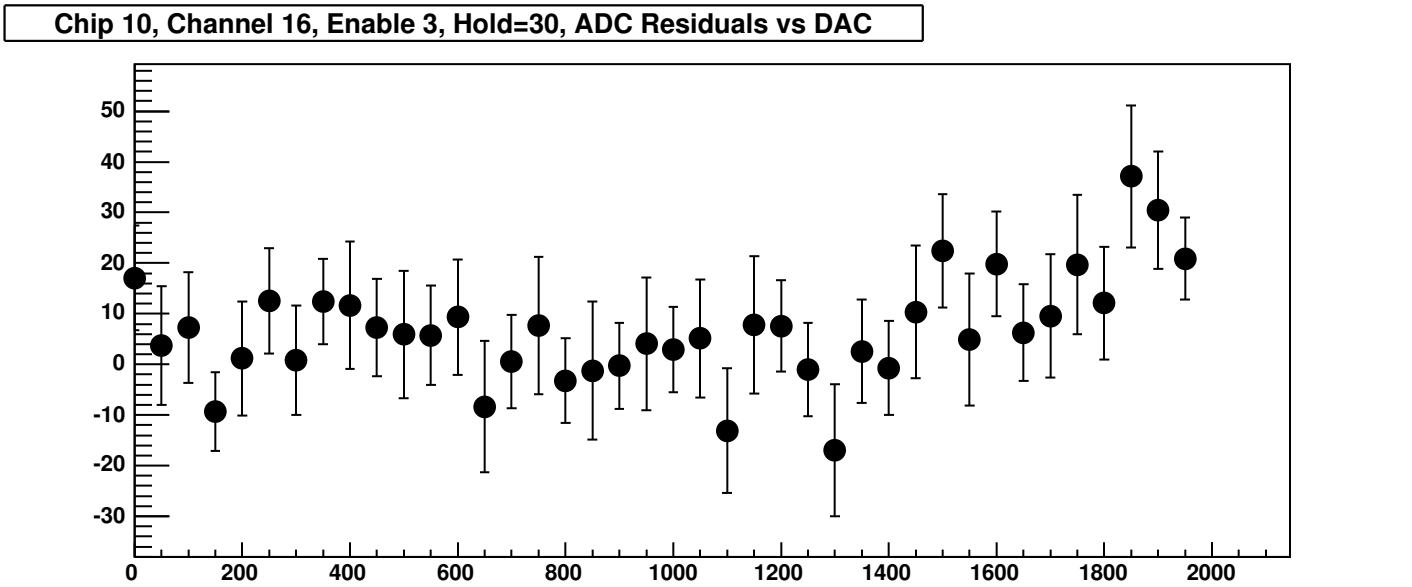

Chip 10, Channel 16, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 21.71 / 11
p0 -1196 ± 16.93
p1 0.2204 ± 0.01539
-700
-750
-800
-850
-900
-950
-1000
-1050
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

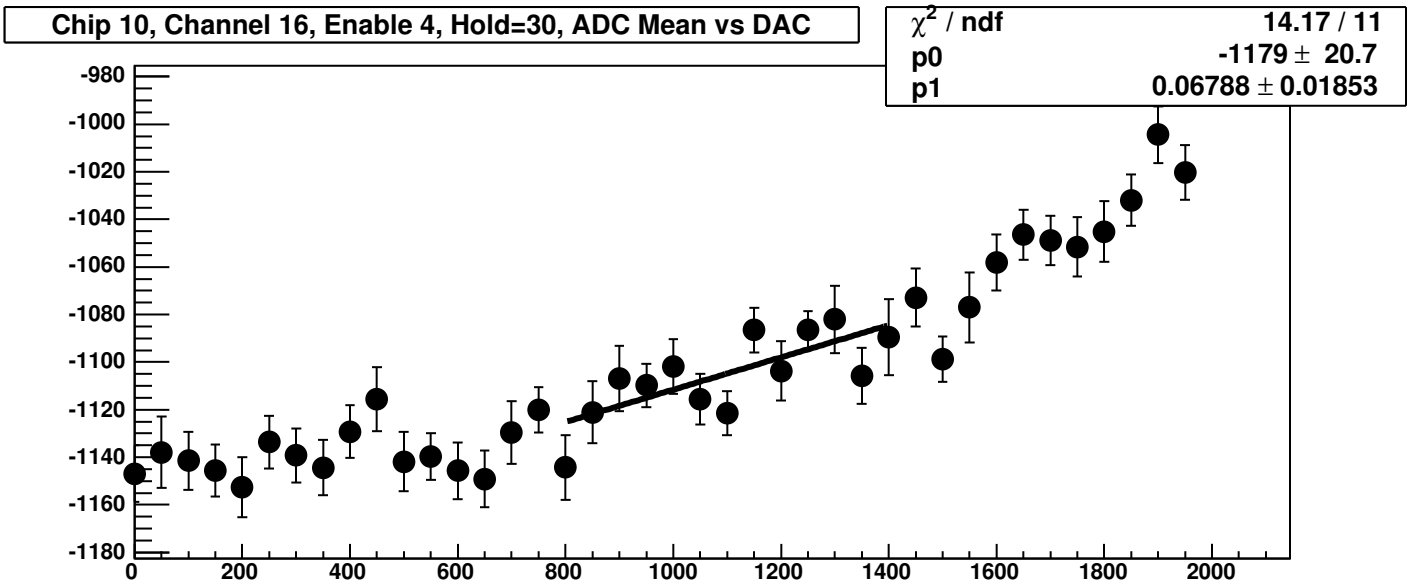

Chip 10, Channel 16, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

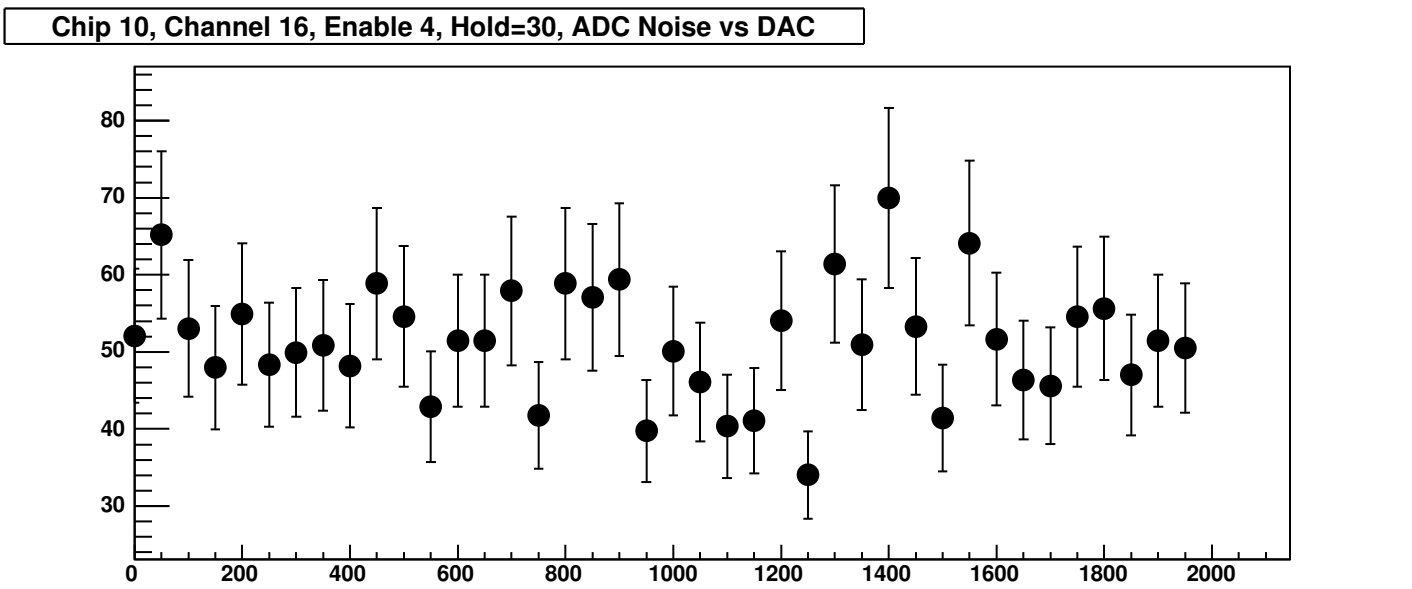

Chip 10, Channel 16, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

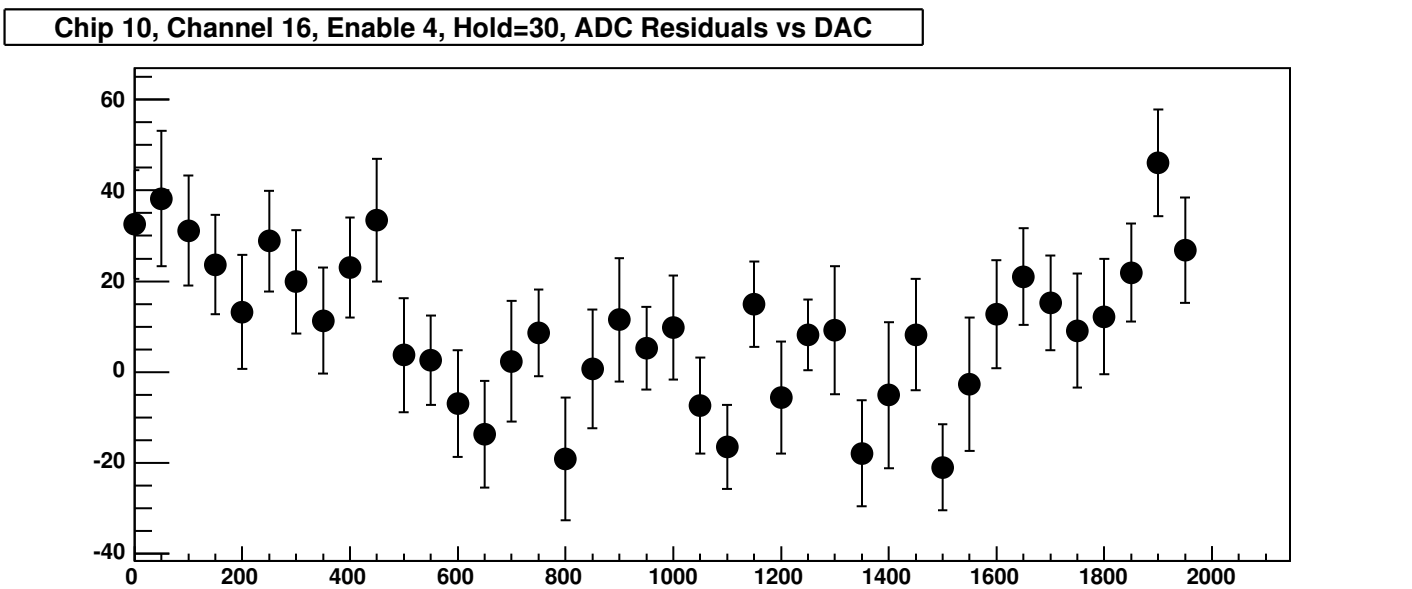

Chip 10, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 4.494 / 11
p0 -1188 ± 16.12
p1 0.03243 ± 0.0145
-1080
-1100
-1120
-1140
-1160
-1180
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

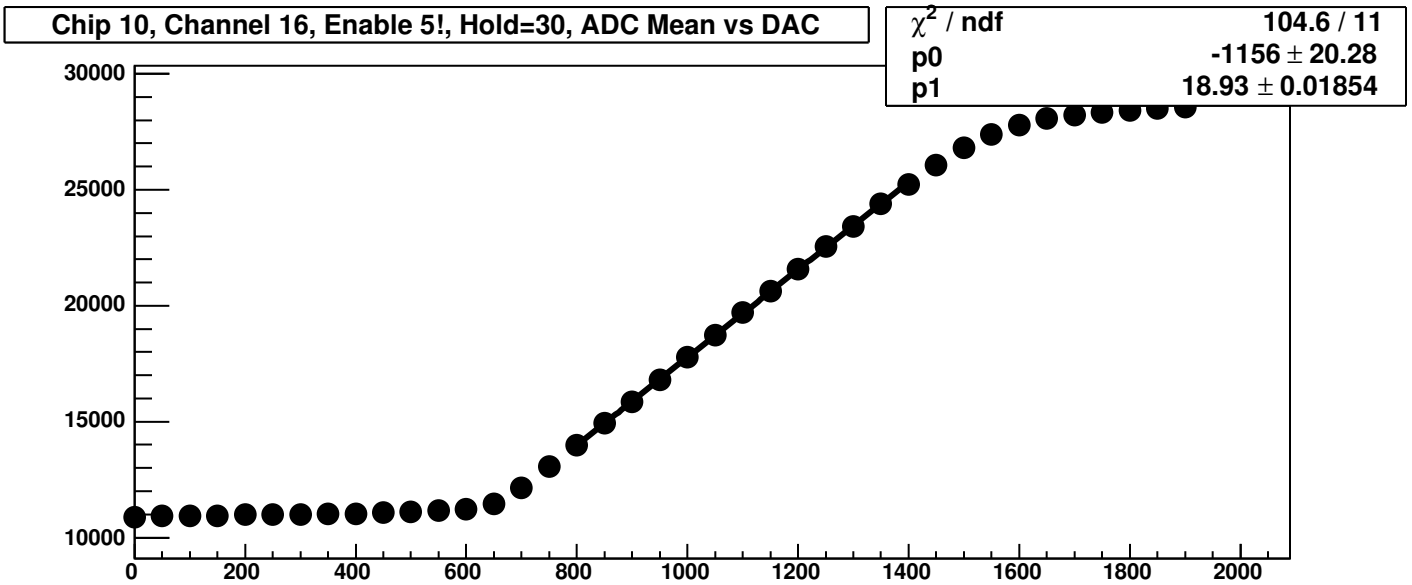

Chip 10, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

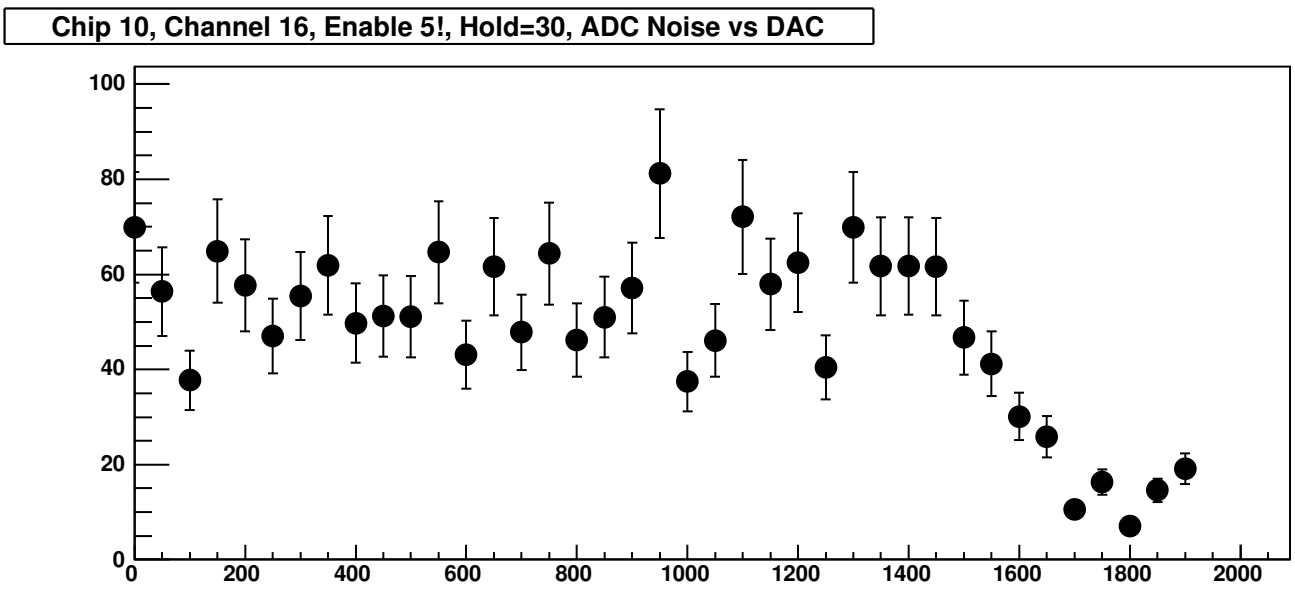

Chip 10, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC
50
40
30
20
10
0
-10
-20
-30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

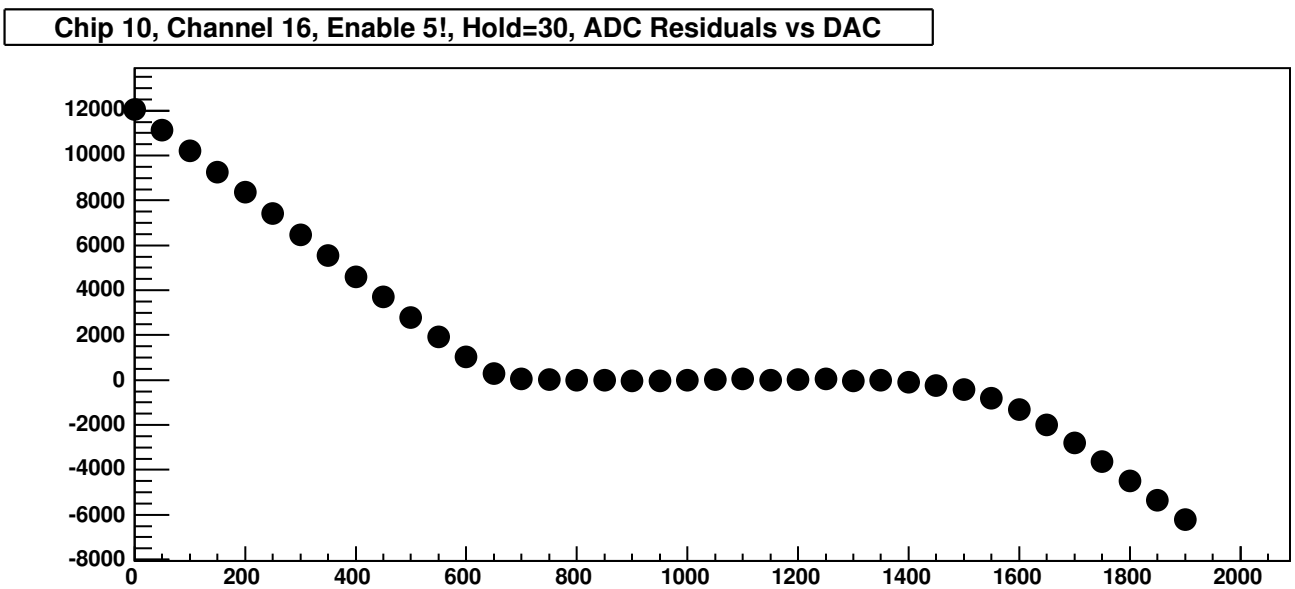

Chip 10, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 14.17 / 11
p0 -1179 ± 20.7
p1 0.06788 ± 0.01853
-980
-1000
-1020
-1040
-1060
-1080
-1100
-1120
-1140
-1160
-1180
0
200
400
600
800
1000
1200
1400
1600
1800
2000

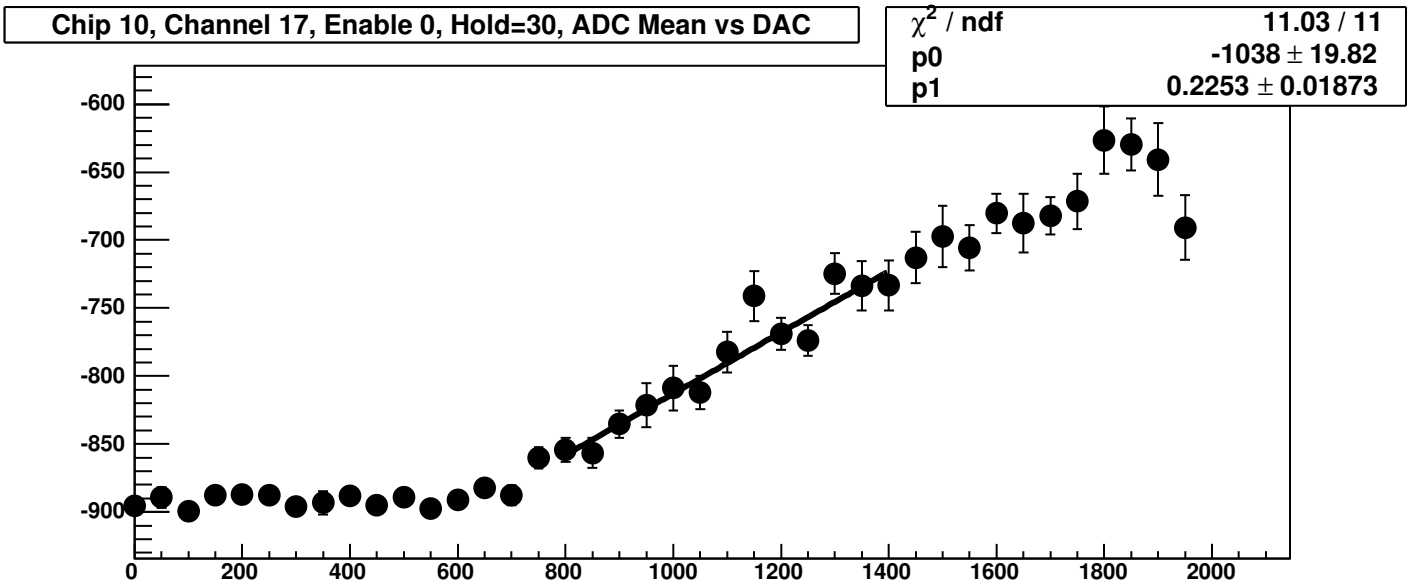

Chip 10, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

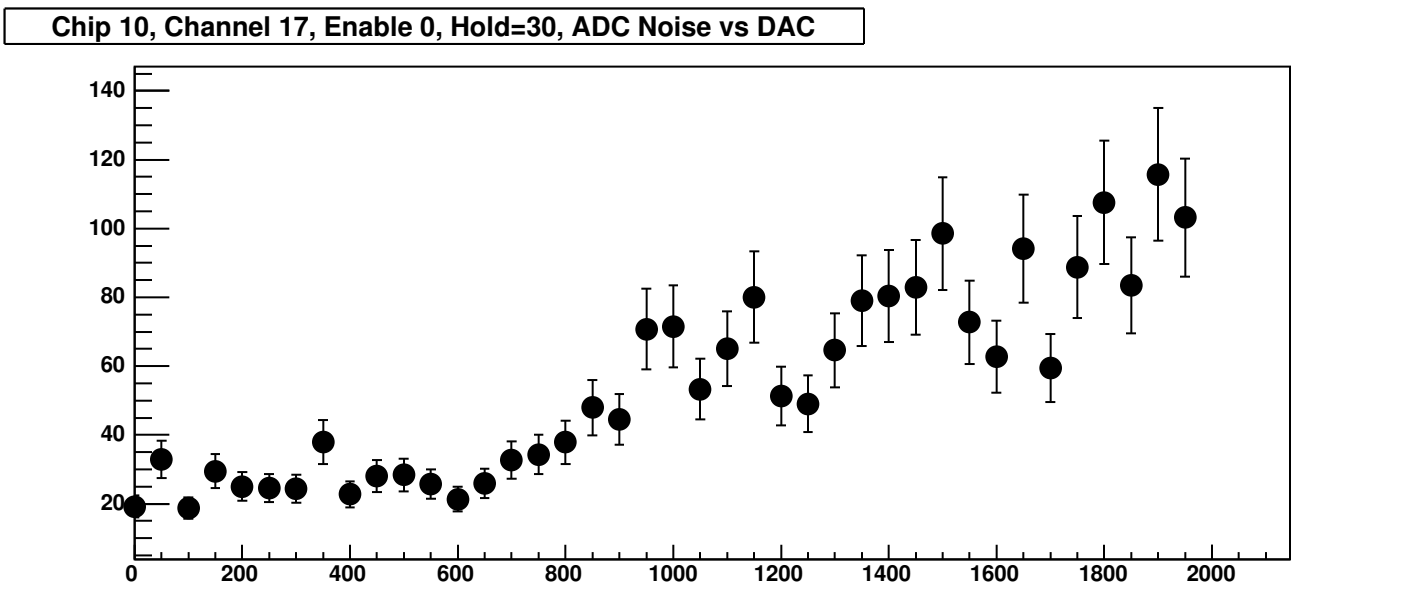

Chip 10, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

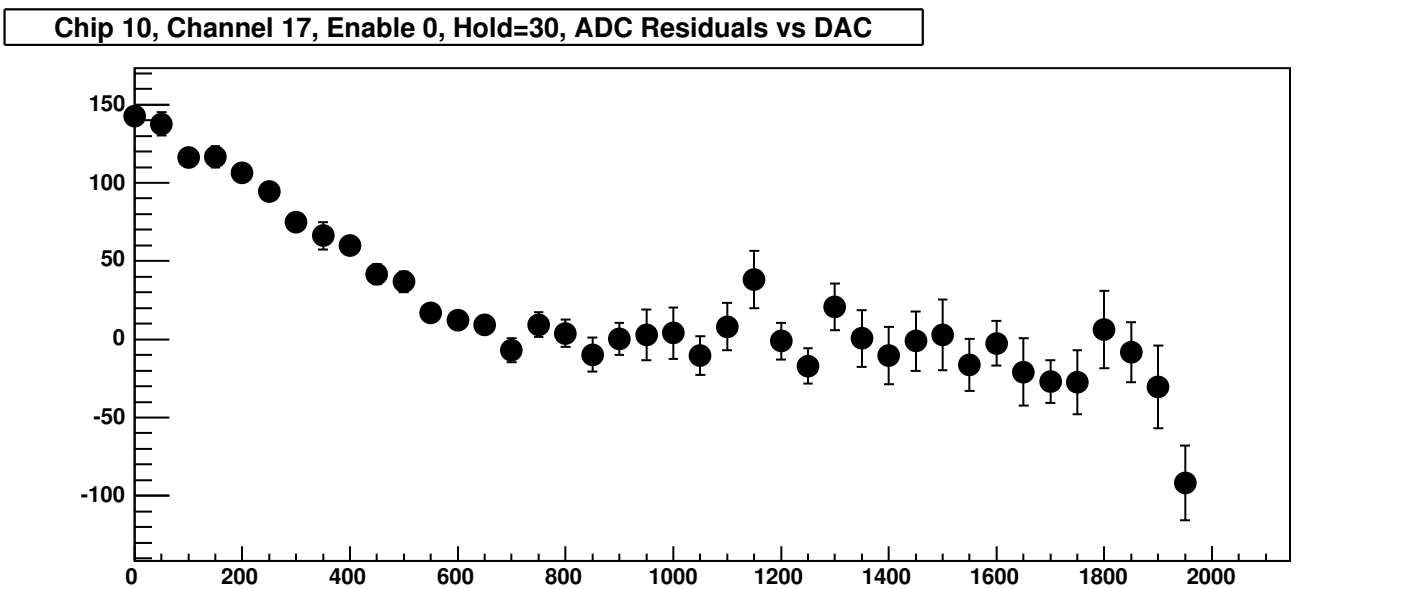

Chip 10, Channel 16, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 104.6 / 11
p0 -1156 ± 20.28
p1 18.93 ± 0.01854
30000
25000
20000
15000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 16, Enable 5!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 16, Enable 5!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

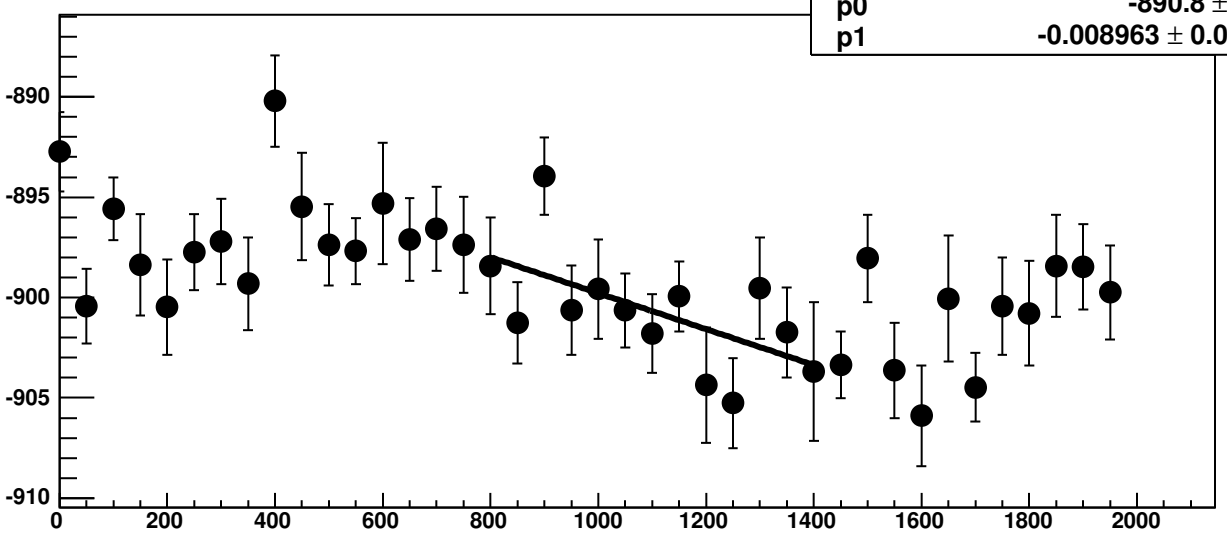

Chip 10, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 11.03 / 11
p0 -1038 ± 19.82
p1 0.2253 ± 0.01873
-600
-650
-700
-750
-800
-850
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

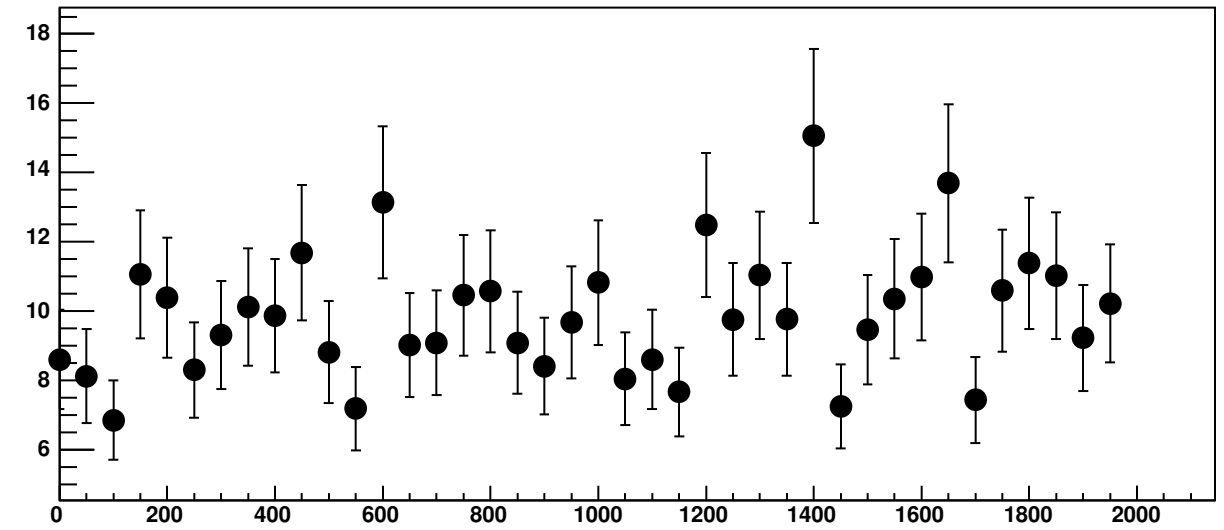

Chip 10, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
-100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

Chip 10, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC

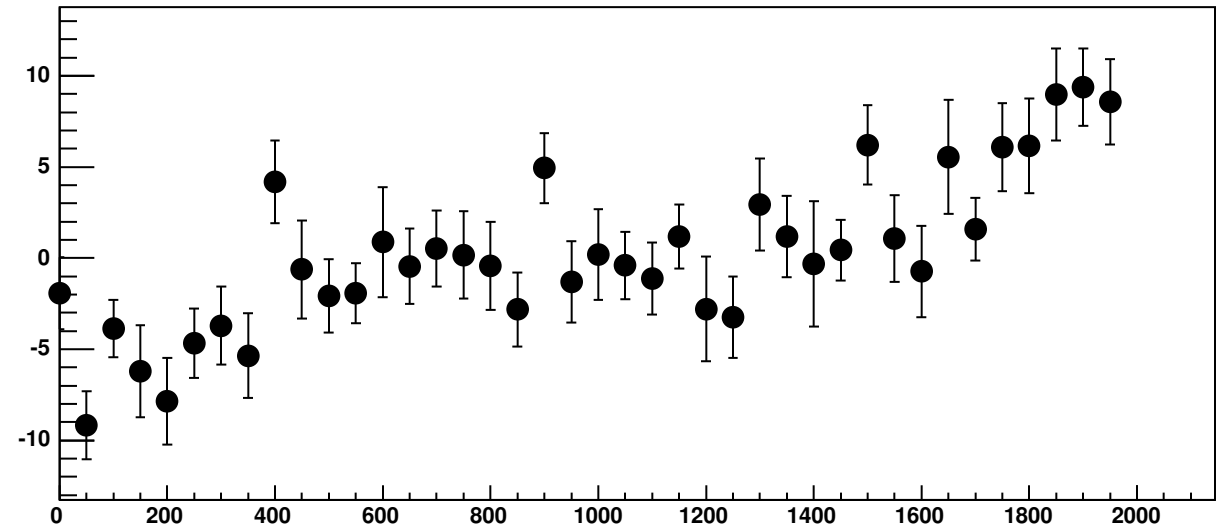
χ^2 / ndf	14.35 / 11
p0	-890.8 ± 3.869
p1	-0.008963 ± 0.003548

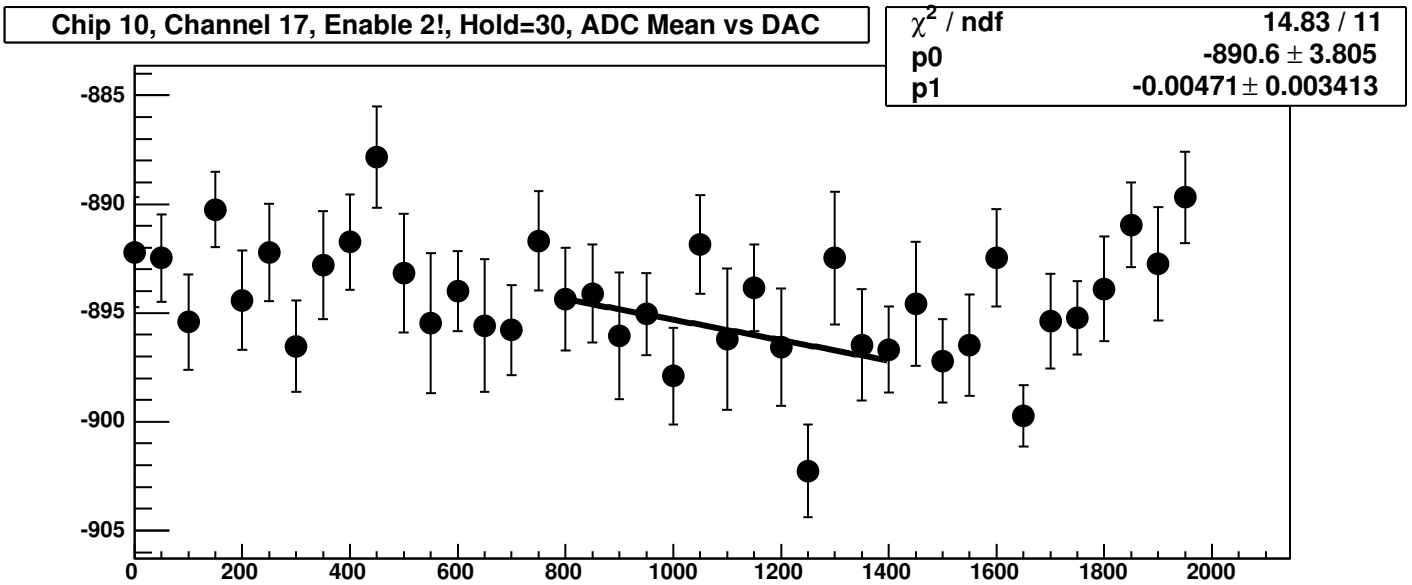


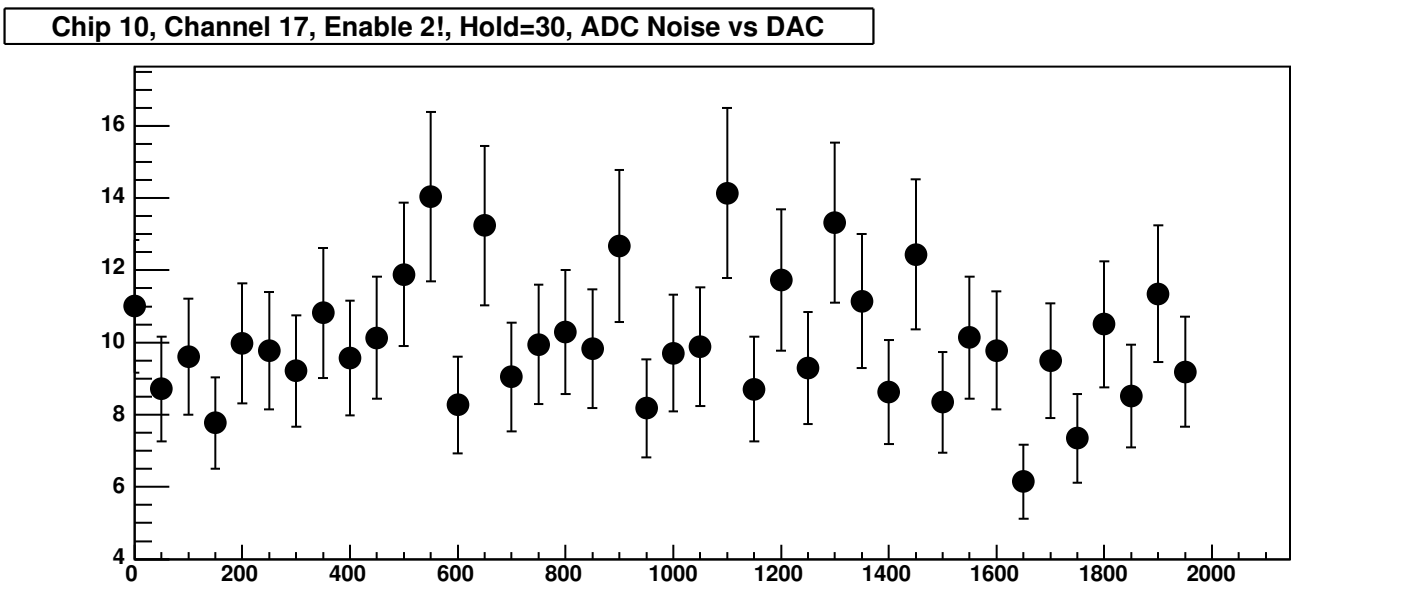
Chip 10, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC

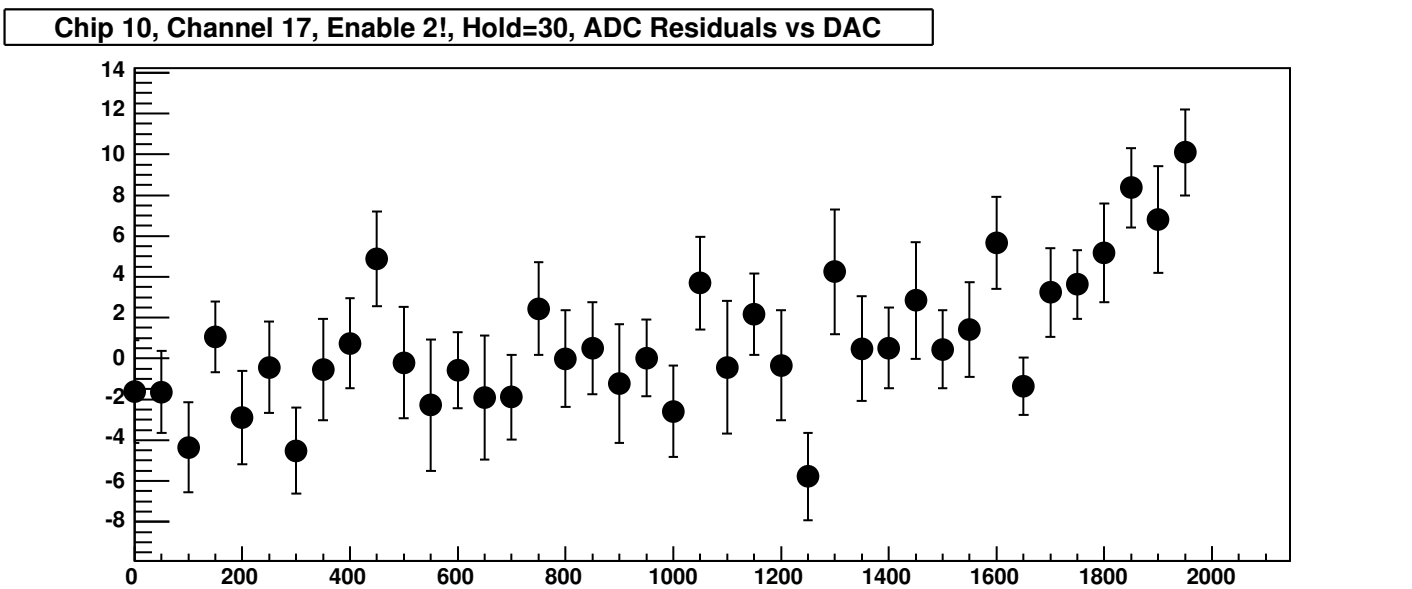


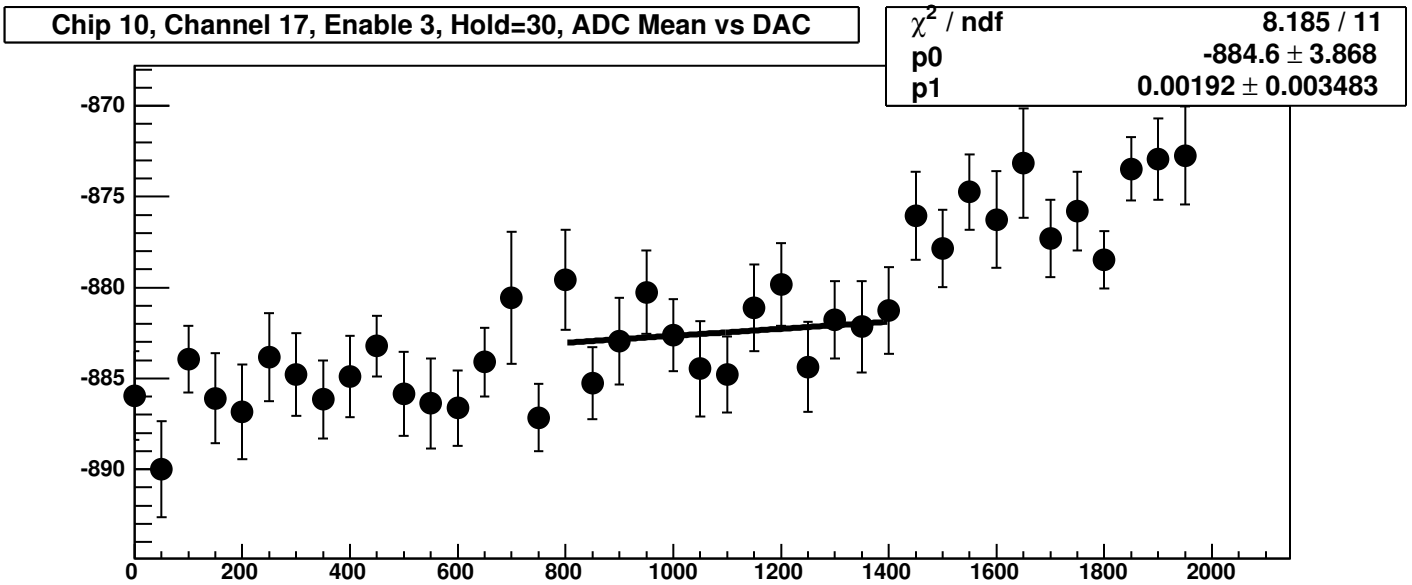
Chip 10, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC

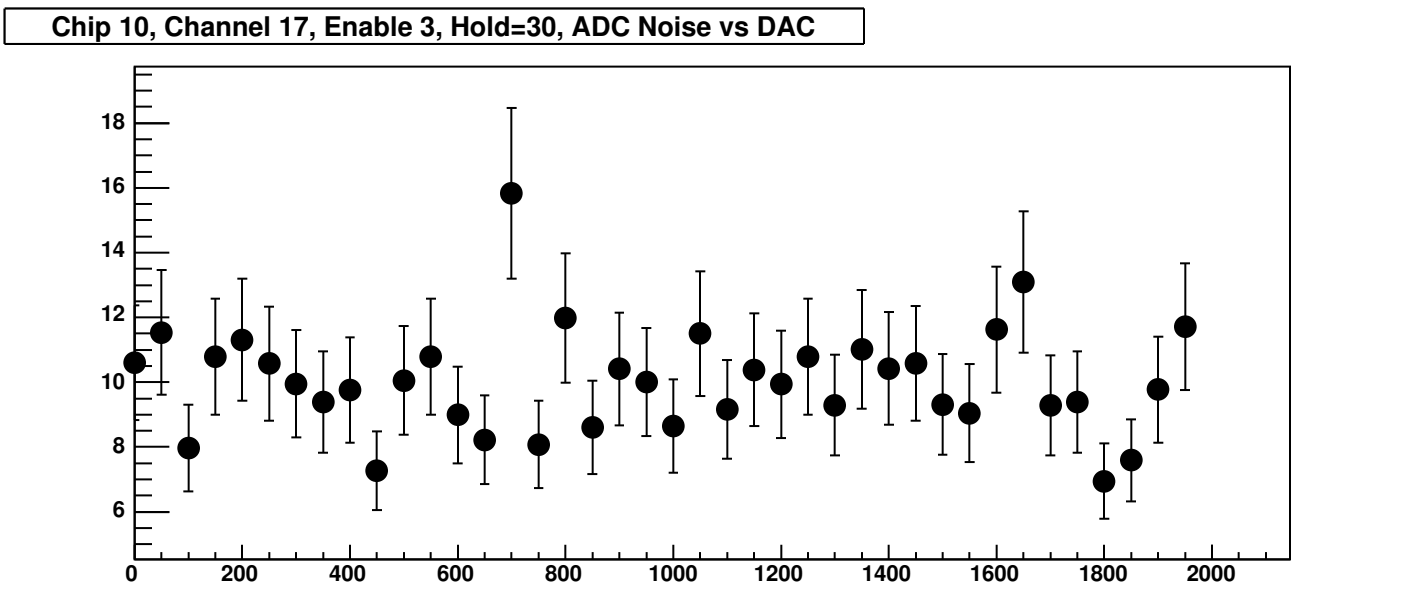


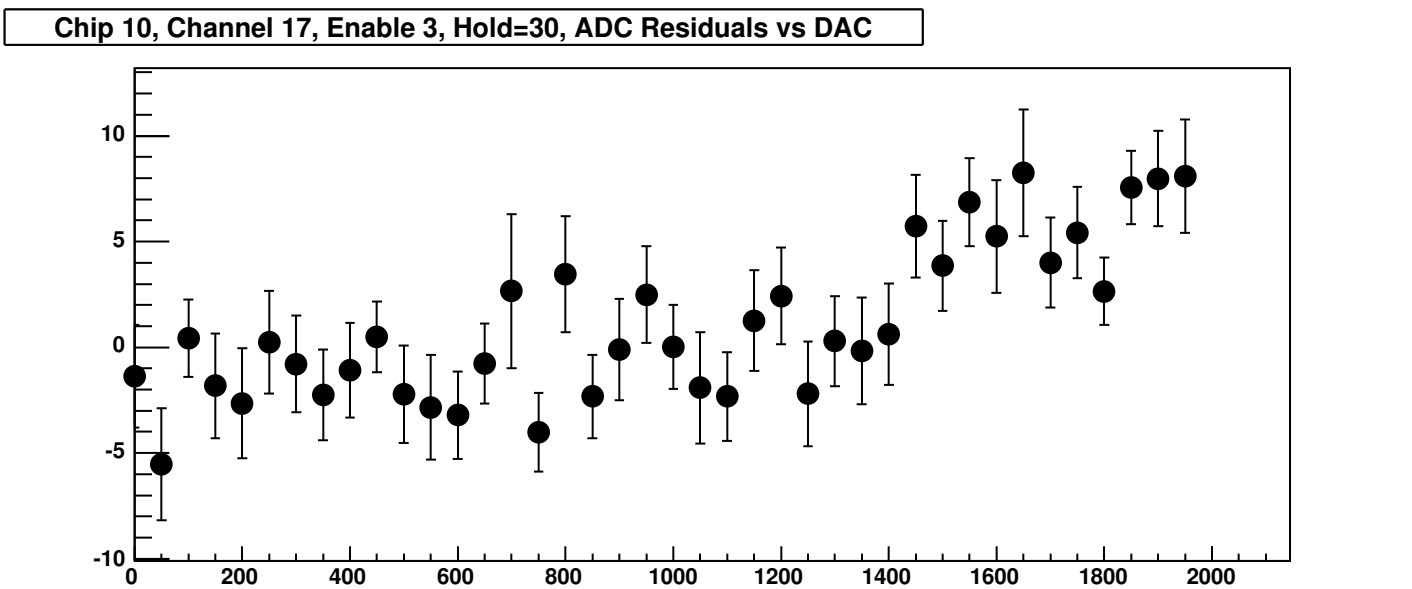

Chip 10, Channel 17, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 14.83 / 11
p0 -890.6 ± 3.805
p1 -0.00471 ± 0.003413
-885
-890
-895
-900
-905
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 17, Enable 2!, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
4
0
200
400
600
800
1000
1200
1400
1600
1800
2000

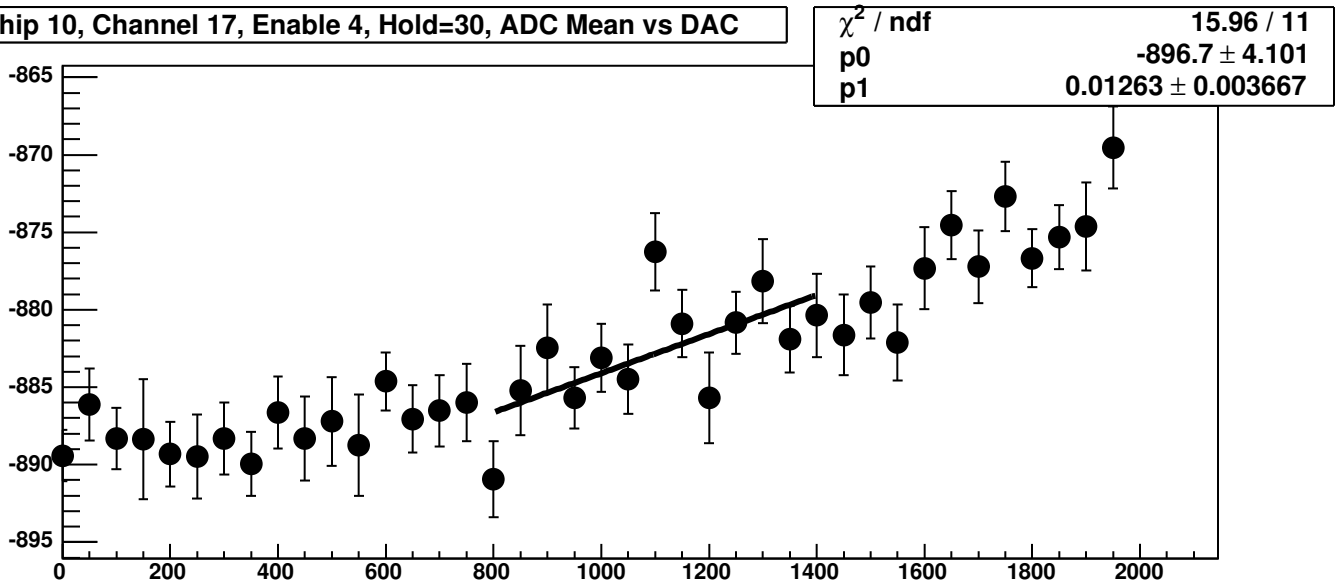

Chip 10, Channel 17, Enable 2!, Hold=30, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 8.185 / 11
p0 -884.6 ± 3.868
p1 0.00192 ± 0.003483
-870
-875
-880
-885
-890
0
200
400
600
800
1000
1200
1400
1600
1800
2000

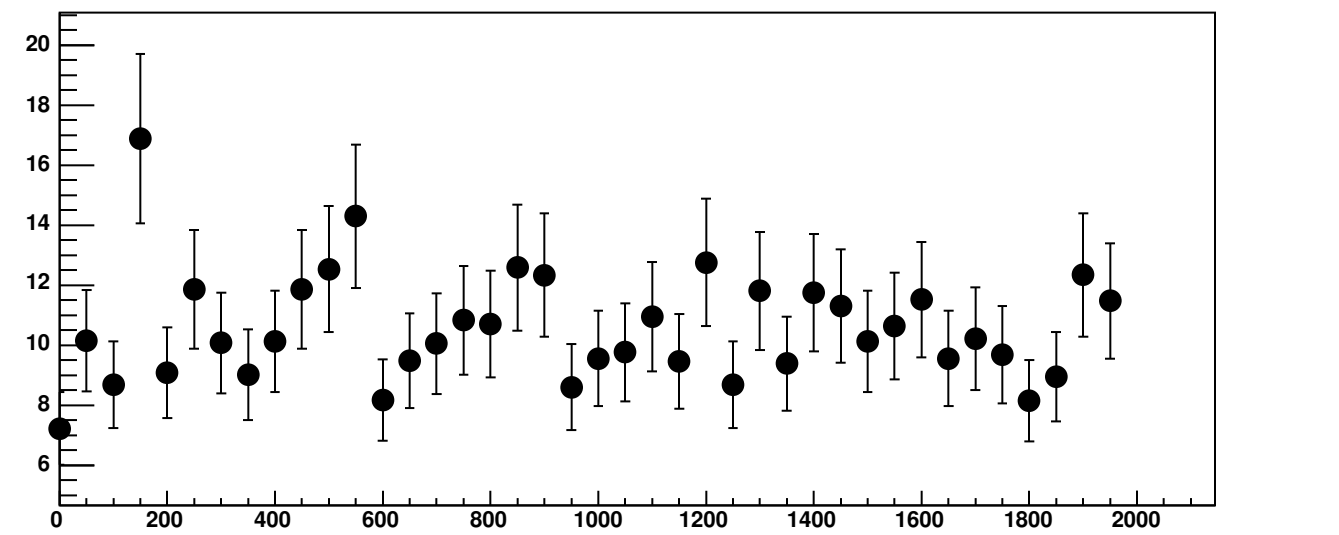

Chip 10, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC
18
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 10, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

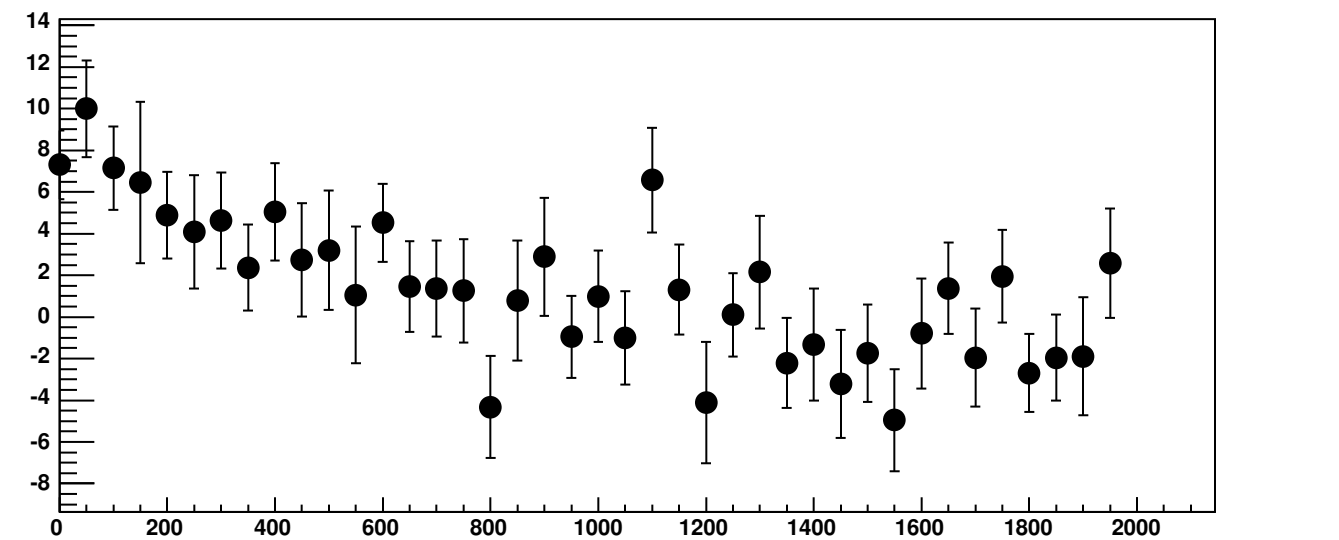
Chip 10, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC



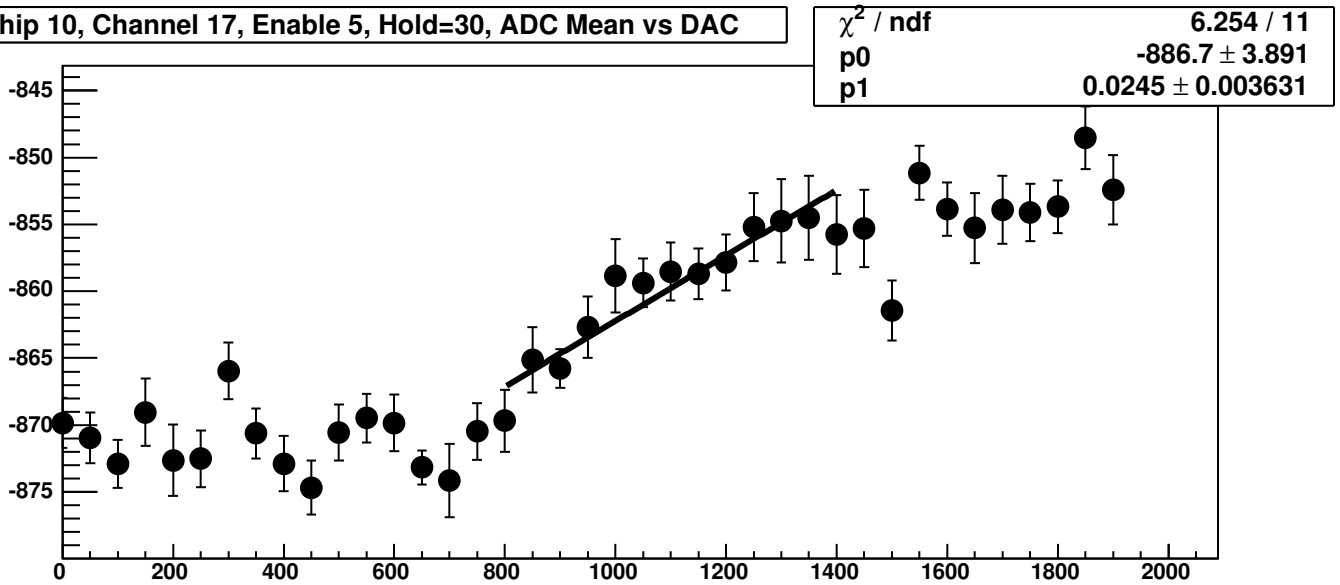
Chip 10, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC



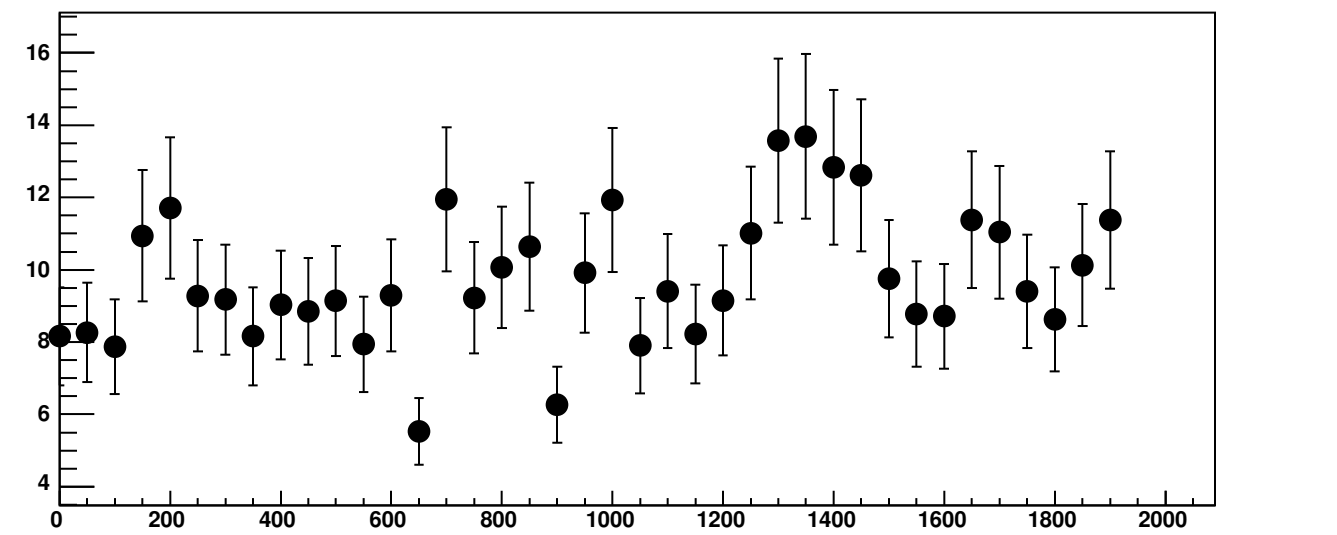
Chip 10, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC



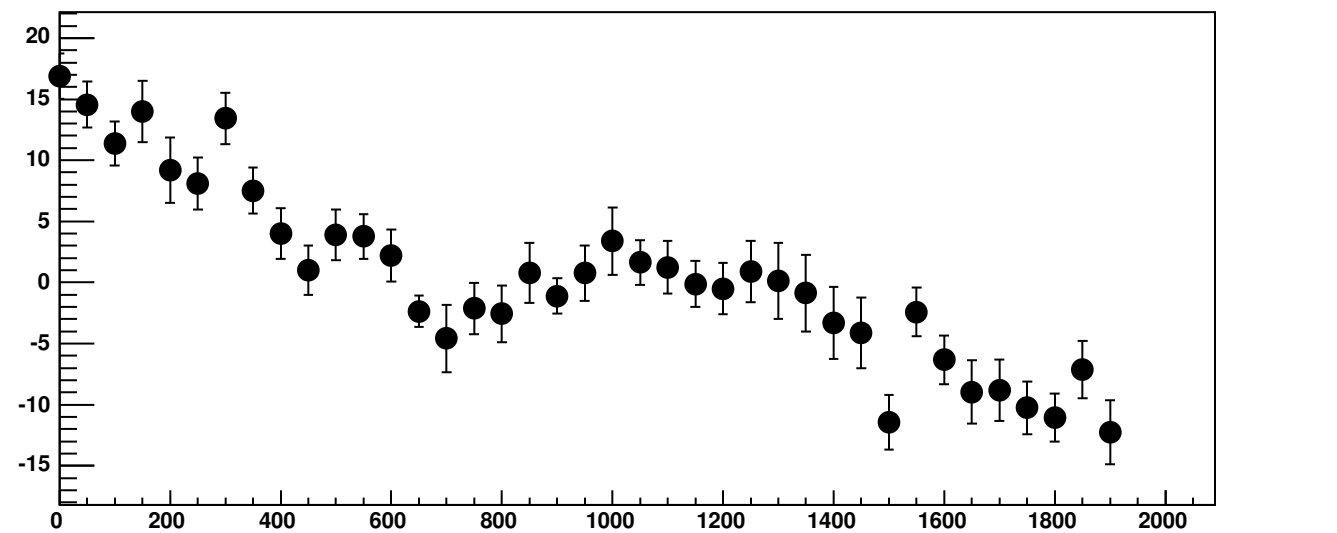
Chip 10, Channel 17, Enable 5, Hold=30, ADC Mean vs DAC



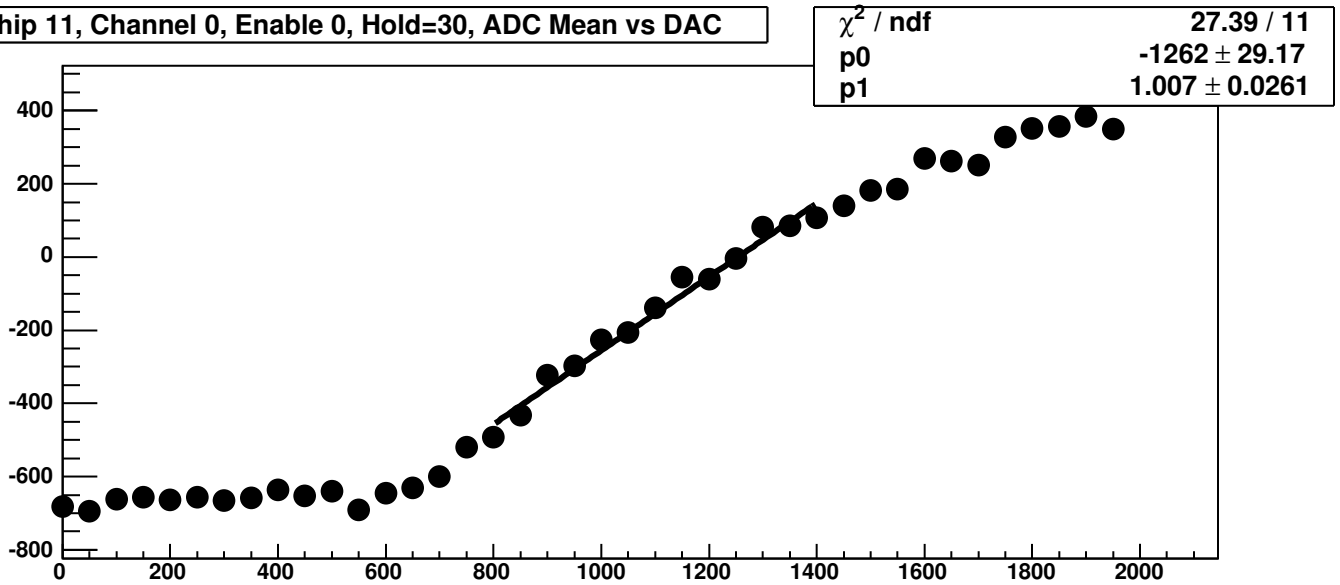
Chip 10, Channel 17, Enable 5, Hold=30, ADC Noise vs DAC



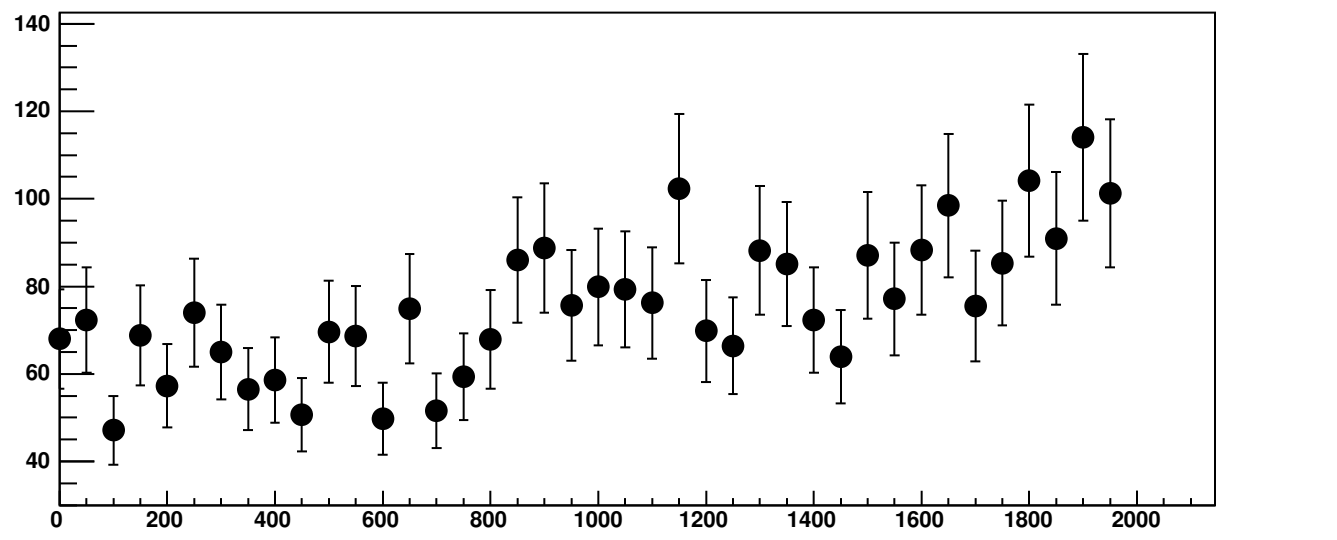
Chip 10, Channel 17, Enable 5, Hold=30, ADC Residuals vs DAC



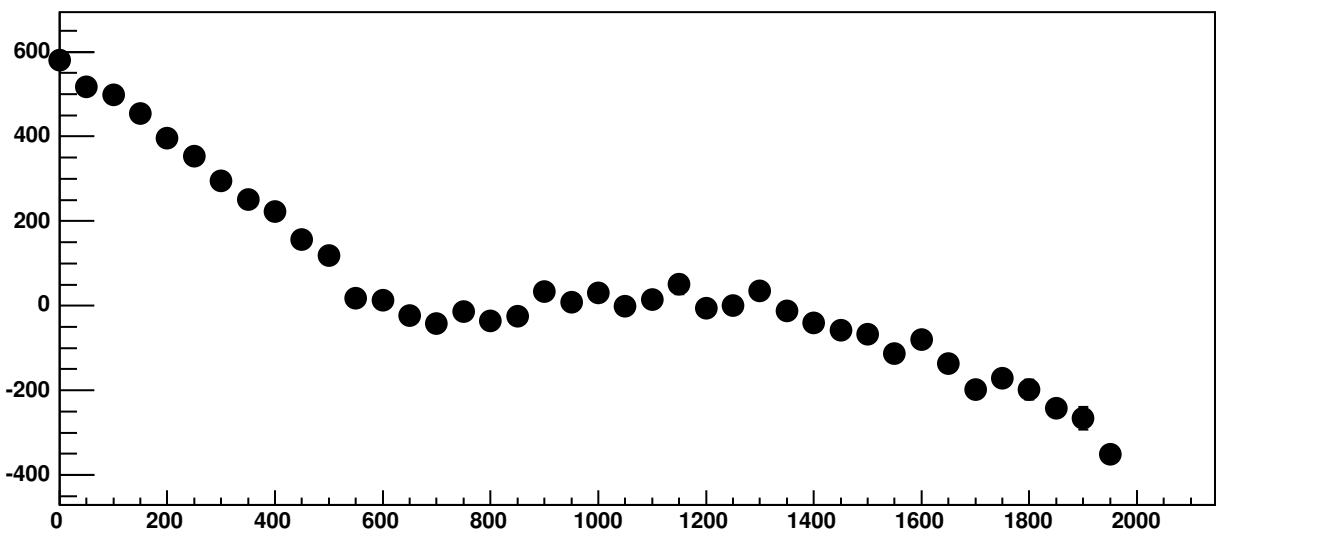
Chip 11, Channel 0, Enable 0, Hold=30, ADC Mean vs DAC

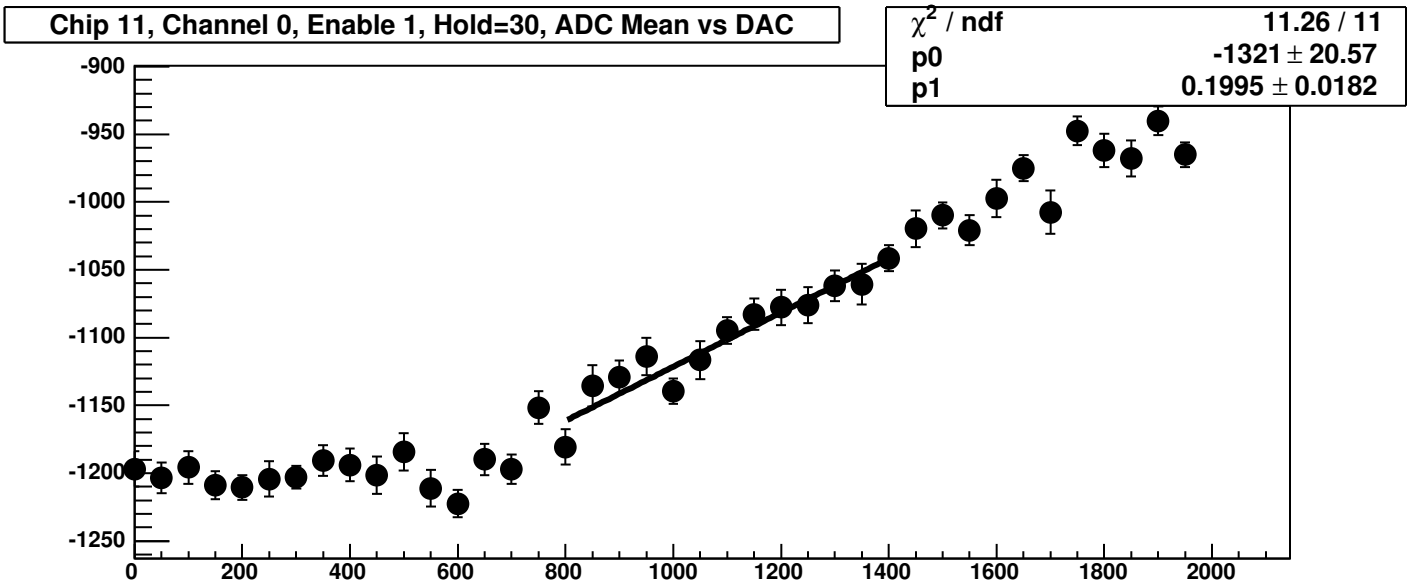


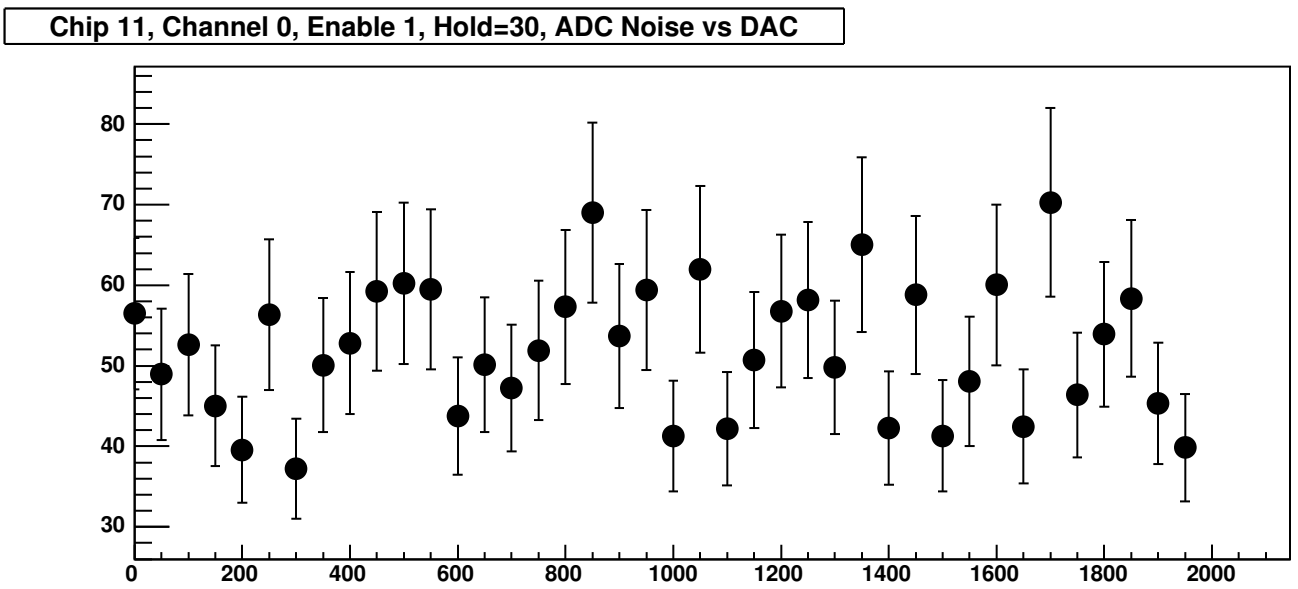
Chip 11, Channel 0, Enable 0, Hold=30, ADC Noise vs DAC

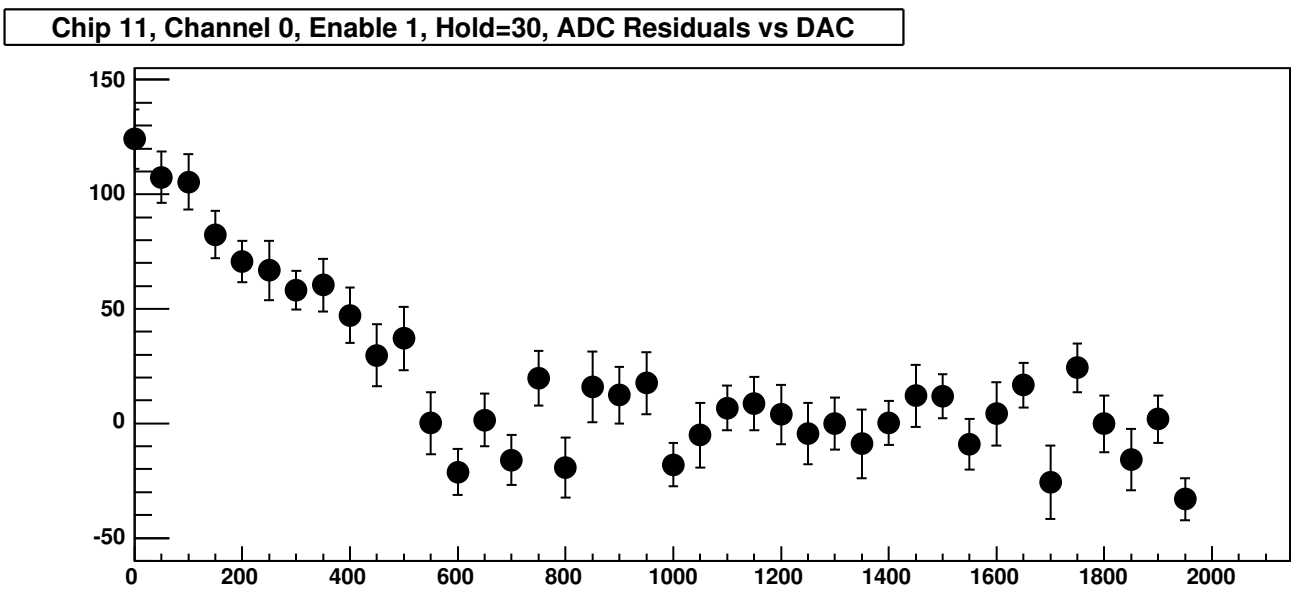


Chip 11, Channel 0, Enable 0, Hold=30, ADC Residuals vs DAC

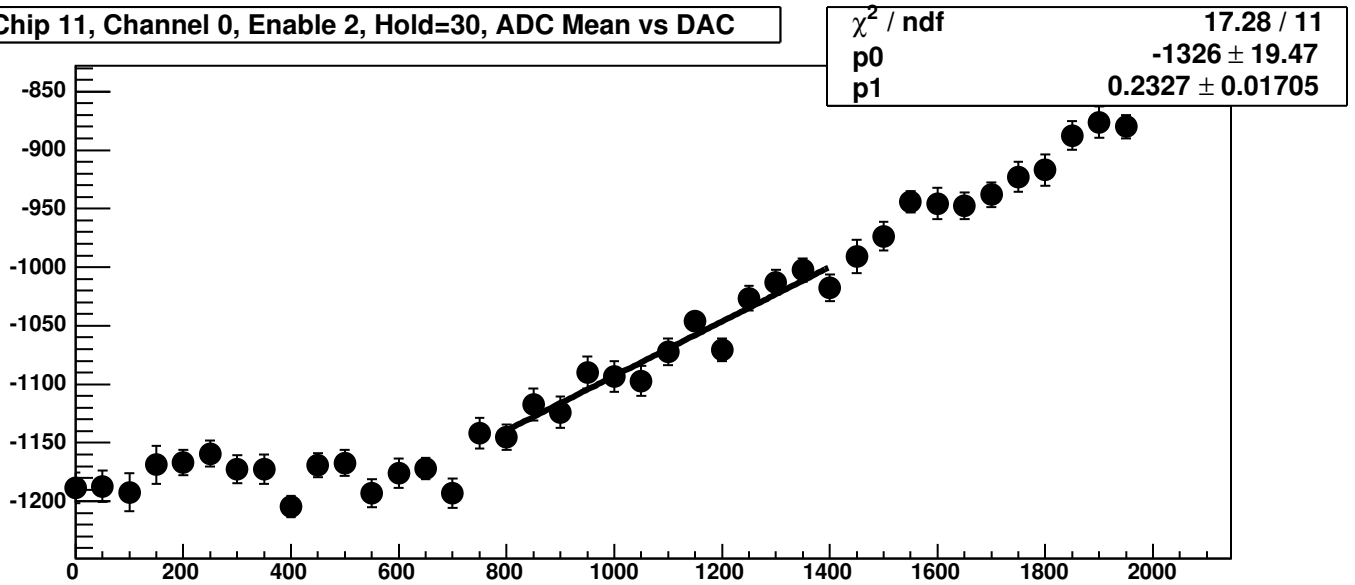



Chip 11, Channel 0, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 11.26 / 11
p0 -1321 ± 20.57
p1 0.1995 ± 0.0182
-900
-950
-1000
-1050
-1100
-1150
-1200
-1250
0
200
400
600
800
1000
1200
1400
1600
1800
2000

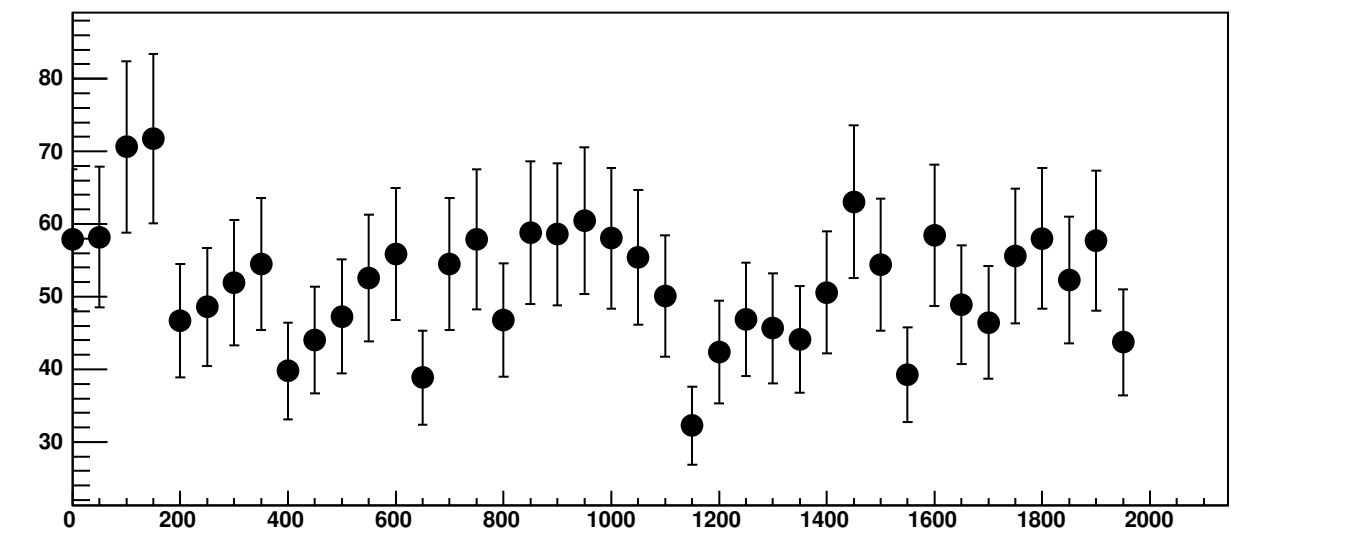

Chip 11, Channel 0, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 0, Enable 1, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

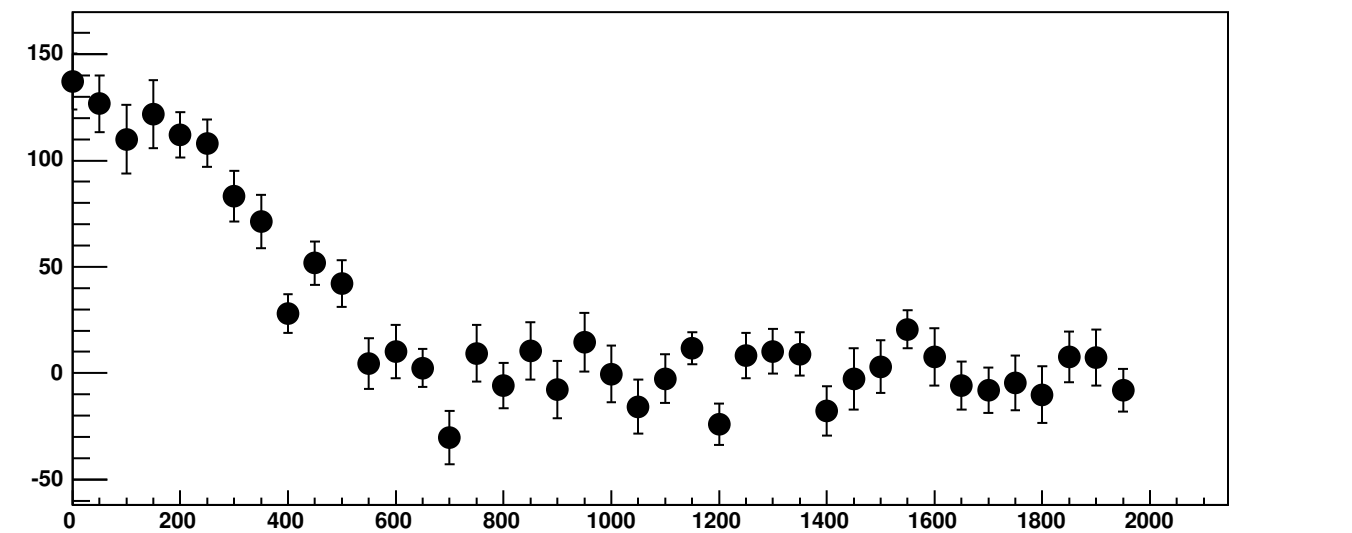
Chip 11, Channel 0, Enable 2, Hold=30, ADC Mean vs DAC

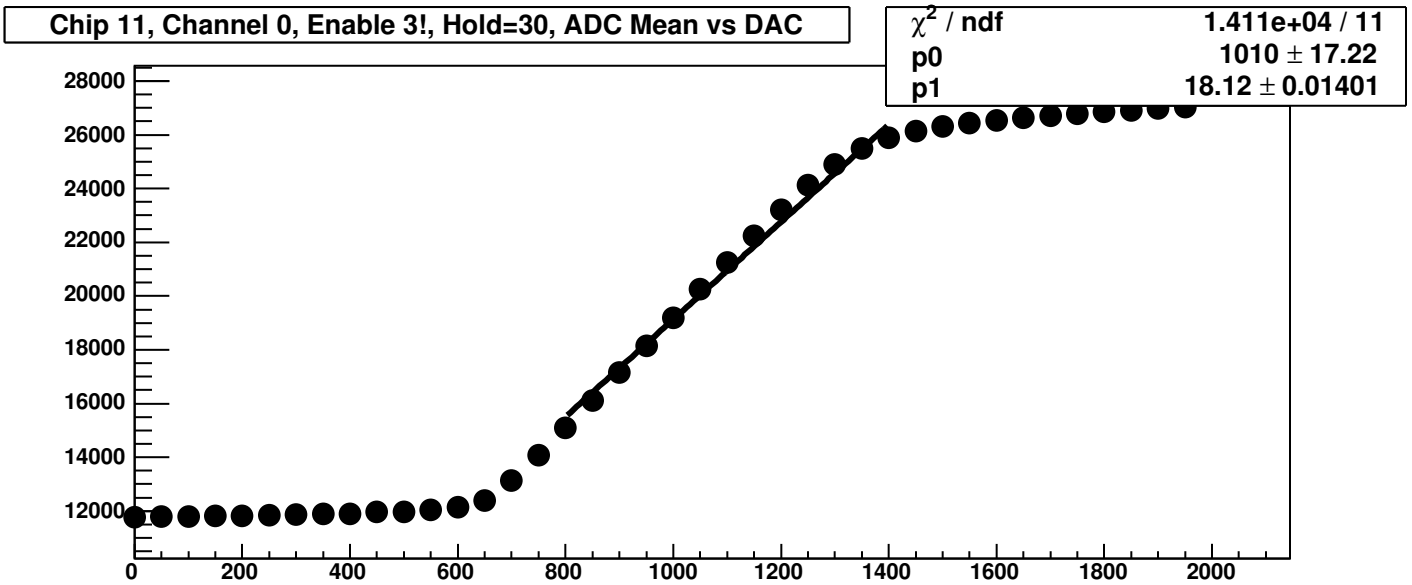


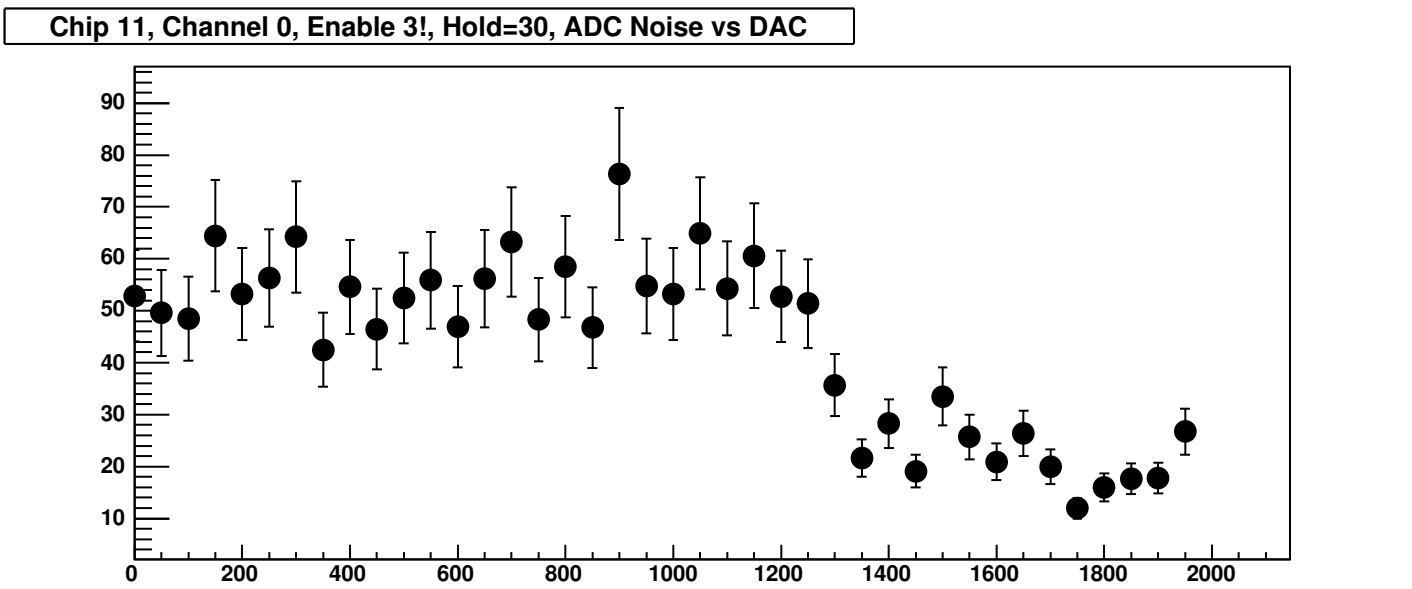
Chip 11, Channel 0, Enable 2, Hold=30, ADC Noise vs DAC

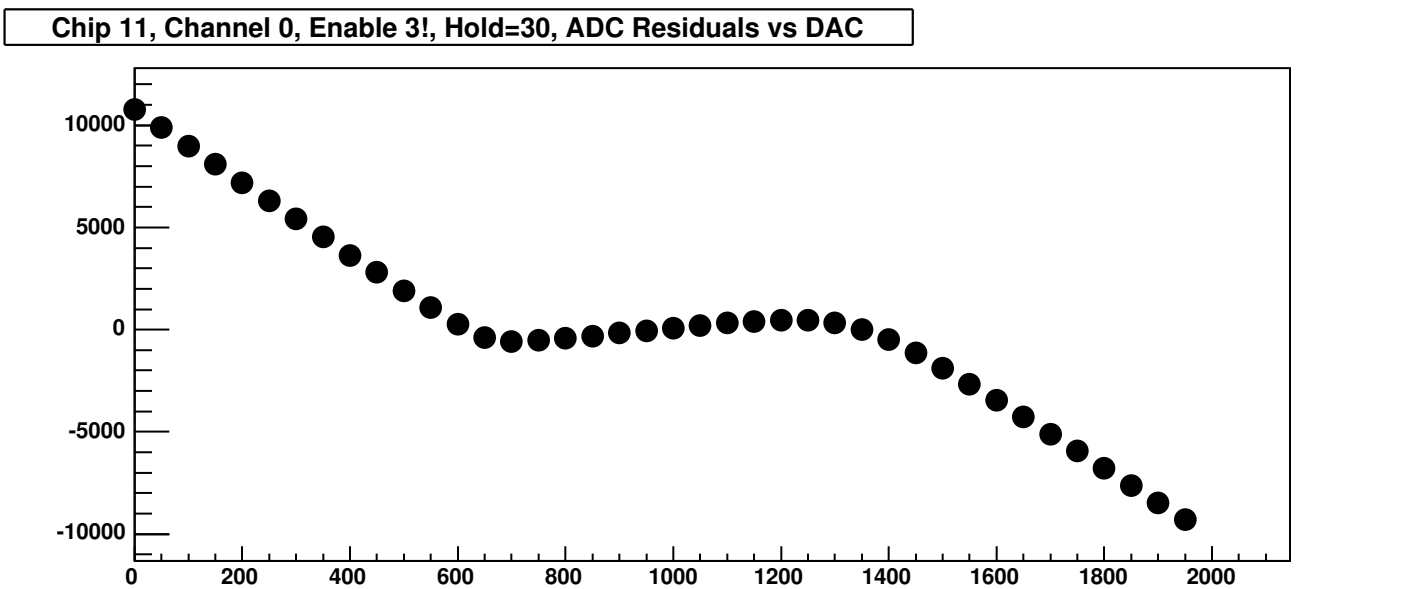


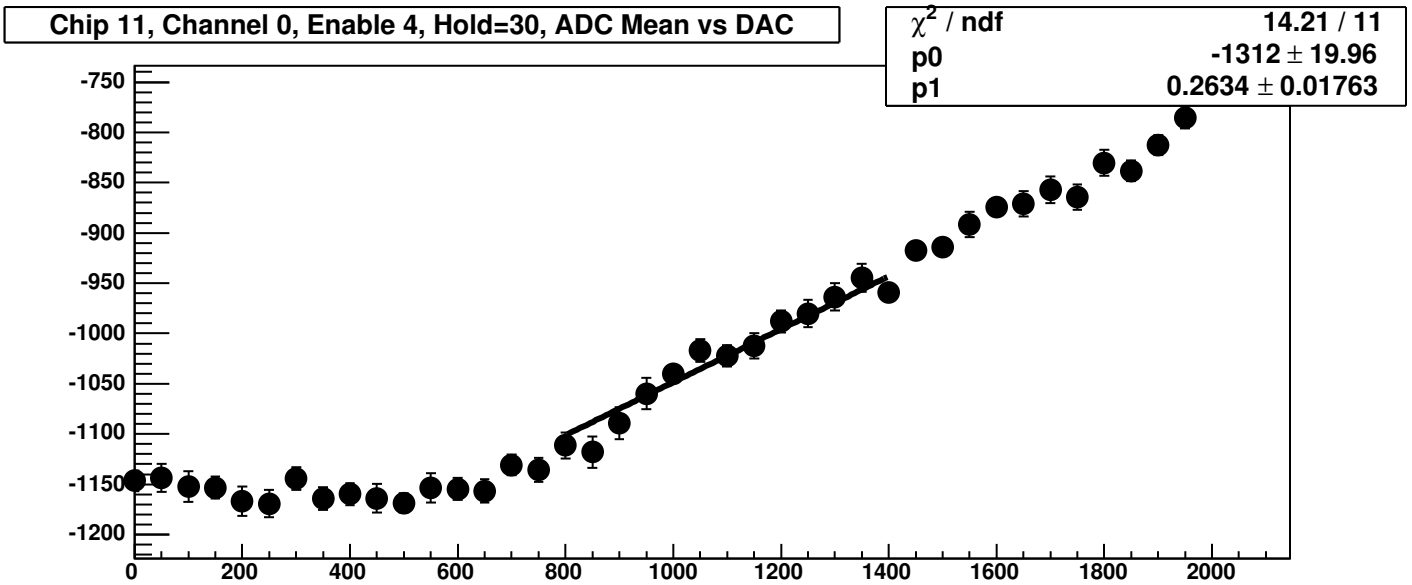
Chip 11, Channel 0, Enable 2, Hold=30, ADC Residuals vs DAC

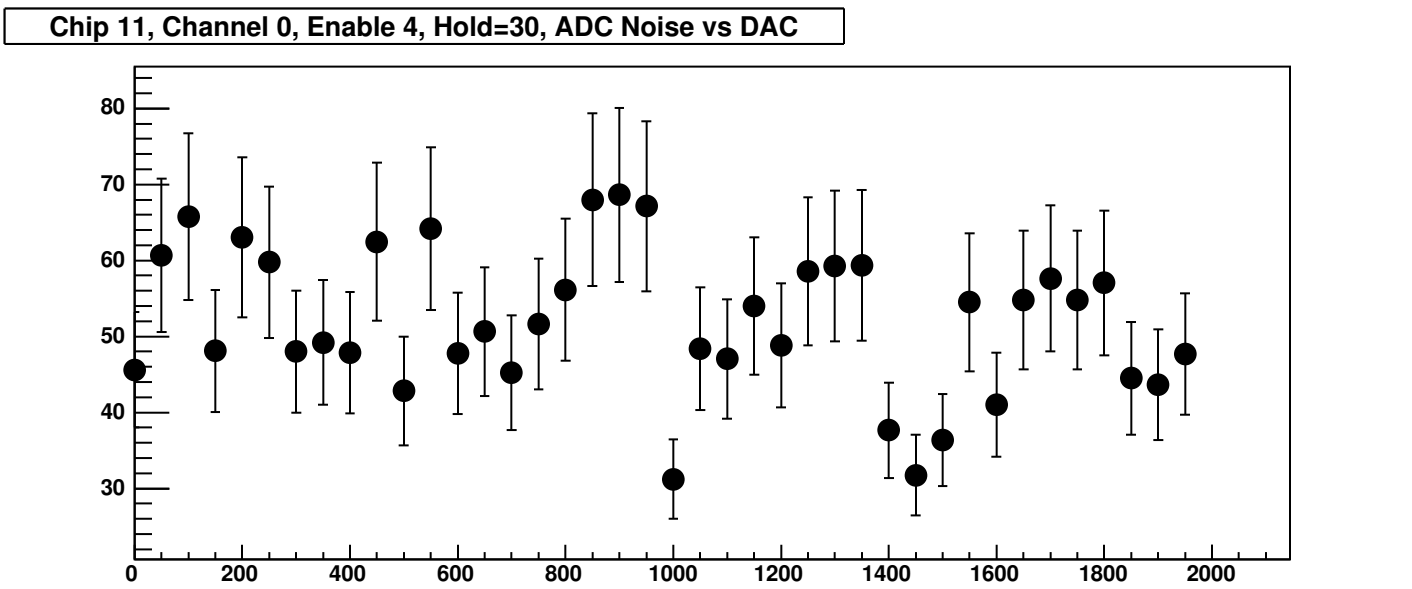


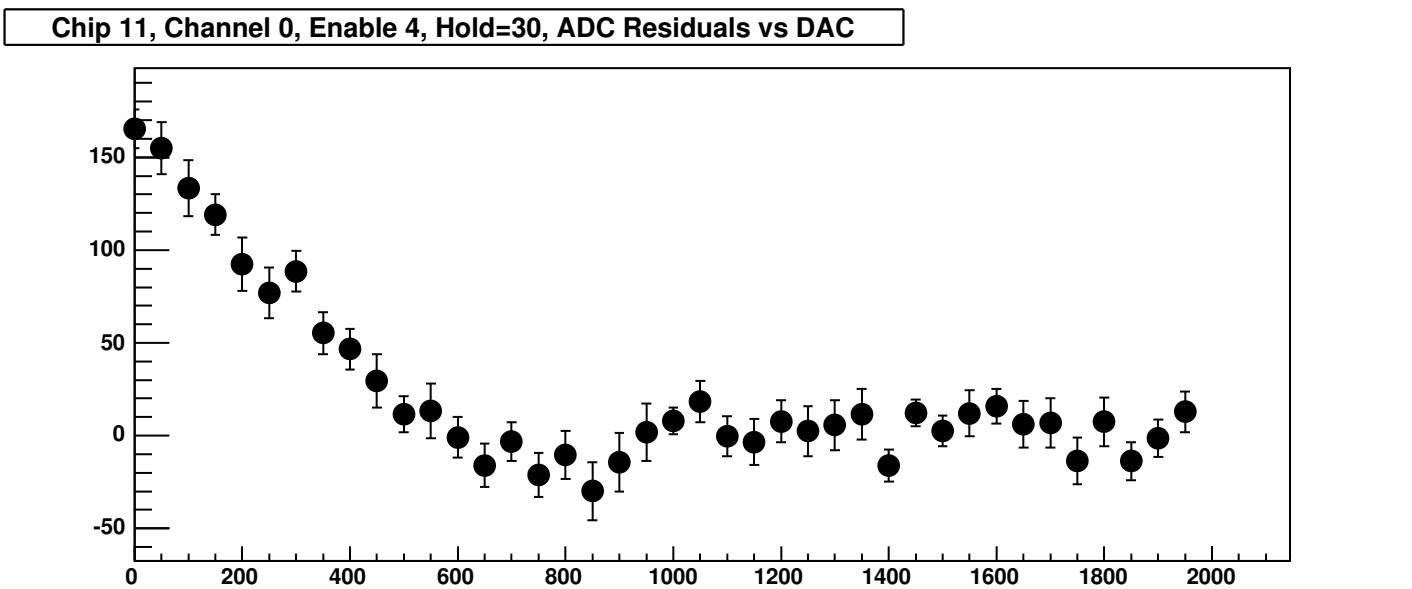

Chip 11, Channel 0, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 1.411e+04 / 11
p0 1010 ± 17.22
p1 18.12 ± 0.01401
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

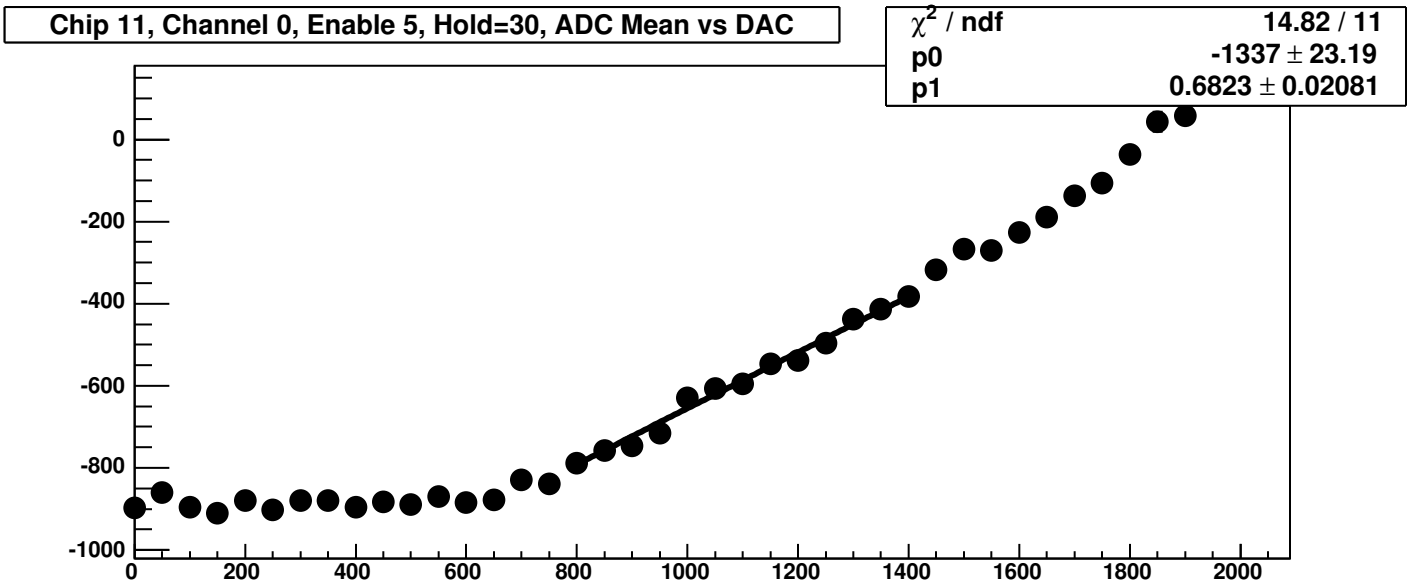

Chip 11, Channel 0, Enable 3!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

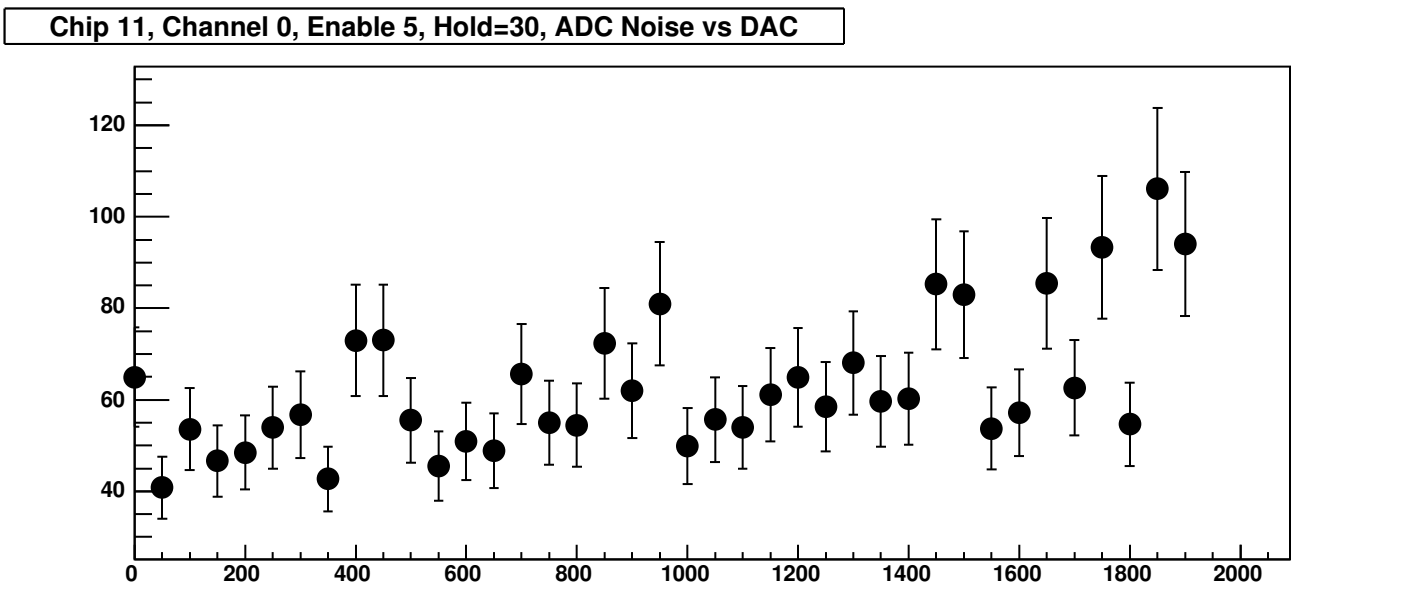

Chip 11, Channel 0, Enable 3!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

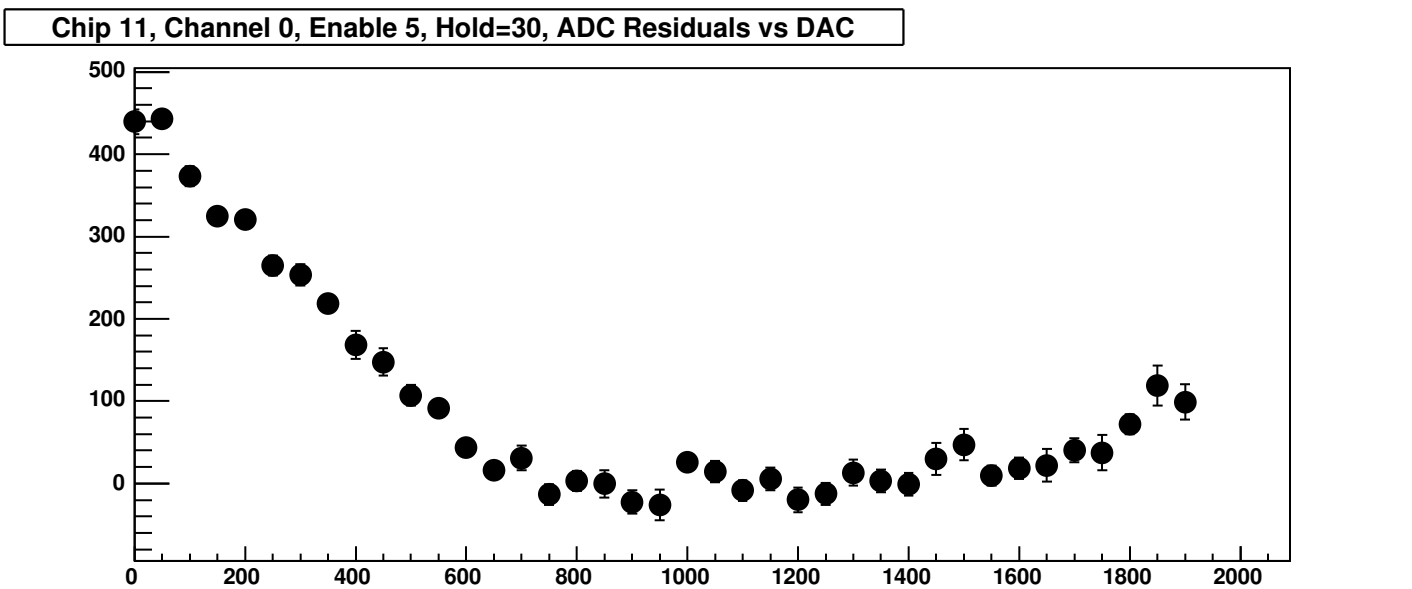

Chip 11, Channel 0, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 14.21 / 11
p0 -1312 ± 19.96
p1 0.2634 ± 0.01763
-750
-800
-850
-900
-950
-1000
-1050
-1100
-1150
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

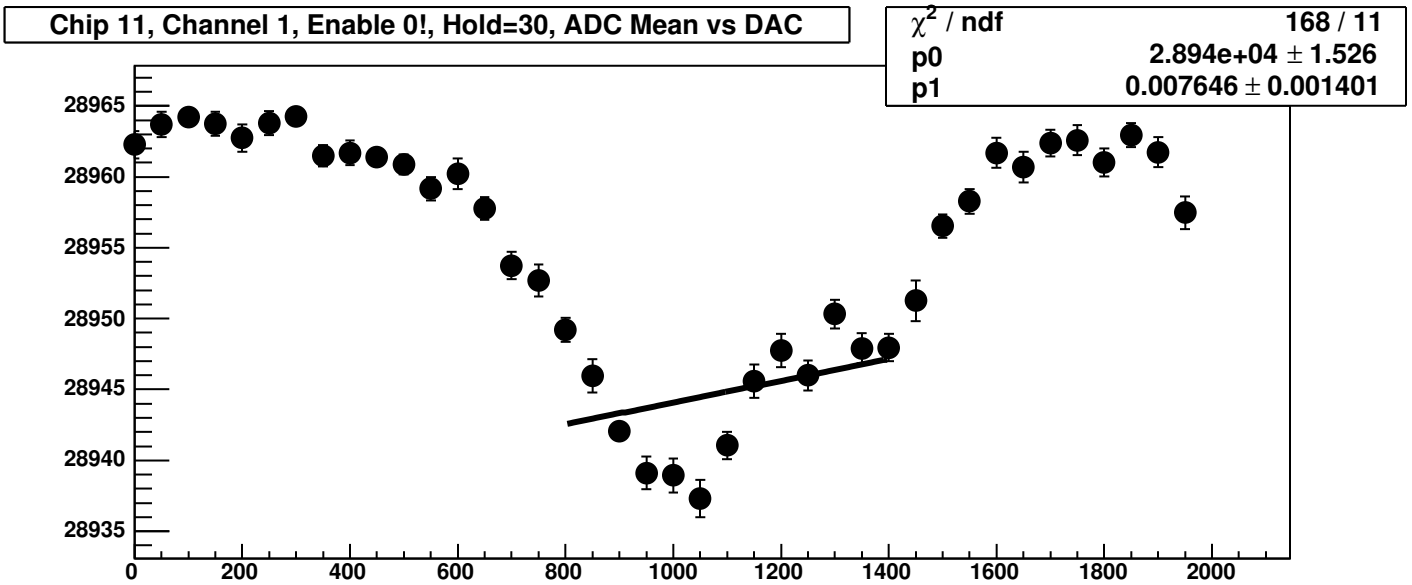

Chip 11, Channel 0, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

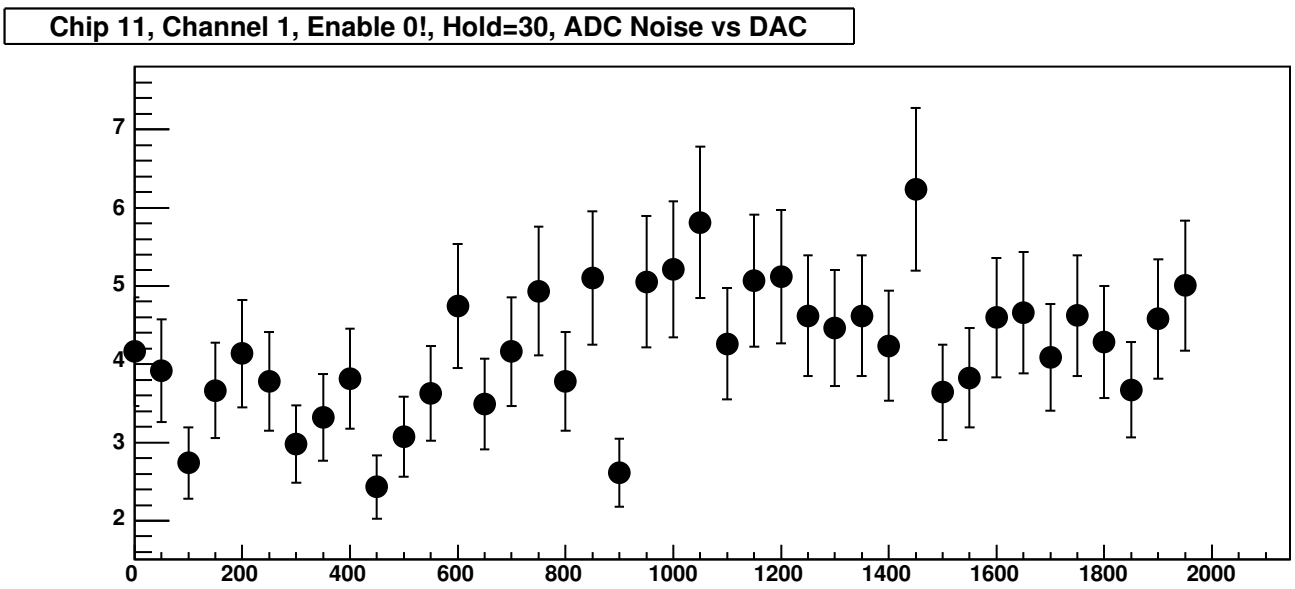

Chip 11, Channel 0, Enable 4, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

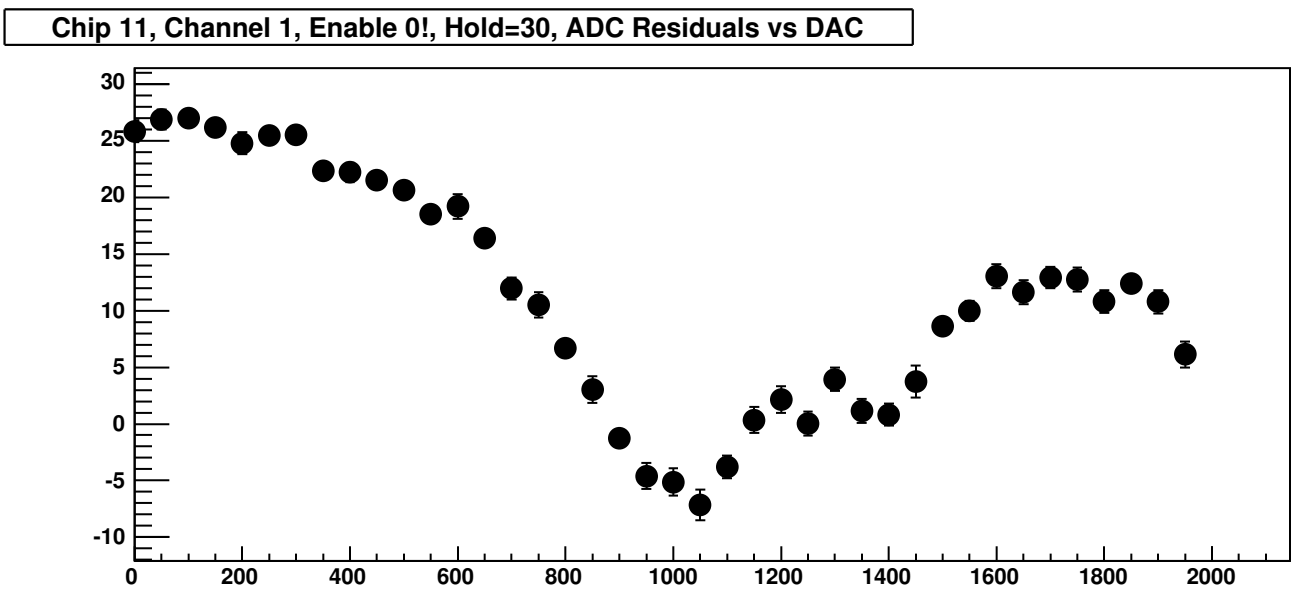

Chip 11, Channel 0, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 14.82 / 11
p0 -1337 ± 23.19
p1 0.6823 ± 0.02081
0
-200
-400
-600
-800
-1000
0 200 400 600 800 1000 1200 1400 1600 1800 2000

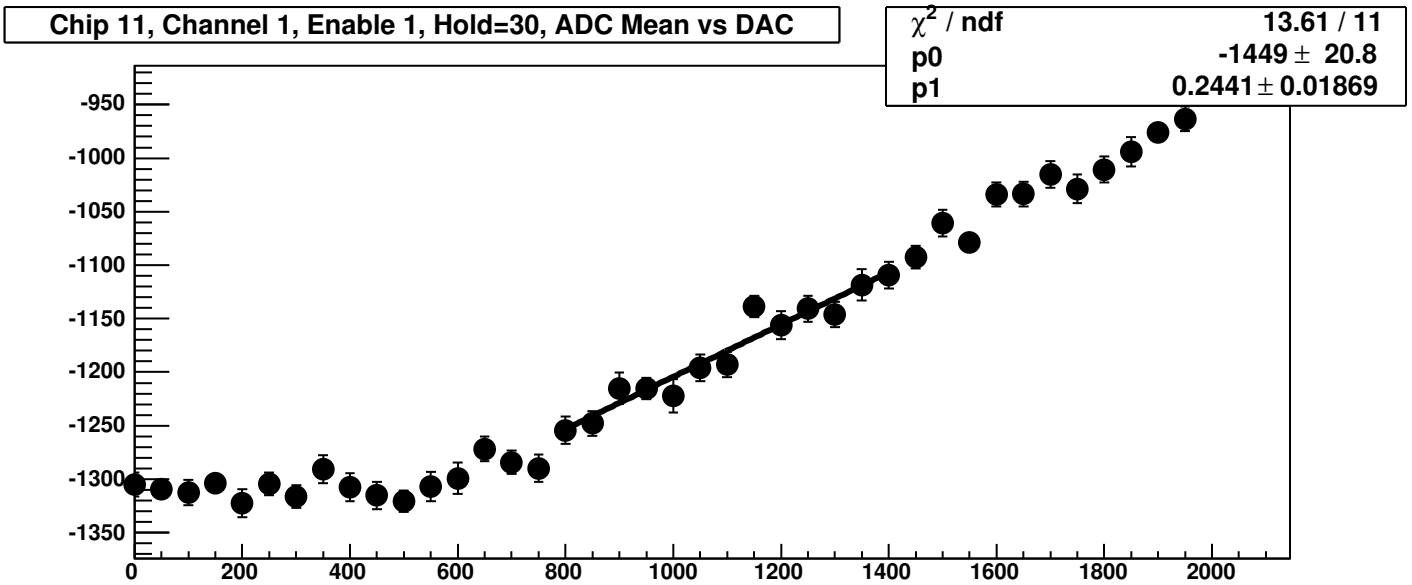

Chip 11, Channel 0, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0 200 400 600 800 1000 1200 1400 1600 1800 2000

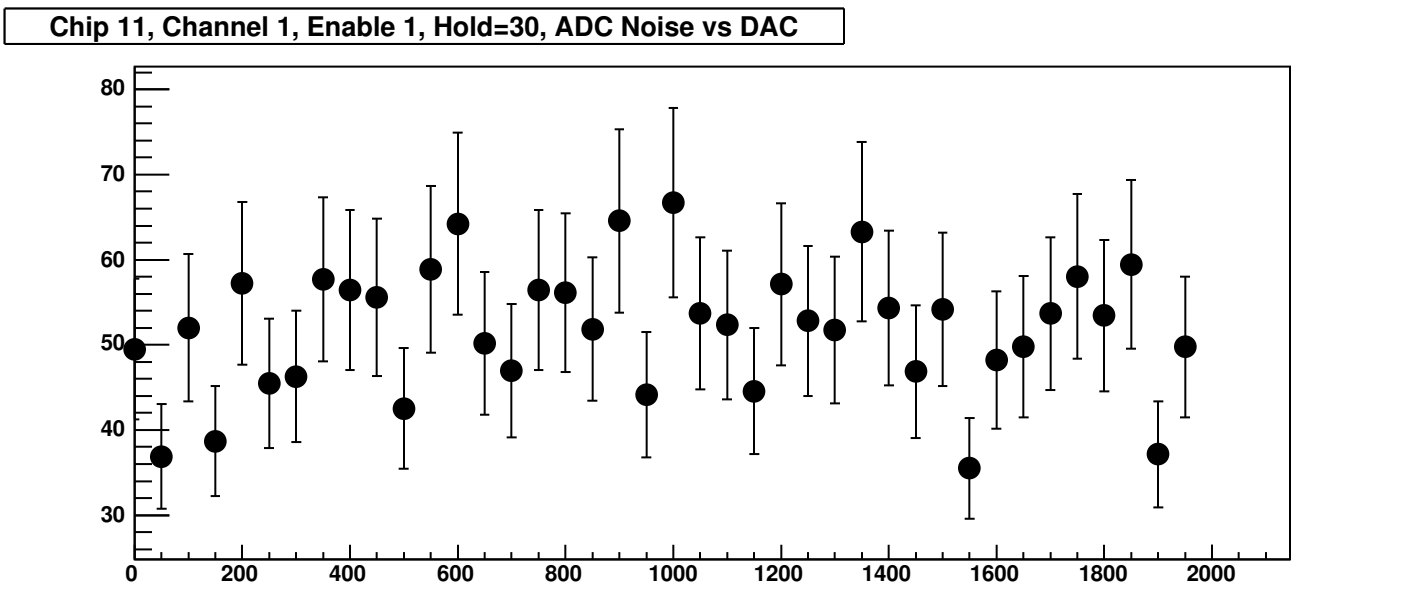

Chip 11, Channel 0, Enable 5, Hold=30, ADC Residuals vs DAC
500
400
300
200
100
0
0 200 400 600 800 1000 1200 1400 1600 1800 2000

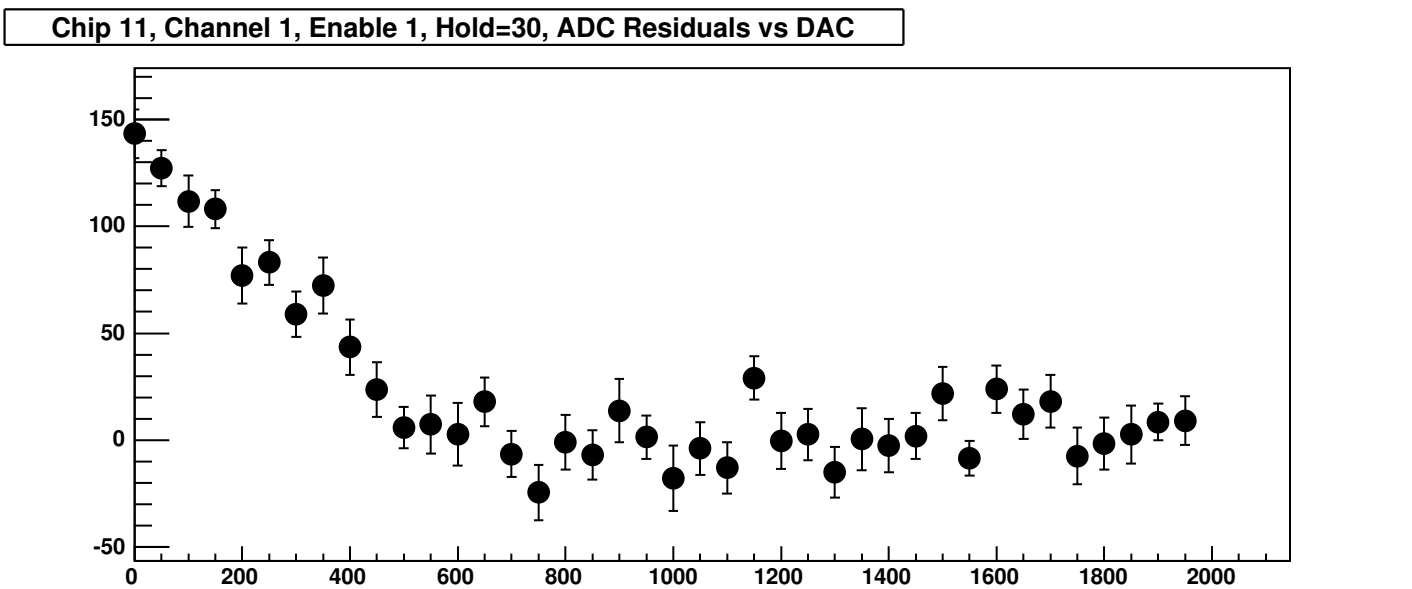

Chip 11, Channel 1, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 168 / 11
p0 2.894e+04 ± 1.526
p1 0.007646 ± 0.001401
28965
28960
28955
28950
28945
28940
28935
0
200
400
600
800
1000
1200
1400
1600
1800
2000

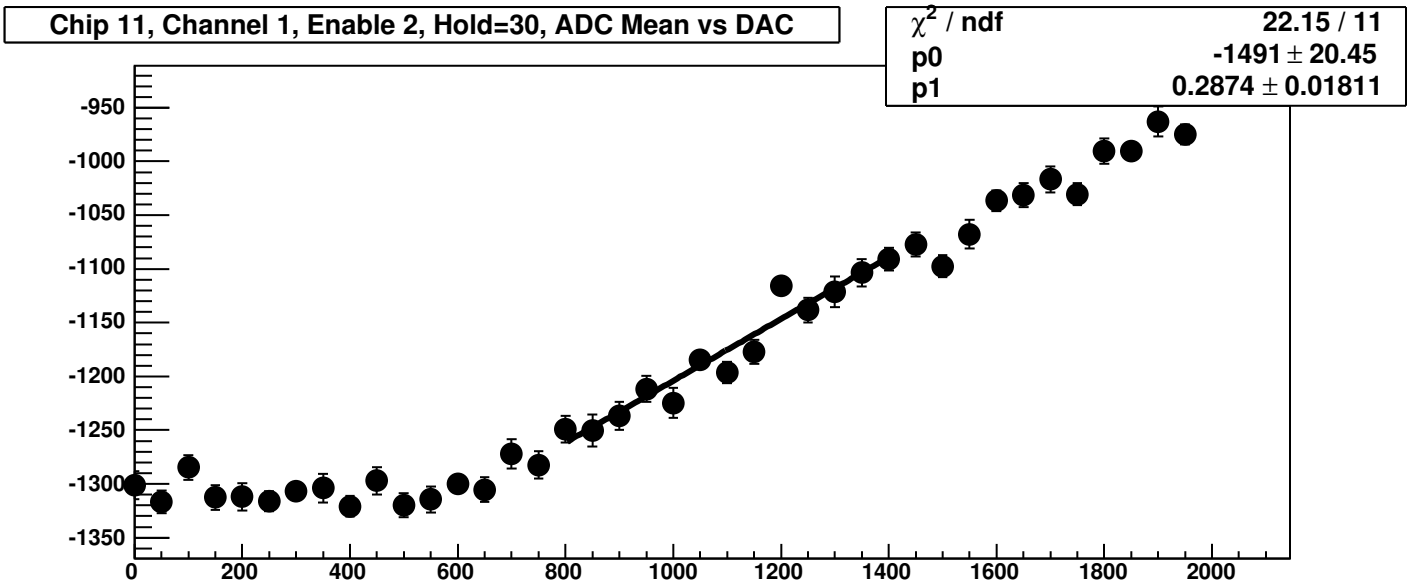

Chip 11, Channel 1, Enable 0!, Hold=30, ADC Noise vs DAC
7
6
5
4
3
2
0
200
400
600
800
1000
1200
1400
1600
1800
2000

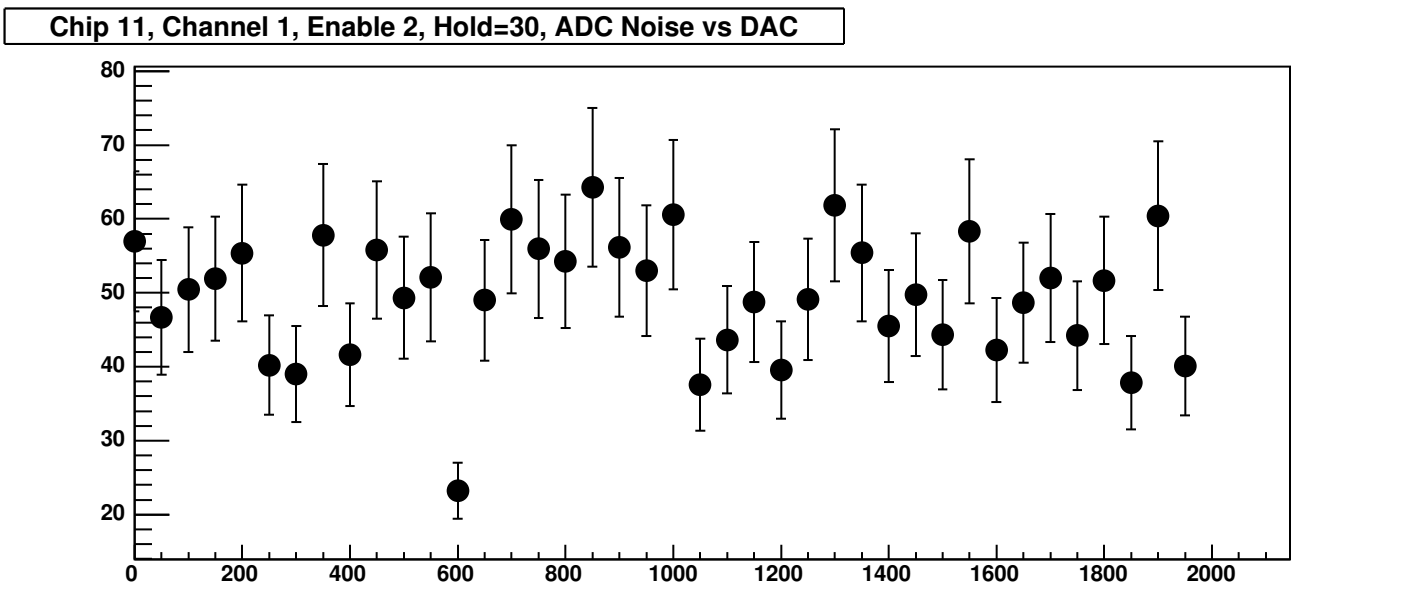

Chip 11, Channel 1, Enable 0!, Hold=30, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

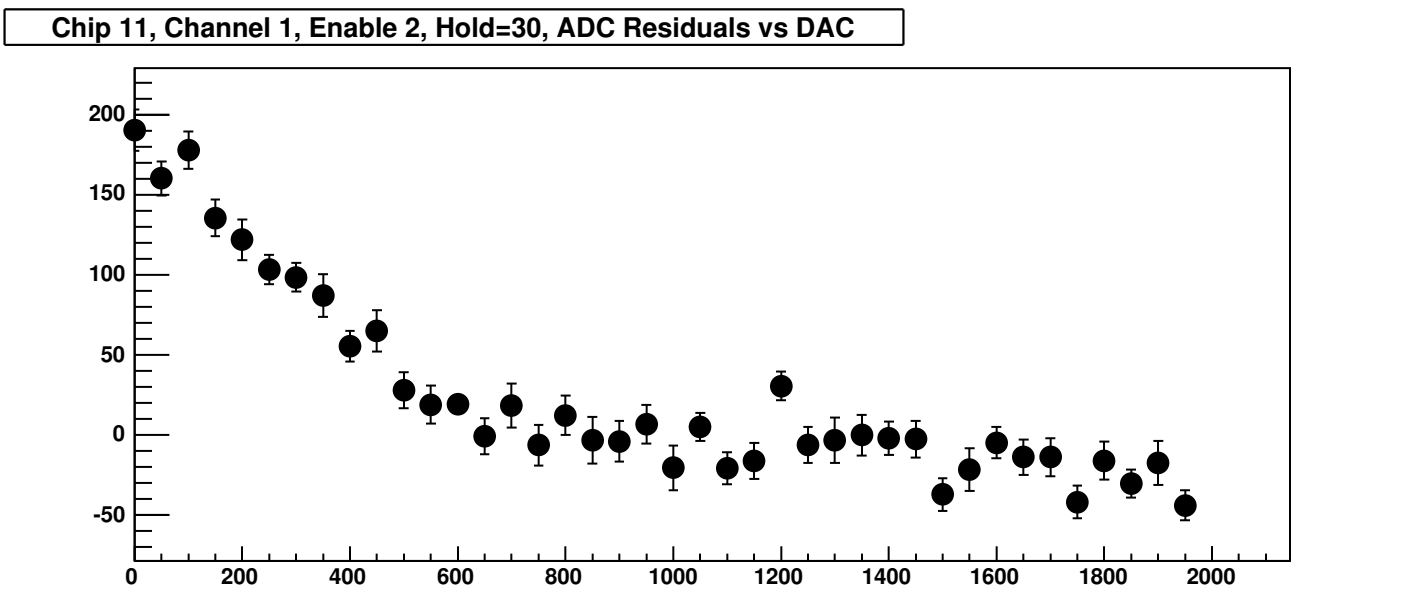

Chip 11, Channel 1, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 13.61 / 11
p0 -1449 ± 20.8
p1 0.2441 ± 0.01869
-950
-1000
-1050
-1100
-1150
-1200
-1250
-1300
-1350
0
200
400
600
800
1000
1200
1400
1600
1800
2000

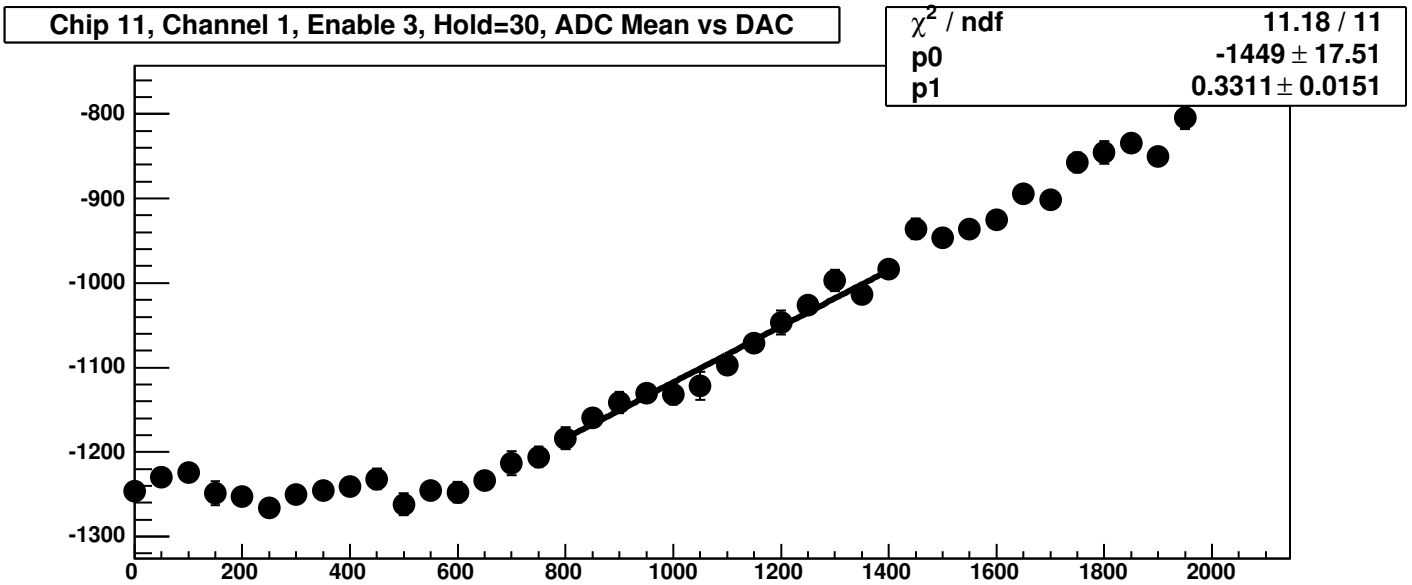

Chip 11, Channel 1, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

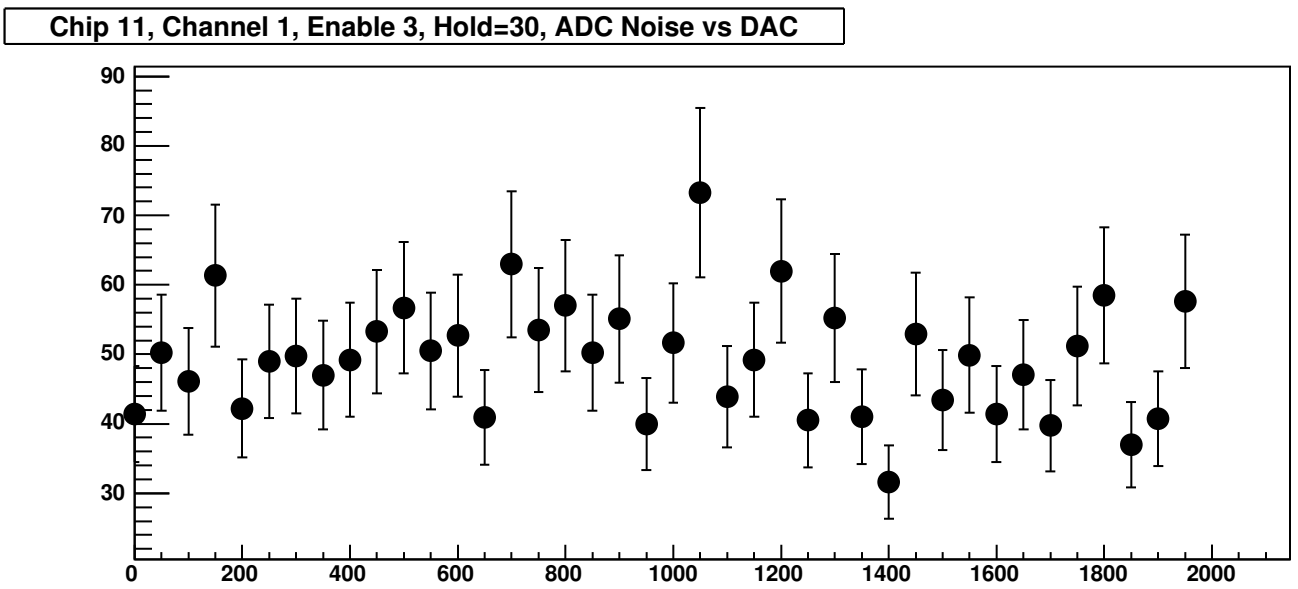

Chip 11, Channel 1, Enable 1, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

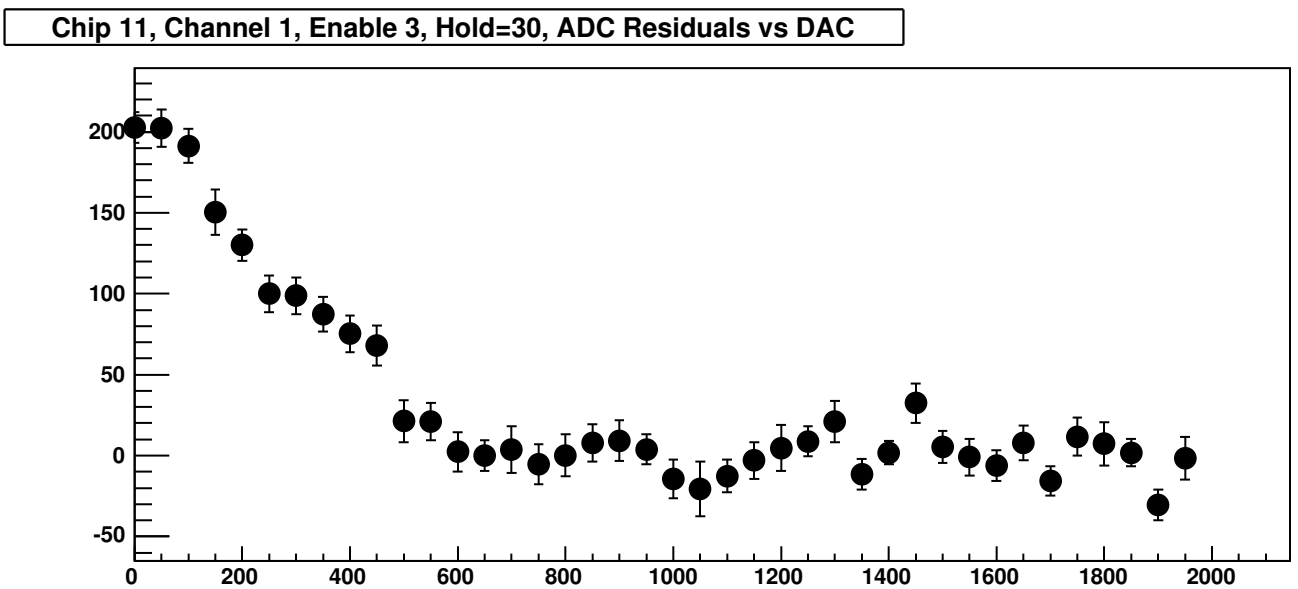

Chip 11, Channel 1, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 22.15 / 11
p0 -1491 ± 20.45
p1 0.2874 ± 0.01811
-950
-1000
-1050
-1100
-1150
-1200
-1250
-1300
-1350
0
200
400
600
800
1000
1200
1400
1600
1800
2000

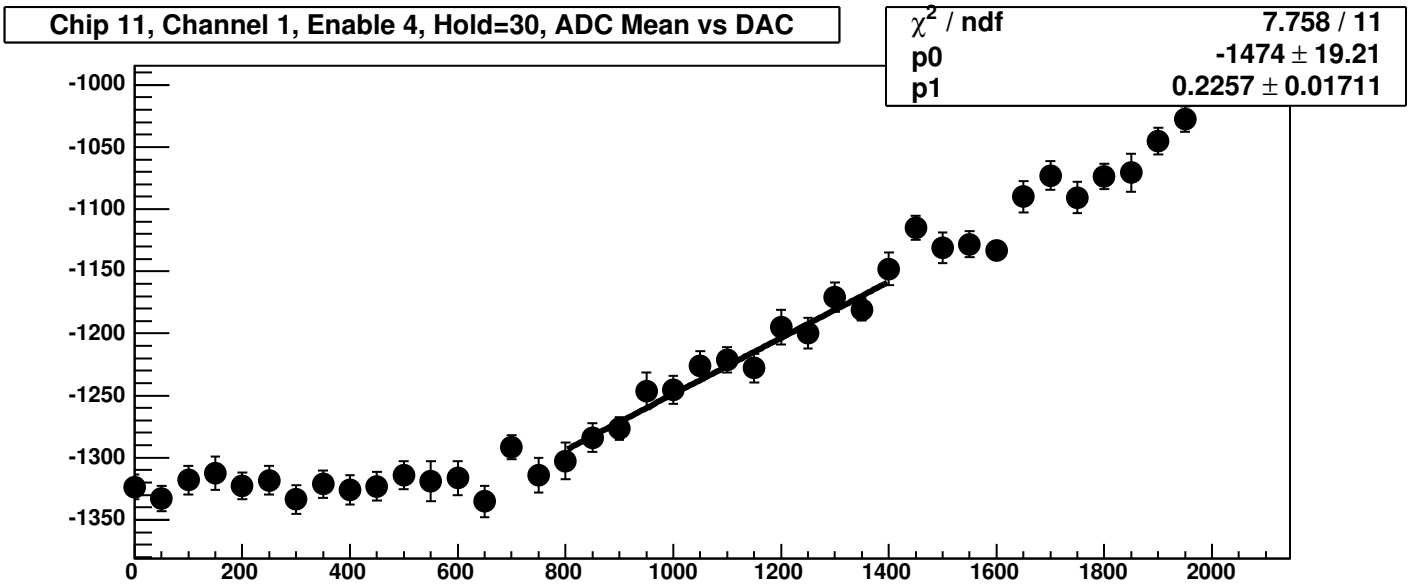

Chip 11, Channel 1, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

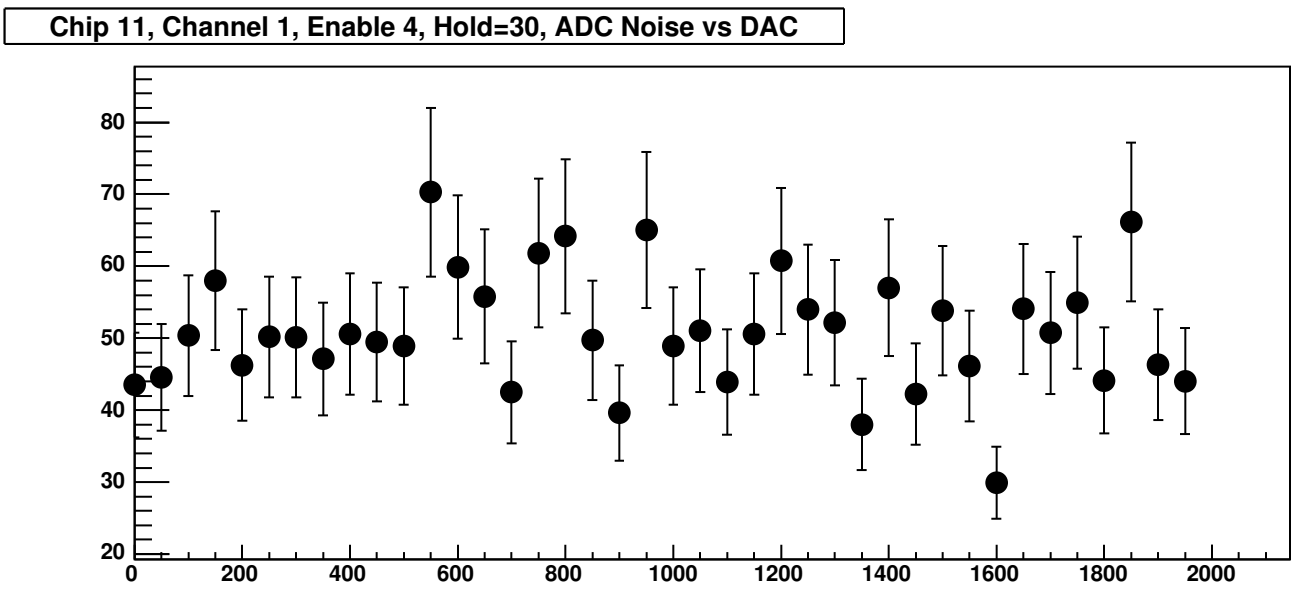

Chip 11, Channel 1, Enable 2, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

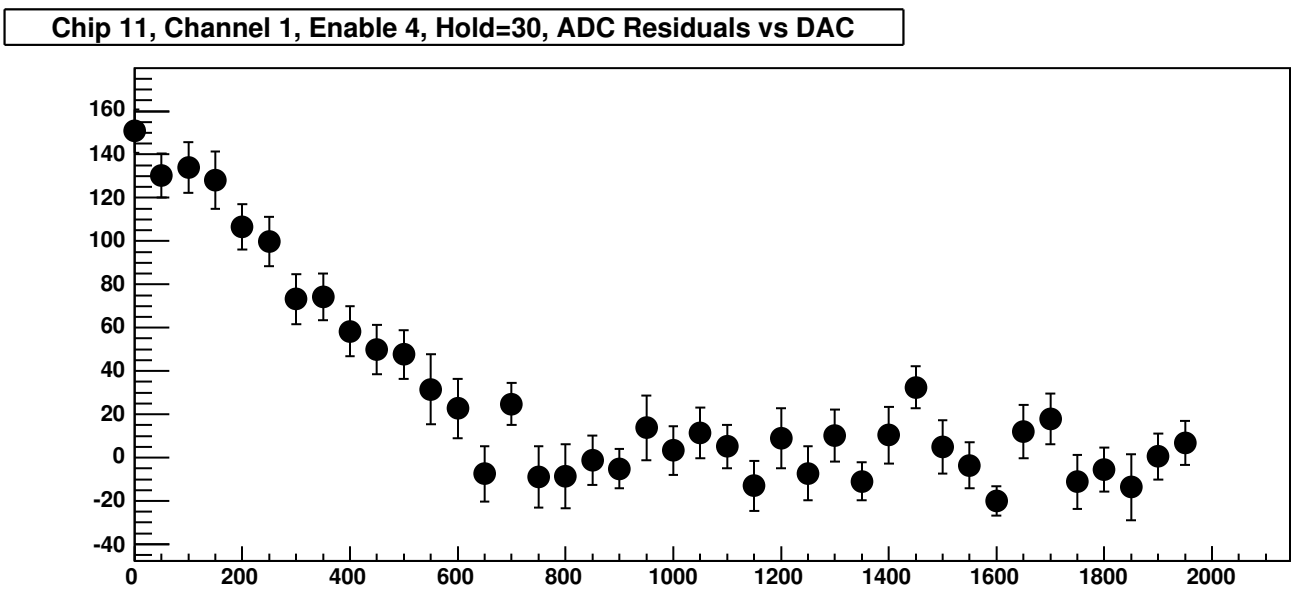

Chip 11, Channel 1, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 11.18 / 11
p0 -1449 ± 17.51
p1 0.3311 ± 0.0151
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

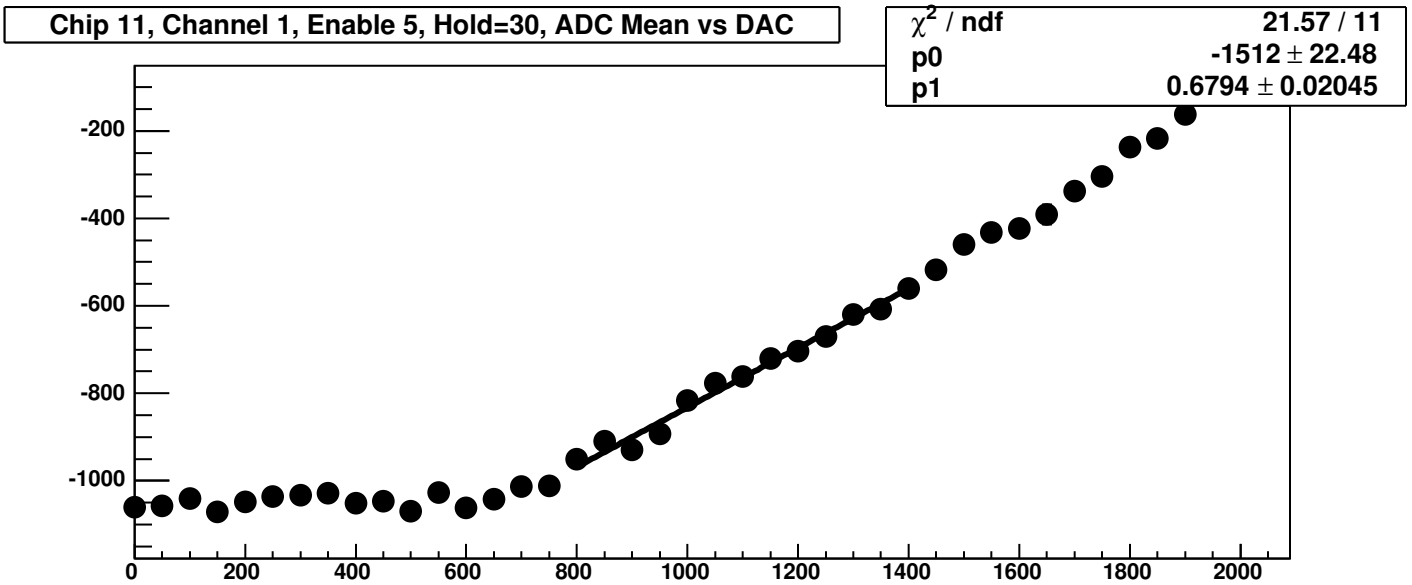

Chip 11, Channel 1, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

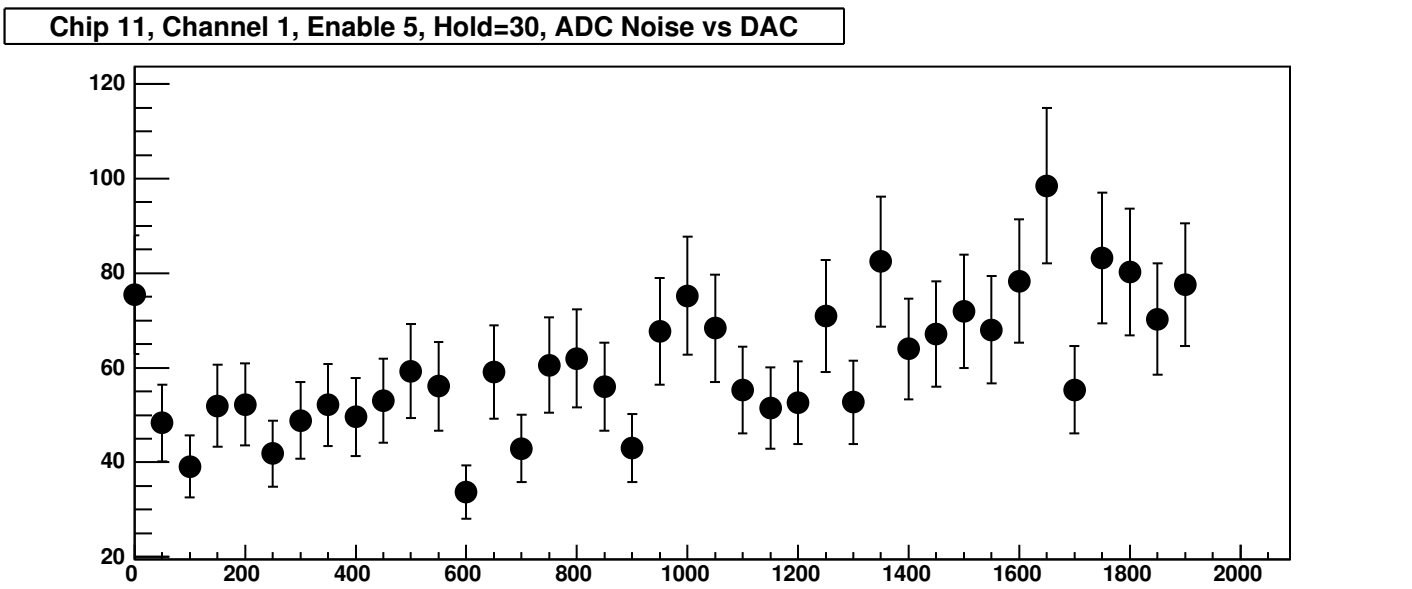

Chip 11, Channel 1, Enable 3, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

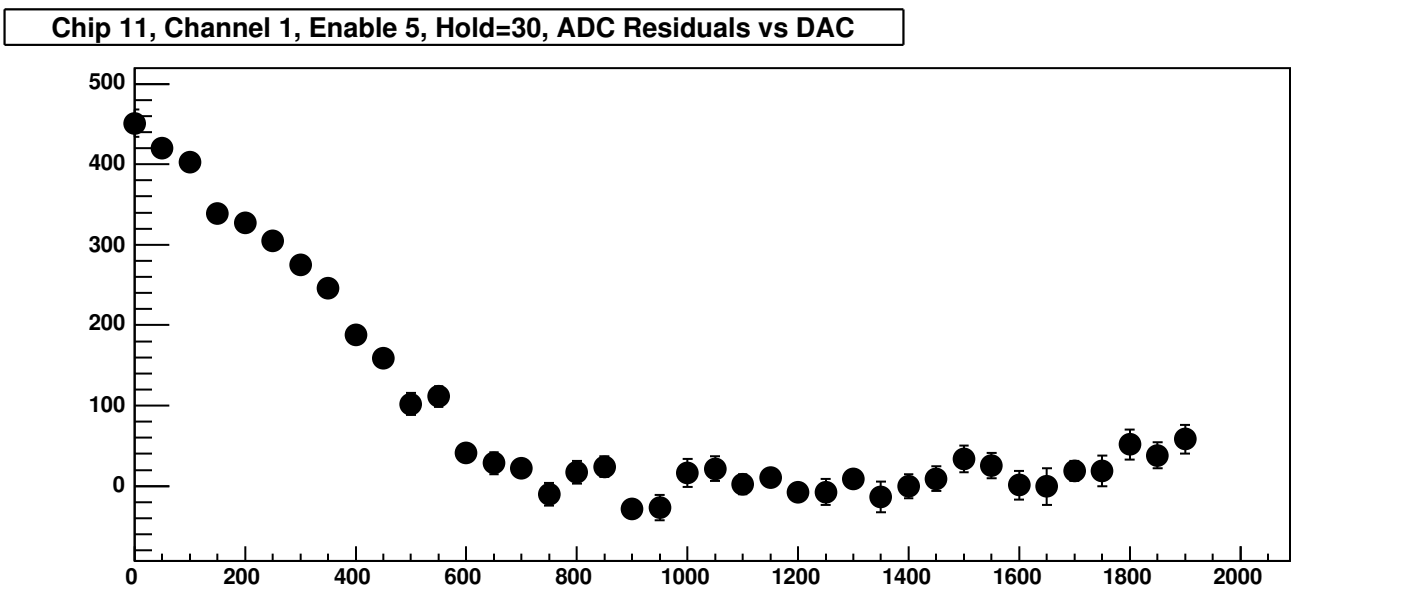

Chip 11, Channel 1, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 7.758 / 11
p0 -1474 ± 19.21
p1 0.2257 ± 0.01711
-1000
-1050
-1100
-1150
-1200
-1250
-1300
-1350
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 1, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 1, Enable 4, Hold=30, ADC Residuals vs DAC
160
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

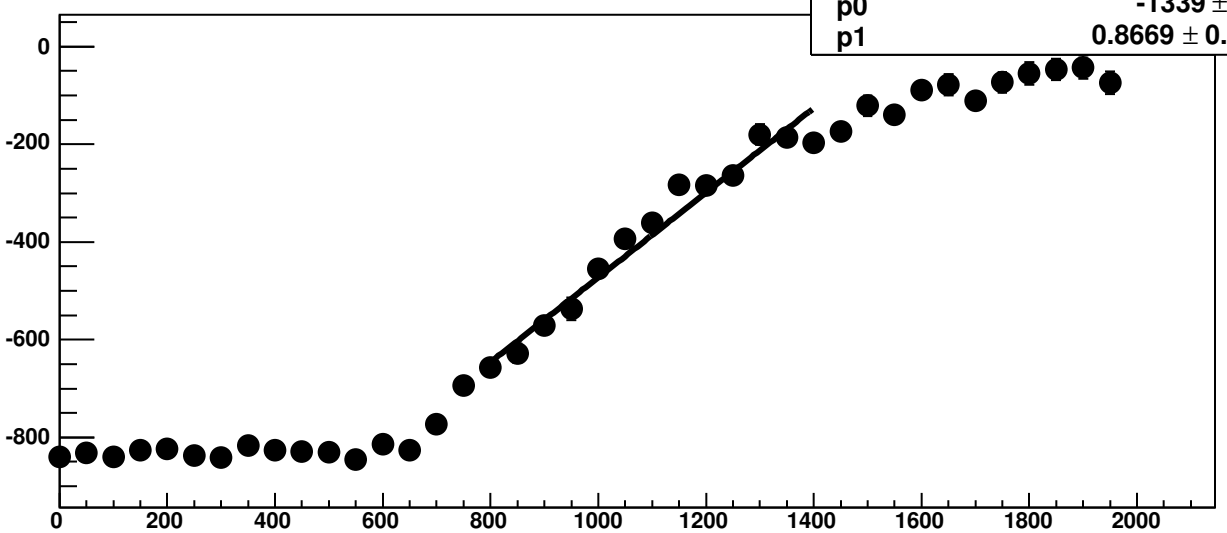

Chip 11, Channel 1, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 21.57 / 11
p0 -1512 ± 22.48
p1 0.6794 ± 0.02045
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 1, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

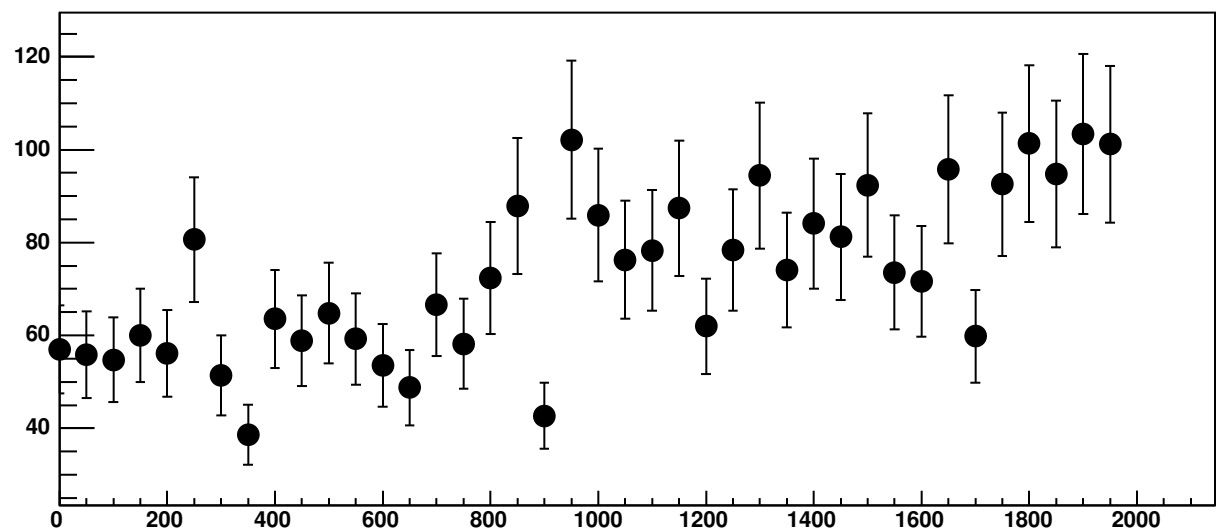

Chip 11, Channel 1, Enable 5, Hold=30, ADC Residuals vs DAC
500
400
300
200
100
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

Chip 11, Channel 2, Enable 0, Hold=30, ADC Mean vs DAC

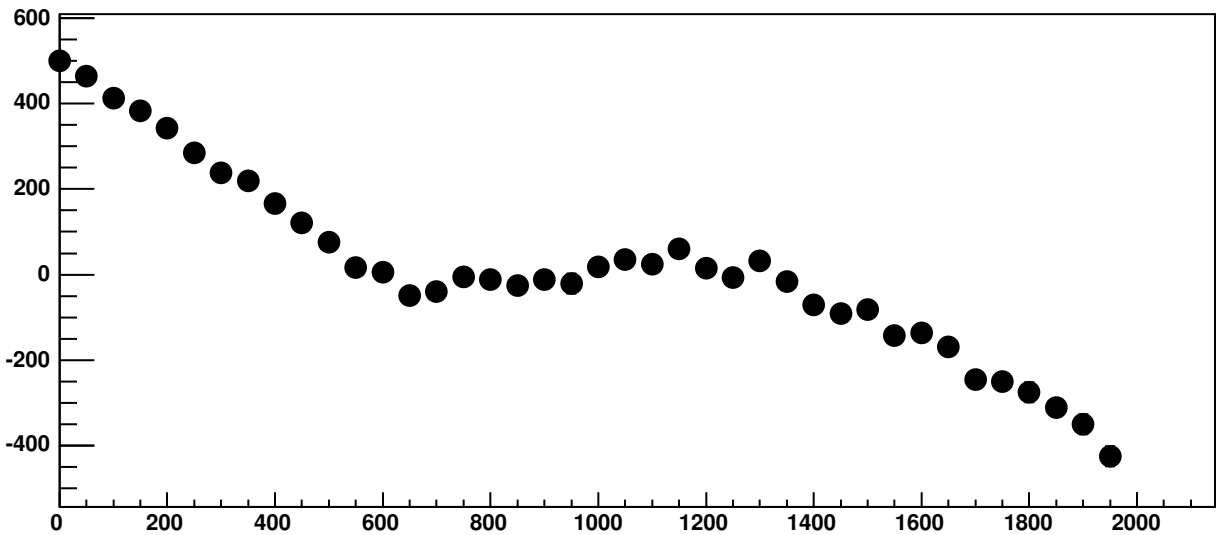
χ^2 / ndf	38.26 / 11
p0	-1339 ± 27.16
p1	0.8669 ± 0.02497



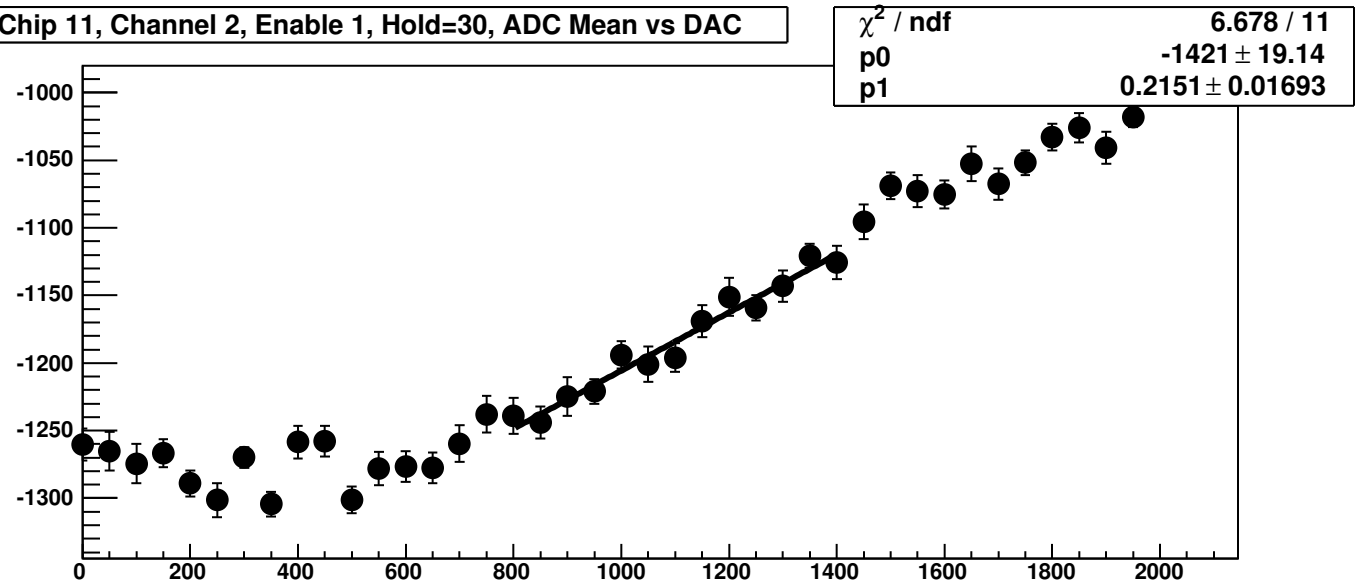
Chip 11, Channel 2, Enable 0, Hold=30, ADC Noise vs DAC



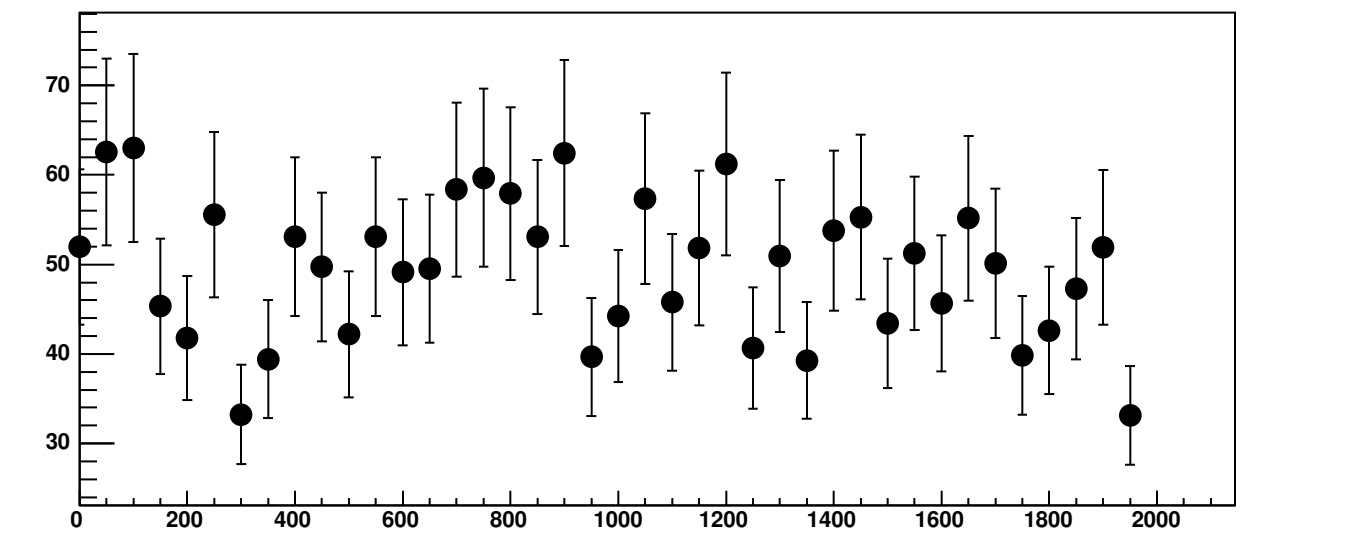
Chip 11, Channel 2, Enable 0, Hold=30, ADC Residuals vs DAC



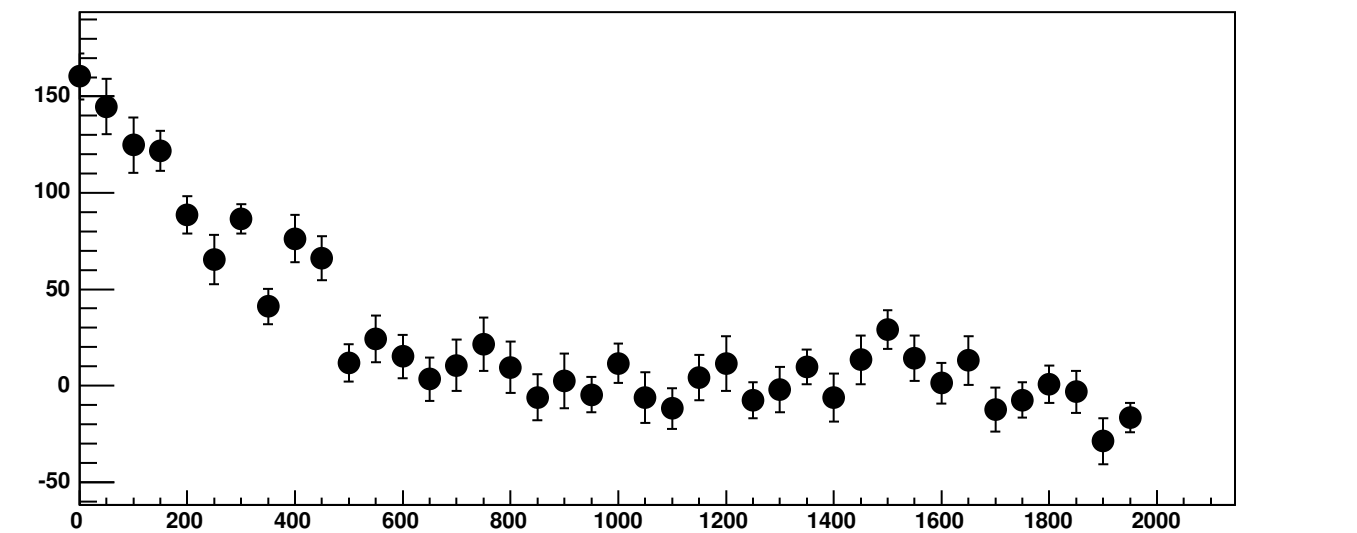
Chip 11, Channel 2, Enable 1, Hold=30, ADC Mean vs DAC

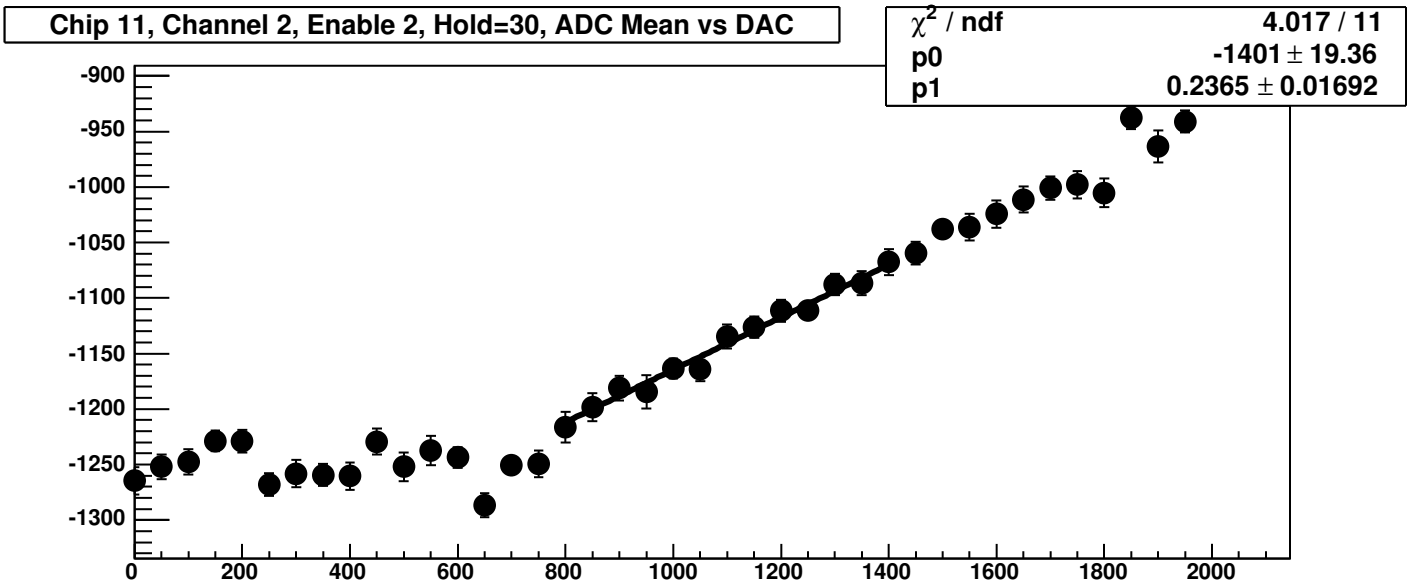


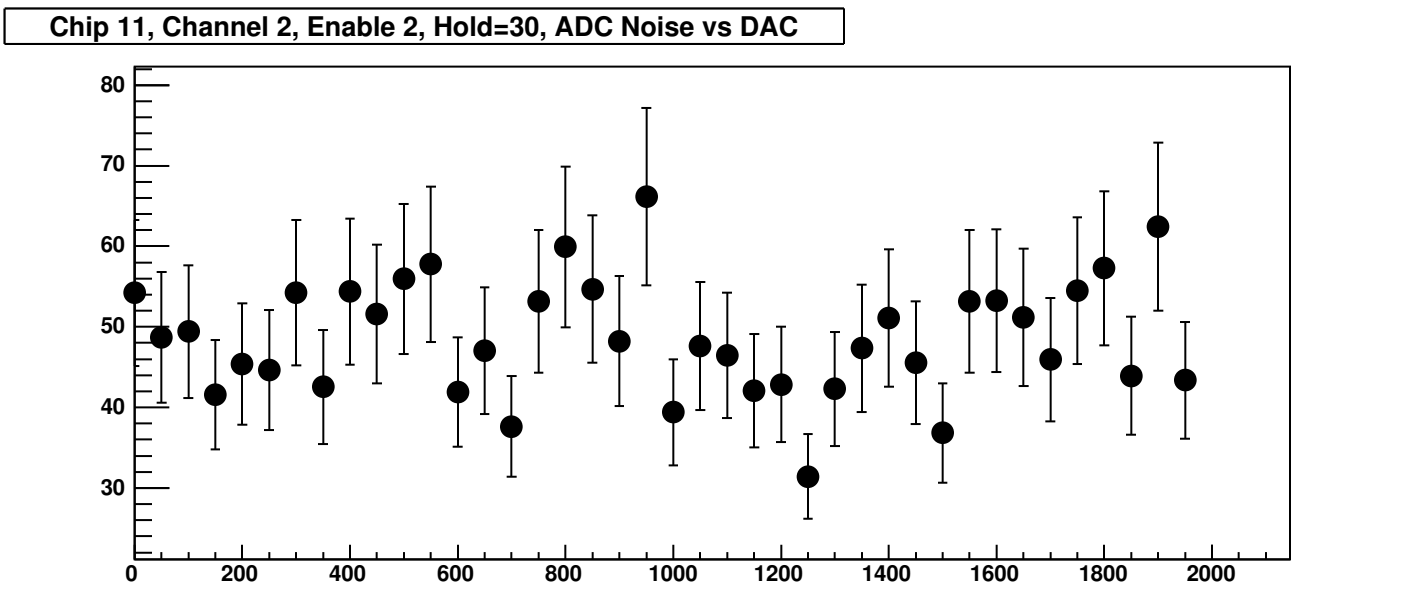
Chip 11, Channel 2, Enable 1, Hold=30, ADC Noise vs DAC

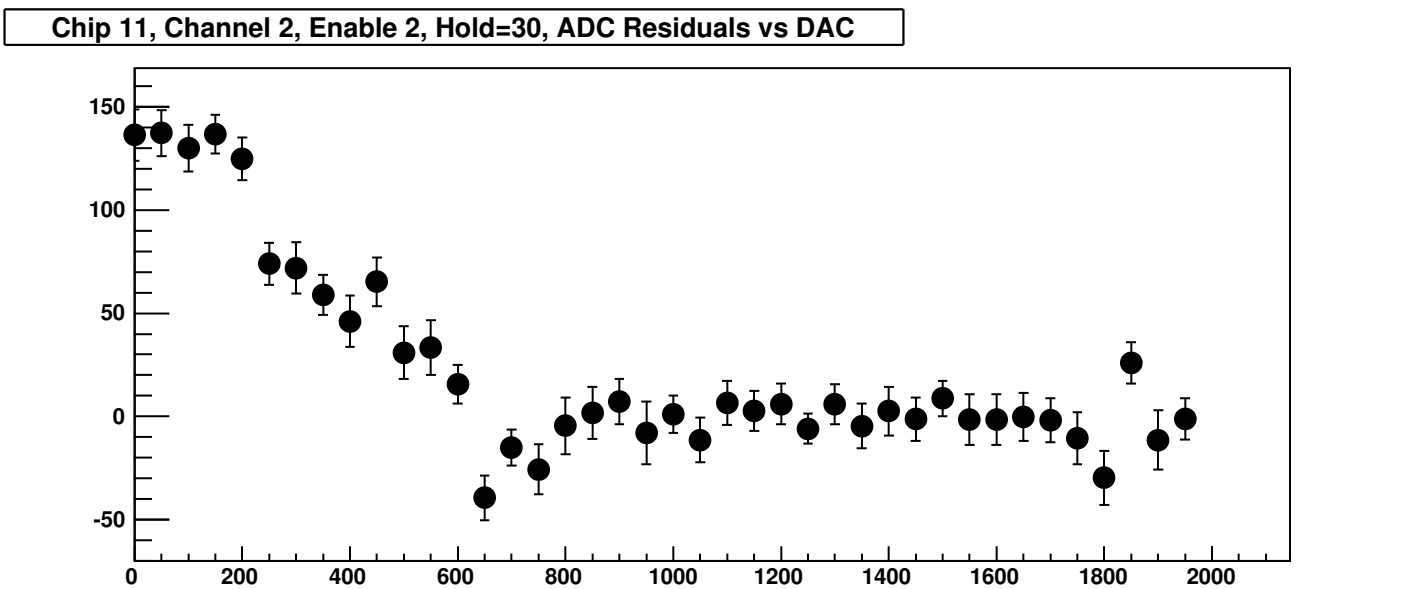


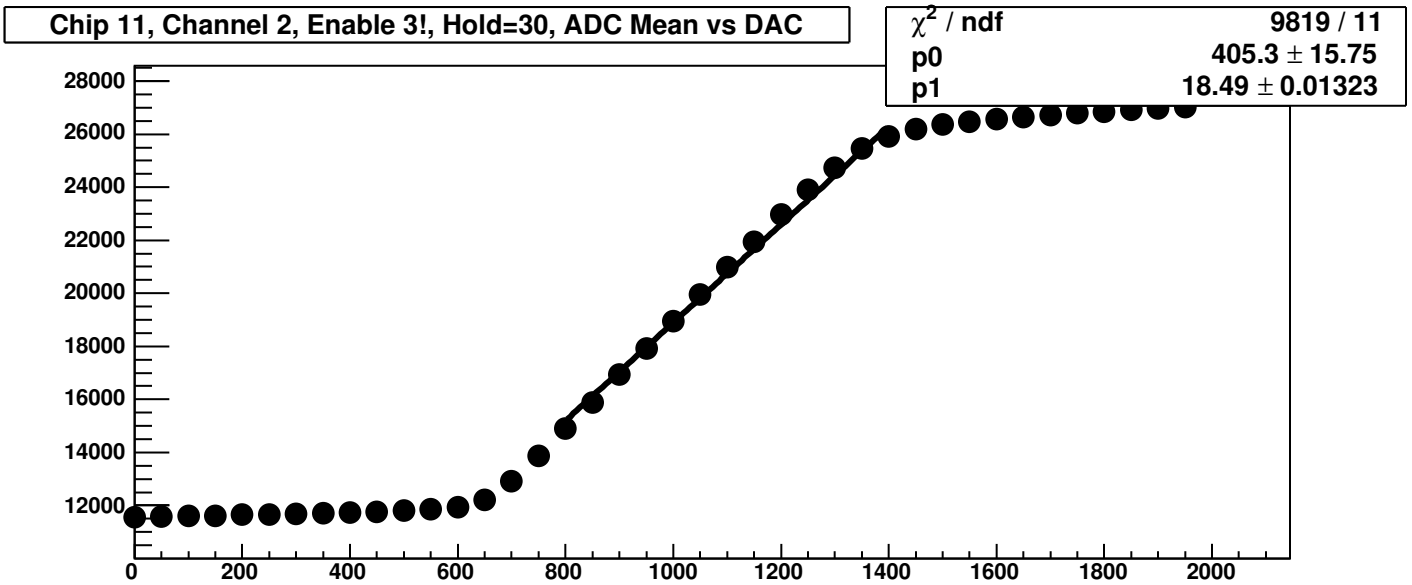
Chip 11, Channel 2, Enable 1, Hold=30, ADC Residuals vs DAC

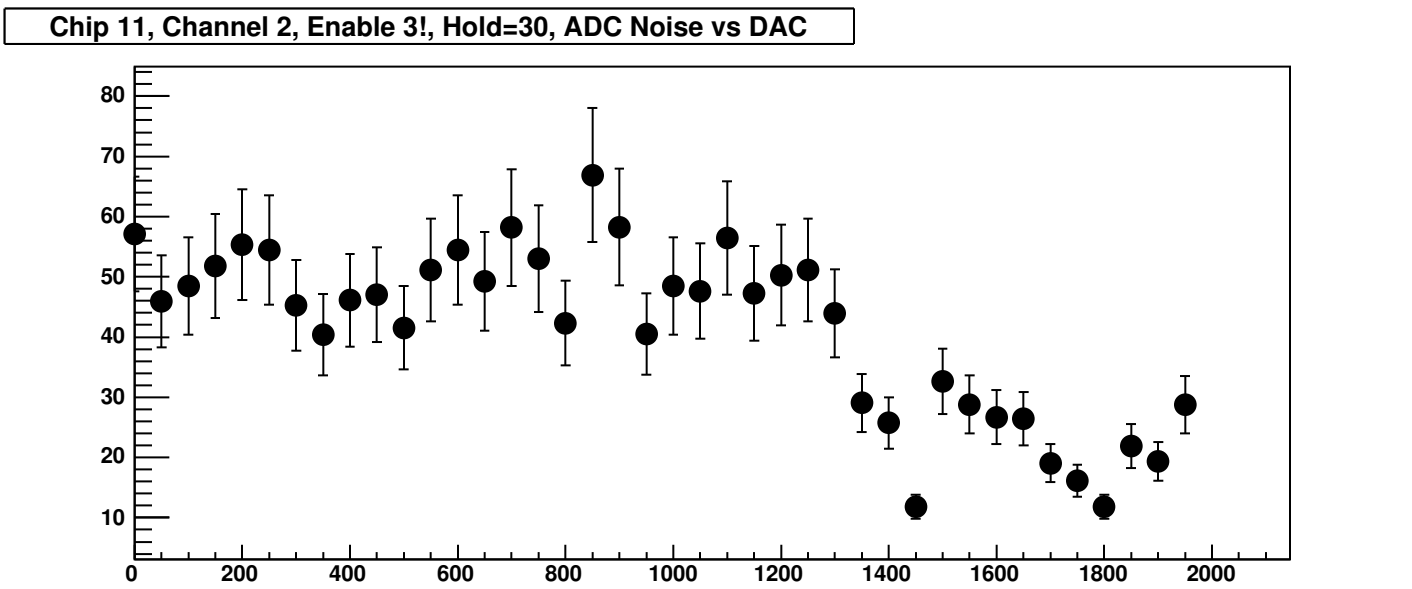


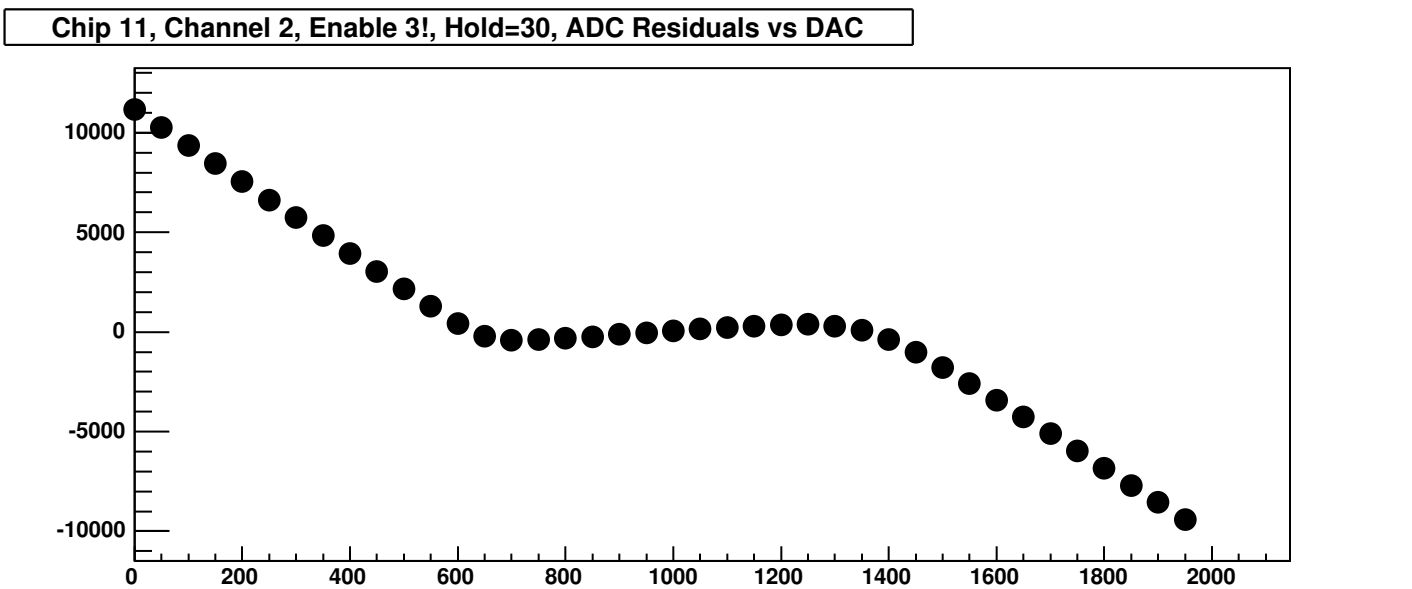

Chip 11, Channel 2, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 4.017 / 11
p0 -1401 ± 19.36
p1 0.2365 ± 0.01692
-900
-950
-1000
-1050
-1100
-1150
-1200
-1250
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

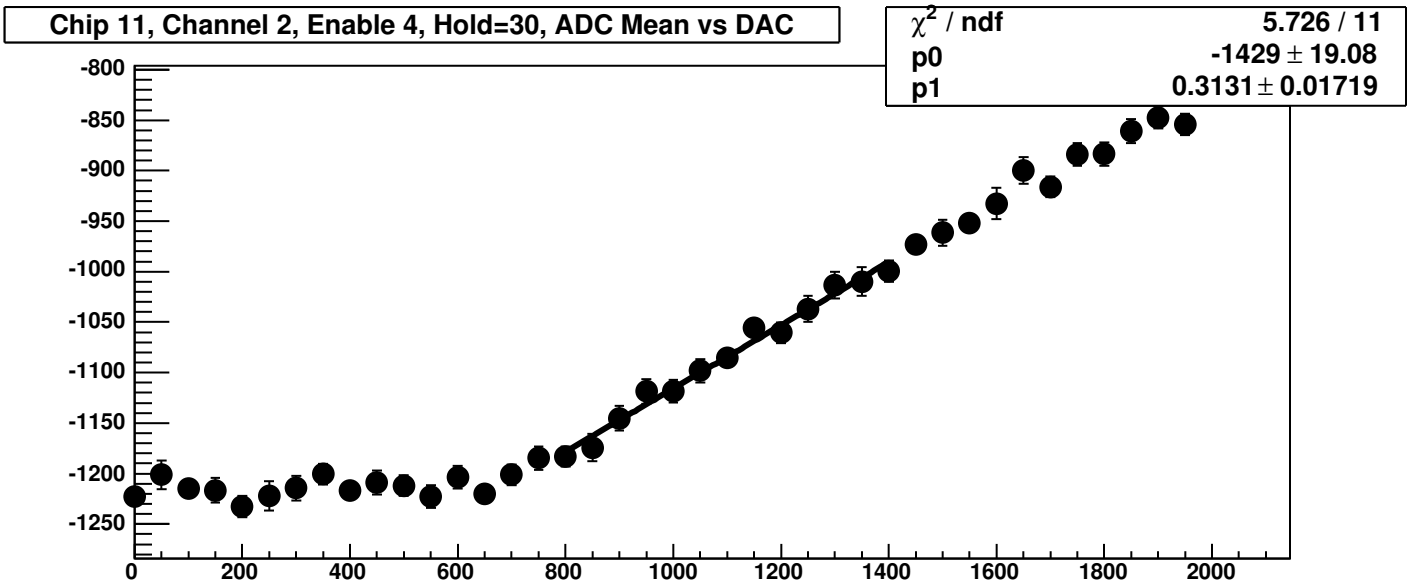

Chip 11, Channel 2, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

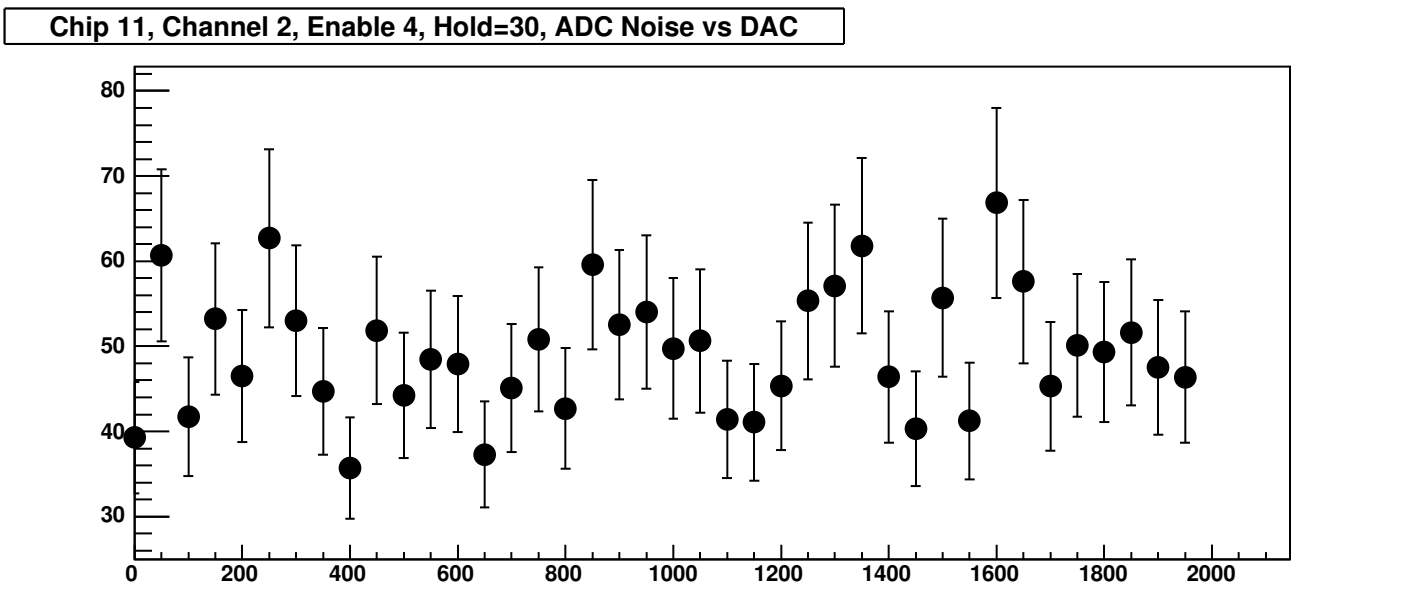

Chip 11, Channel 2, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

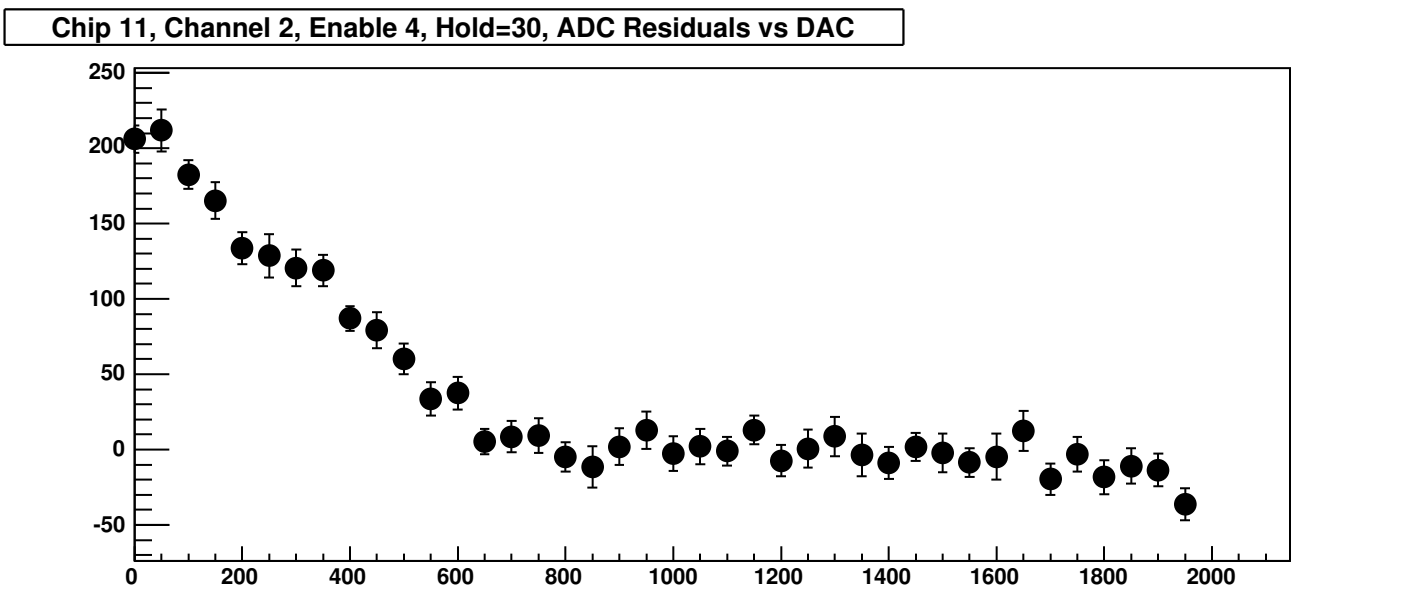

Chip 11, Channel 2, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 9819 / 11
p0 405.3 ± 15.75
p1 18.49 ± 0.01323
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

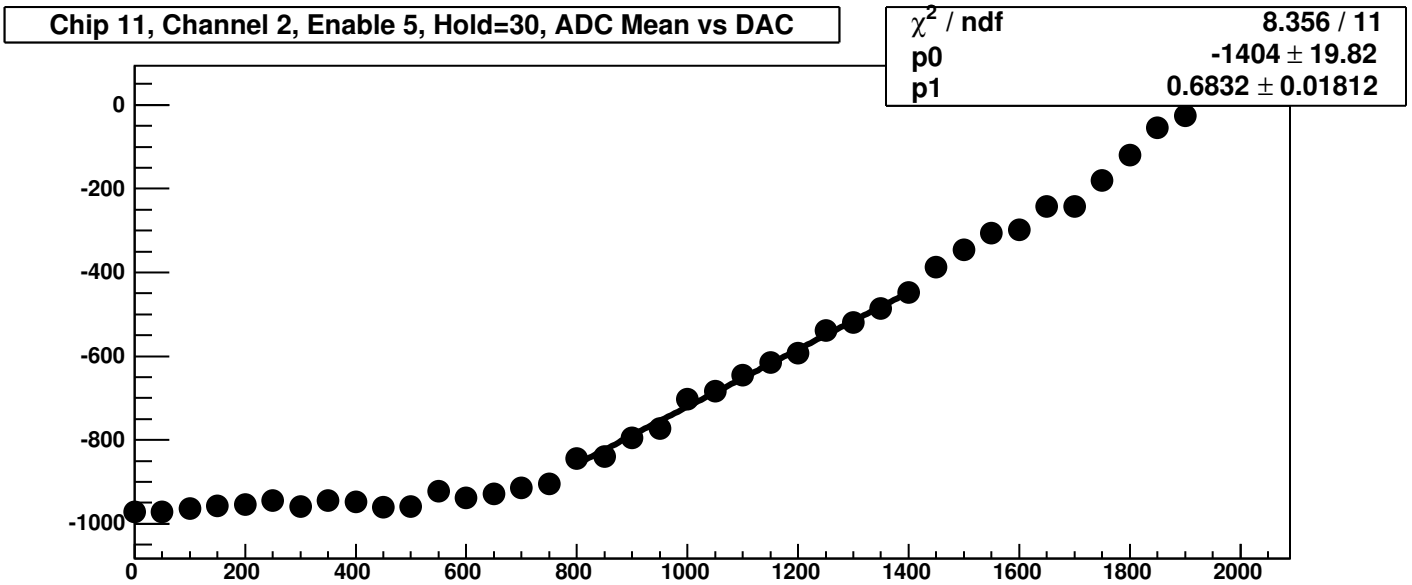

Chip 11, Channel 2, Enable 3!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

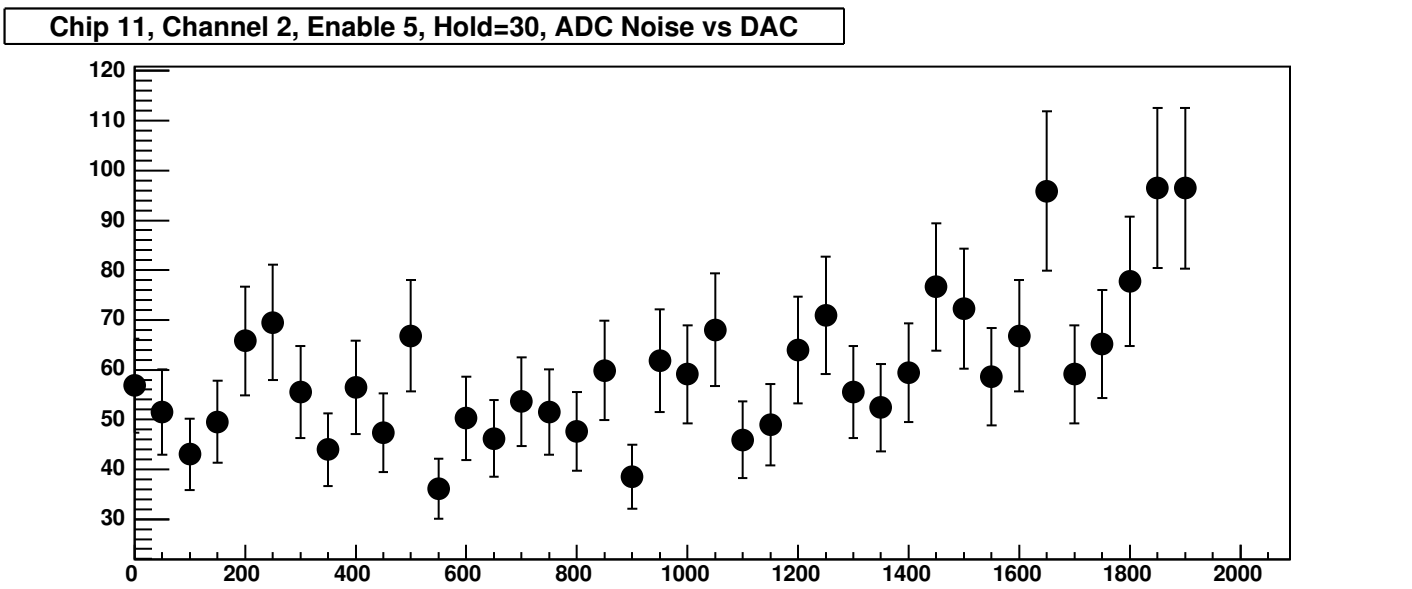

Chip 11, Channel 2, Enable 3!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

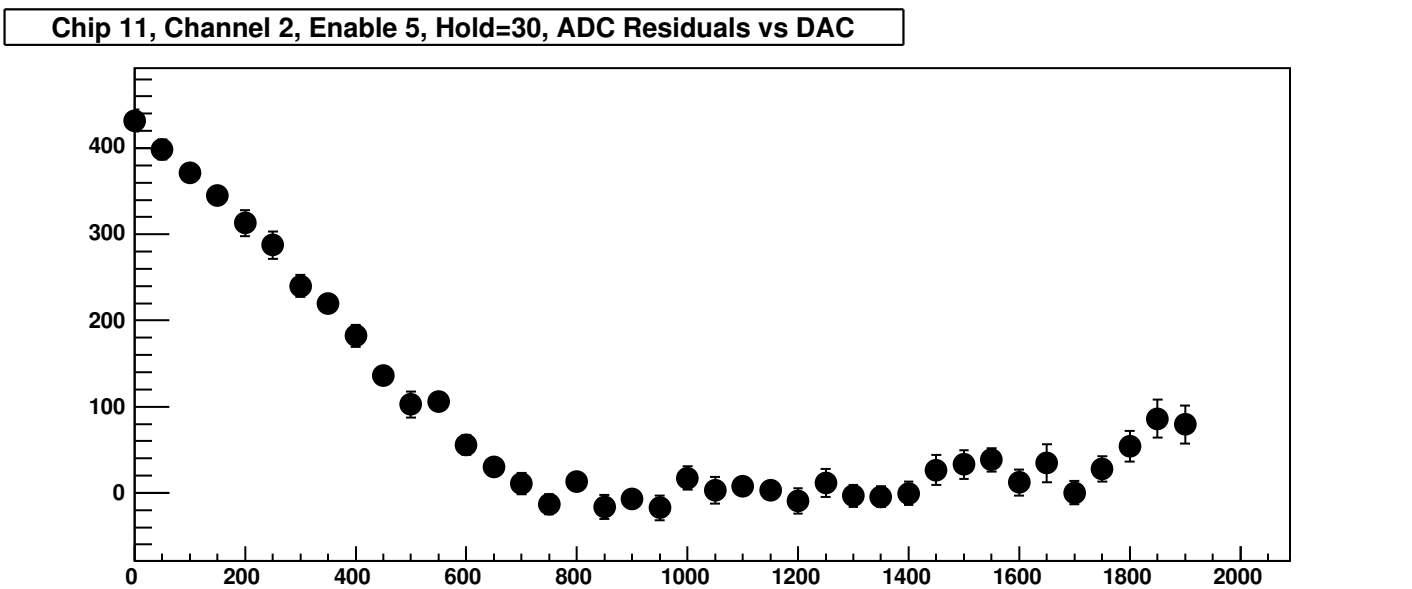

Chip 11, Channel 2, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5.726 / 11
p0 -1429 ± 19.08
p1 0.3131 ± 0.01719
-800
-850
-900
-950
-1000
-1050
-1100
-1150
-1200
-1250
0
200
400
600
800
1000
1200
1400
1600
1800
2000

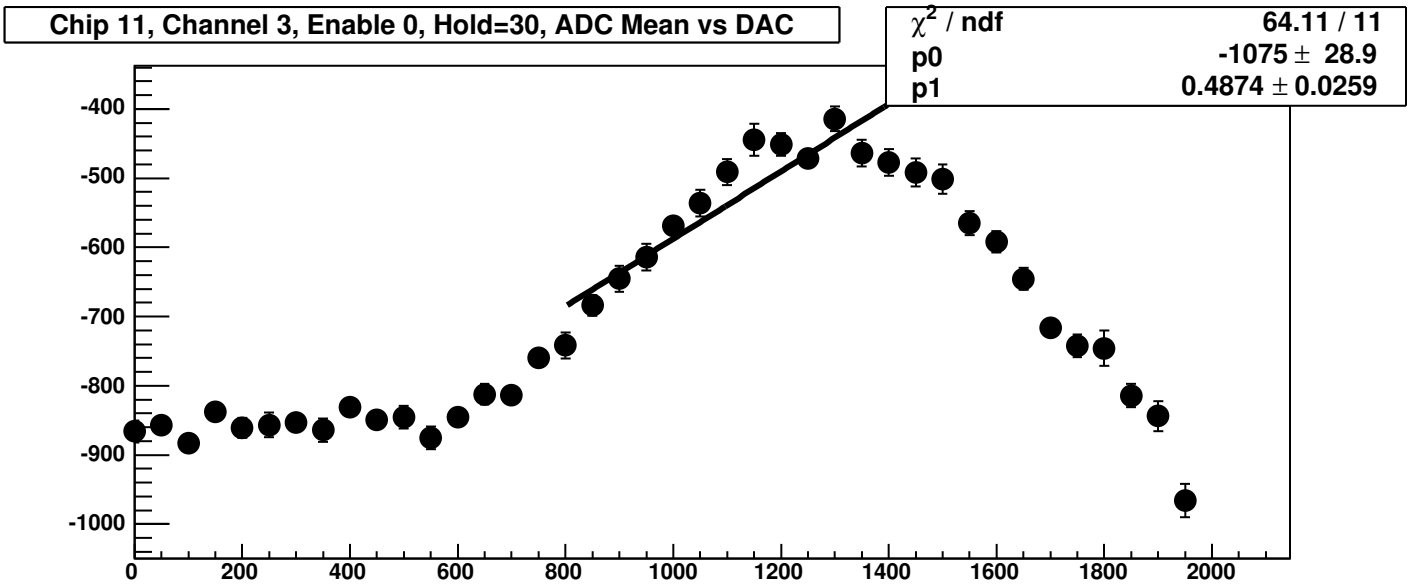

Chip 11, Channel 2, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

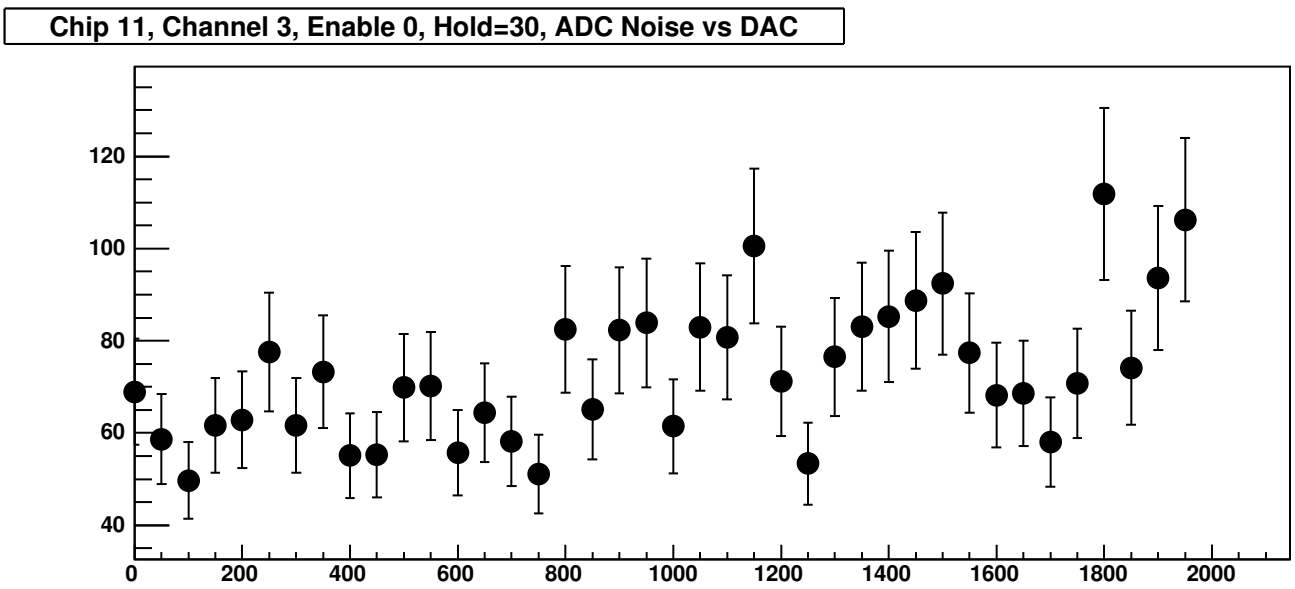

Chip 11, Channel 2, Enable 4, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

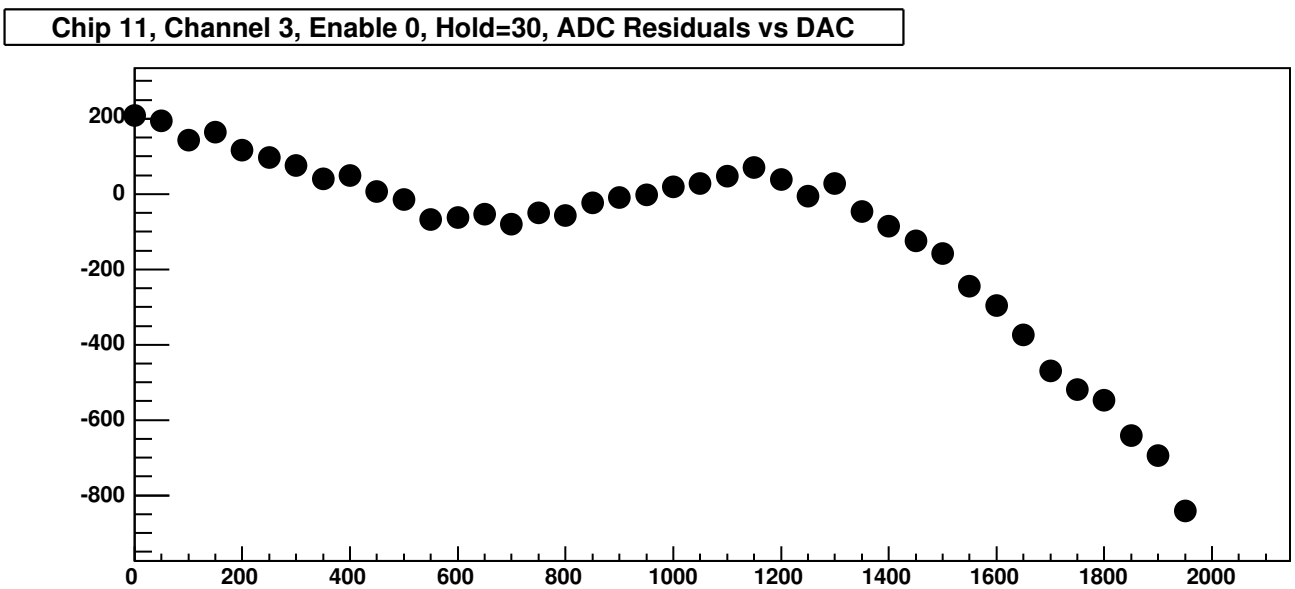

Chip 11, Channel 2, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8.356 / 11
p0 -1404 ± 19.82
p1 0.6832 ± 0.01812
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

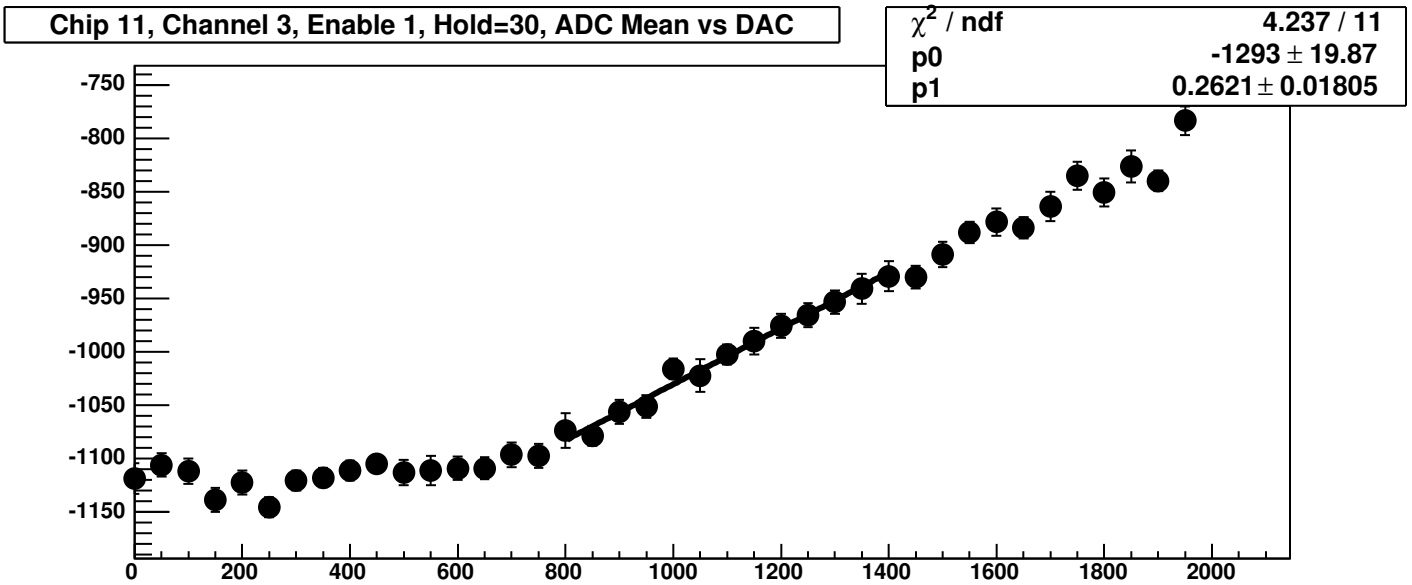

Chip 11, Channel 2, Enable 5, Hold=30, ADC Noise vs DAC
120
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

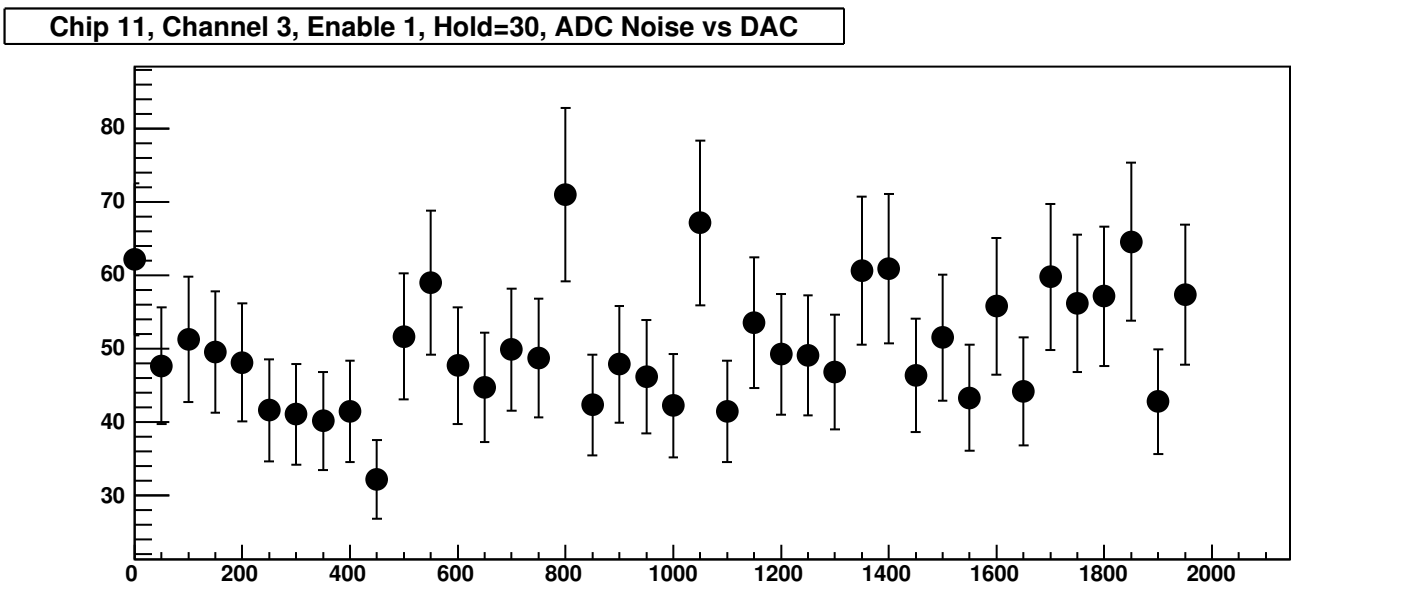

Chip 11, Channel 2, Enable 5, Hold=30, ADC Residuals vs DAC
400
300
200
100
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

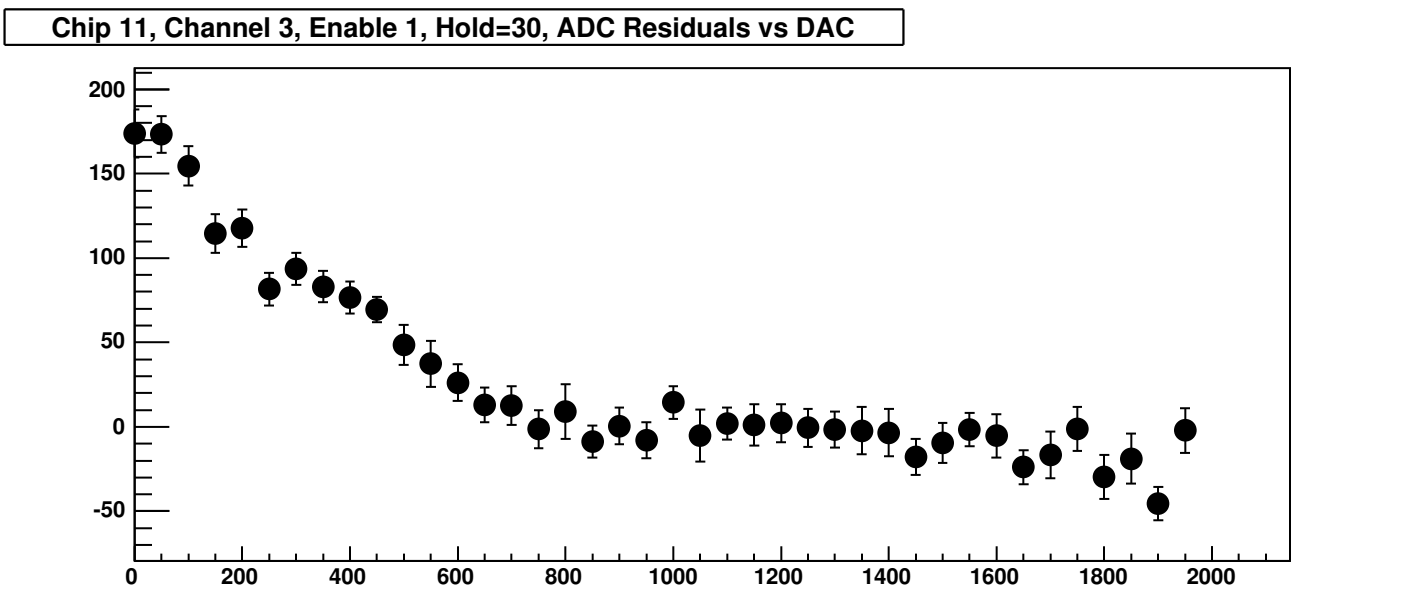

Chip 11, Channel 3, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 64.11 / 11
p0 -1075 ± 28.9
p1 0.4874 ± 0.0259
-400
-500
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

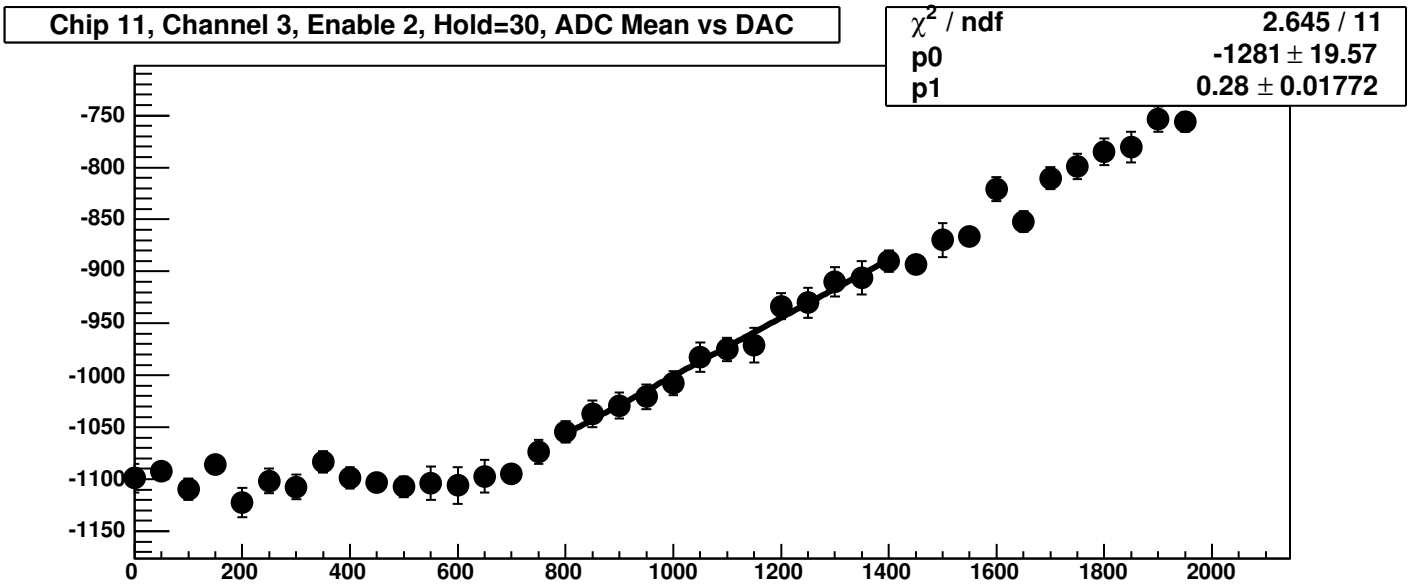

Chip 11, Channel 3, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

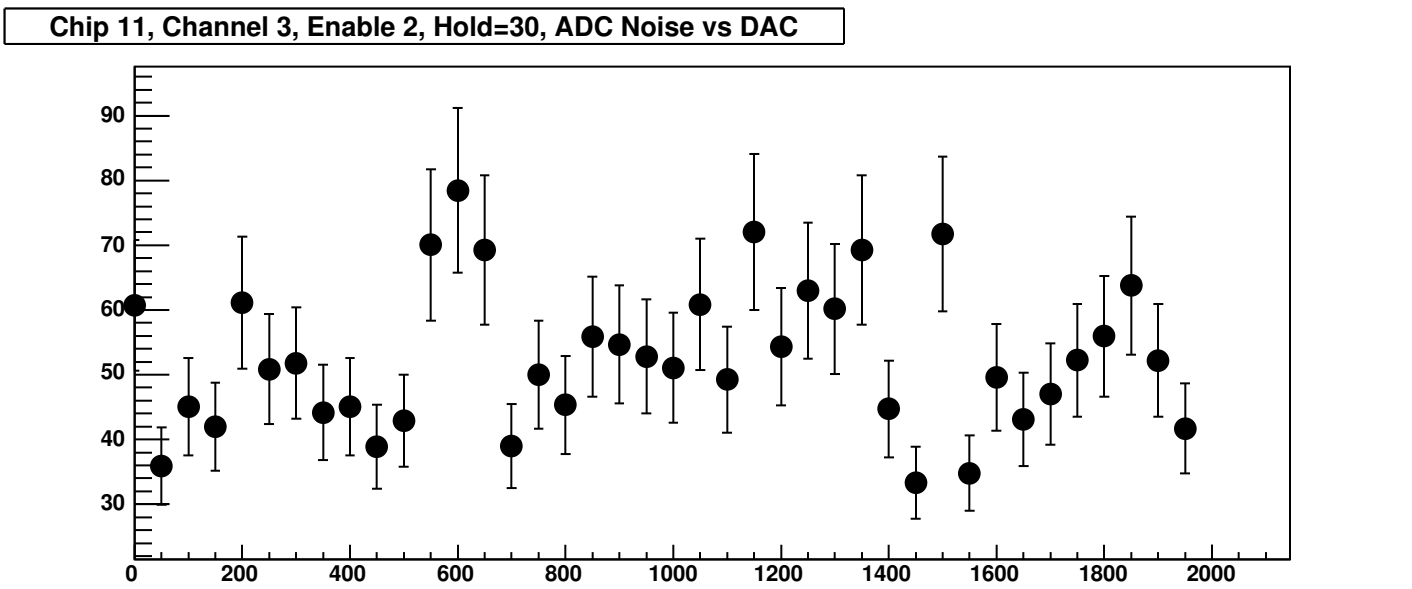

Chip 11, Channel 3, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

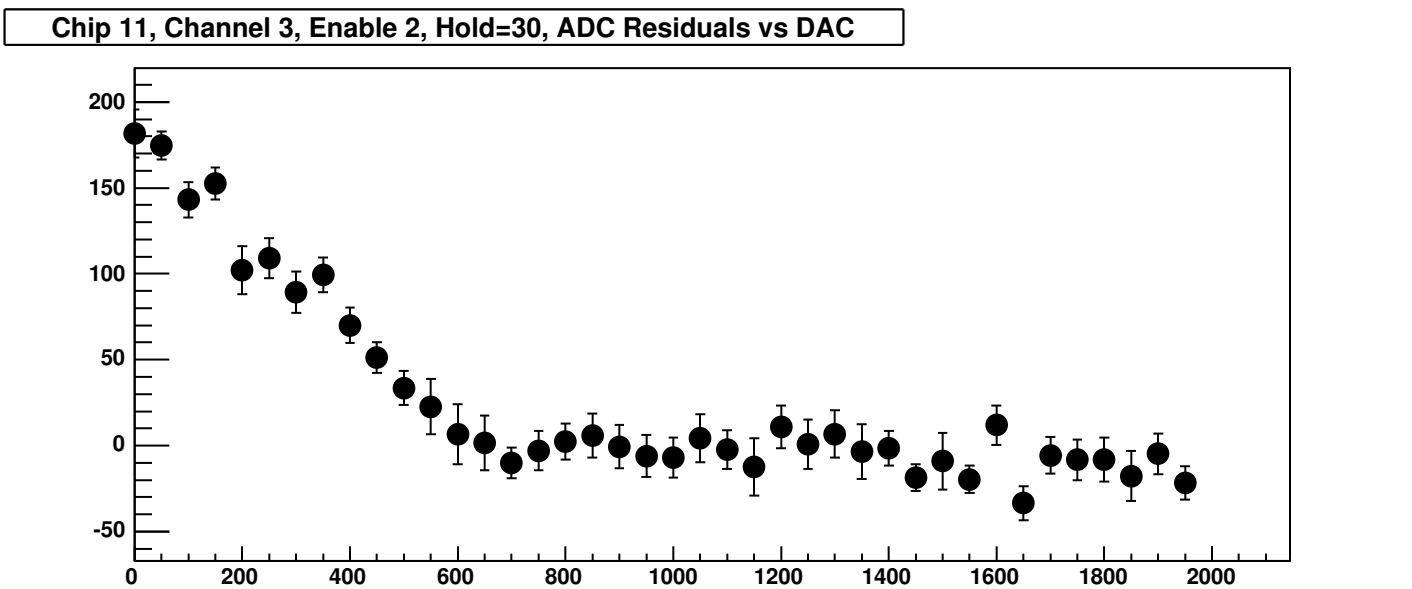

Chip 11, Channel 3, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 4.237 / 11
p0 -1293 ± 19.87
p1 0.2621 ± 0.01805
-750
-800
-850
-900
-950
-1000
-1050
-1100
-1150
0
200
400
600
800
1000
1200
1400
1600
1800
2000

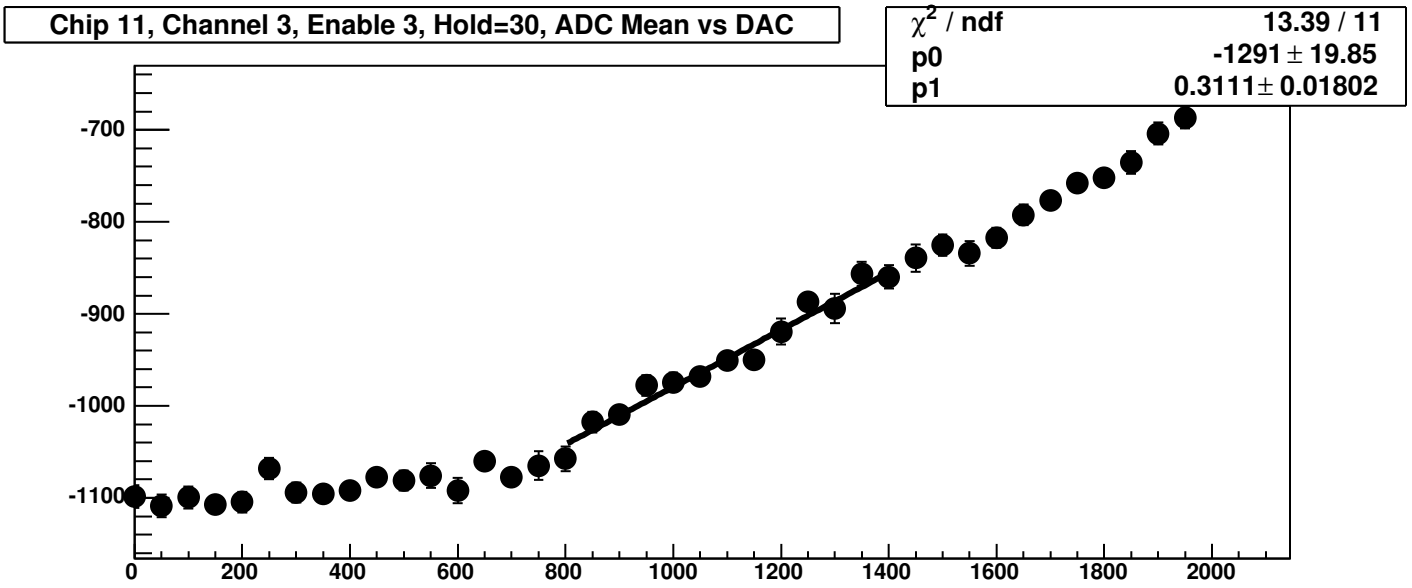

Chip 11, Channel 3, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

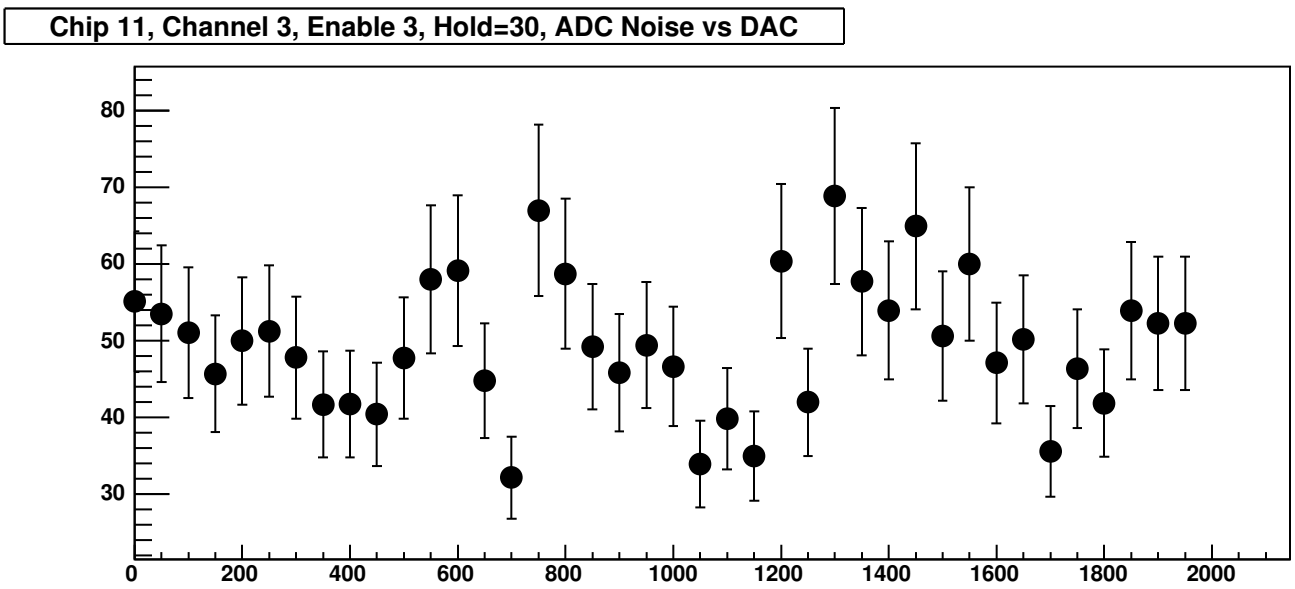

Chip 11, Channel 3, Enable 1, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

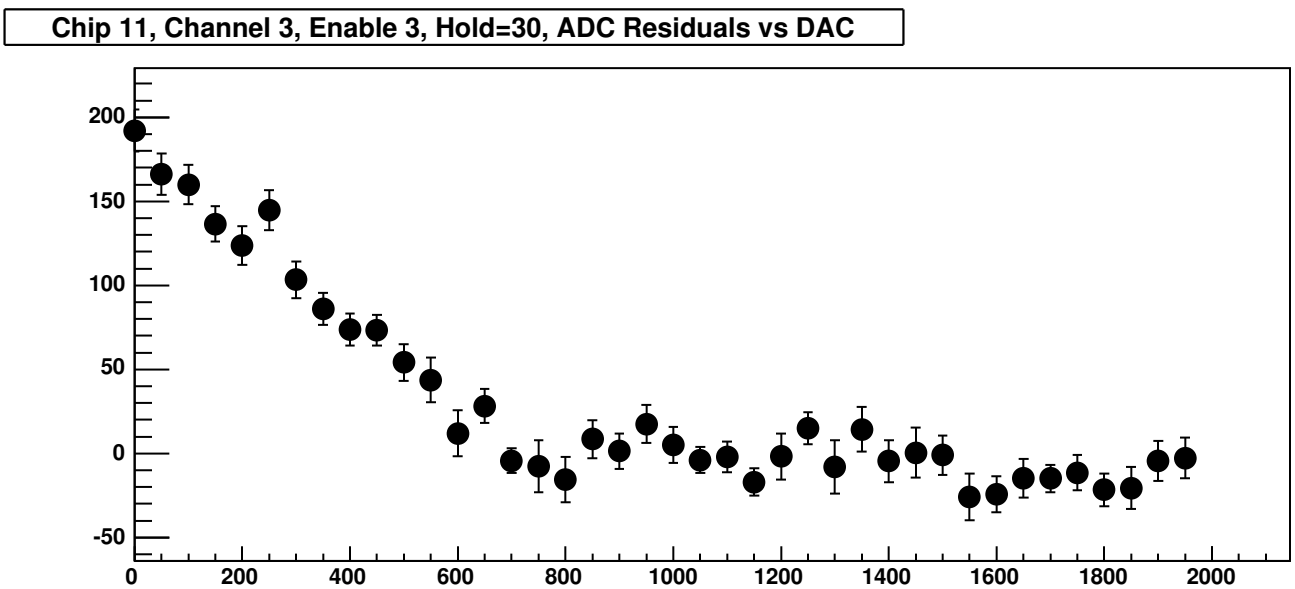

Chip 11, Channel 3, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2.645 / 11
p0 -1281 ± 19.57
p1 0.28 ± 0.01772
-750
-800
-850
-900
-950
-1000
-1050
-1100
-1150
0
200
400
600
800
1000
1200
1400
1600
1800
2000

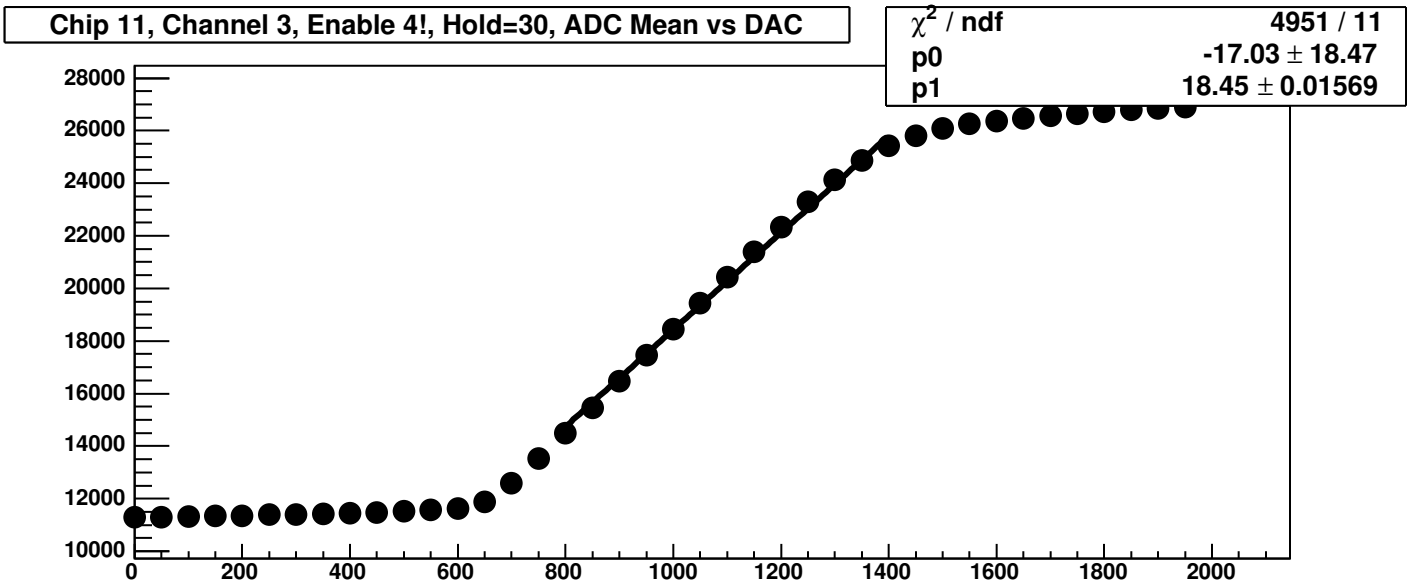

Chip 11, Channel 3, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

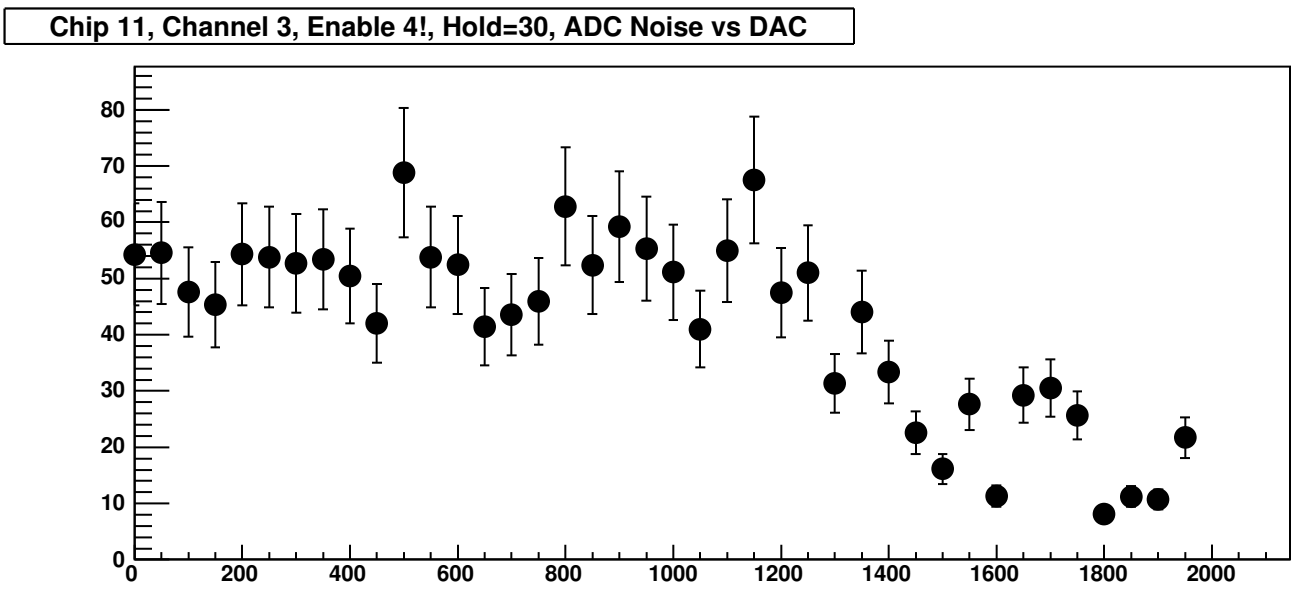

Chip 11, Channel 3, Enable 2, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

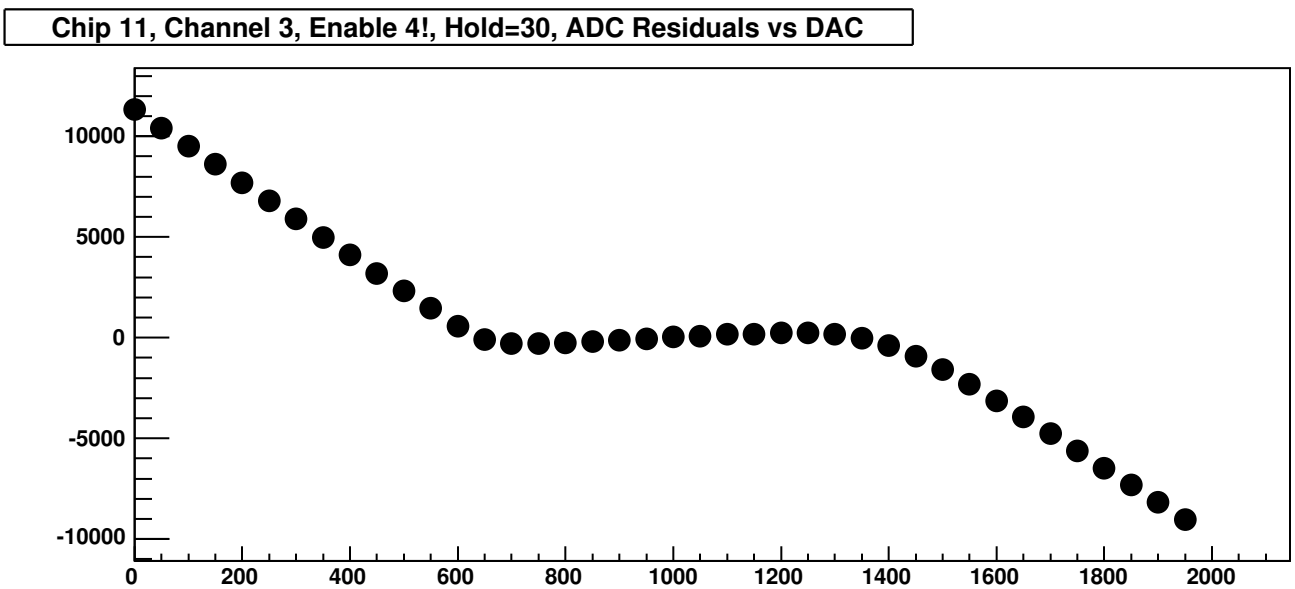

Chip 11, Channel 3, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 13.39 / 11
p0 -1291 ± 19.85
p1 0.3111 ± 0.01802
-700
-800
-900
-1000
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

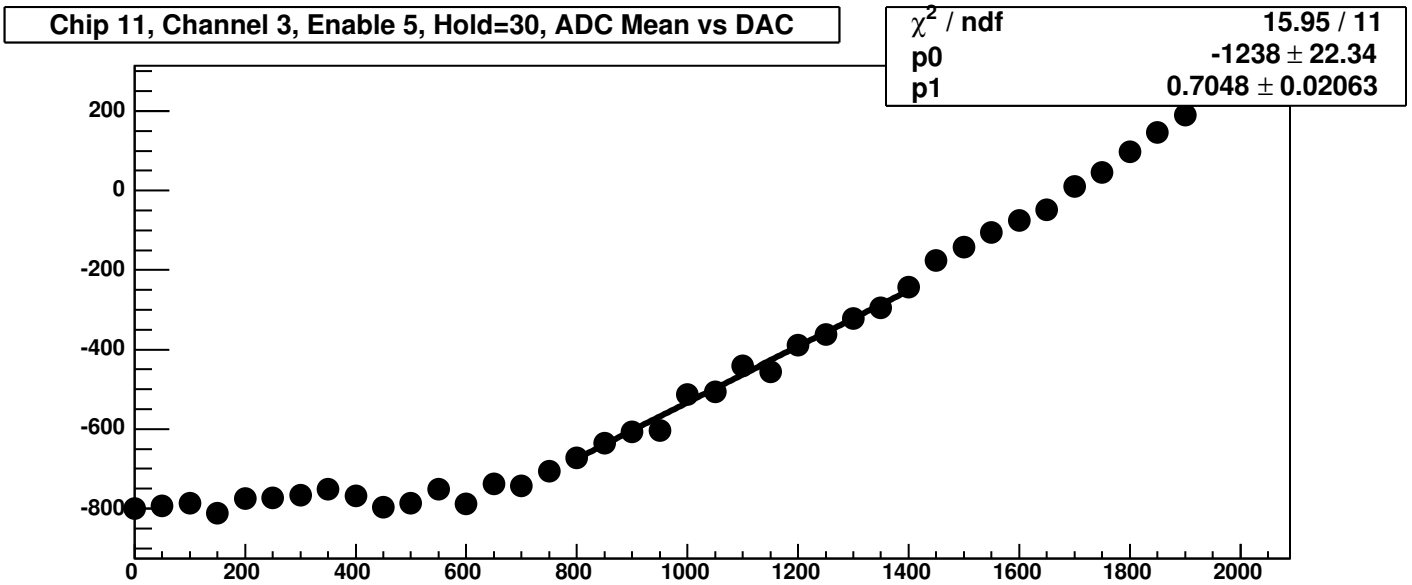

Chip 11, Channel 3, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

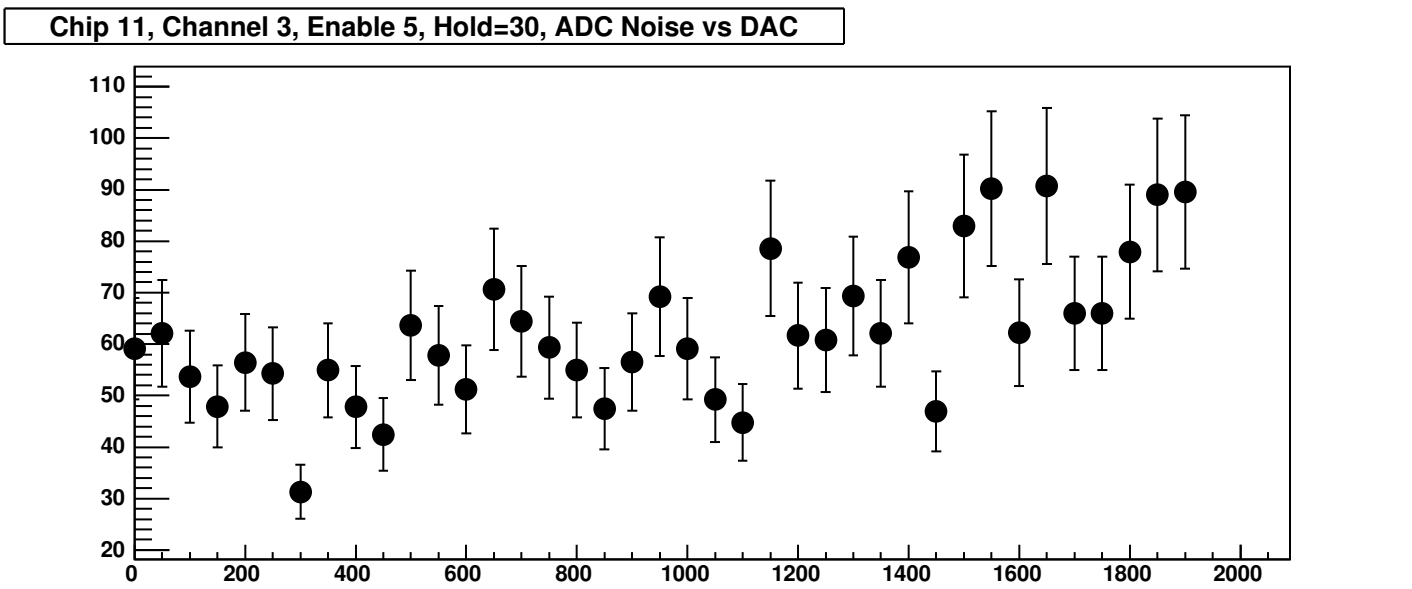

Chip 11, Channel 3, Enable 3, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

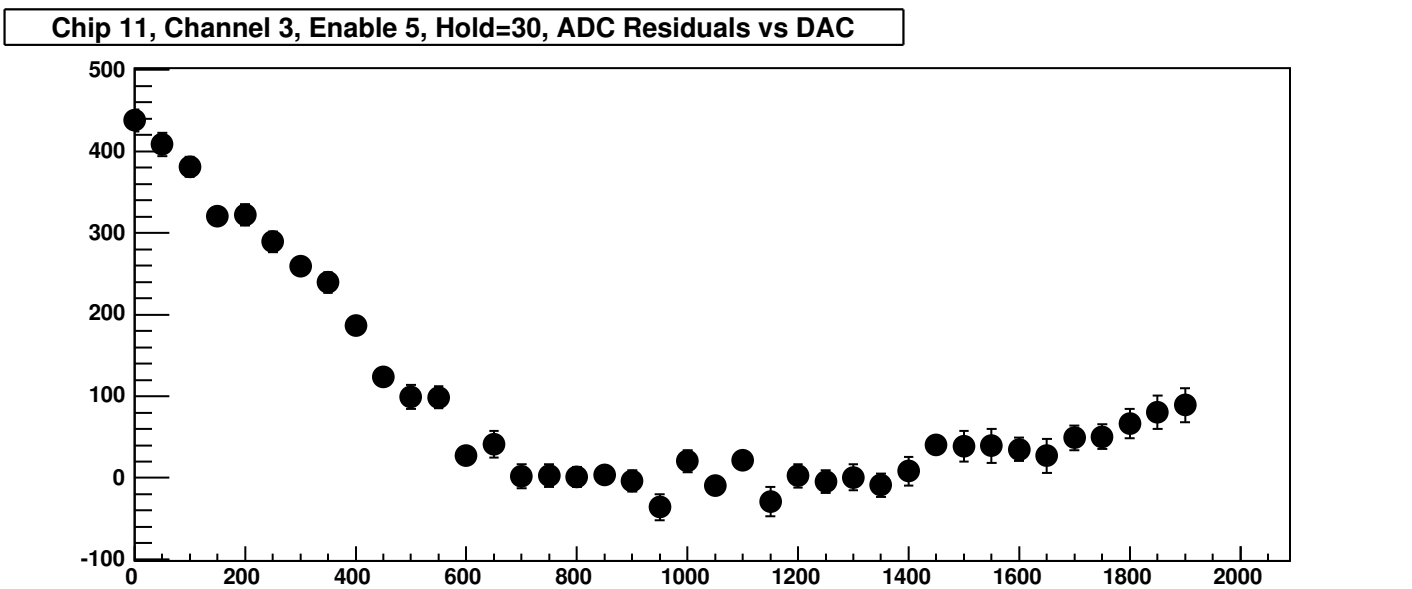

Chip 11, Channel 3, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 4951 / 11
p0 -17.03 ± 18.47
p1 18.45 ± 0.01569
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 3, Enable 4!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

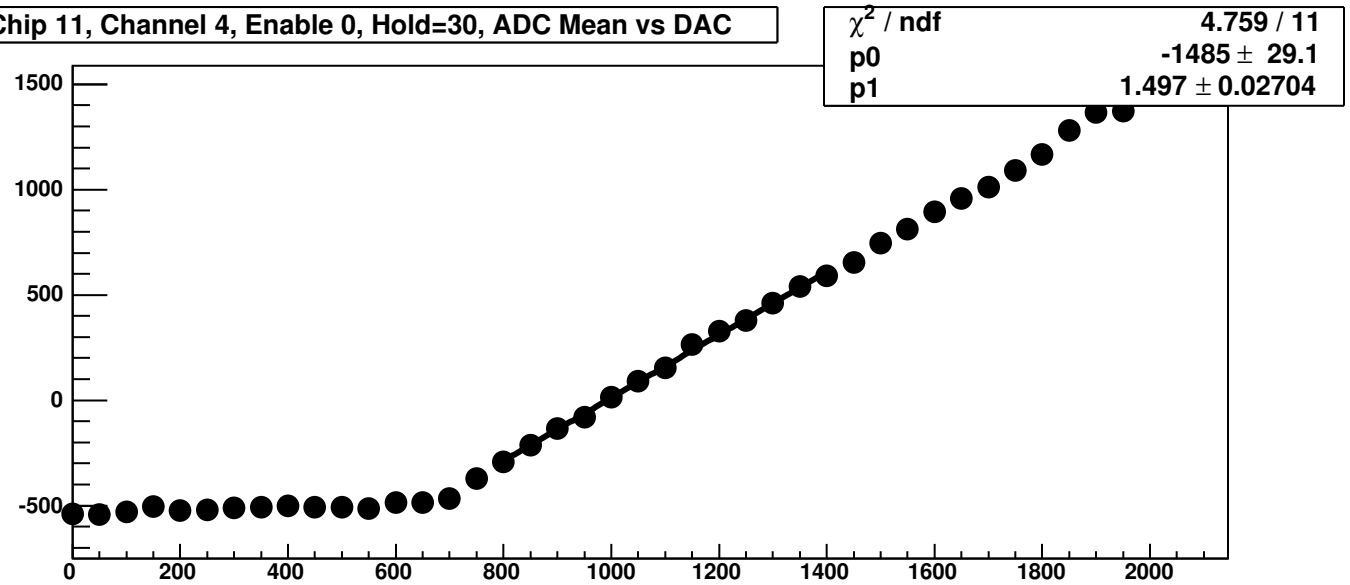

Chip 11, Channel 3, Enable 4!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 3, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 15.95 / 11
p0 -1238 ± 22.34
p1 0.7048 ± 0.02063
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

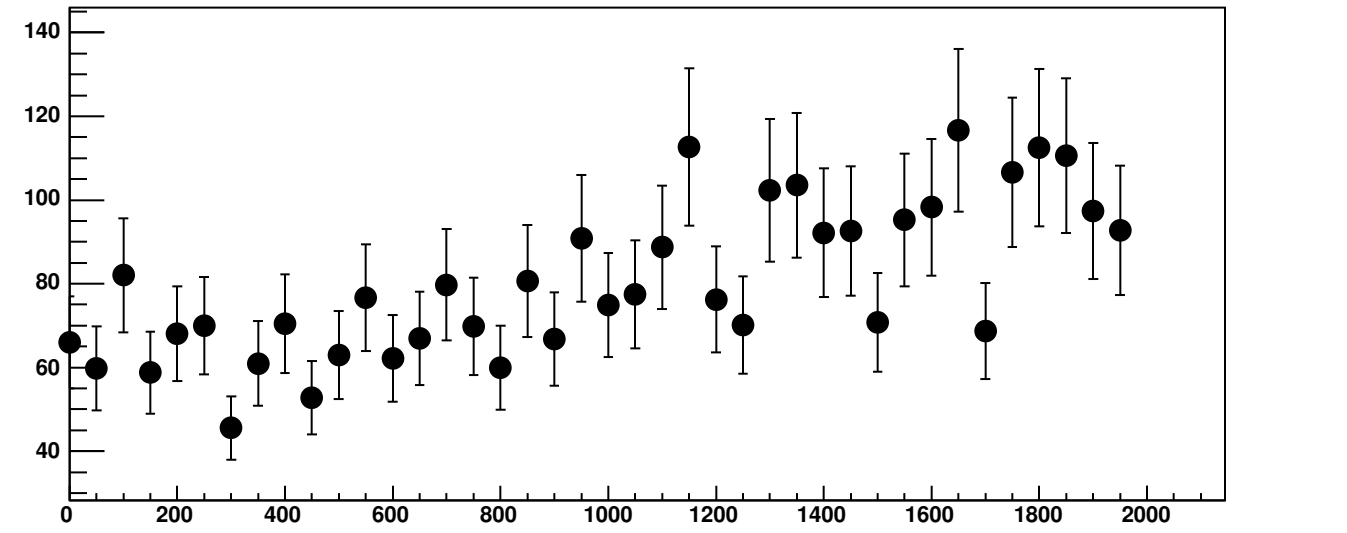

Chip 11, Channel 3, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 3, Enable 5, Hold=30, ADC Residuals vs DAC
500
400
300
200
100
0
-100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

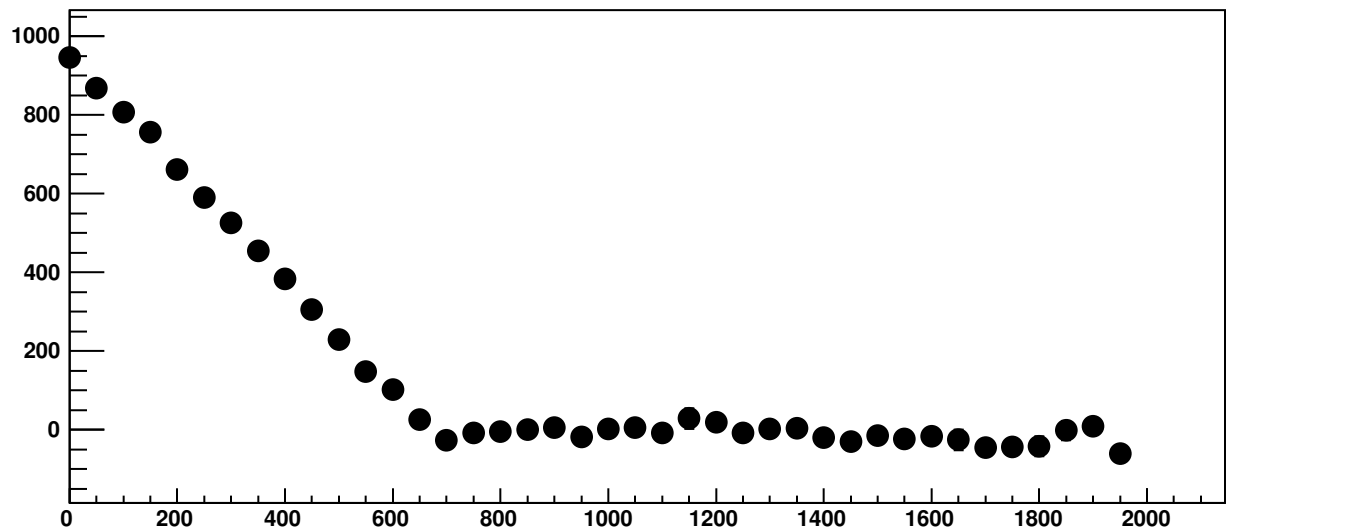
Chip 11, Channel 4, Enable 0, Hold=30, ADC Mean vs DAC

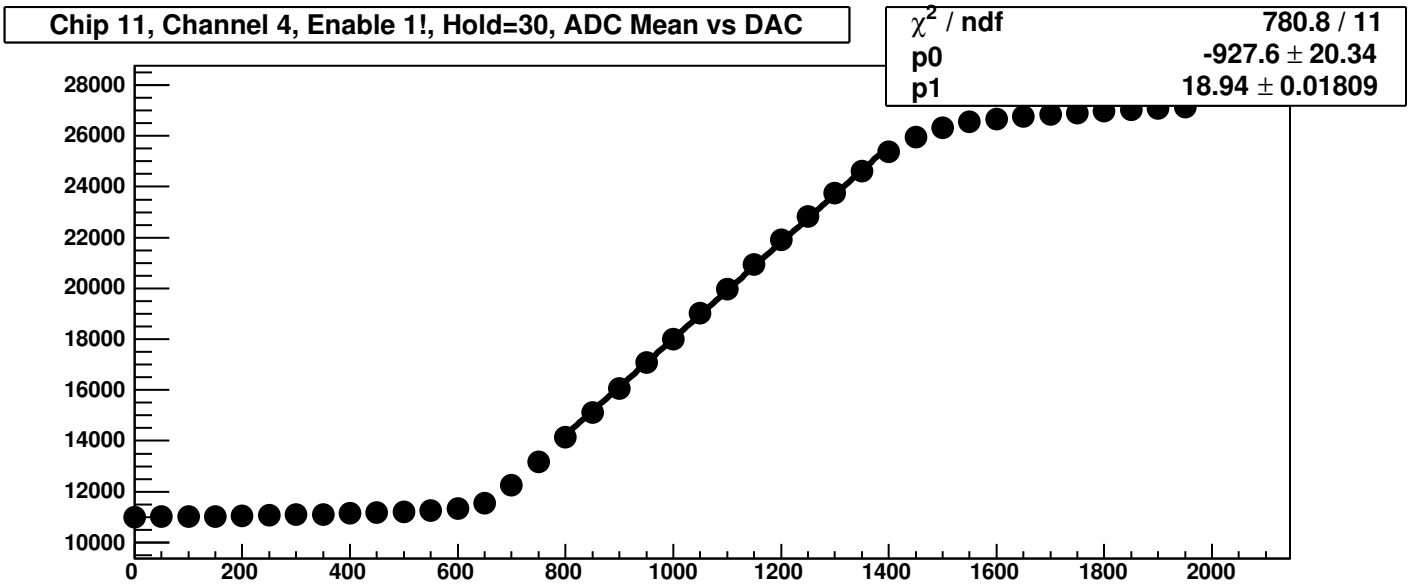


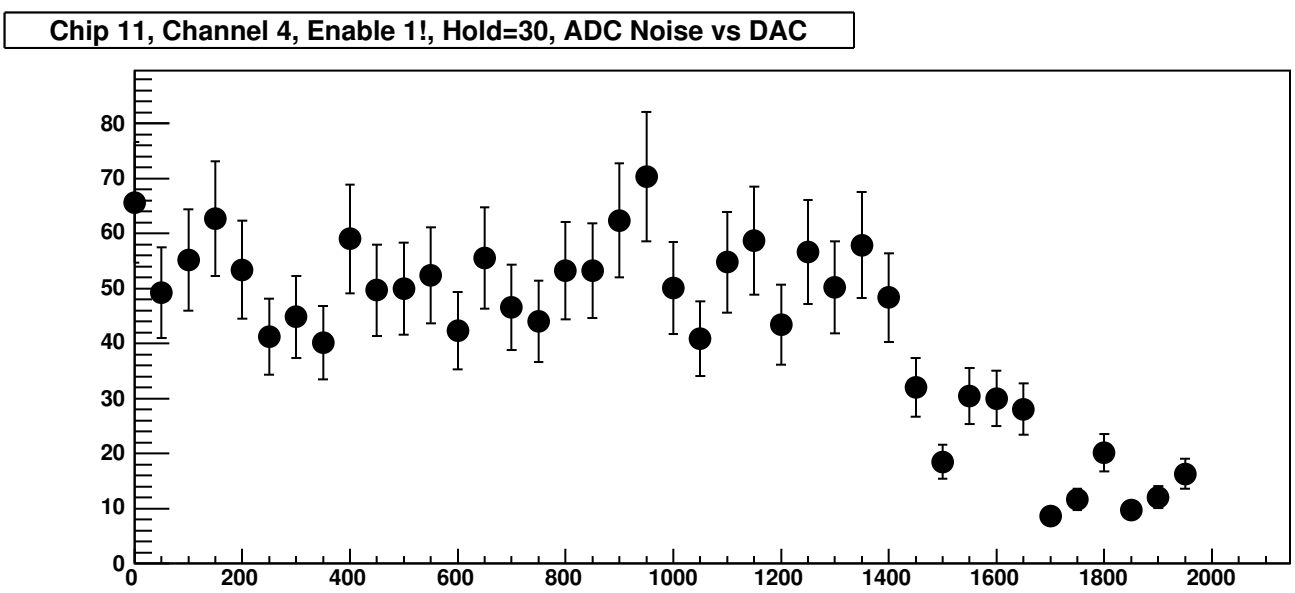
Chip 11, Channel 4, Enable 0, Hold=30, ADC Noise vs DAC

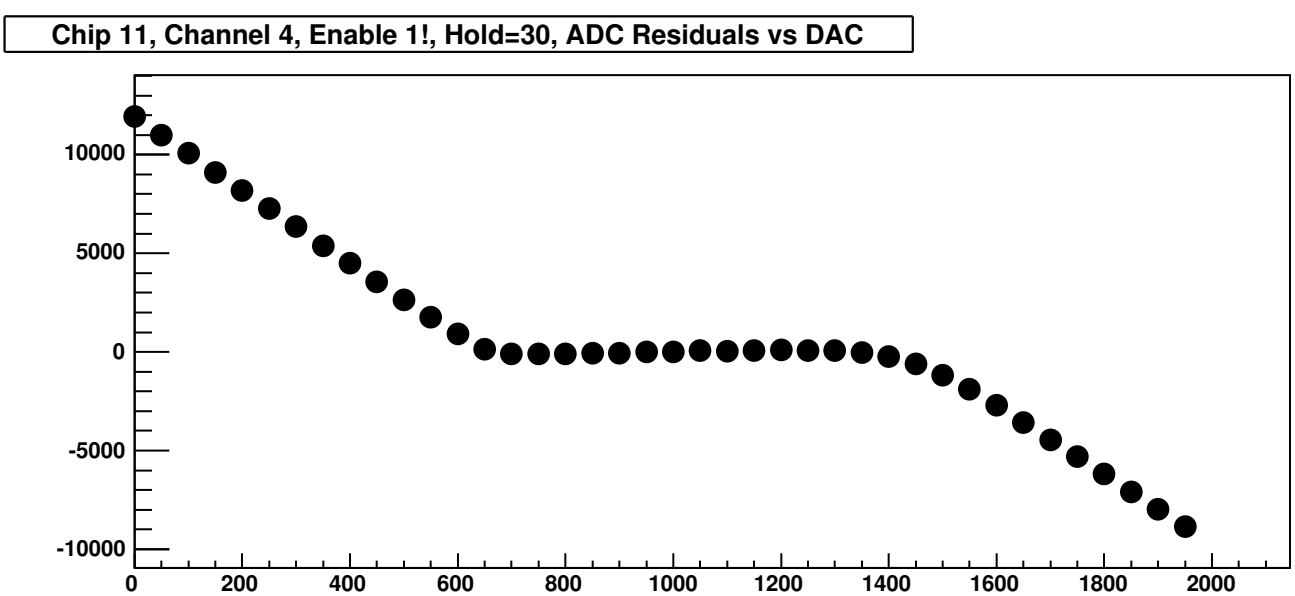


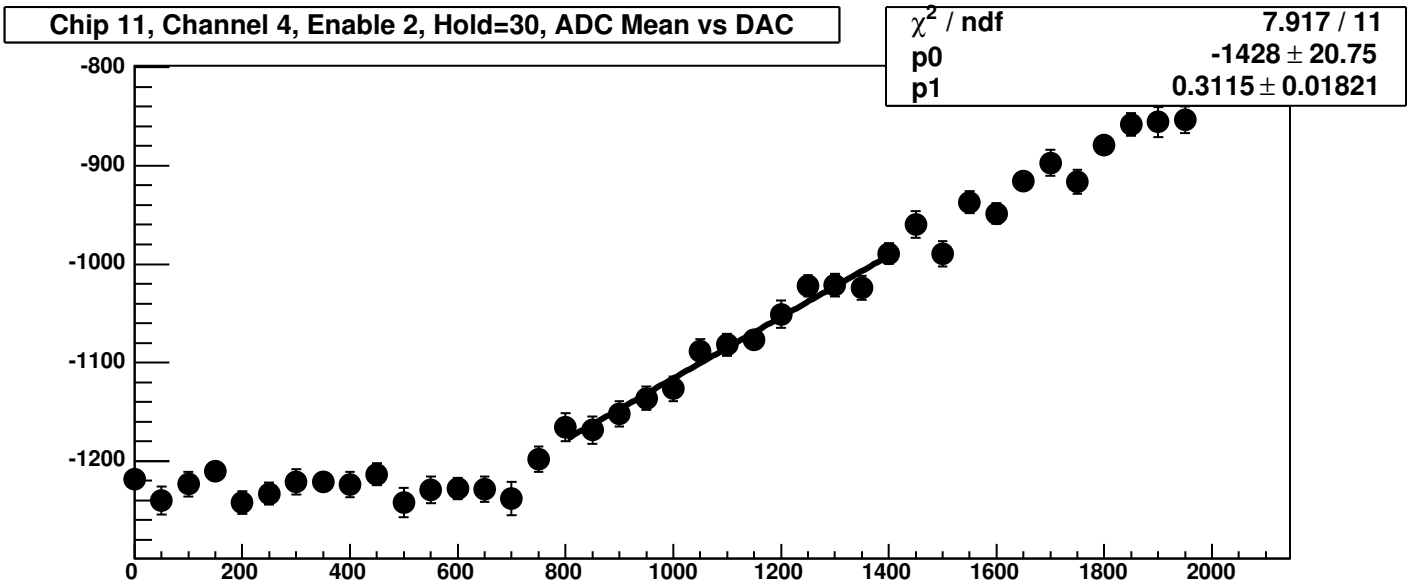
Chip 11, Channel 4, Enable 0, Hold=30, ADC Residuals vs DAC

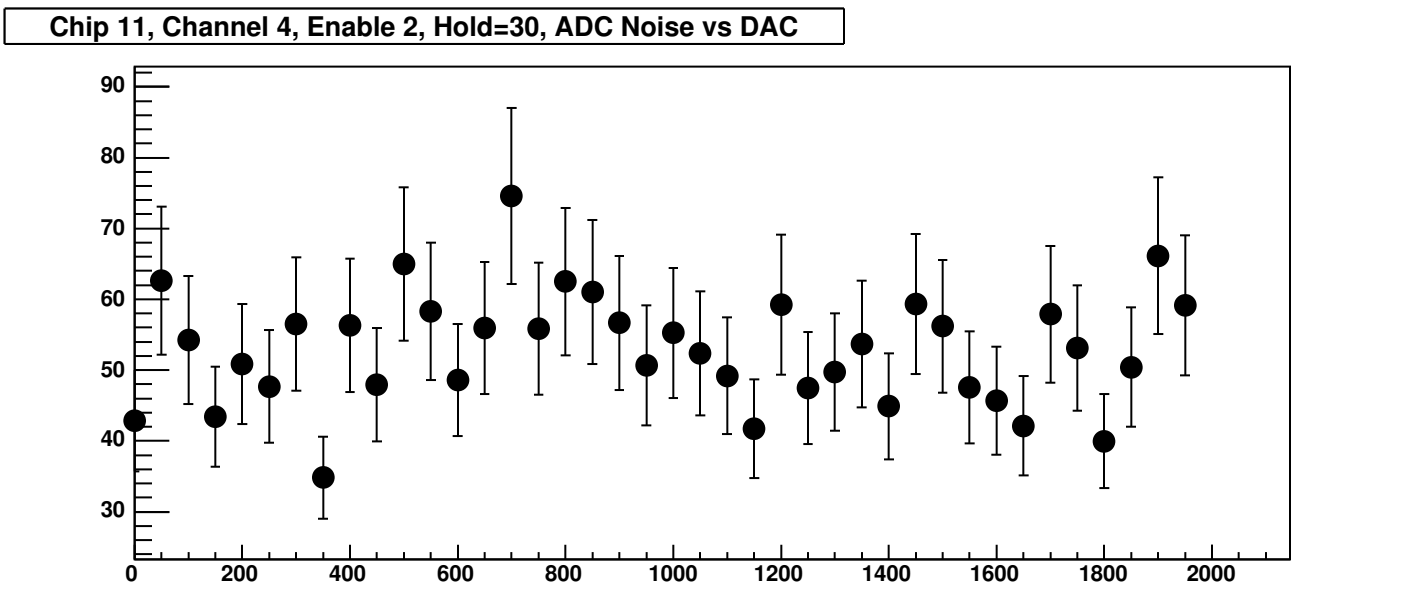


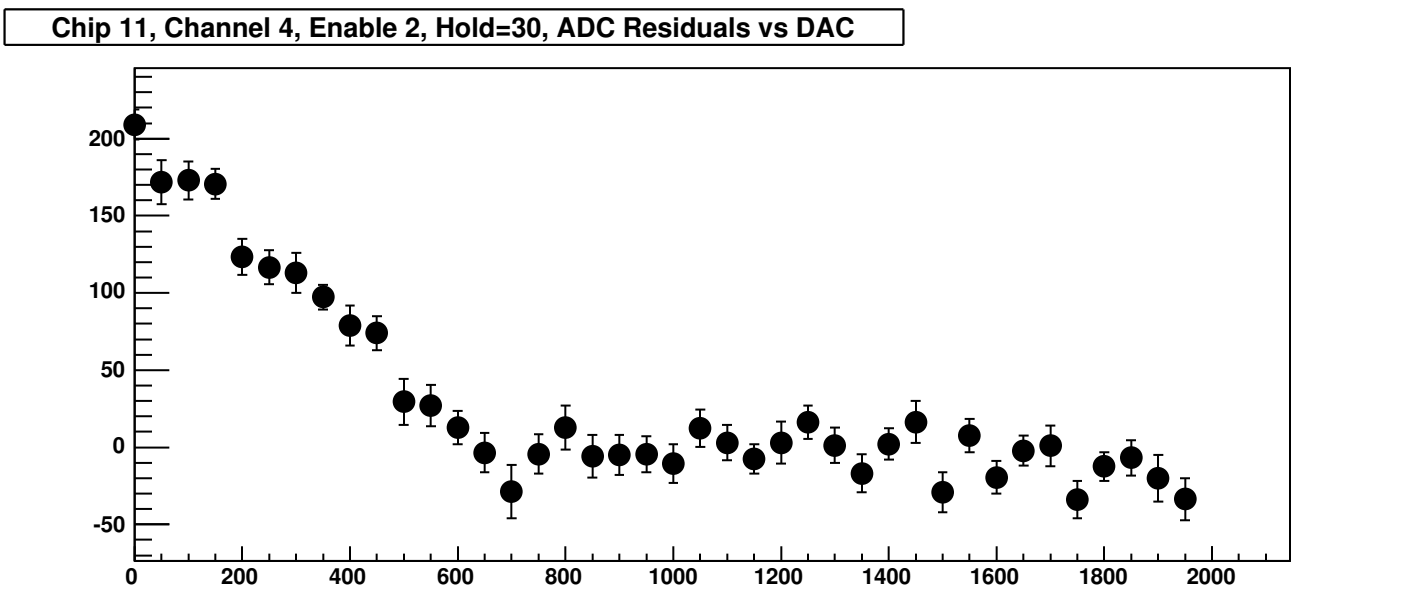

Chip 11, Channel 4, Enable 1!, Hold=30, ADC Mean vs DAC
χ² / ndf 780.8 / 11
p0 -927.6 ± 20.34
p1 18.94 ± 0.01809
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

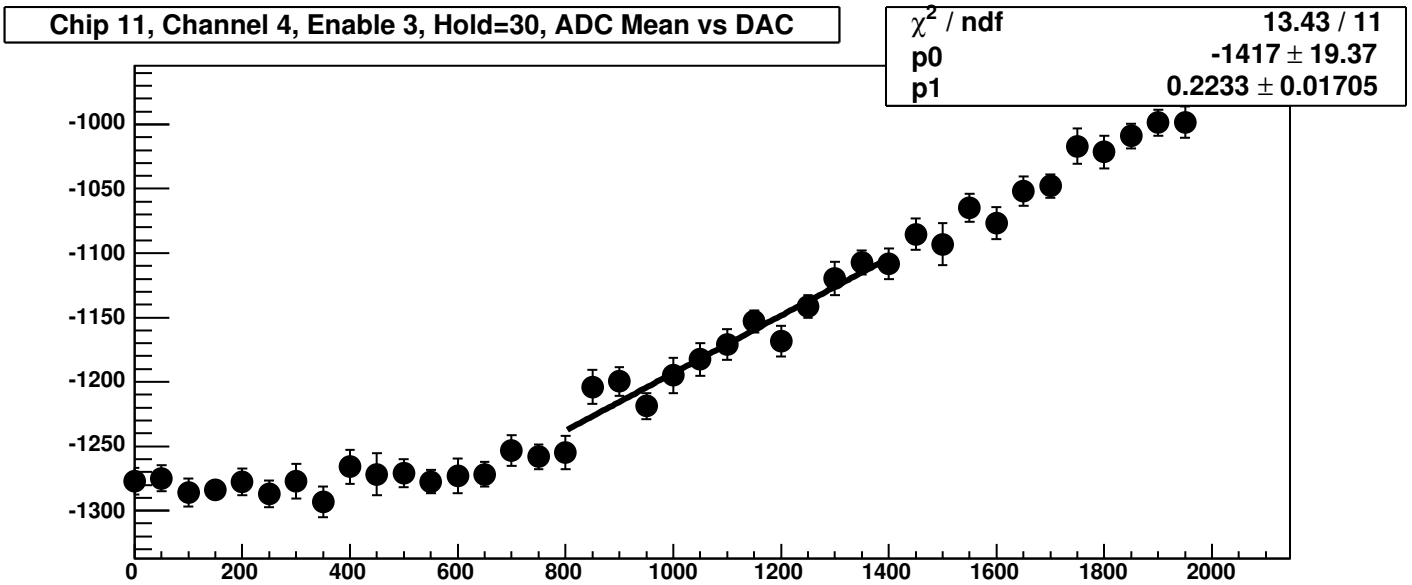

Chip 11, Channel 4, Enable 1!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

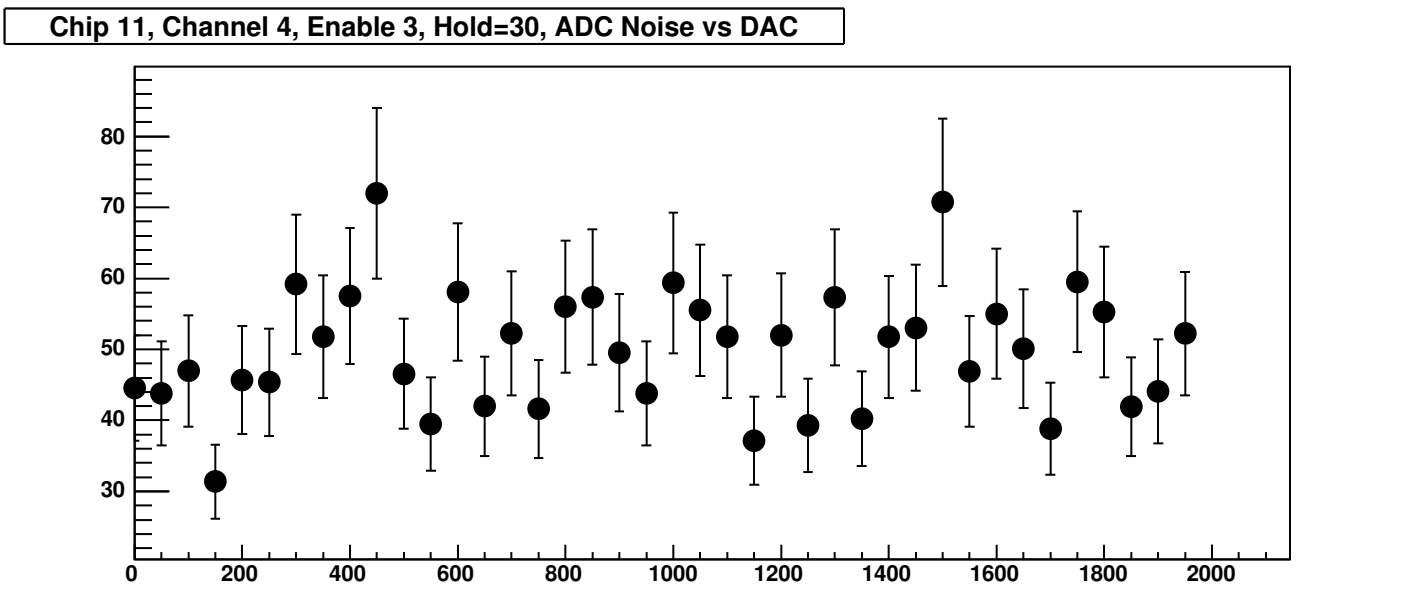

Chip 11, Channel 4, Enable 1!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

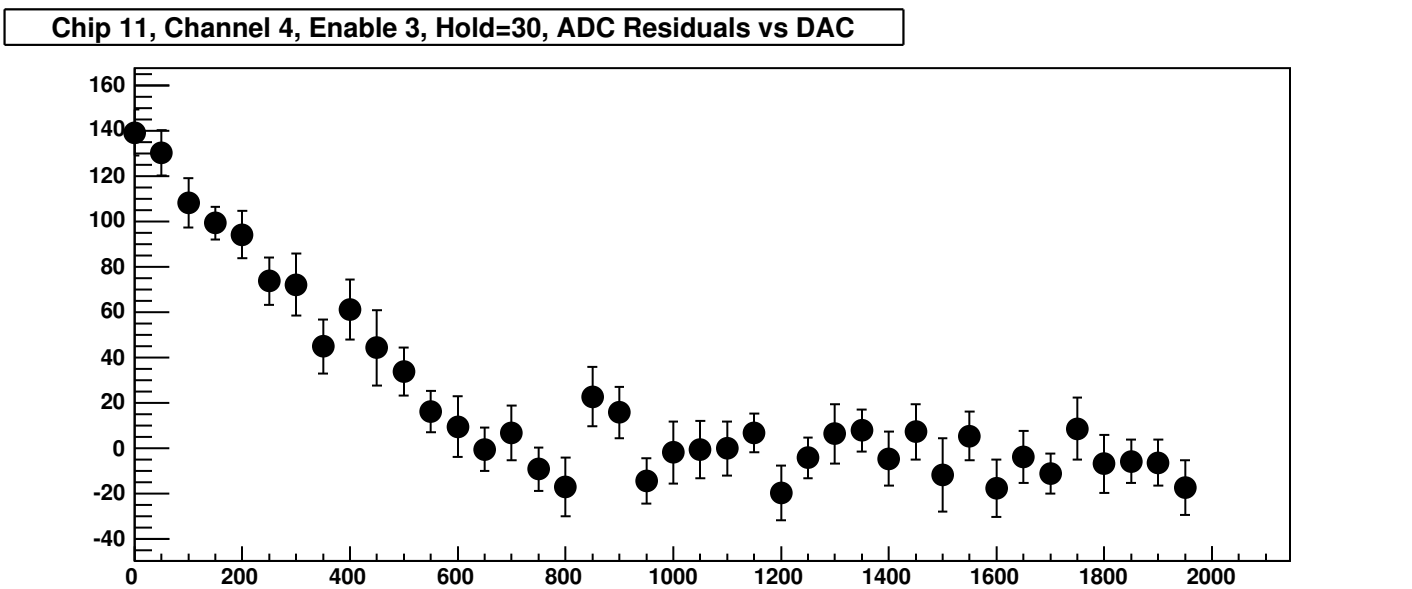

Chip 11, Channel 4, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 7.917 / 11
p0 -1428 ± 20.75
p1 0.3115 ± 0.01821
-800
-900
-1000
-1100
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

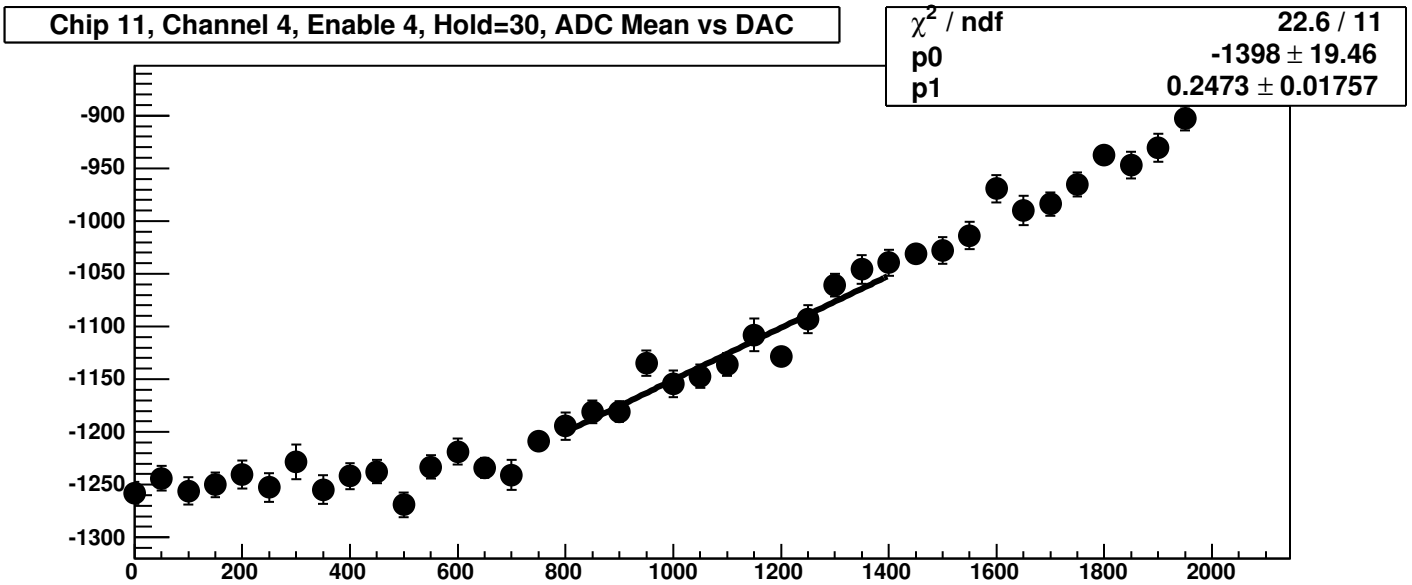

Chip 11, Channel 4, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

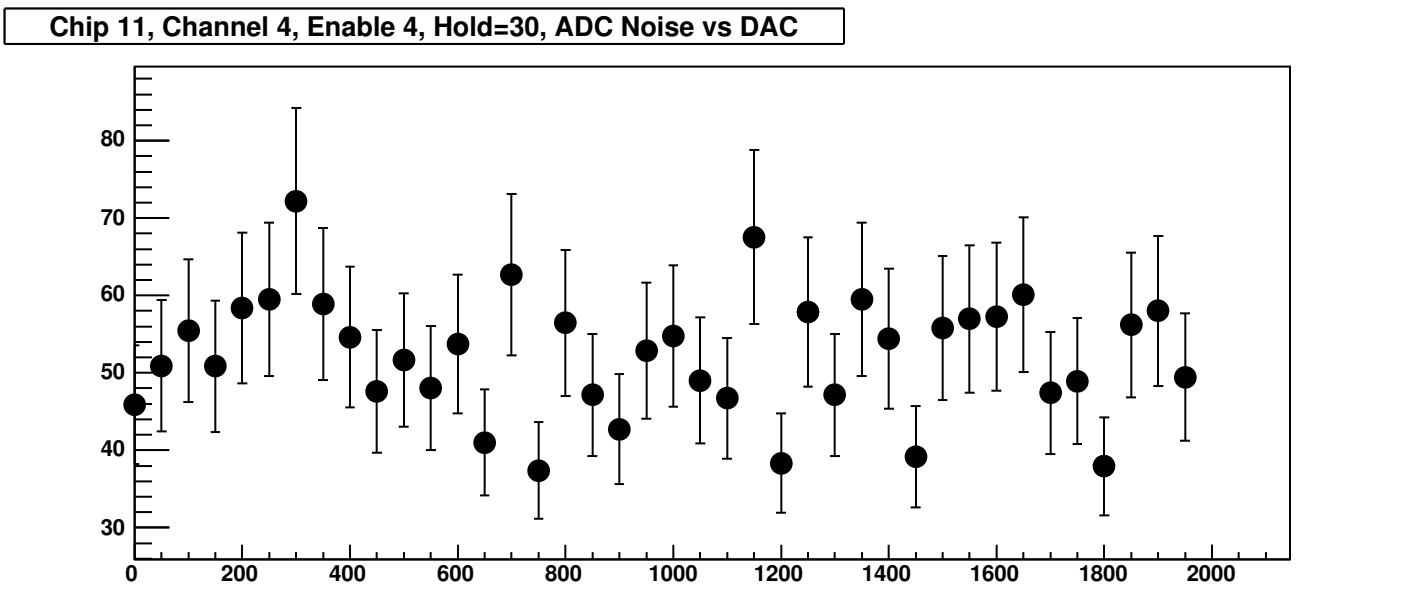

Chip 11, Channel 4, Enable 2, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

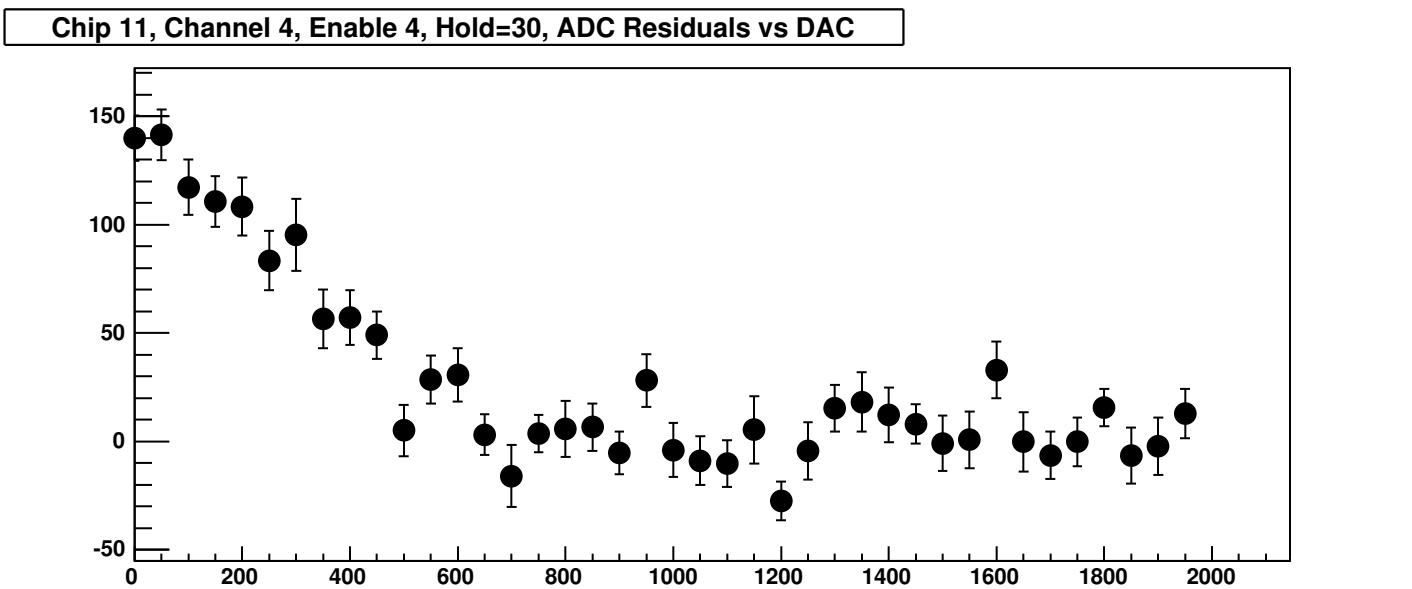

Chip 11, Channel 4, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 13.43 / 11
p0 -1417 ± 19.37
p1 0.2233 ± 0.01705
-1000
-1050
-1100
-1150
-1200
-1250
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 4, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

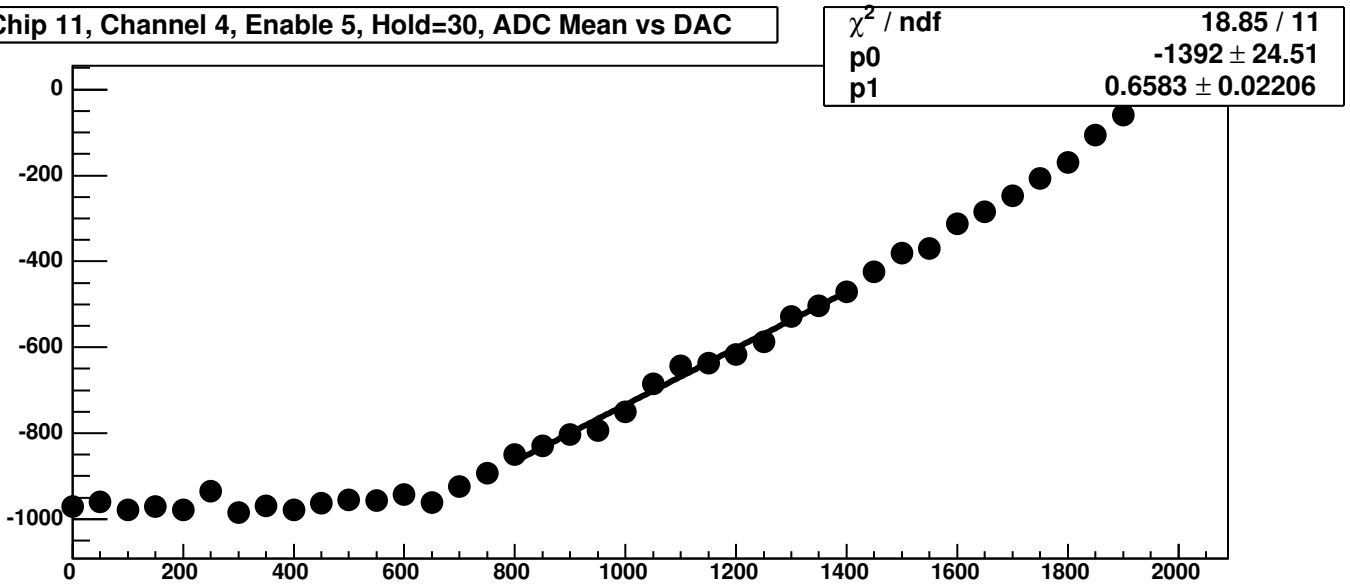

Chip 11, Channel 4, Enable 3, Hold=30, ADC Residuals vs DAC
160
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 4, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 22.6 / 11
p0 -1398 ± 19.46
p1 0.2473 ± 0.01757
-900
-950
-1000
-1050
-1100
-1150
-1200
-1250
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

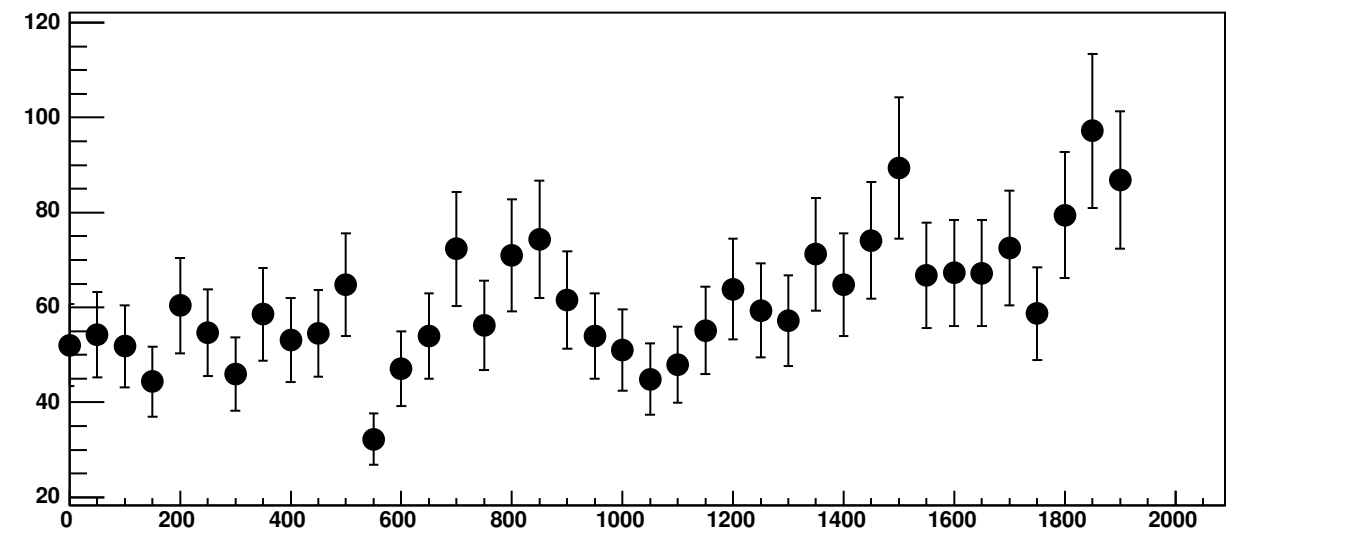

Chip 11, Channel 4, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 4, Enable 4, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

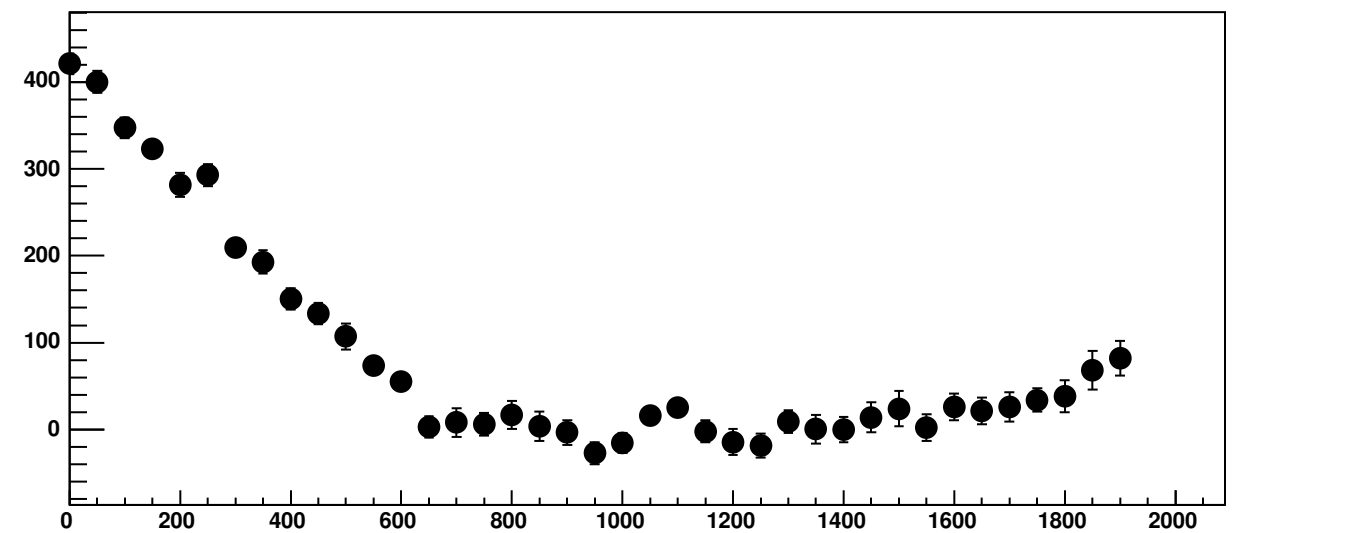
Chip 11, Channel 4, Enable 5, Hold=30, ADC Mean vs DAC



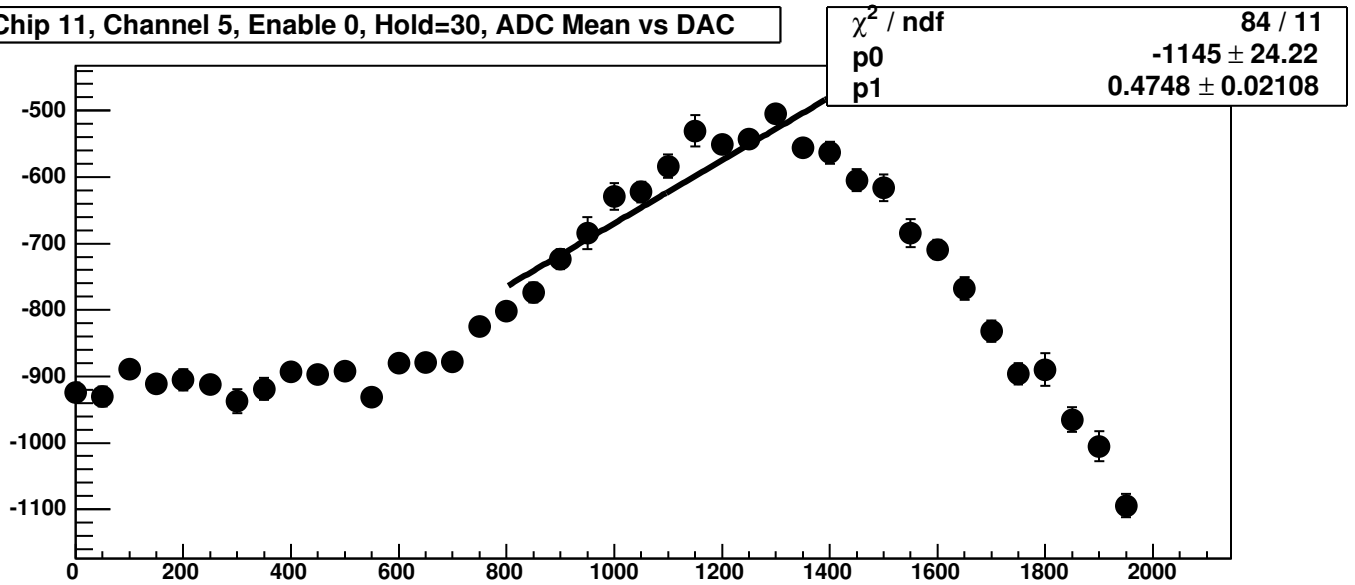
Chip 11, Channel 4, Enable 5, Hold=30, ADC Noise vs DAC



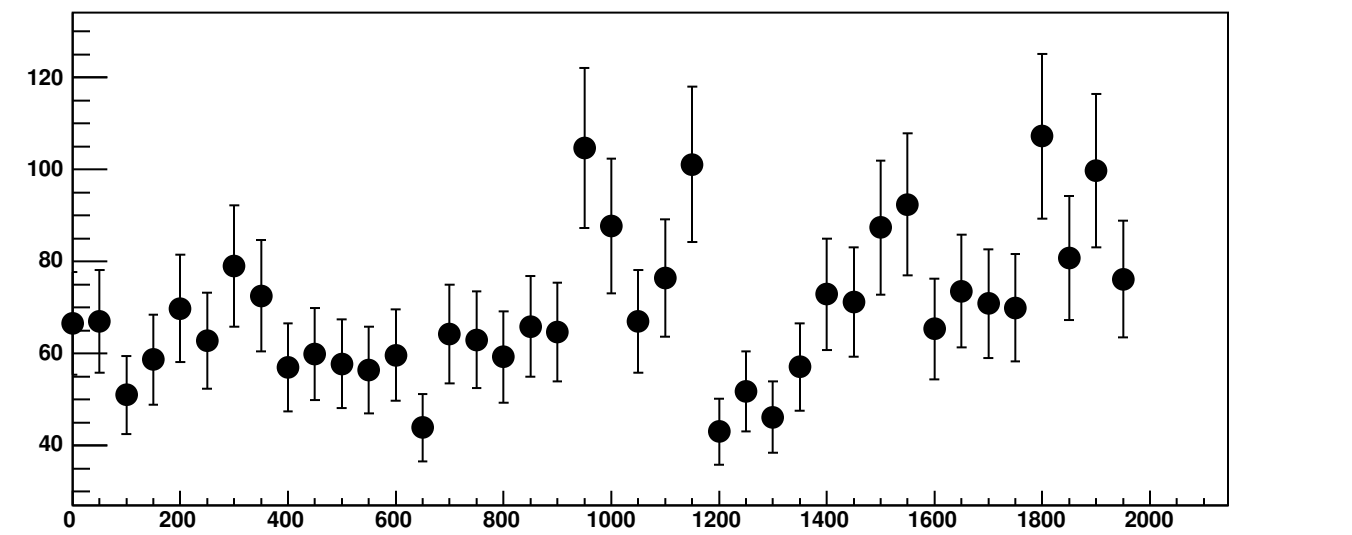
Chip 11, Channel 4, Enable 5, Hold=30, ADC Residuals vs DAC



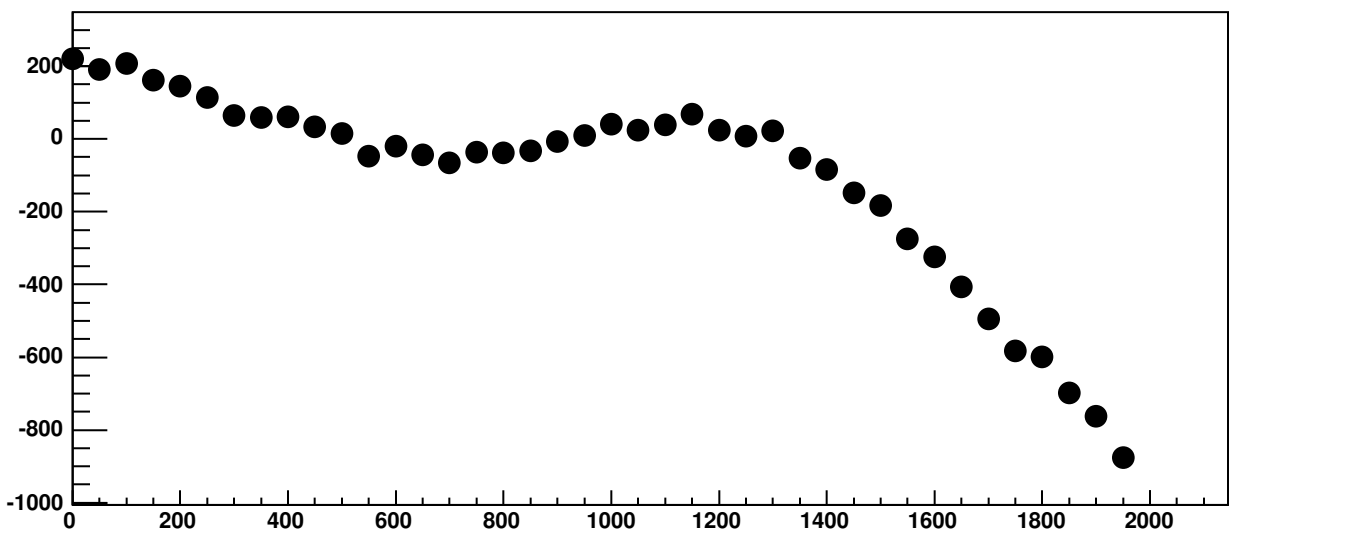
Chip 11, Channel 5, Enable 0, Hold=30, ADC Mean vs DAC

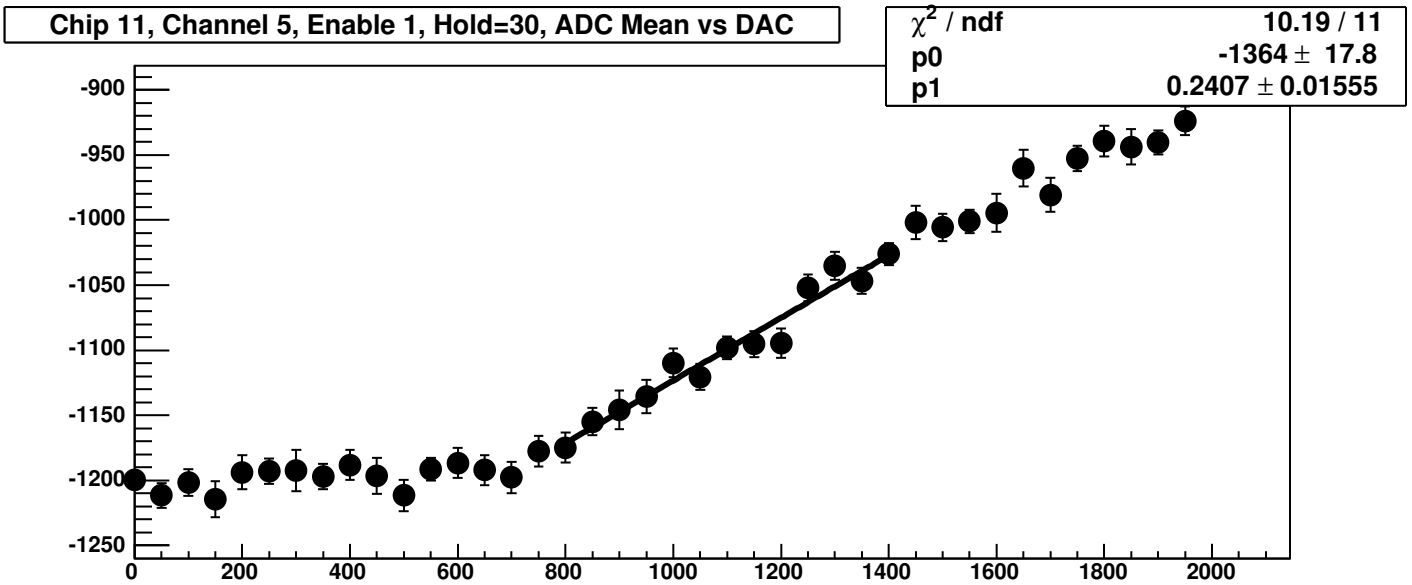


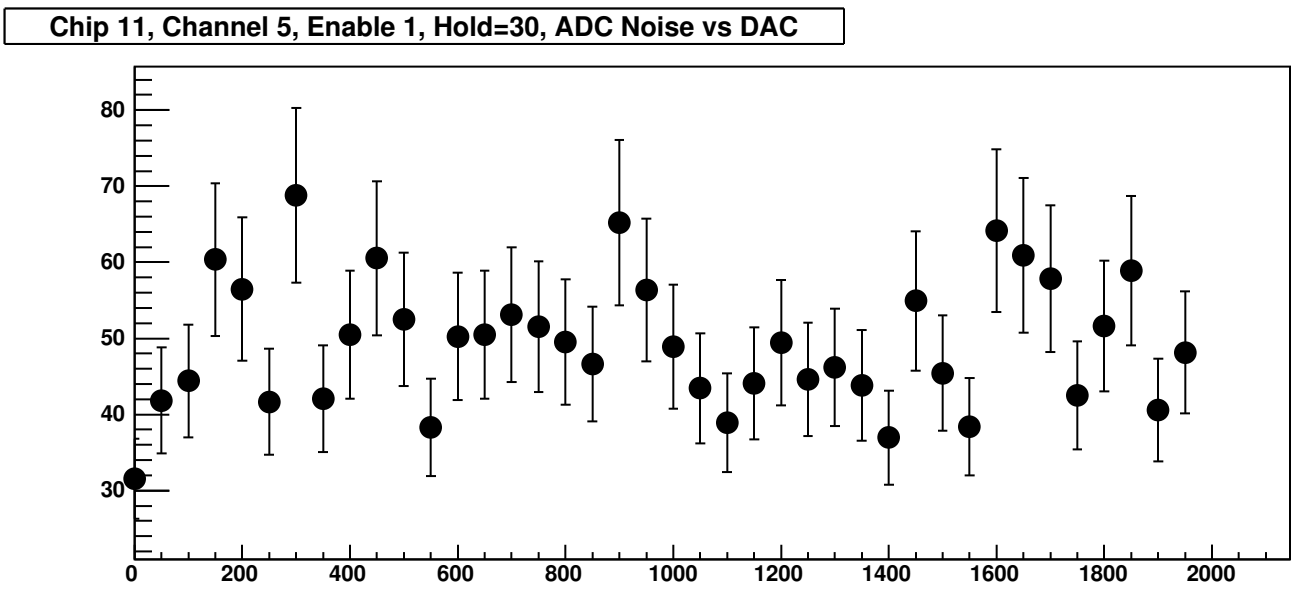
Chip 11, Channel 5, Enable 0, Hold=30, ADC Noise vs DAC

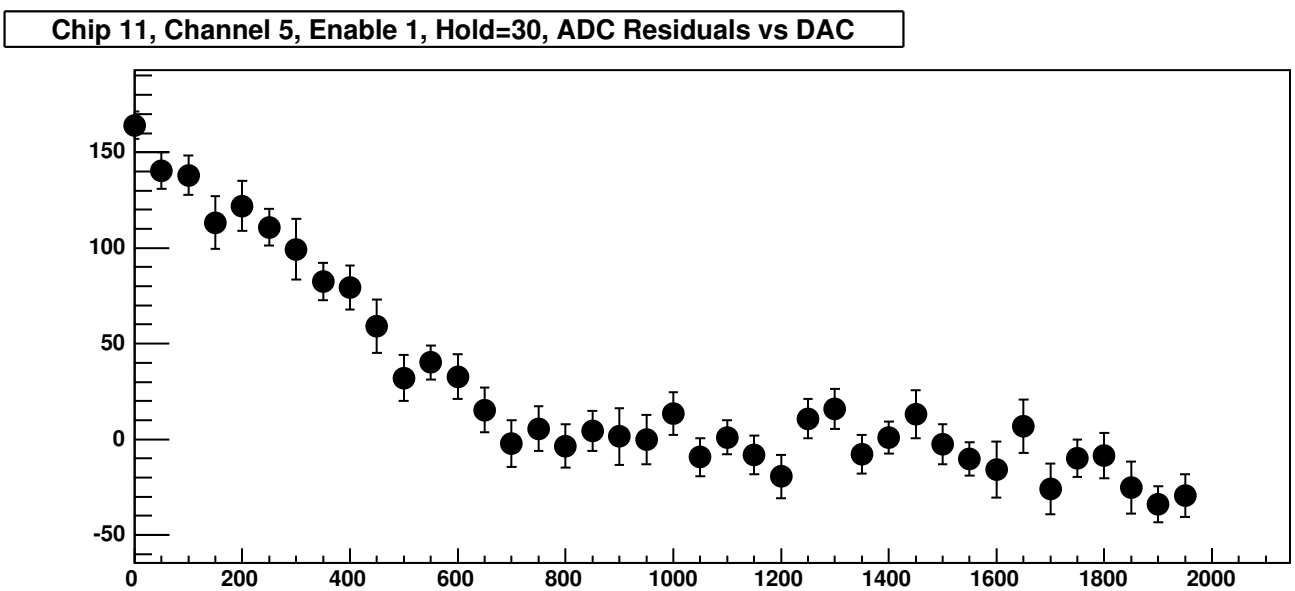


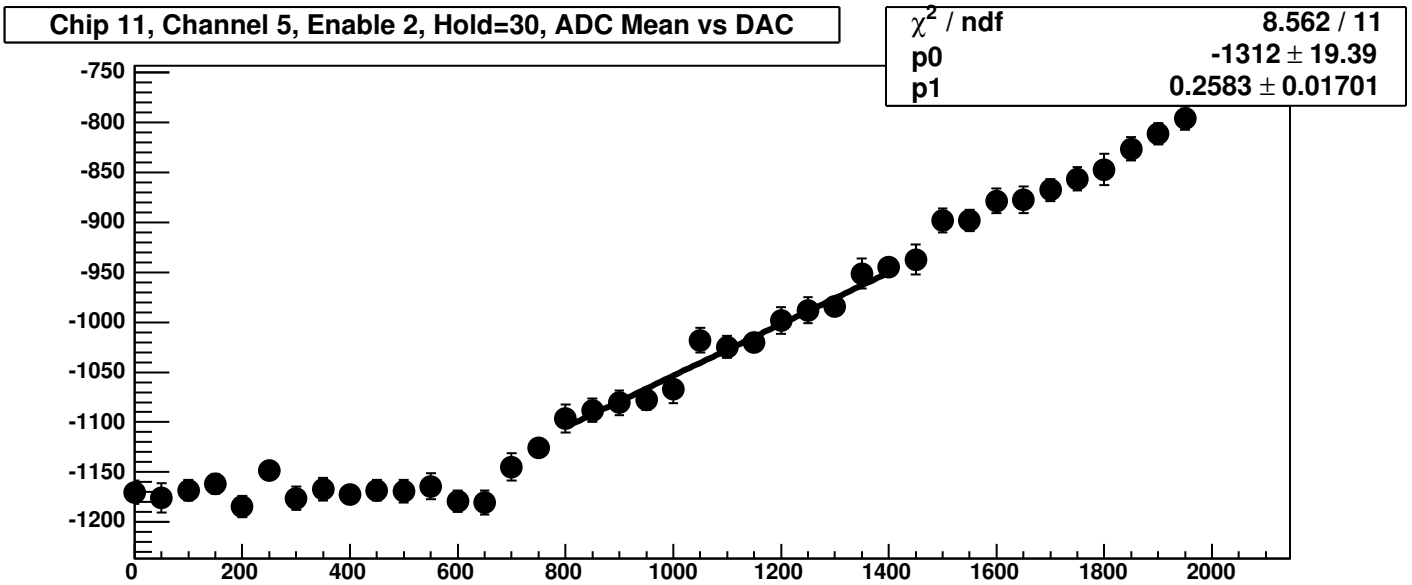
Chip 11, Channel 5, Enable 0, Hold=30, ADC Residuals vs DAC

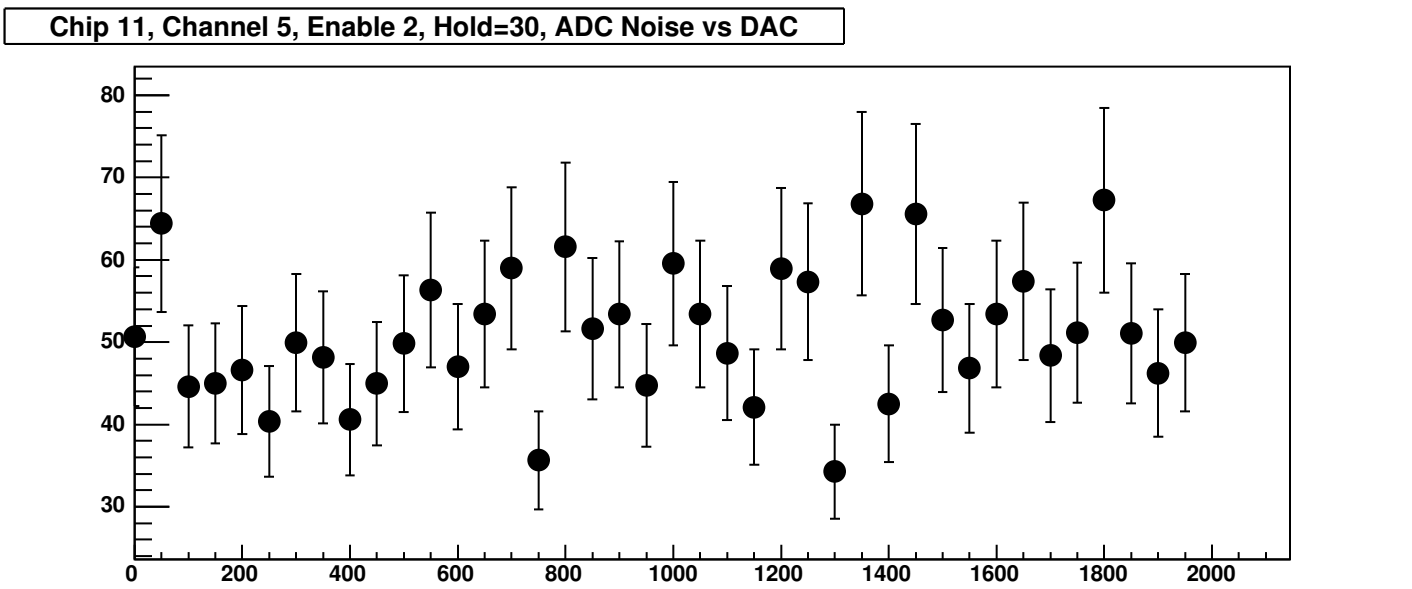


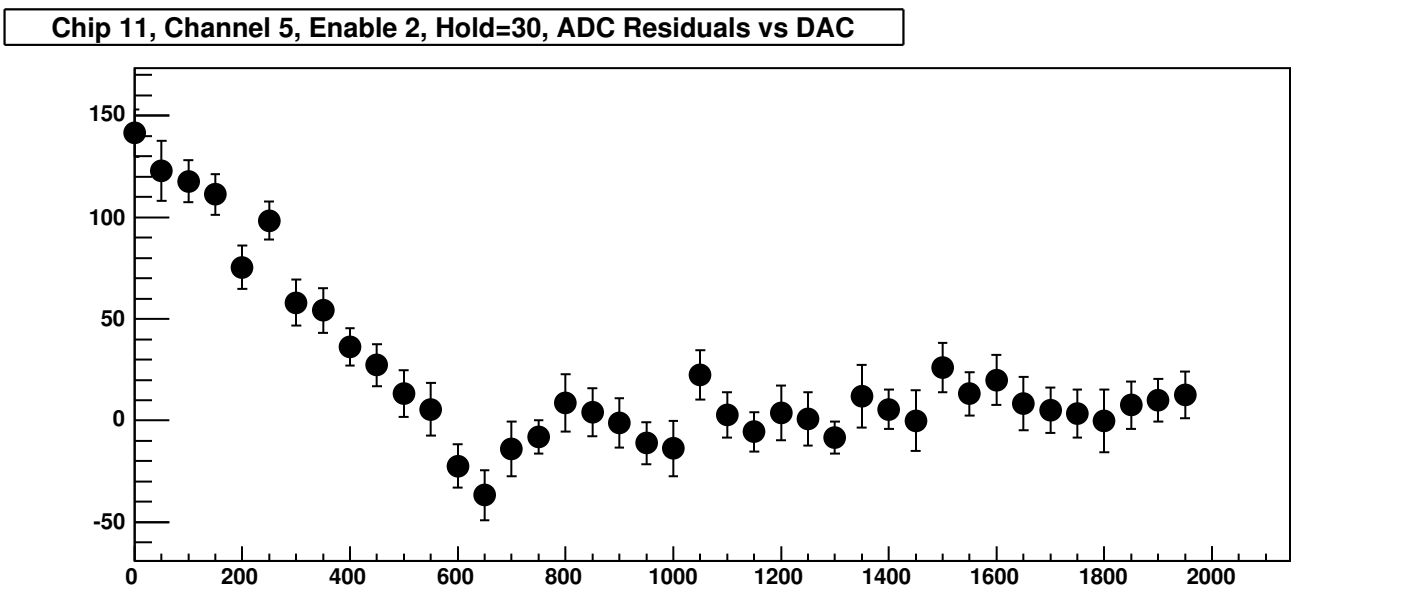

Chip 11, Channel 5, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 10.19 / 11
p0 -1364 ± 17.8
p1 0.2407 ± 0.01555
-900
-950
-1000
-1050
-1100
-1150
-1200
-1250
0
200
400
600
800
1000
1200
1400
1600
1800
2000

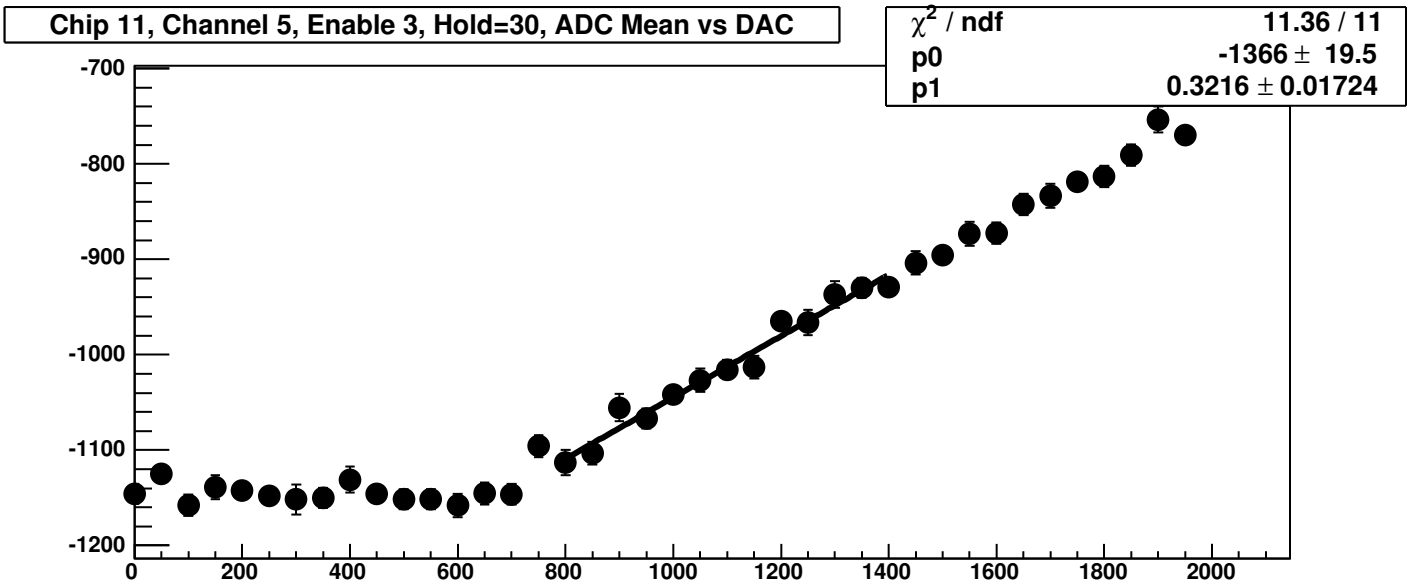

Chip 11, Channel 5, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

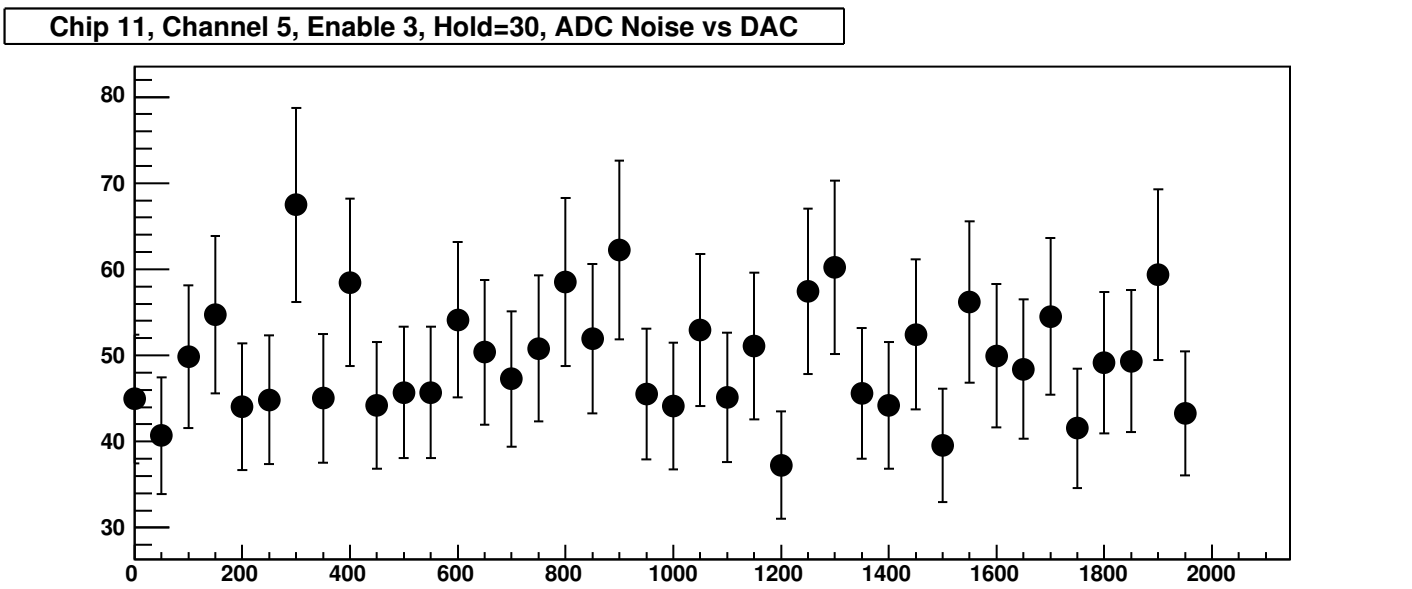

Chip 11, Channel 5, Enable 1, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

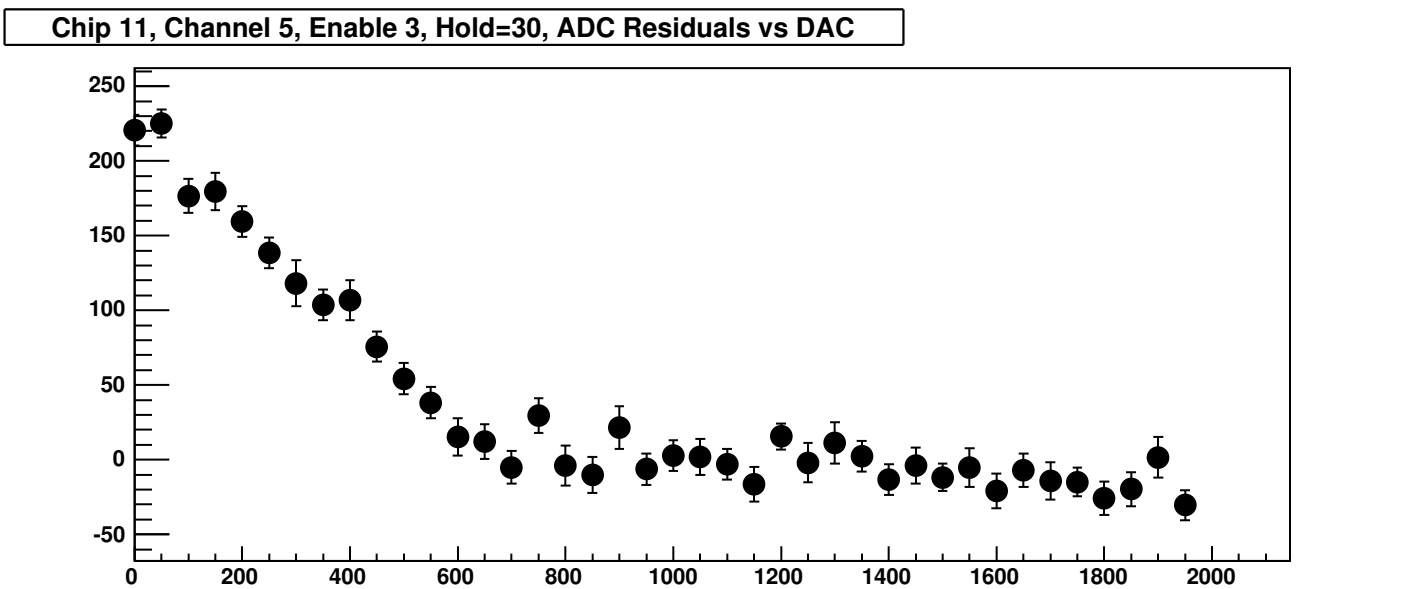

Chip 11, Channel 5, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 8.562 / 11
p0 -1312 ± 19.39
p1 0.2583 ± 0.01701
-750
-800
-850
-900
-950
-1000
-1050
-1100
-1150
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 5, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

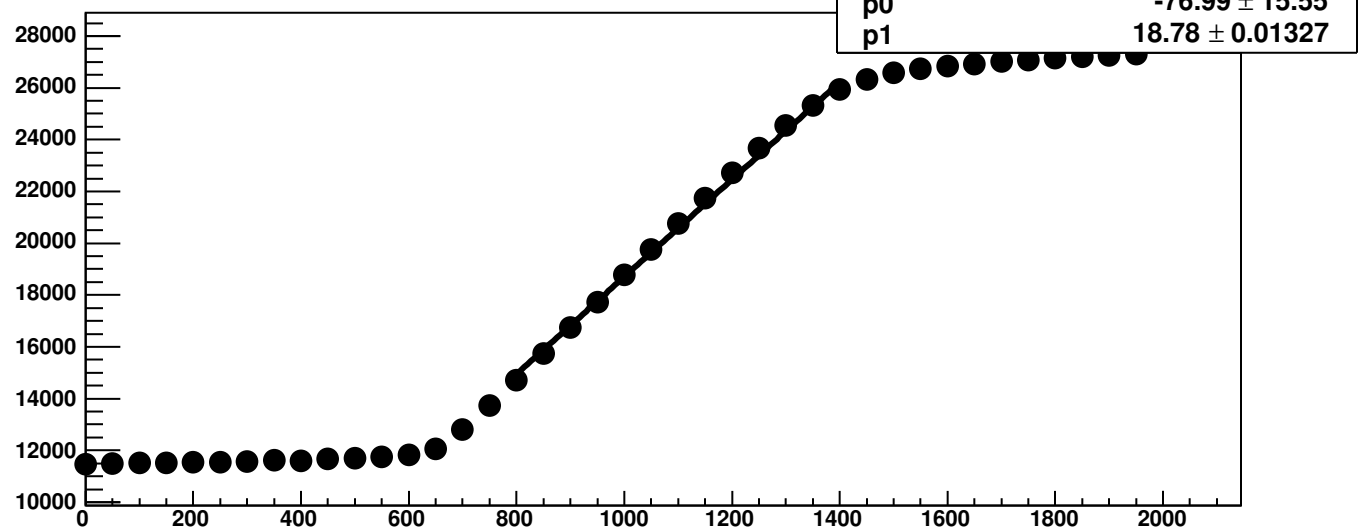

Chip 11, Channel 5, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 5, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 11.36 / 11
p0 -1366 ± 19.5
p1 0.3216 ± 0.01724
-700
-800
-900
-1000
-1100
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

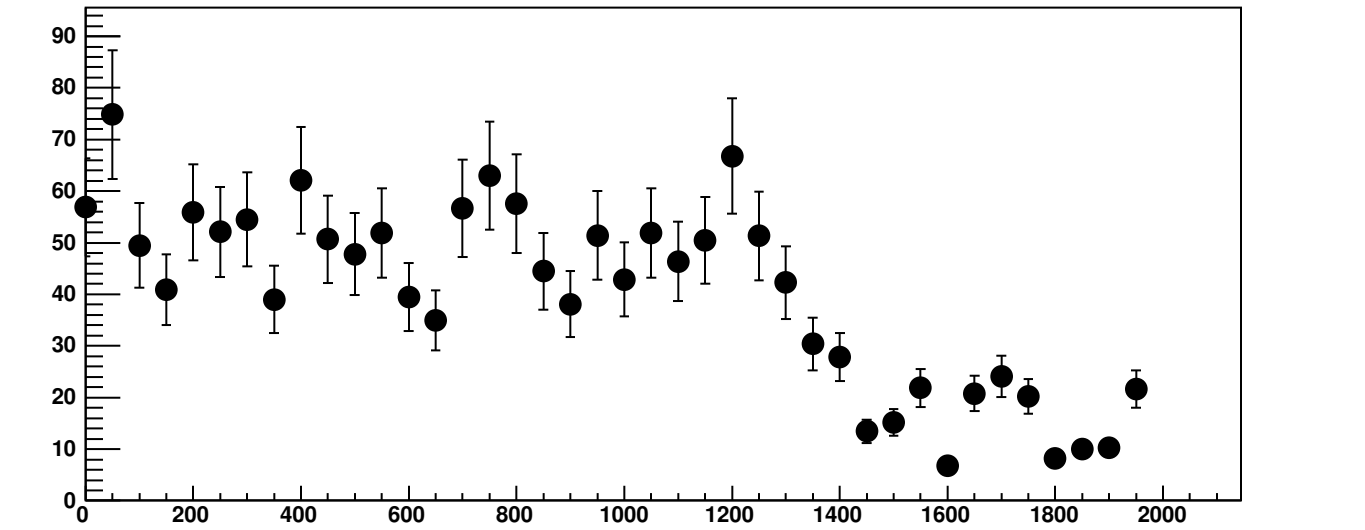

Chip 11, Channel 5, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 5, Enable 3, Hold=30, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

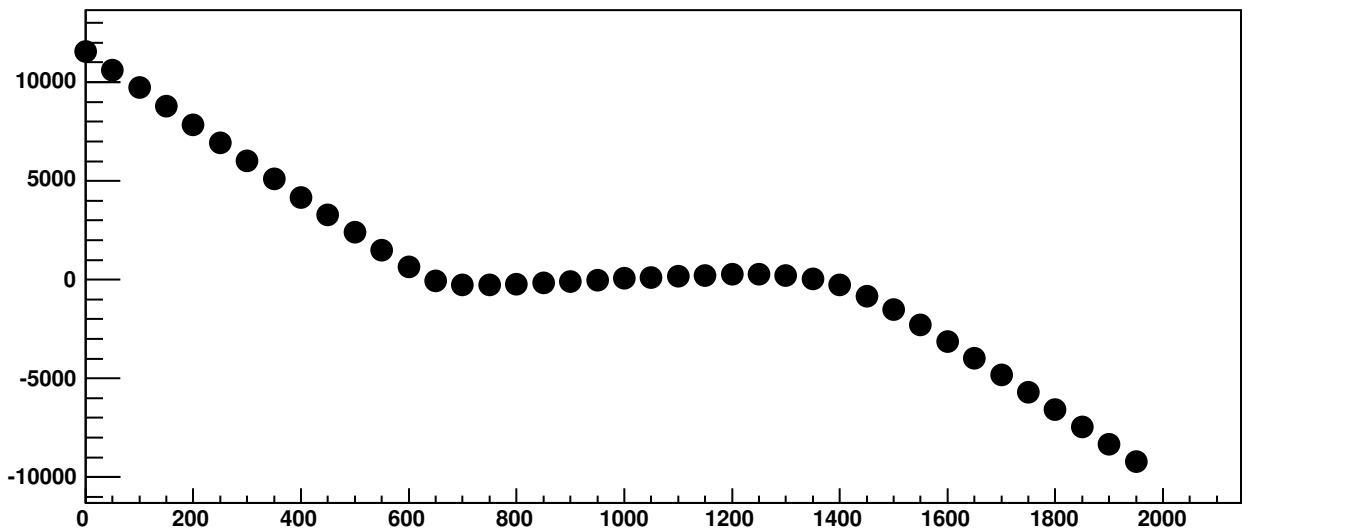
Chip 11, Channel 5, Enable 4!, Hold=30, ADC Mean vs DAC

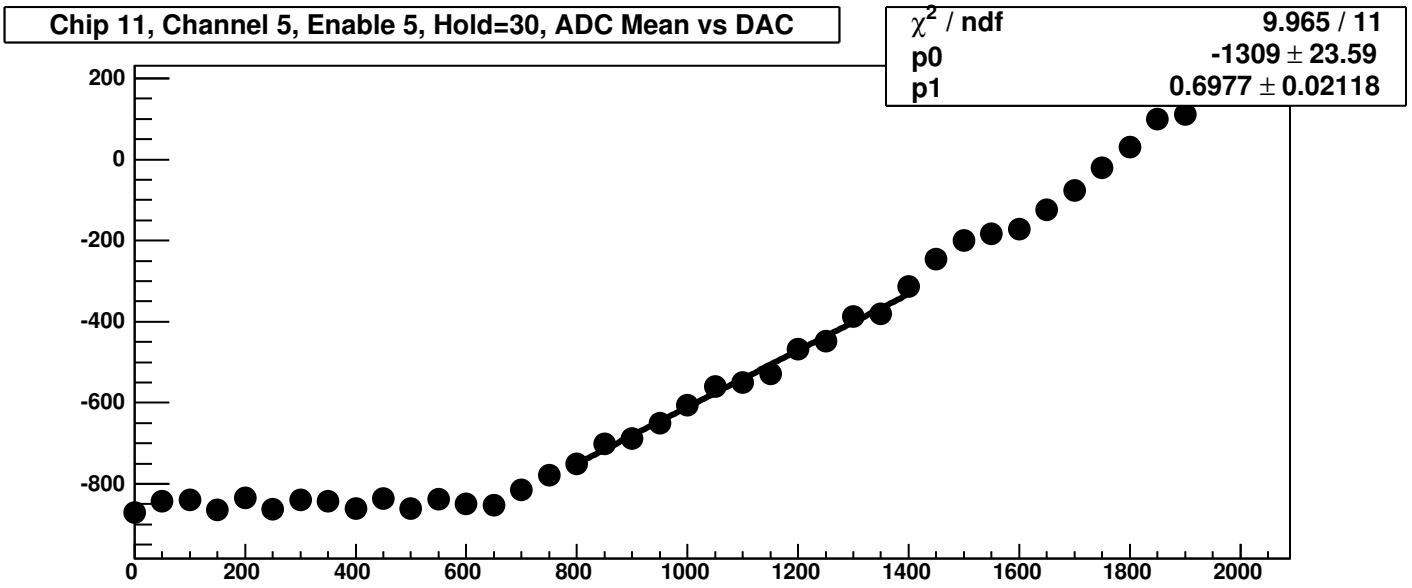


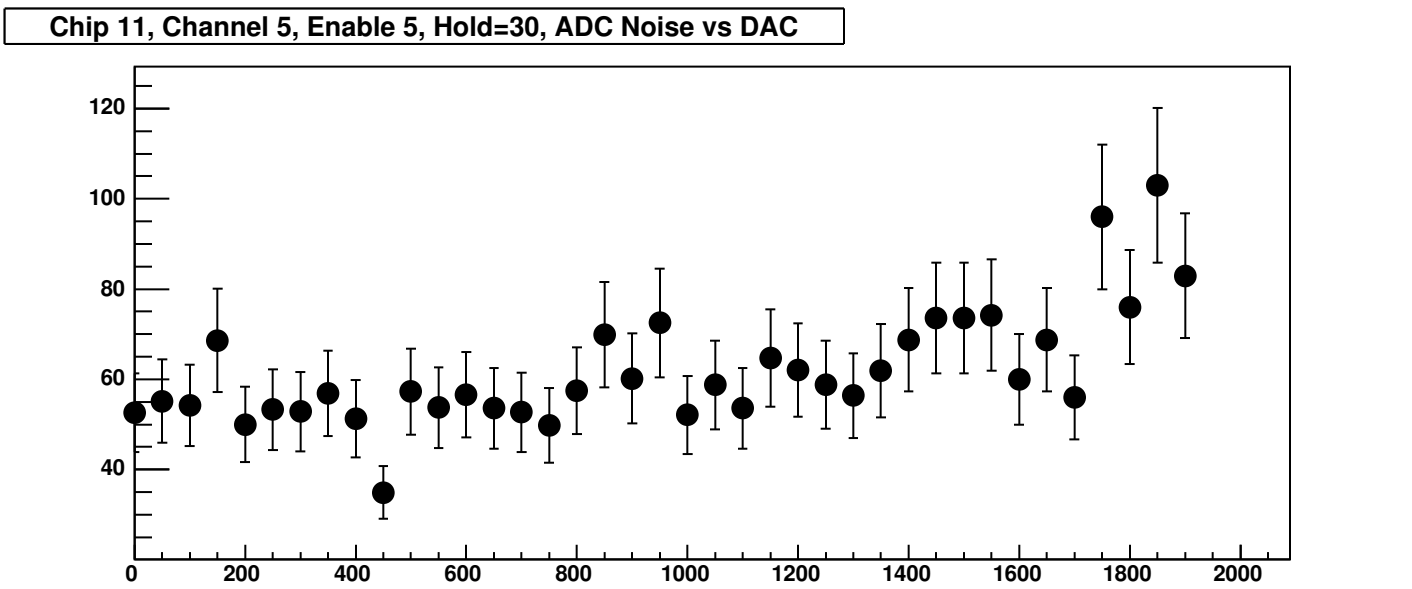
Chip 11, Channel 5, Enable 4!, Hold=30, ADC Noise vs DAC

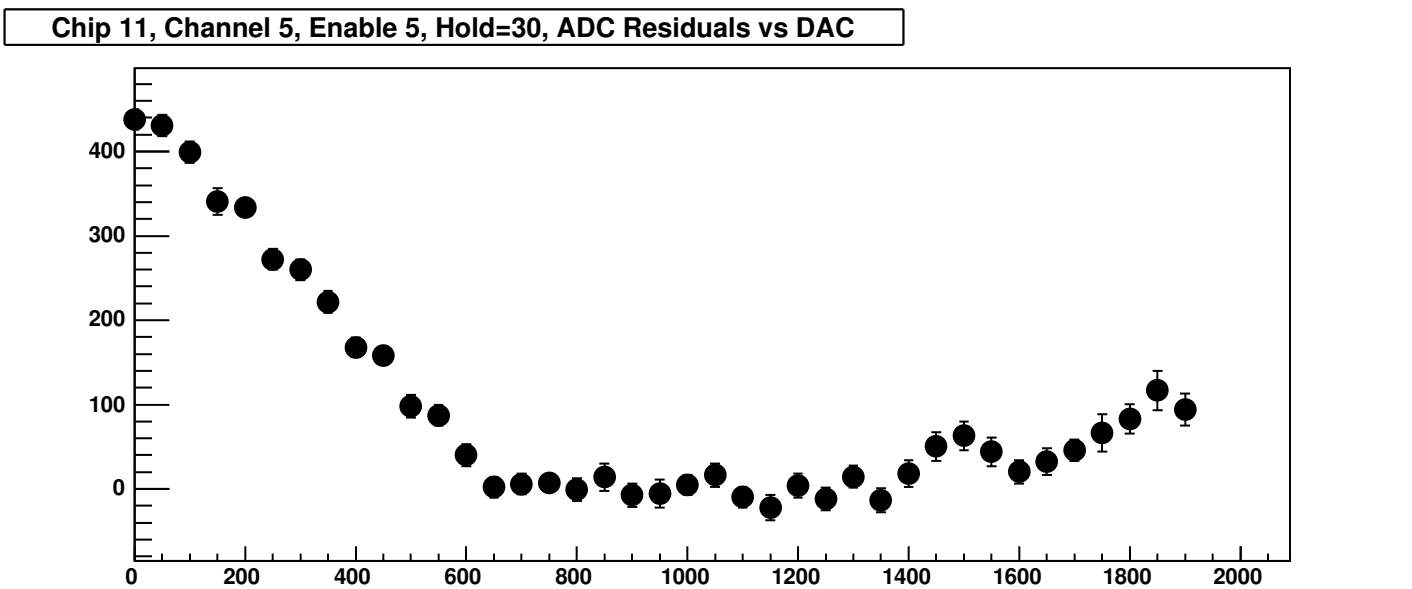


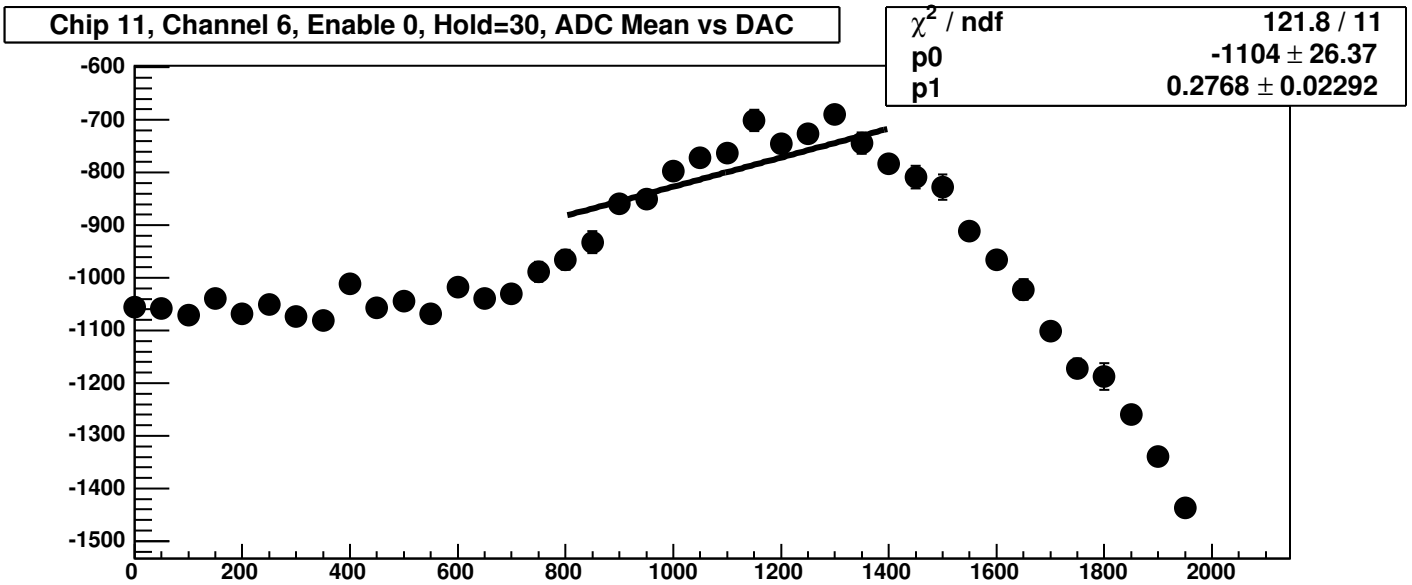
Chip 11, Channel 5, Enable 4!, Hold=30, ADC Residuals vs DAC

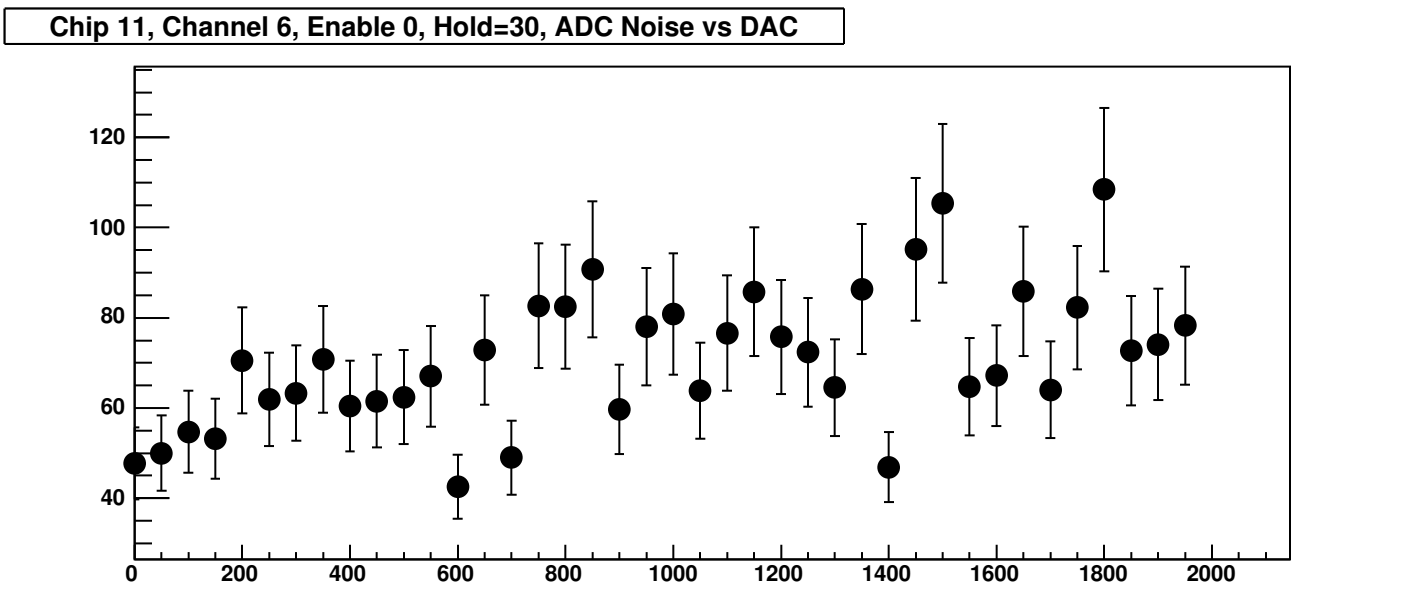


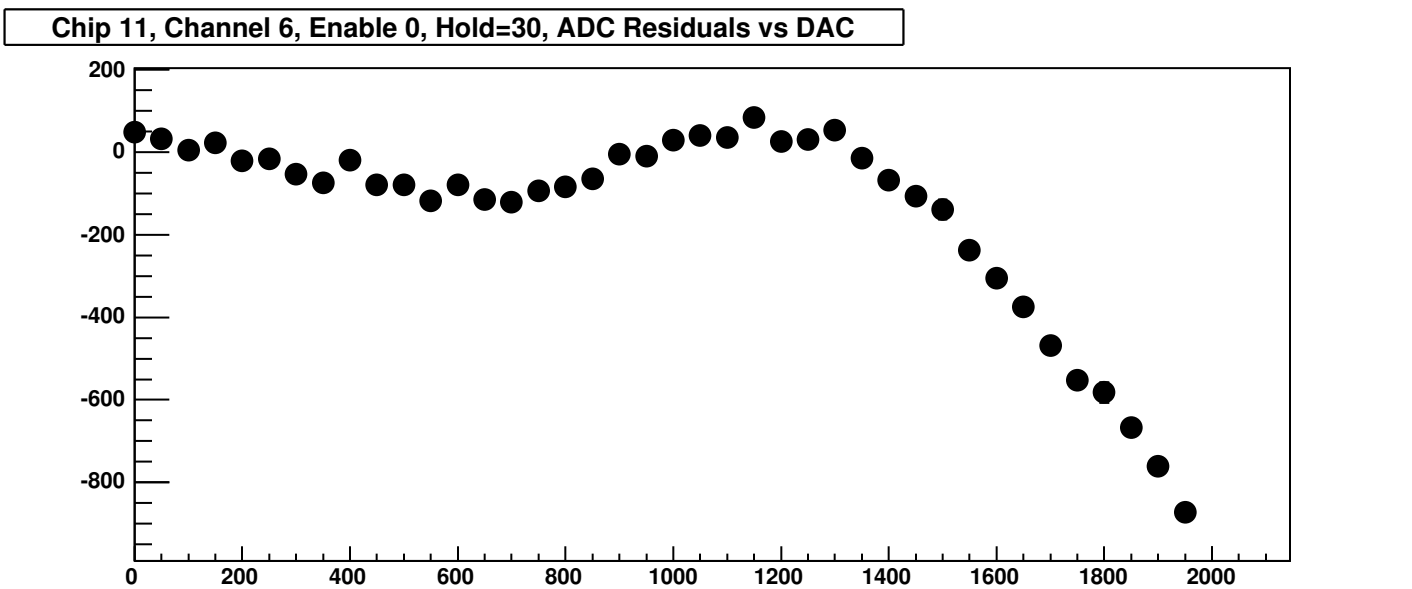

Chip 11, Channel 5, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9.965 / 11
p0 -1309 ± 23.59
p1 0.6977 ± 0.02118
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

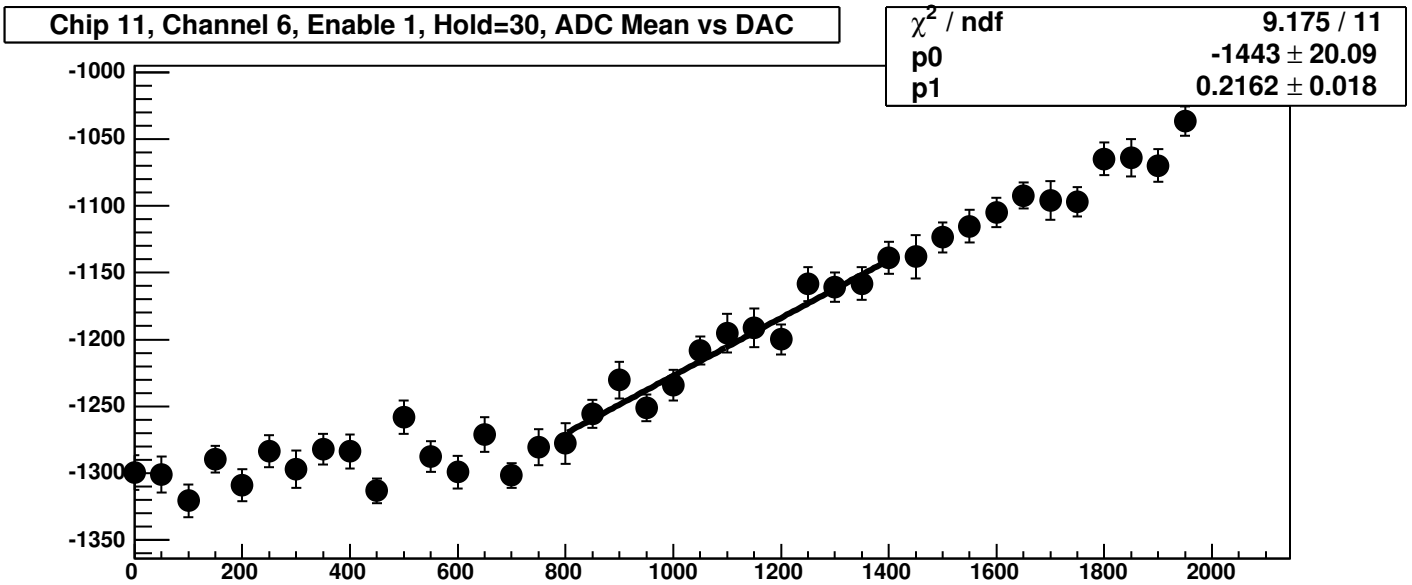

Chip 11, Channel 5, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

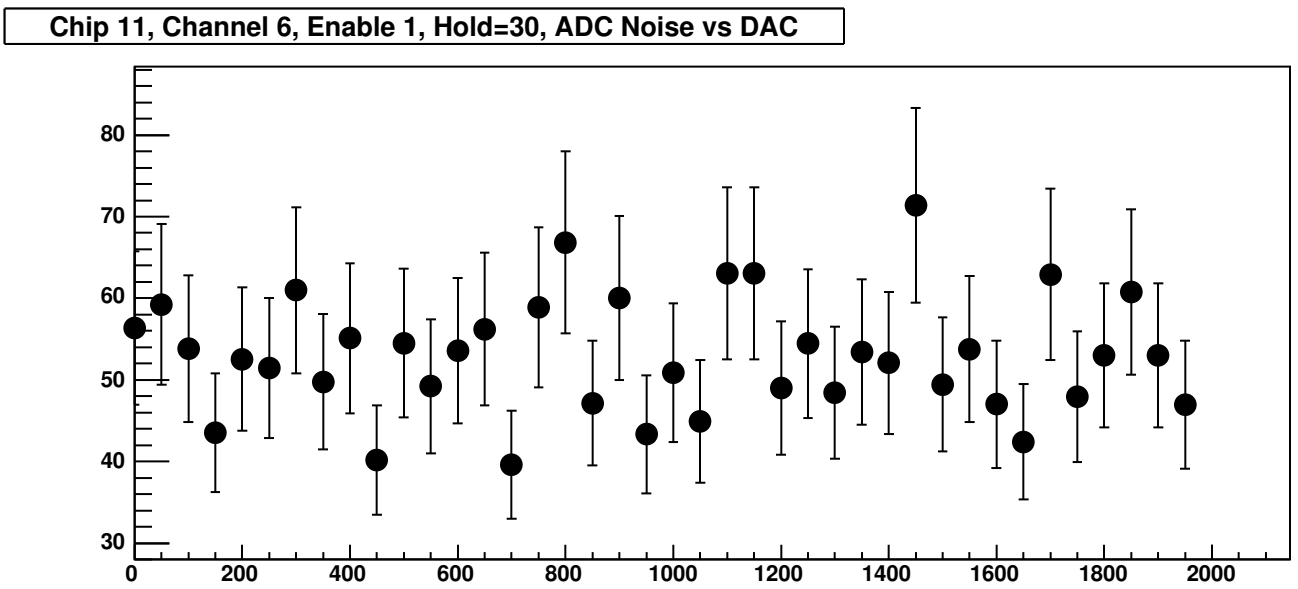

Chip 11, Channel 5, Enable 5, Hold=30, ADC Residuals vs DAC
400
300
200
100
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

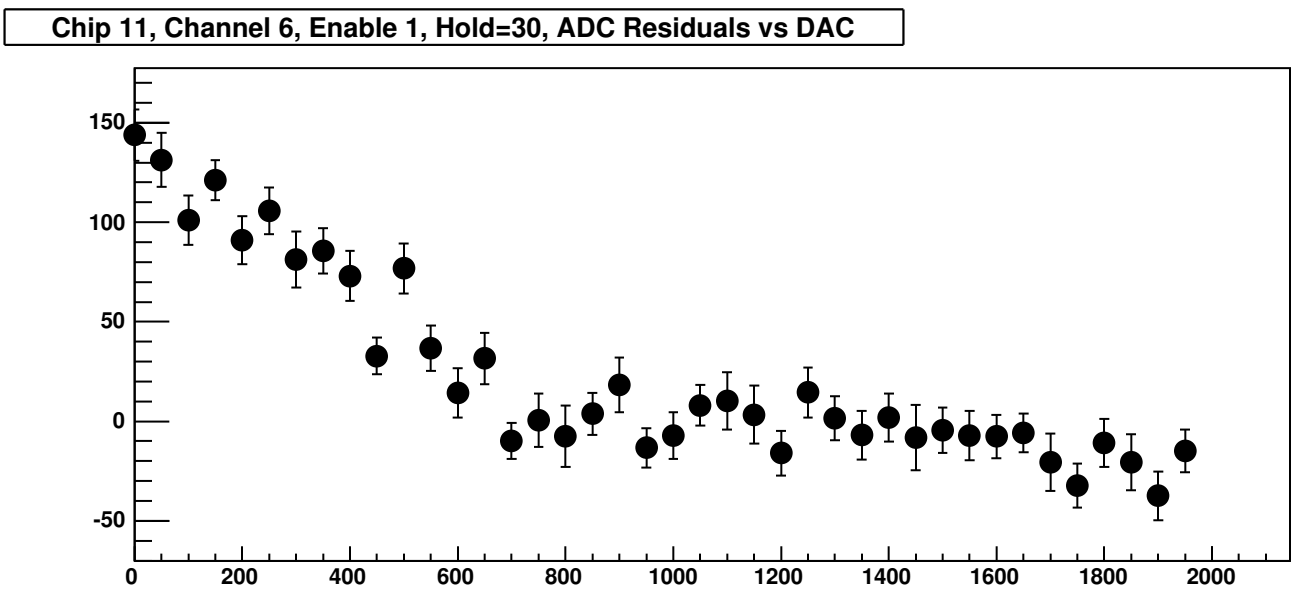

Chip 11, Channel 6, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 121.8 / 11
p0 -1104 ± 26.37
p1 0.2768 ± 0.02292
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

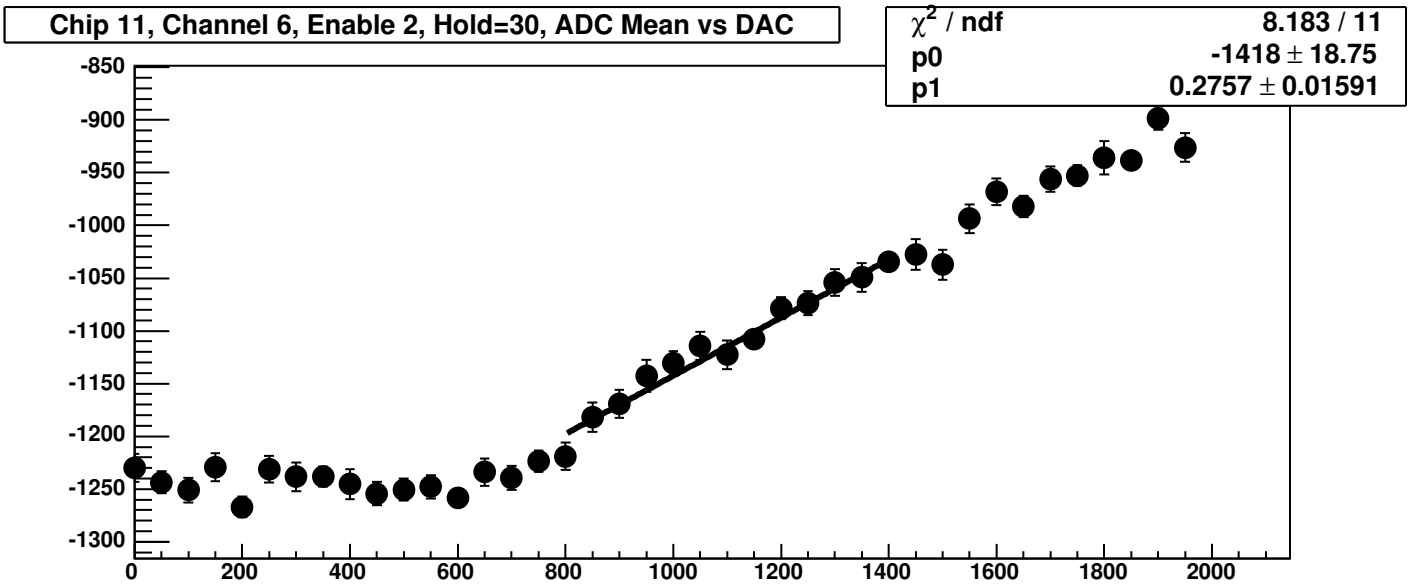

Chip 11, Channel 6, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

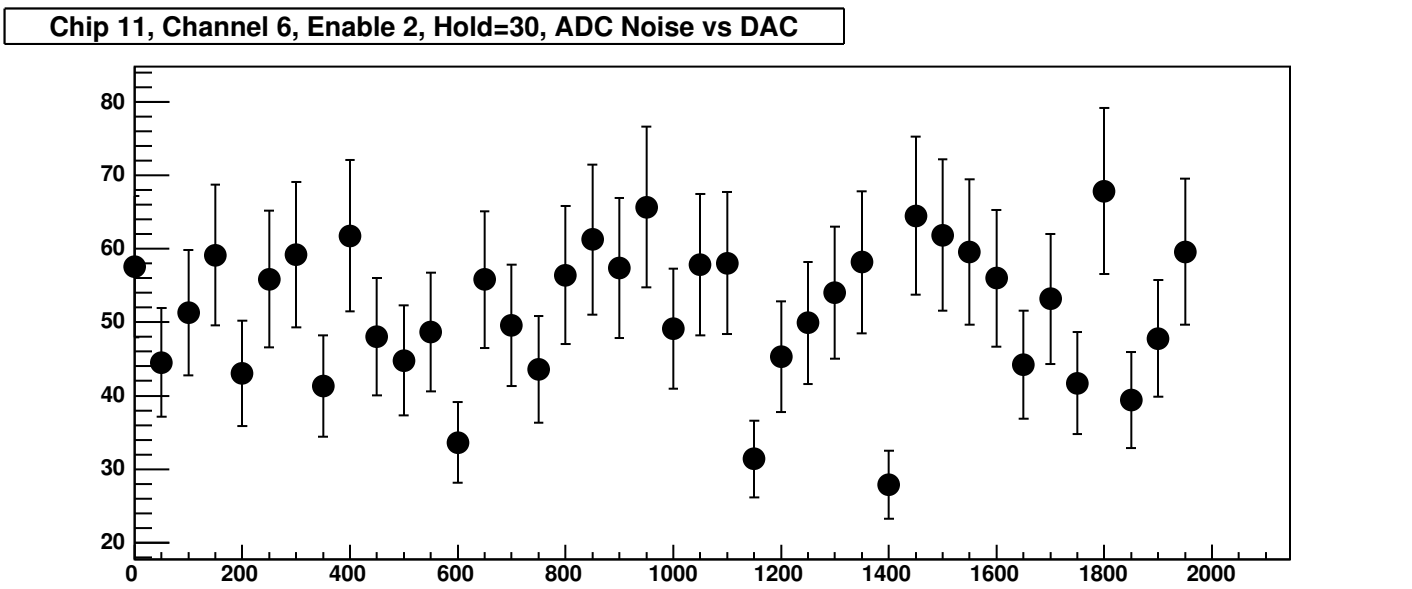

Chip 11, Channel 6, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

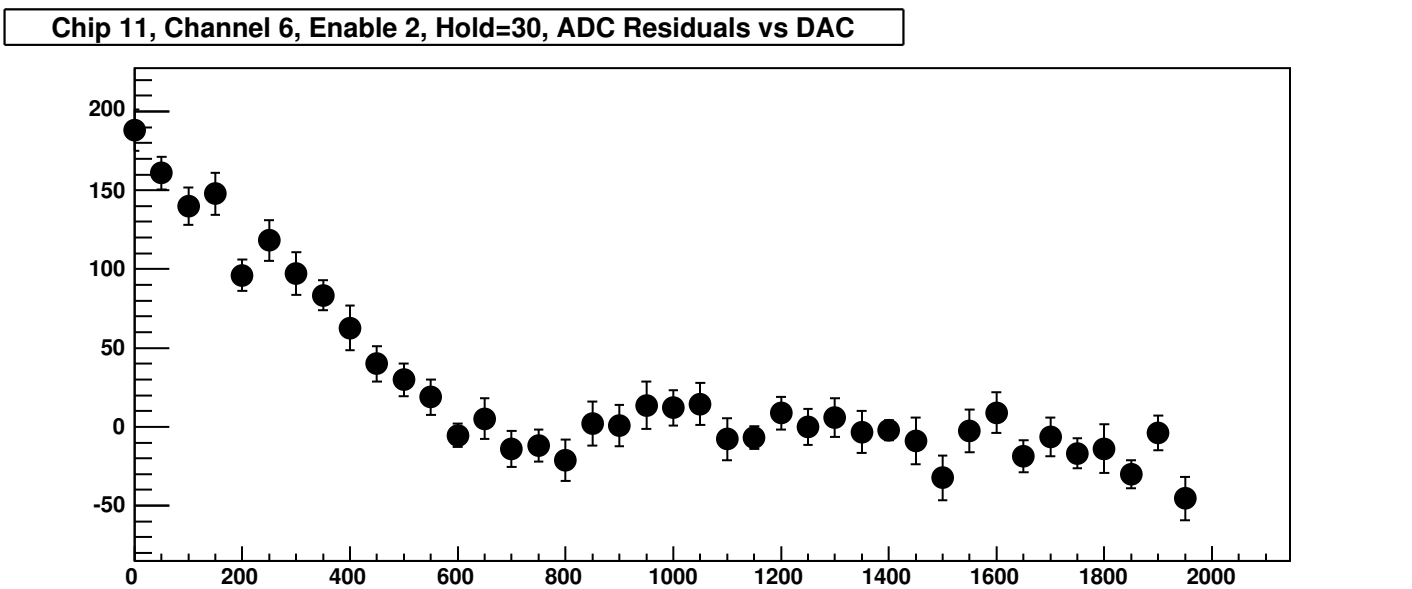

Chip 11, Channel 6, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 9.175 / 11
p0 -1443 ± 20.09
p1 0.2162 ± 0.018
-1000
-1050
-1100
-1150
-1200
-1250
-1300
-1350
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 6, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

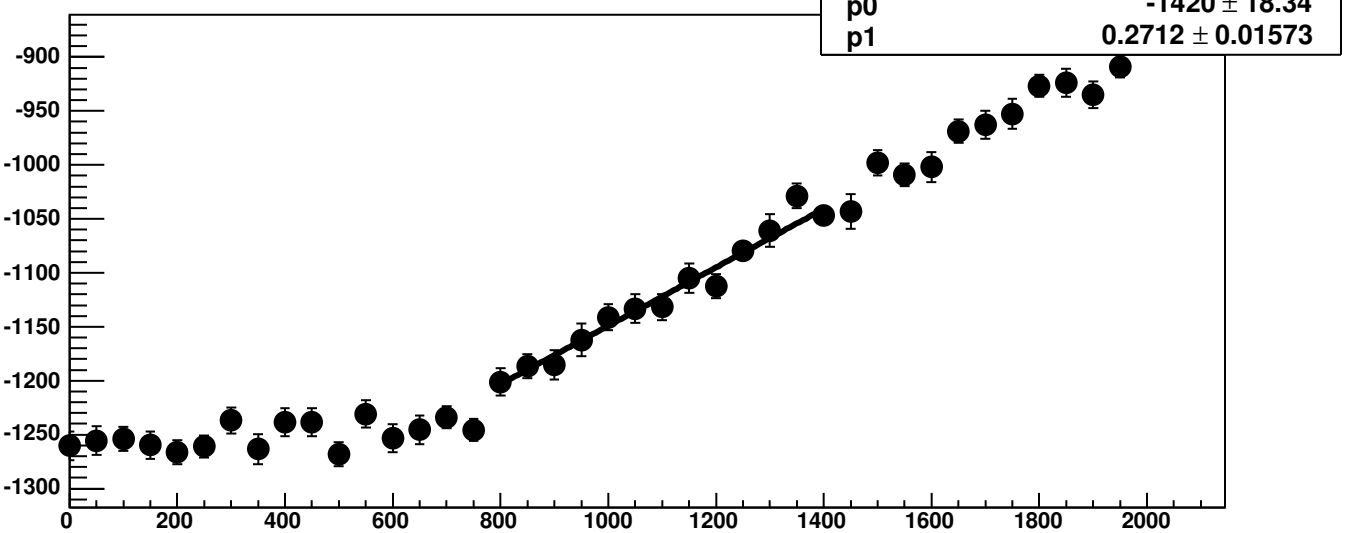

Chip 11, Channel 6, Enable 1, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 6, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 8.183 / 11
p0 -1418 ± 18.75
p1 0.2757 ± 0.01591
-850
-900
-950
-1000
-1050
-1100
-1150
-1200
-1250
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

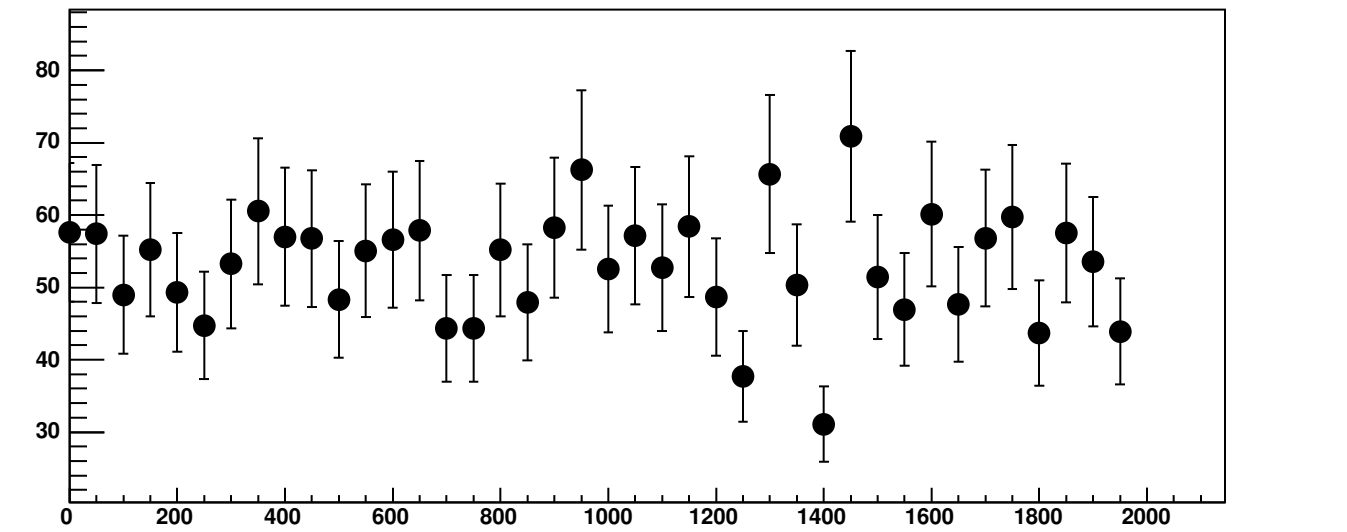

Chip 11, Channel 6, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 6, Enable 2, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

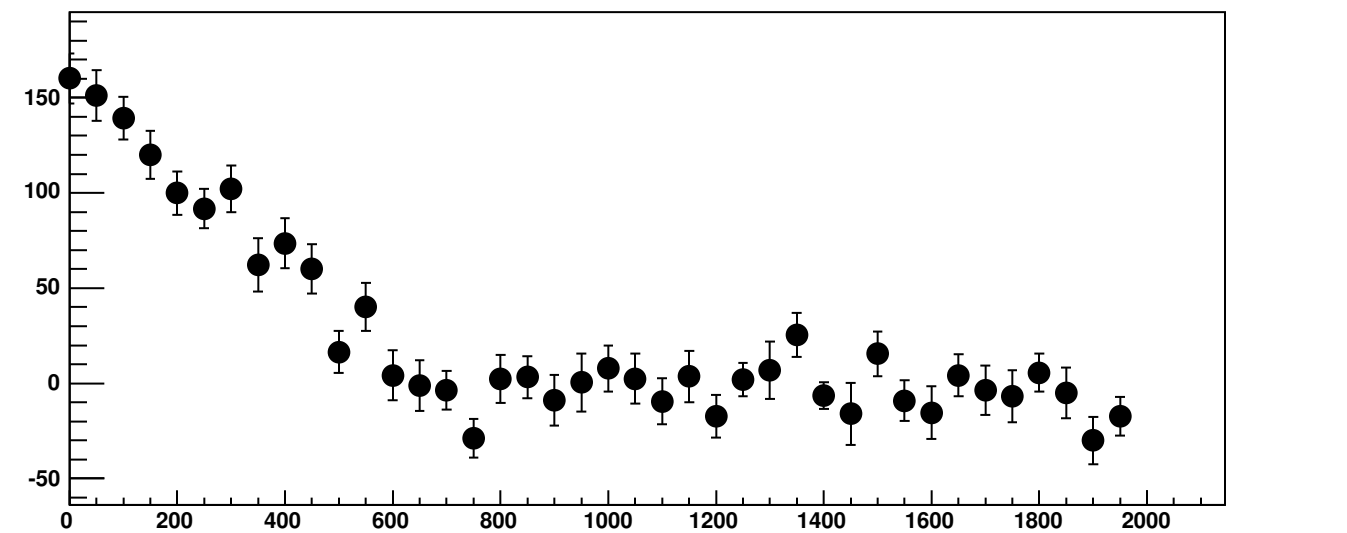
Chip 11, Channel 6, Enable 3, Hold=30, ADC Mean vs DAC



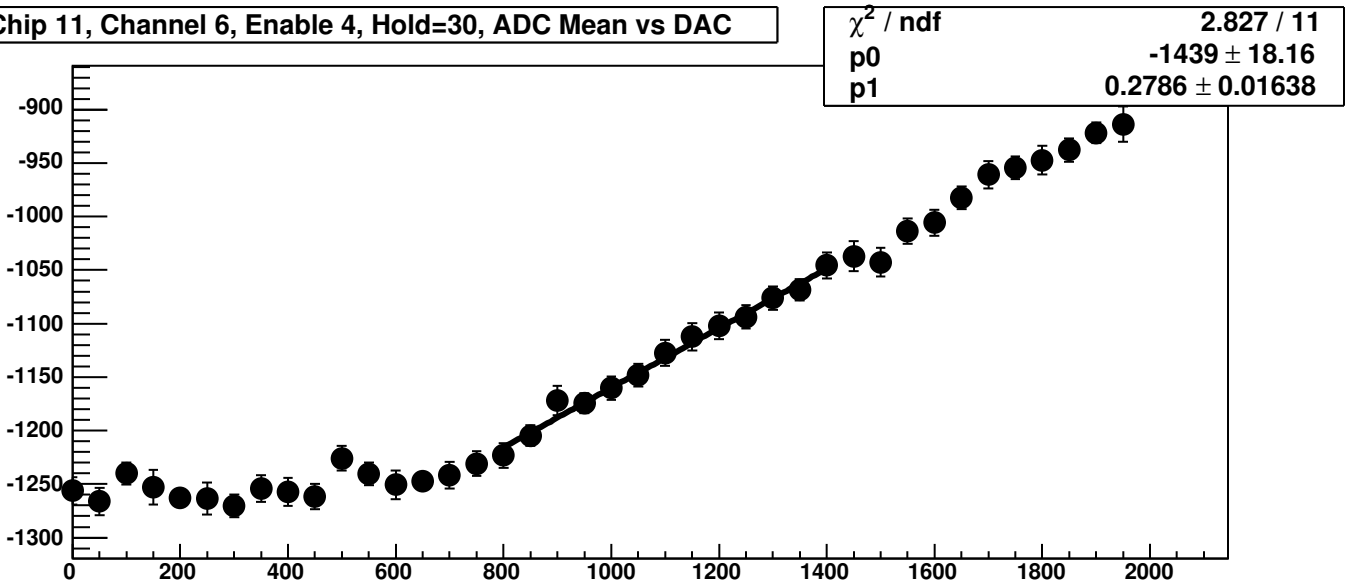
Chip 11, Channel 6, Enable 3, Hold=30, ADC Noise vs DAC



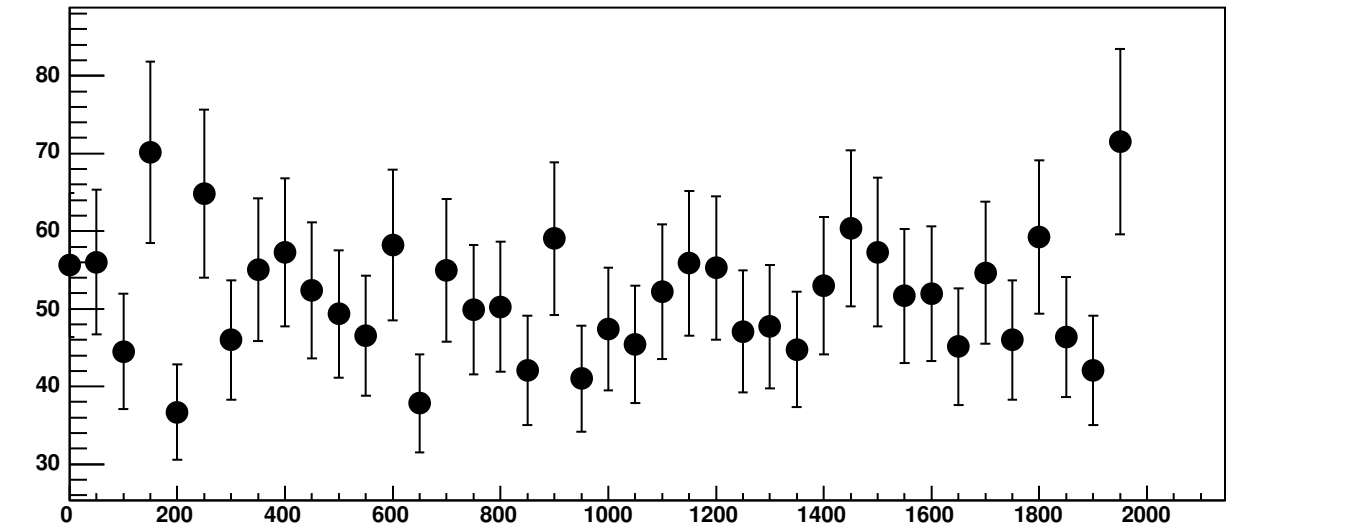
Chip 11, Channel 6, Enable 3, Hold=30, ADC Residuals vs DAC



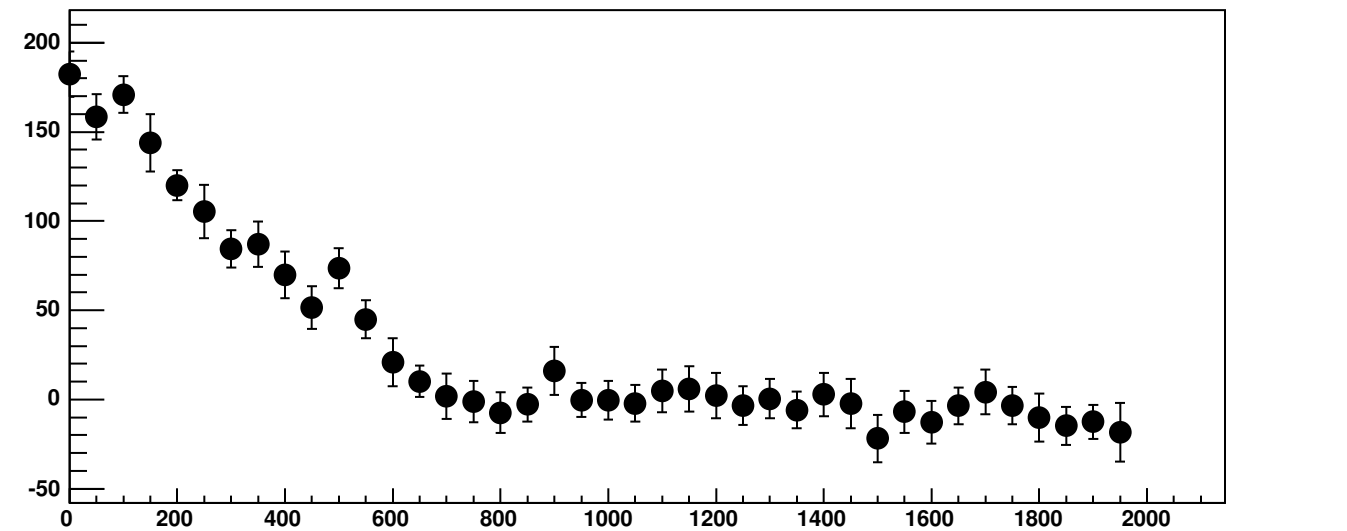
Chip 11, Channel 6, Enable 4, Hold=30, ADC Mean vs DAC

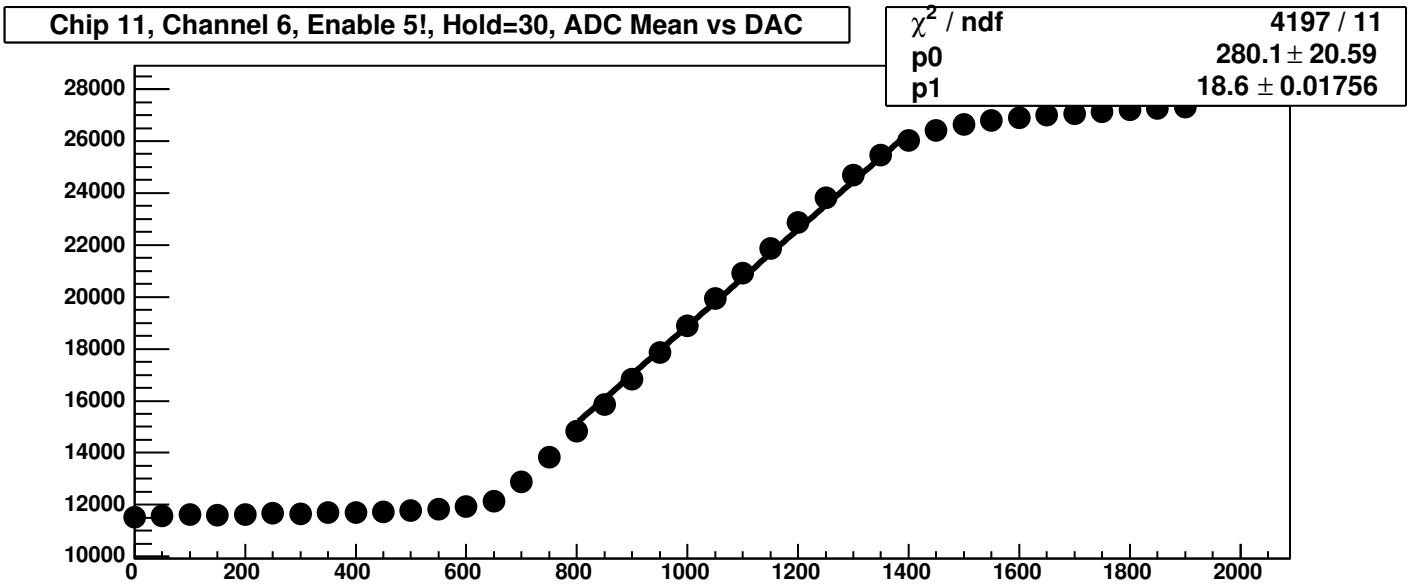


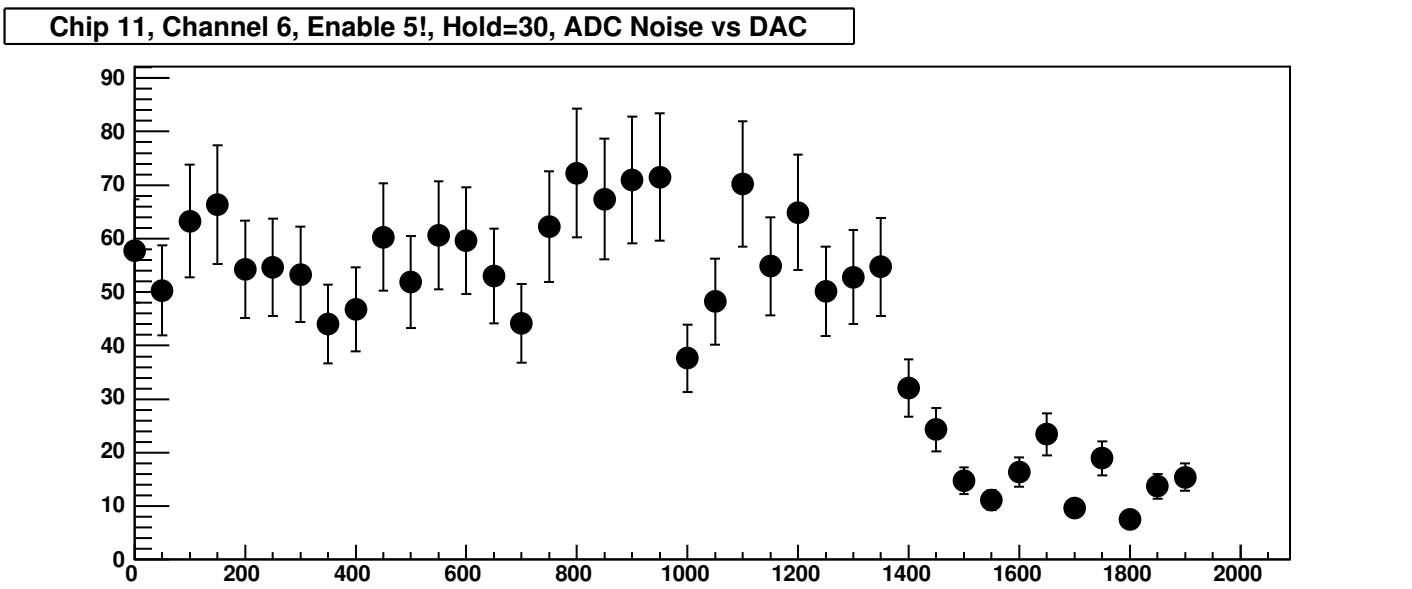
Chip 11, Channel 6, Enable 4, Hold=30, ADC Noise vs DAC

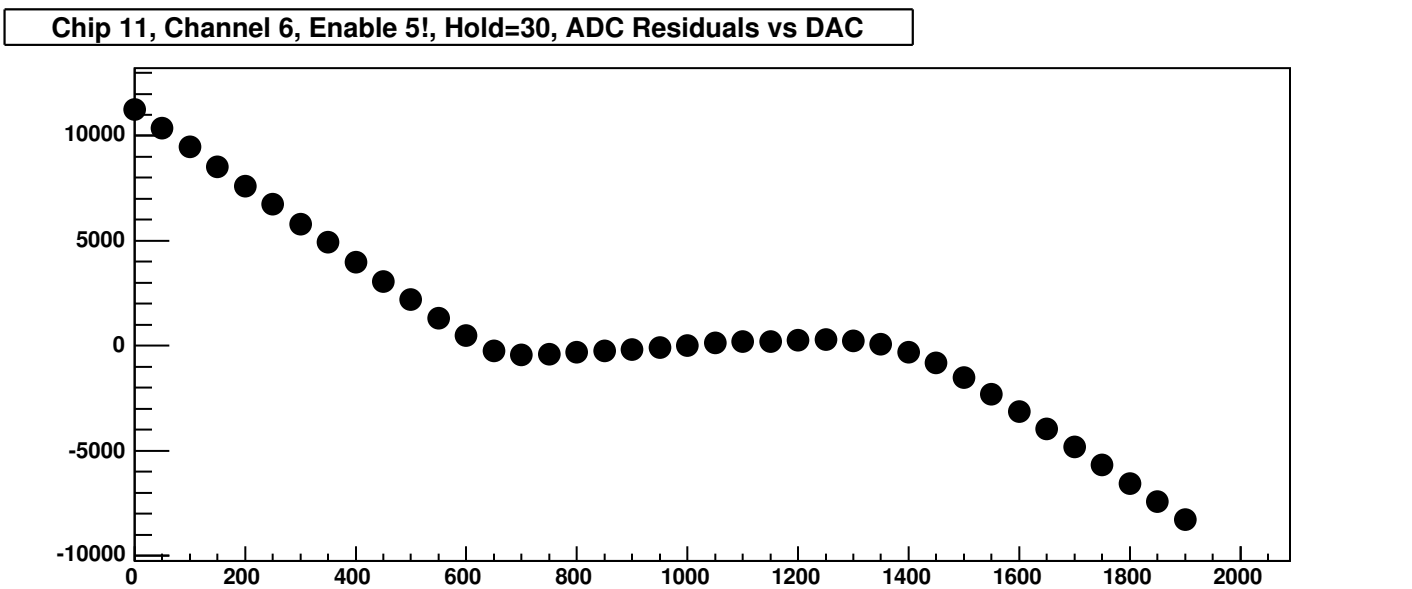


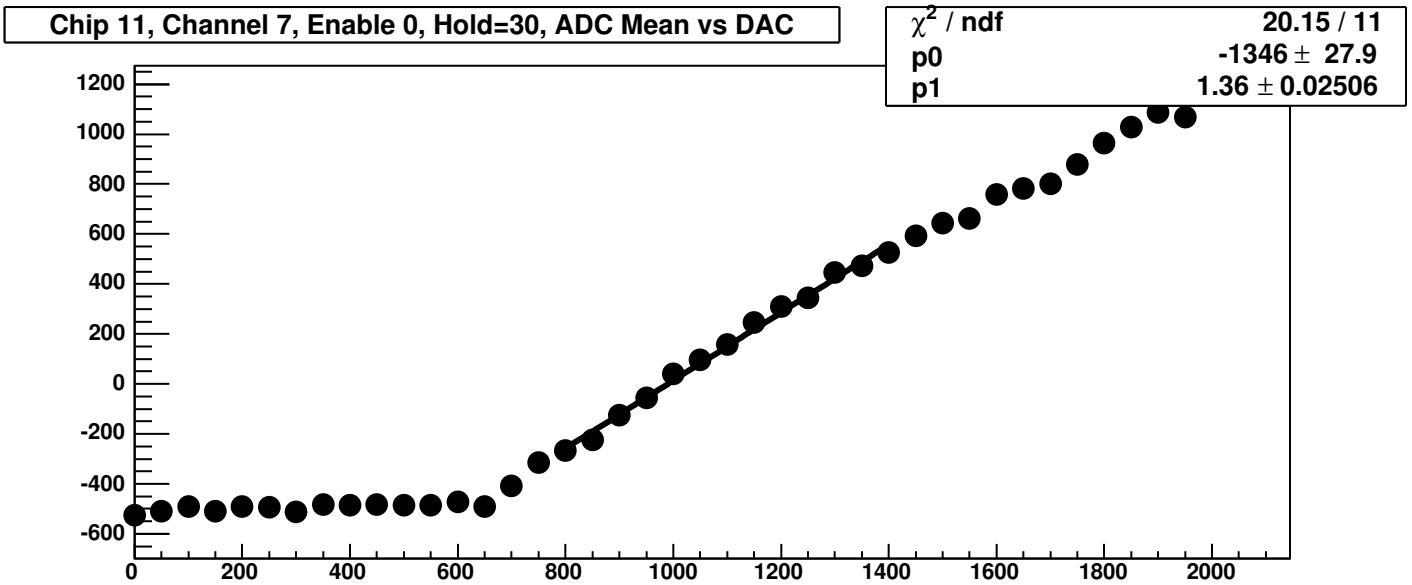
Chip 11, Channel 6, Enable 4, Hold=30, ADC Residuals vs DAC

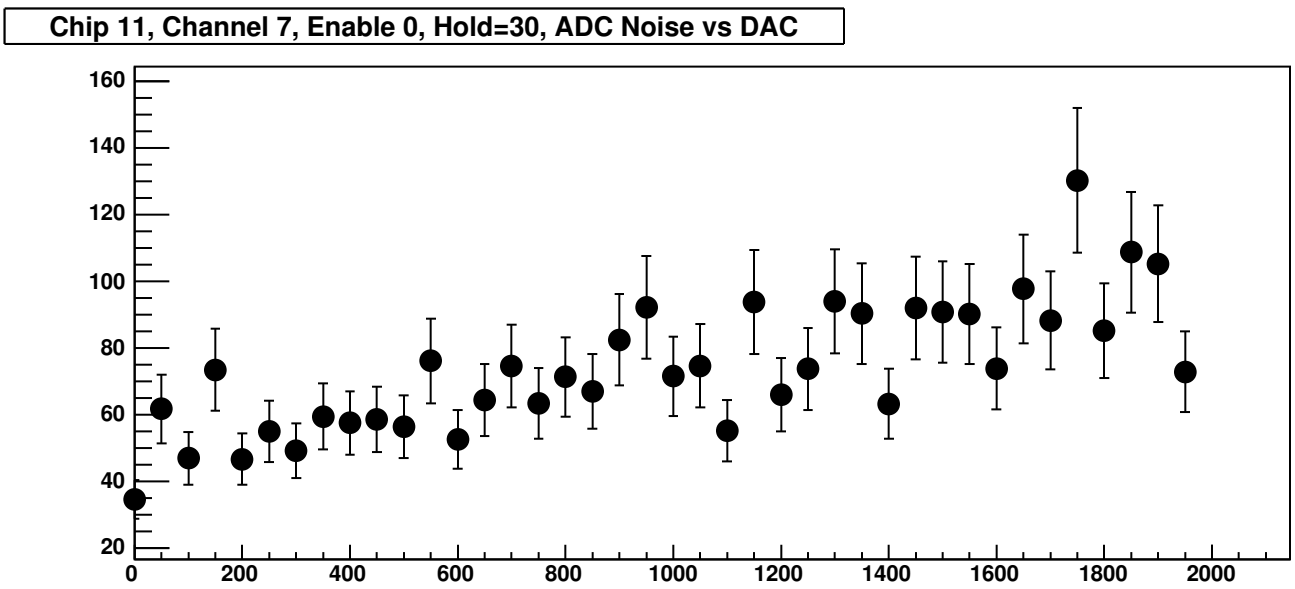


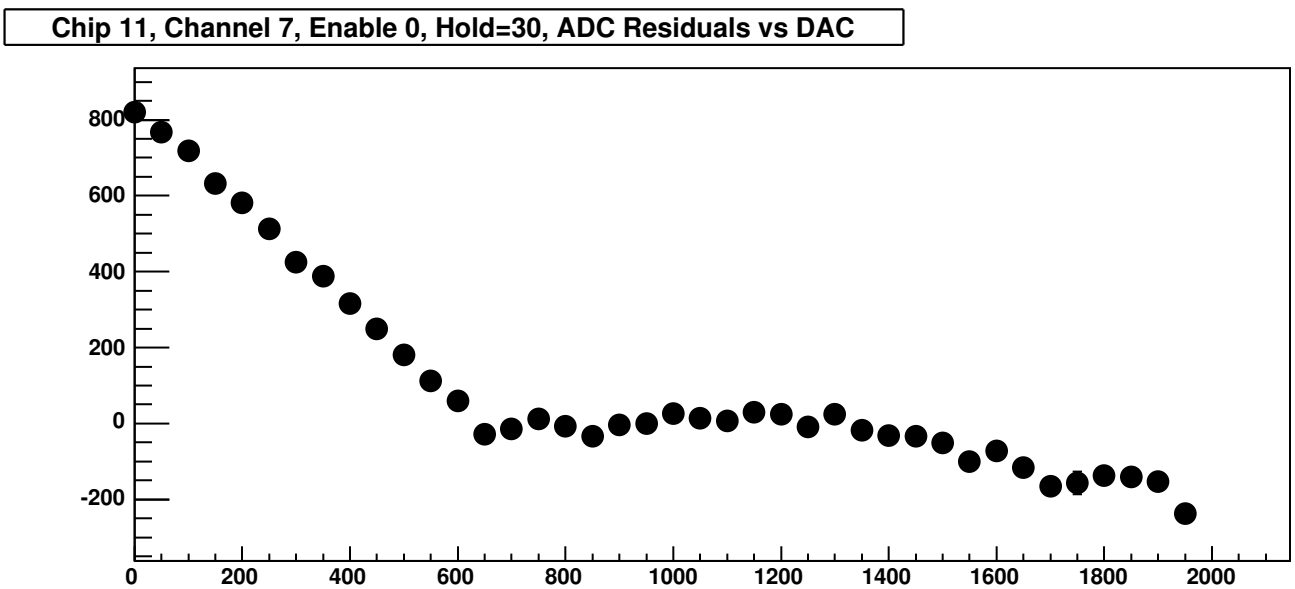

Chip 11, Channel 6, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 4197 / 11
p0 280.1 ± 20.59
p1 18.6 ± 0.01756
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

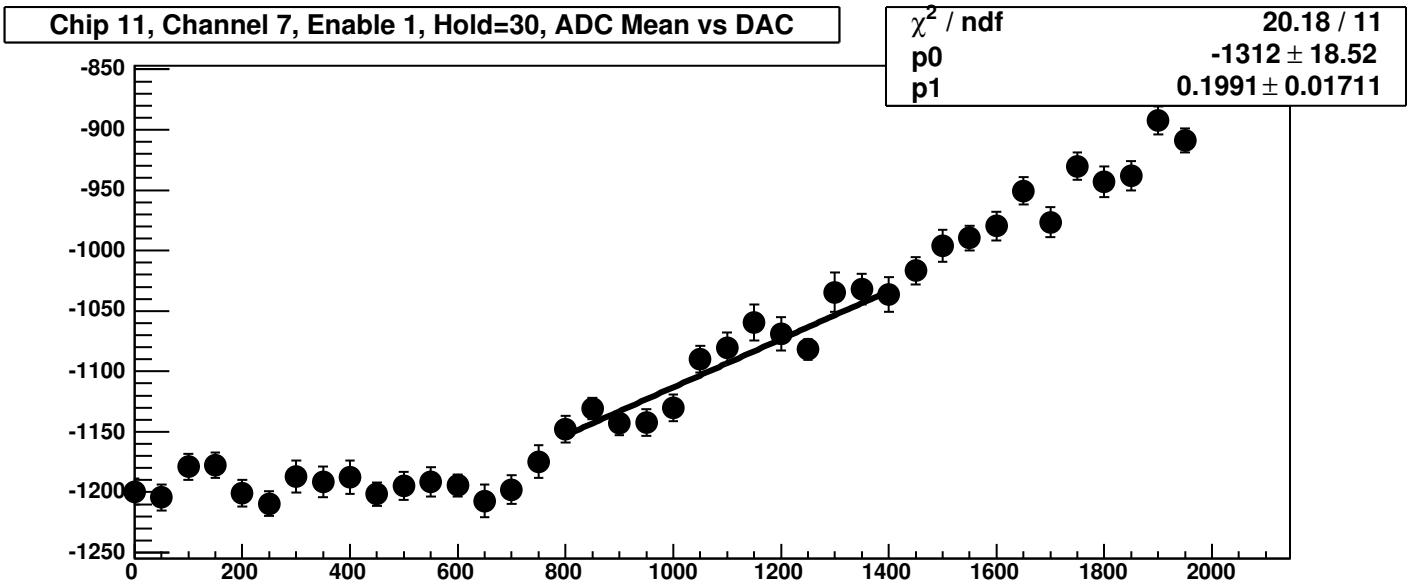

Chip 11, Channel 6, Enable 5!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

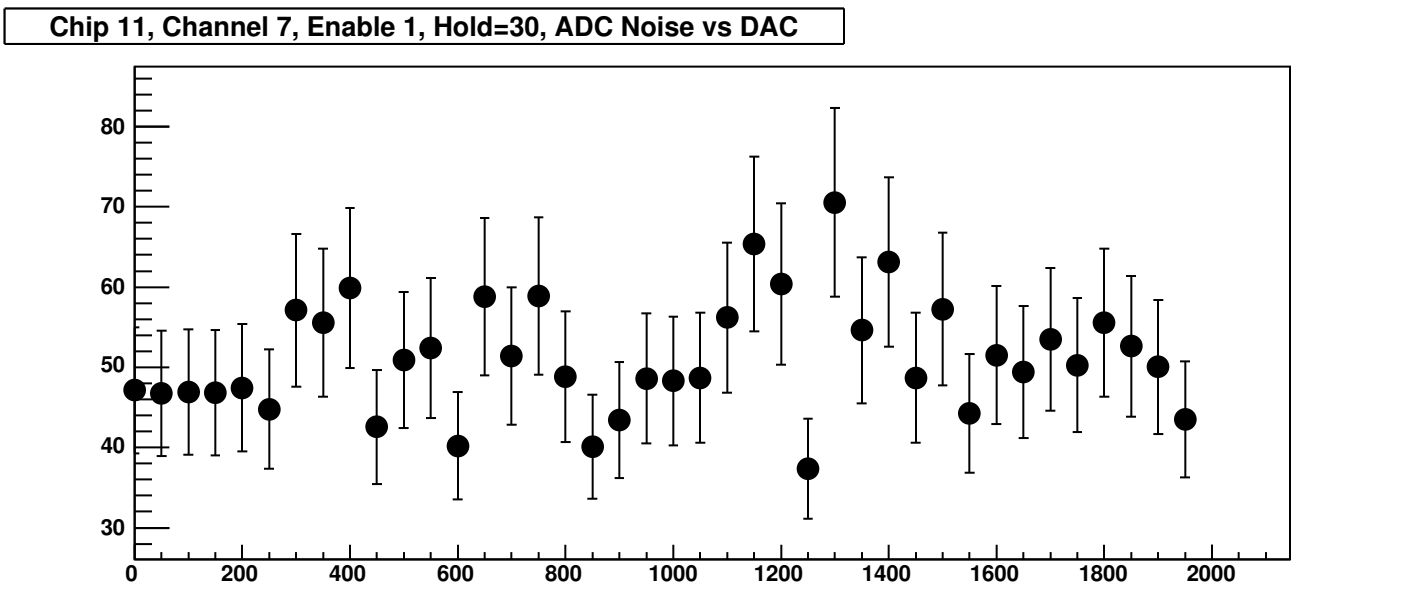

Chip 11, Channel 6, Enable 5!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

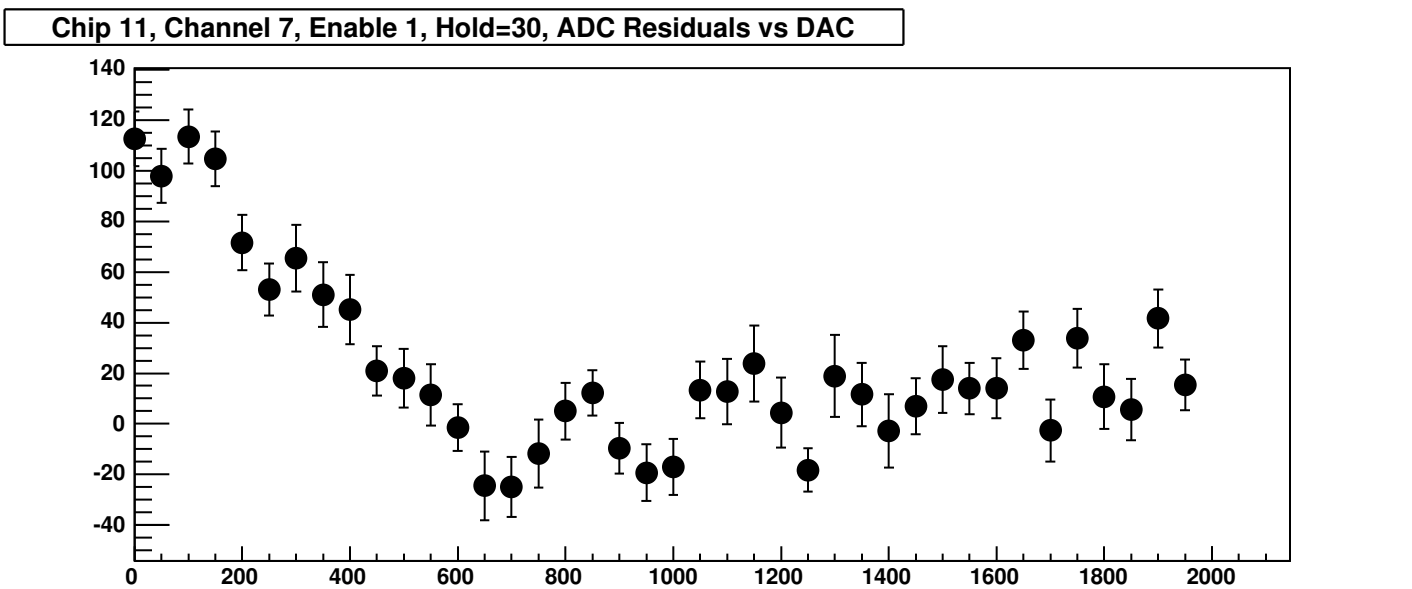

Chip 11, Channel 7, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 20.15 / 11
p0 -1346 ± 27.9
p1 1.36 ± 0.02506
1200
1000
800
600
400
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

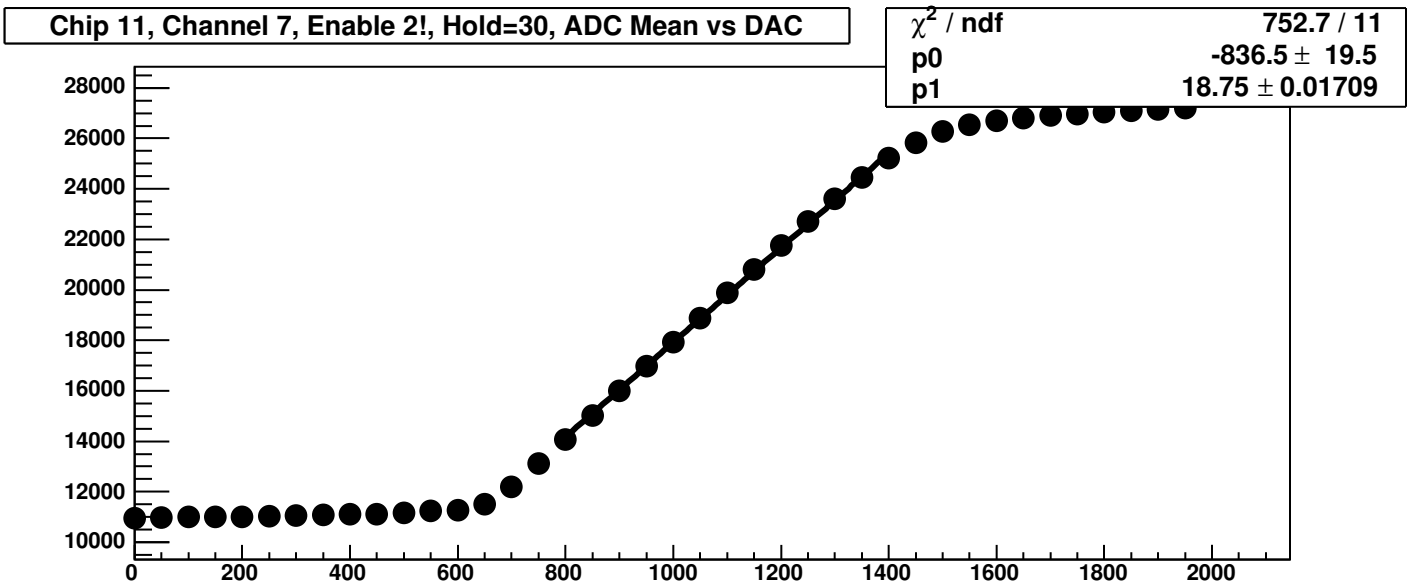

Chip 11, Channel 7, Enable 0, Hold=30, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

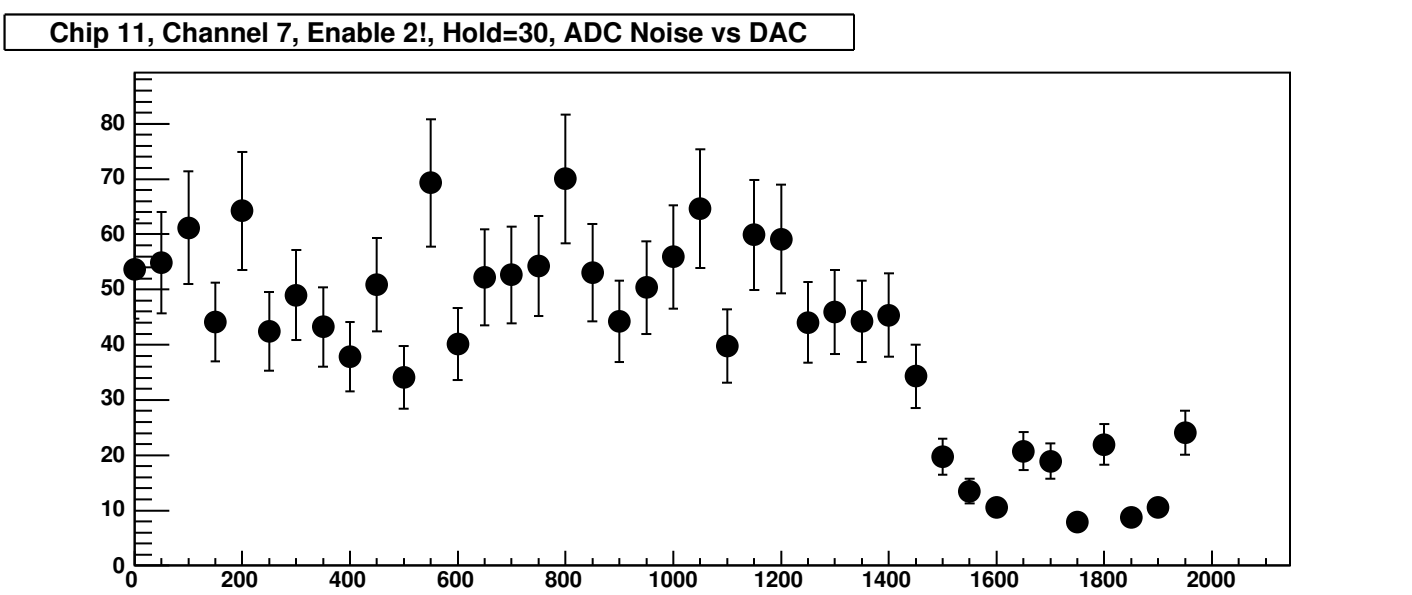

Chip 11, Channel 7, Enable 0, Hold=30, ADC Residuals vs DAC
800
600
400
200
0
-200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

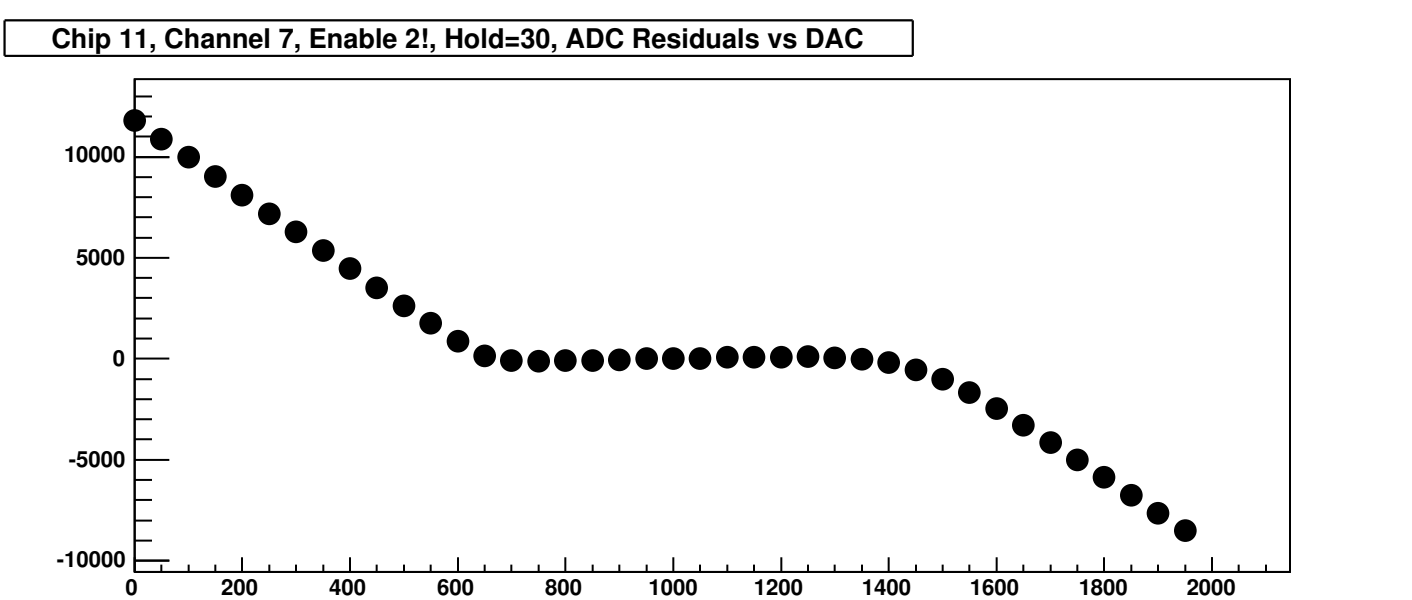

Chip 11, Channel 7, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 20.18 / 11
p0 -1312 ± 18.52
p1 0.1991 ± 0.01711
-850
-900
-950
-1000
-1050
-1100
-1150
-1200
-1250
0
200
400
600
800
1000
1200
1400
1600
1800
2000

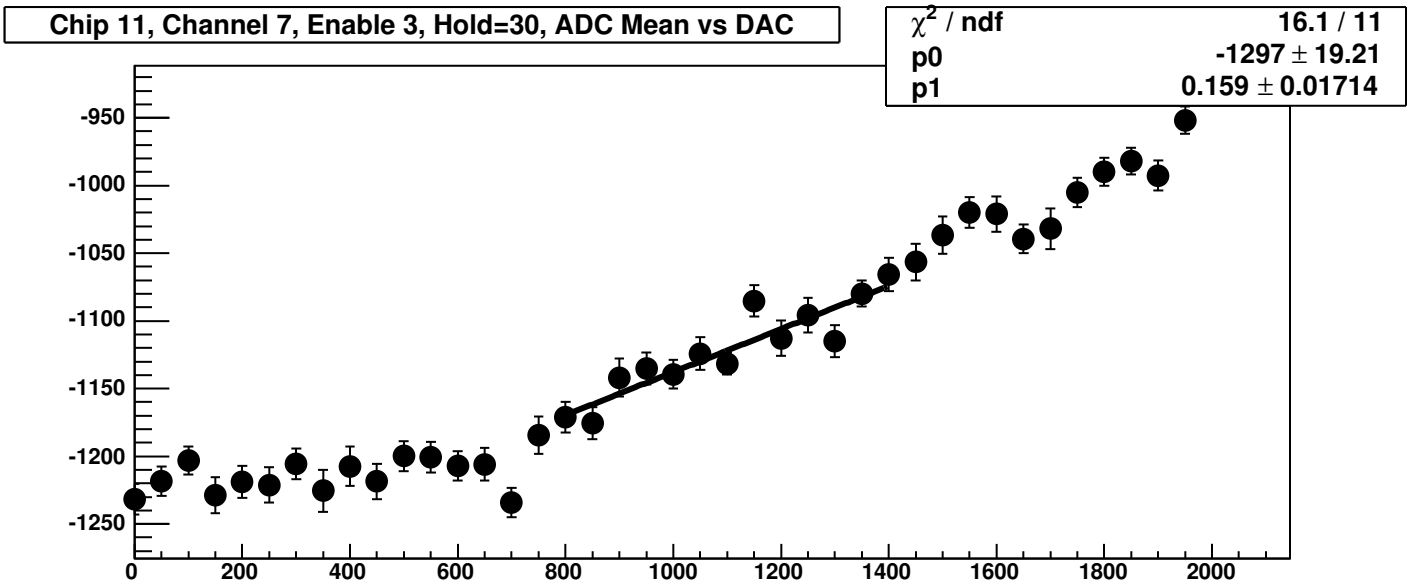

Chip 11, Channel 7, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

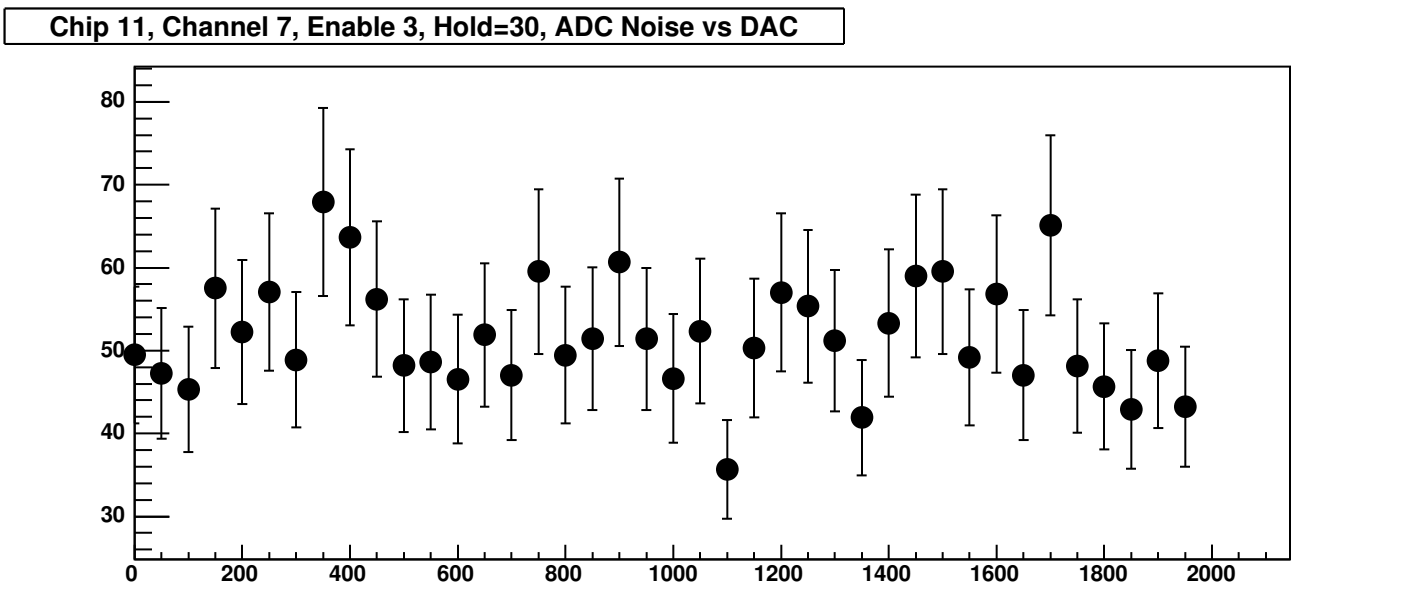

Chip 11, Channel 7, Enable 1, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

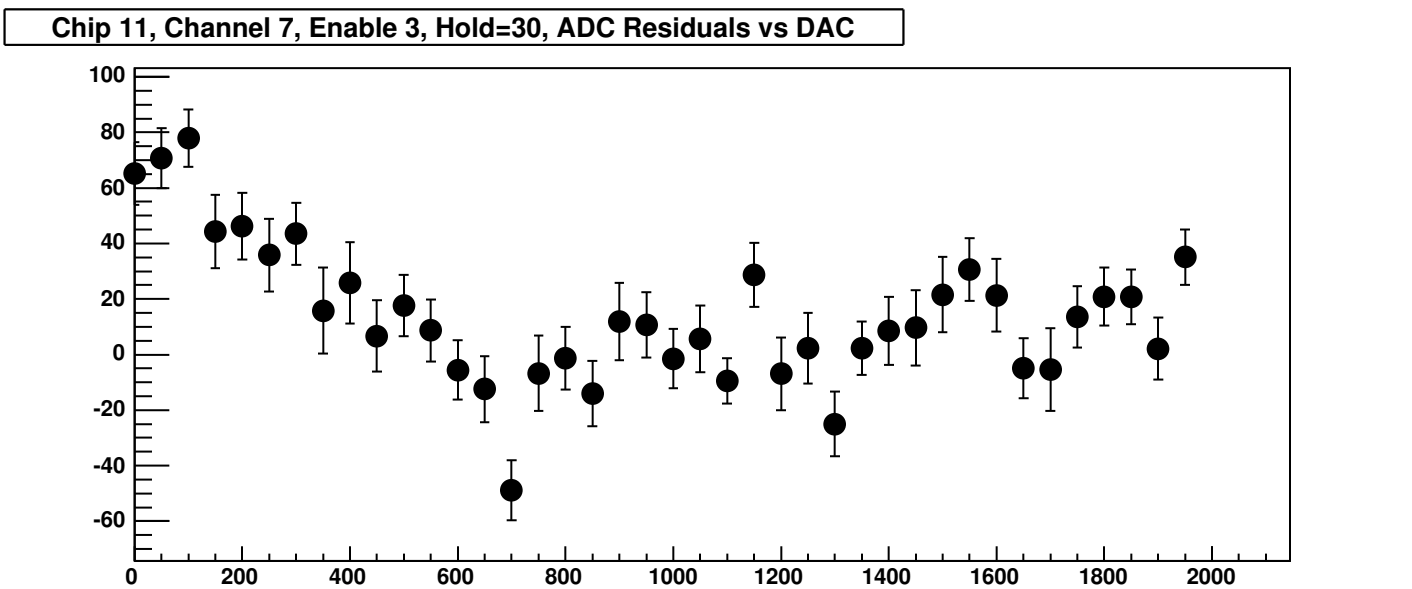

Chip 11, Channel 7, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 752.7 / 11
p0 -836.5 ± 19.5
p1 18.75 ± 0.01709
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

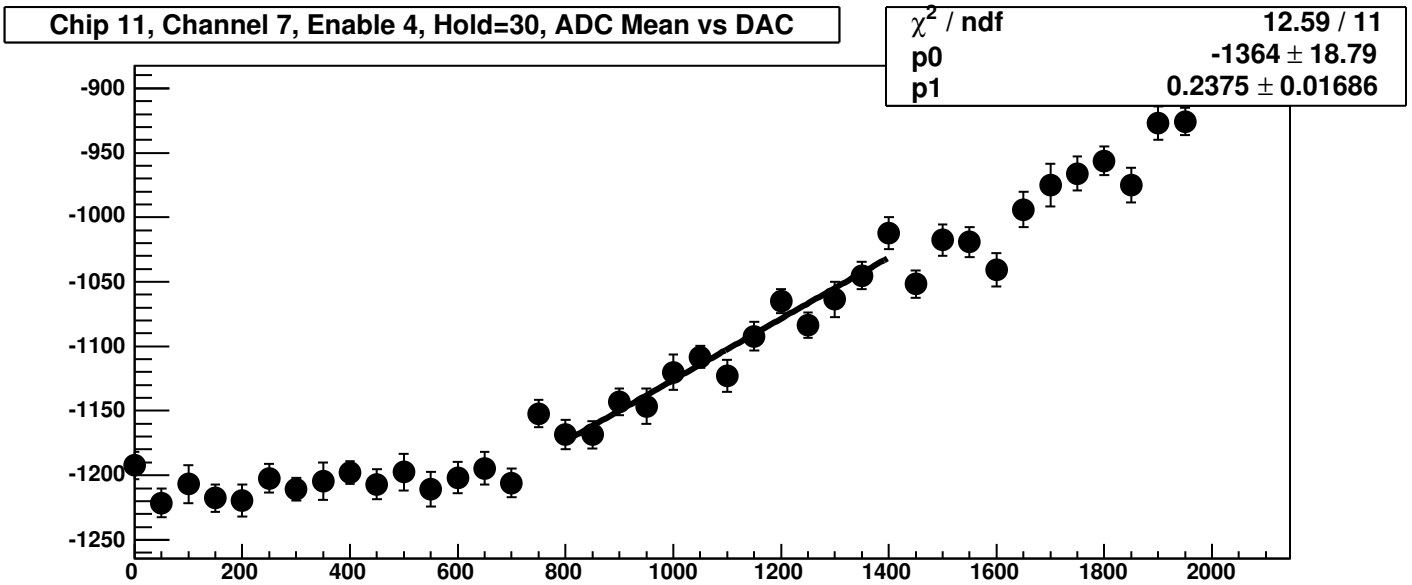

Chip 11, Channel 7, Enable 2!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

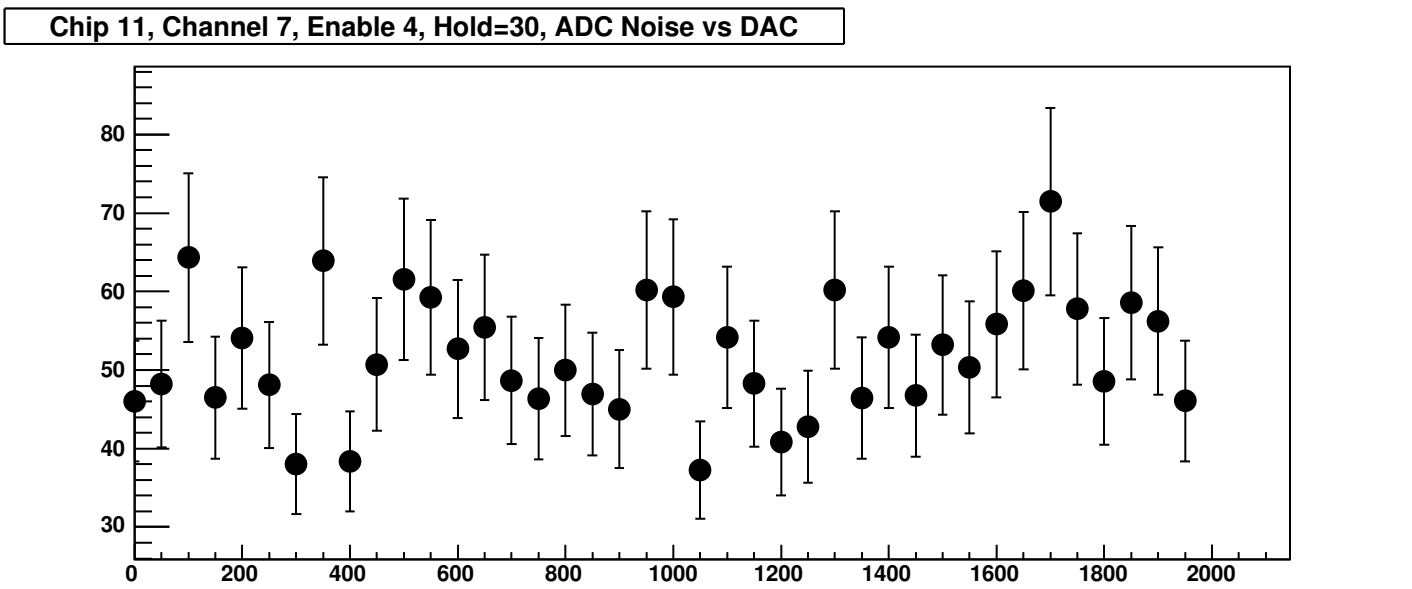

Chip 11, Channel 7, Enable 2!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

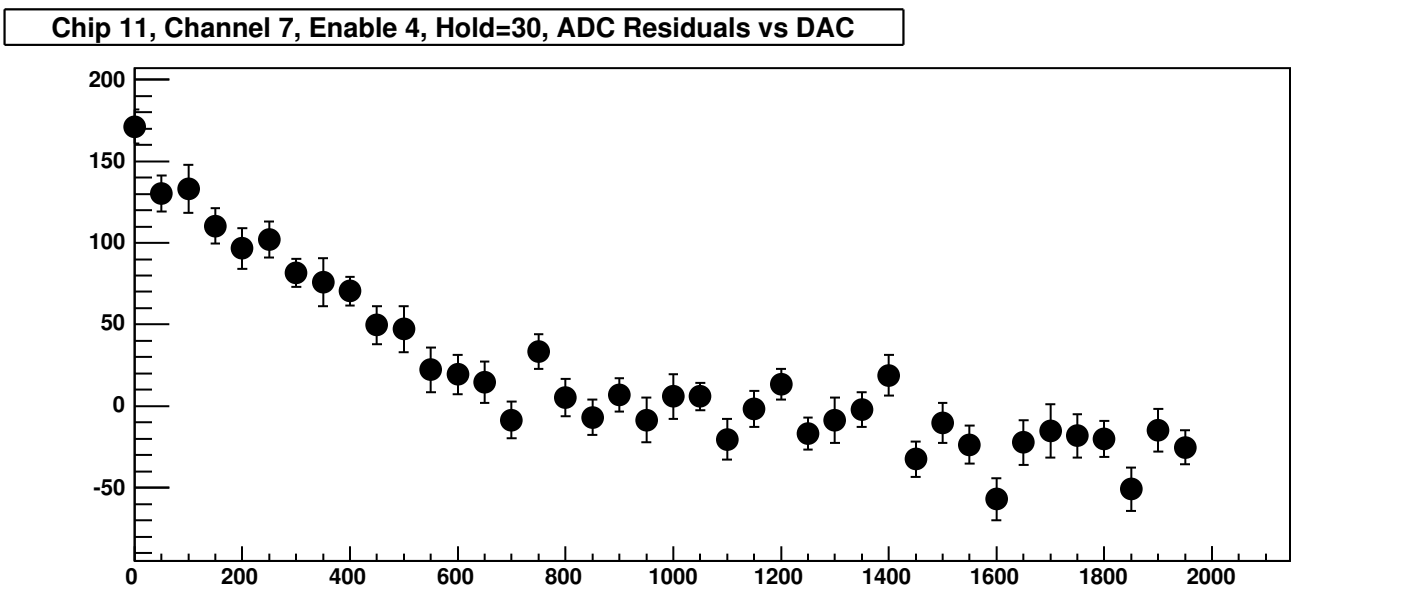

Chip 11, Channel 7, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 16.1 / 11
p0 -1297 ± 19.21
p1 0.159 ± 0.01714
-950
-1000
-1050
-1100
-1150
-1200
-1250
0
200
400
600
800
1000
1200
1400
1600
1800
2000

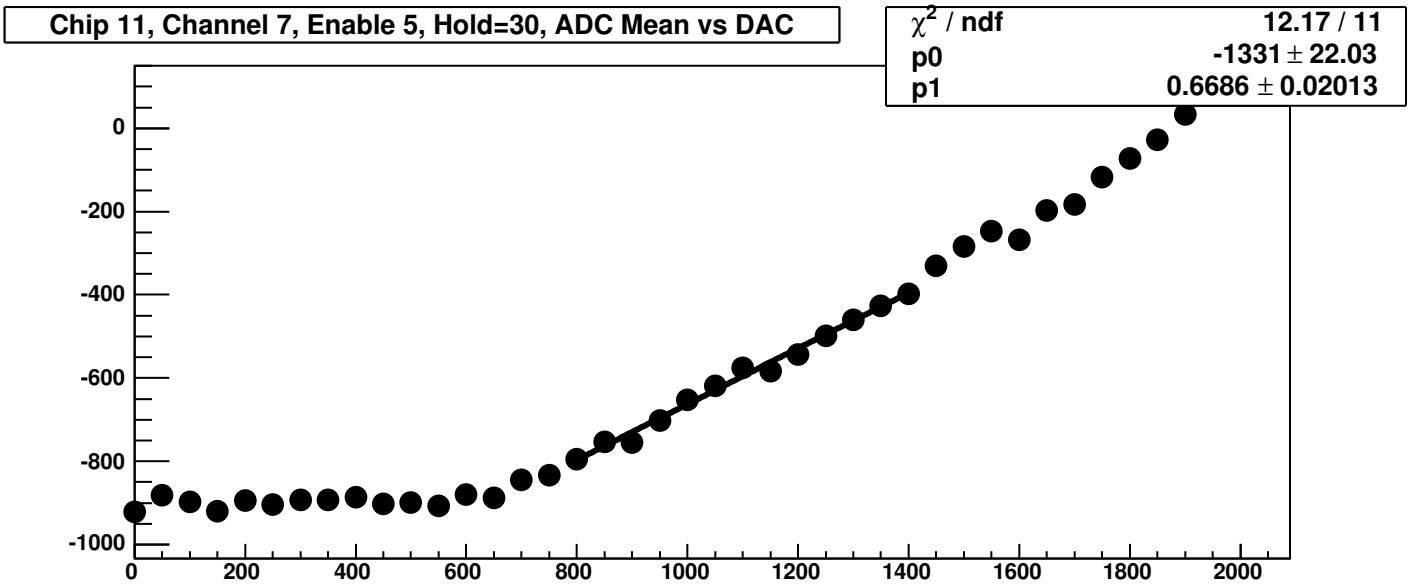

Chip 11, Channel 7, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

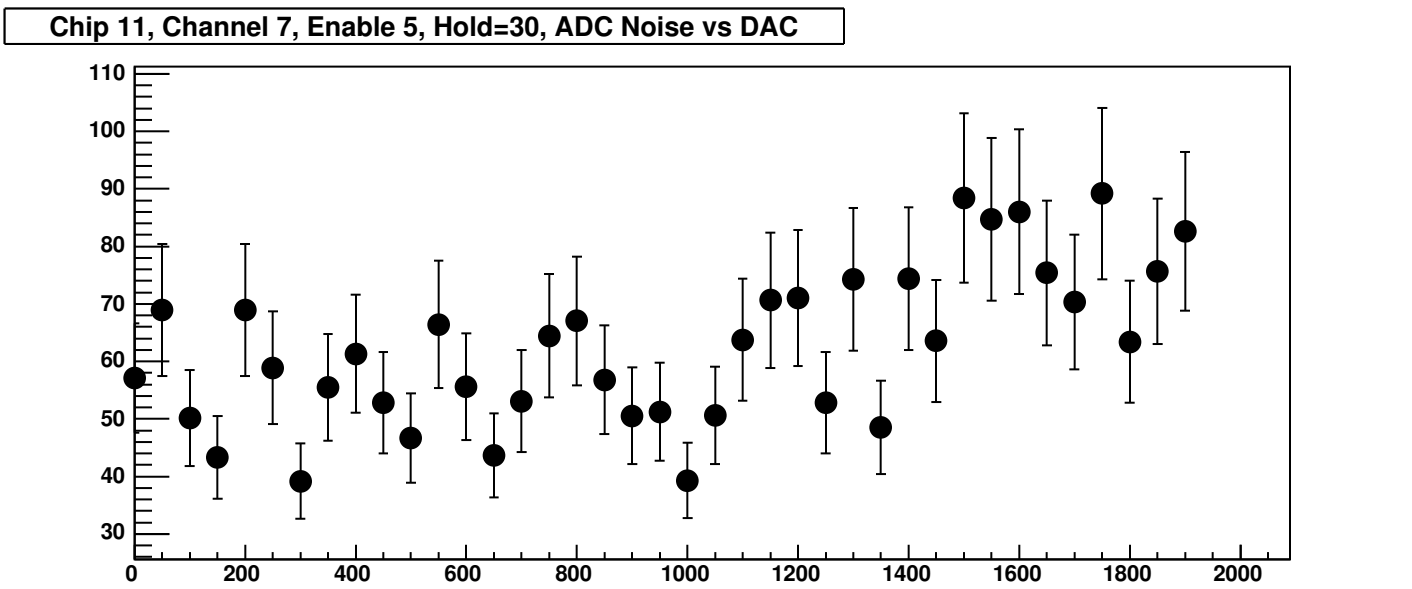

Chip 11, Channel 7, Enable 3, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

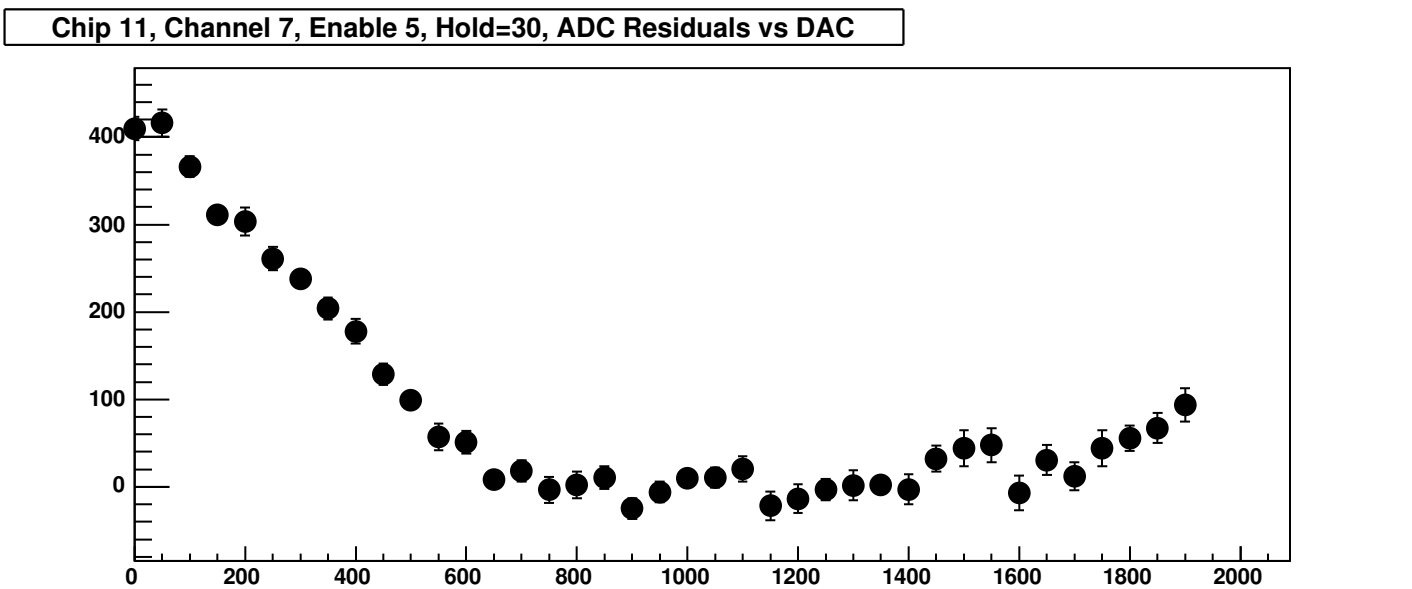

Chip 11, Channel 7, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 12.59 / 11
p0 -1364 ± 18.79
p1 0.2375 ± 0.01686
-900
-950
-1000
-1050
-1100
-1150
-1200
-1250
0
200
400
600
800
1000
1200
1400
1600
1800
2000

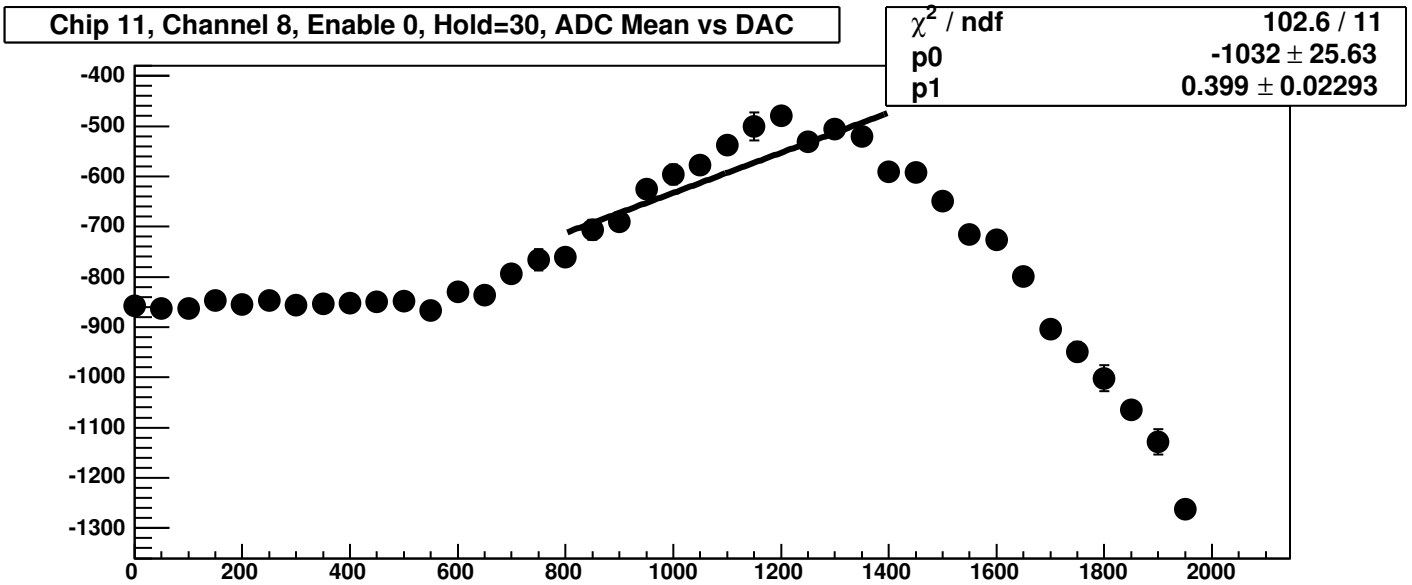

Chip 11, Channel 7, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

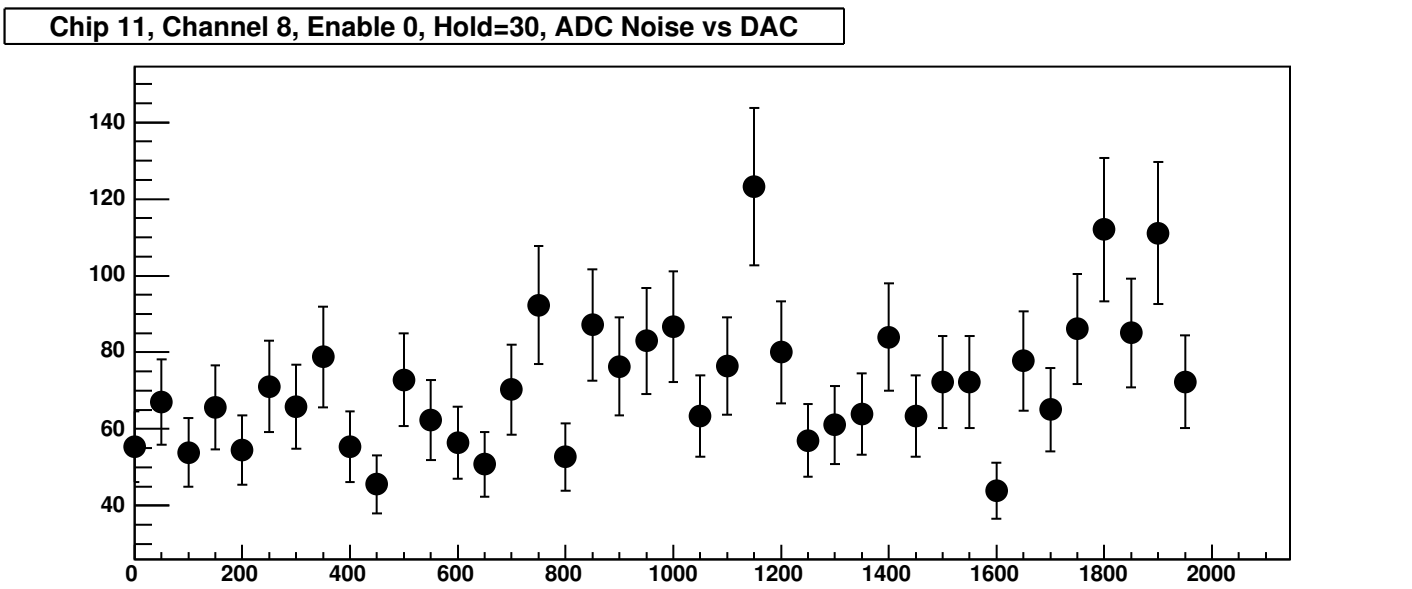

Chip 11, Channel 7, Enable 4, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

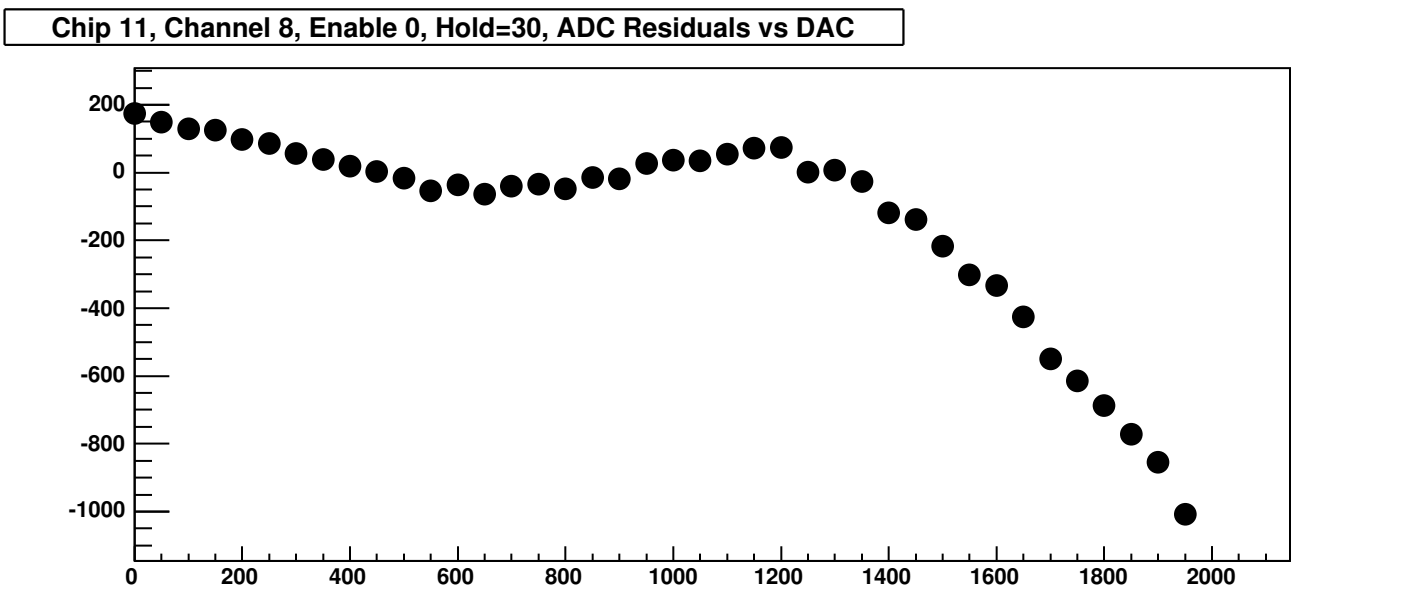

Chip 11, Channel 7, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 12.17 / 11
p0 -1331 ± 22.03
p1 0.6686 ± 0.02013
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

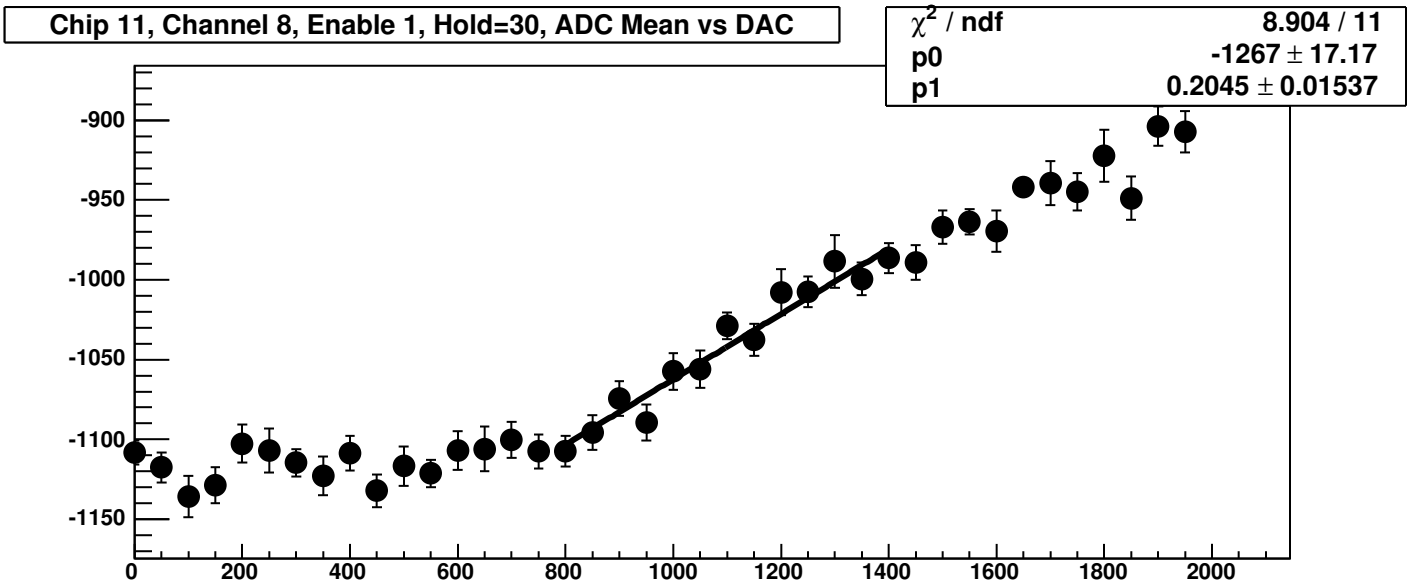

Chip 11, Channel 7, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

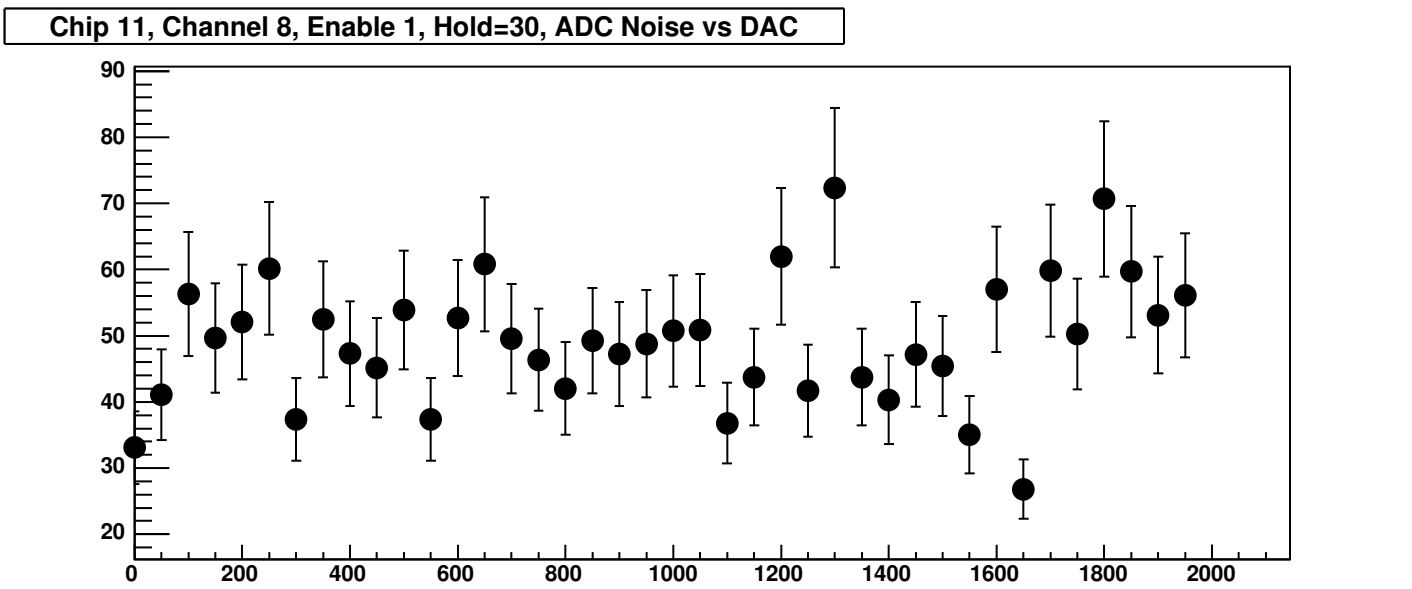

Chip 11, Channel 7, Enable 5, Hold=30, ADC Residuals vs DAC
400
300
200
100
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

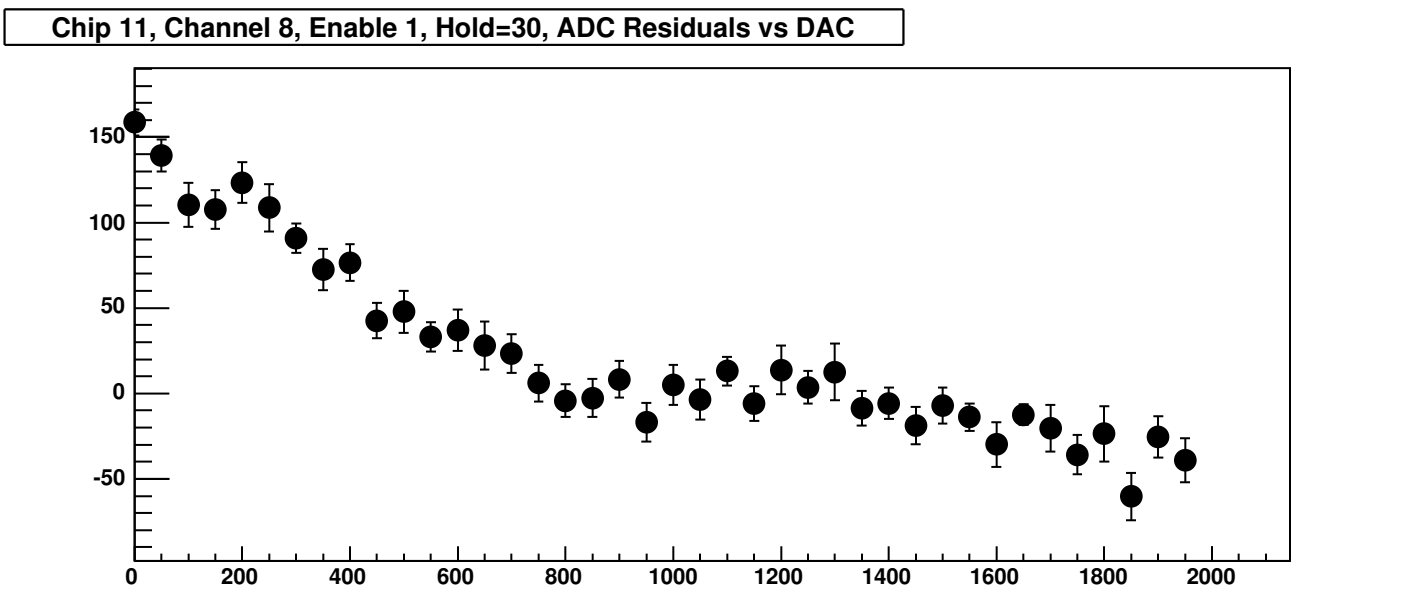

Chip 11, Channel 8, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 102.6 / 11
p0 -1032 ± 25.63
p1 0.399 ± 0.02293
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

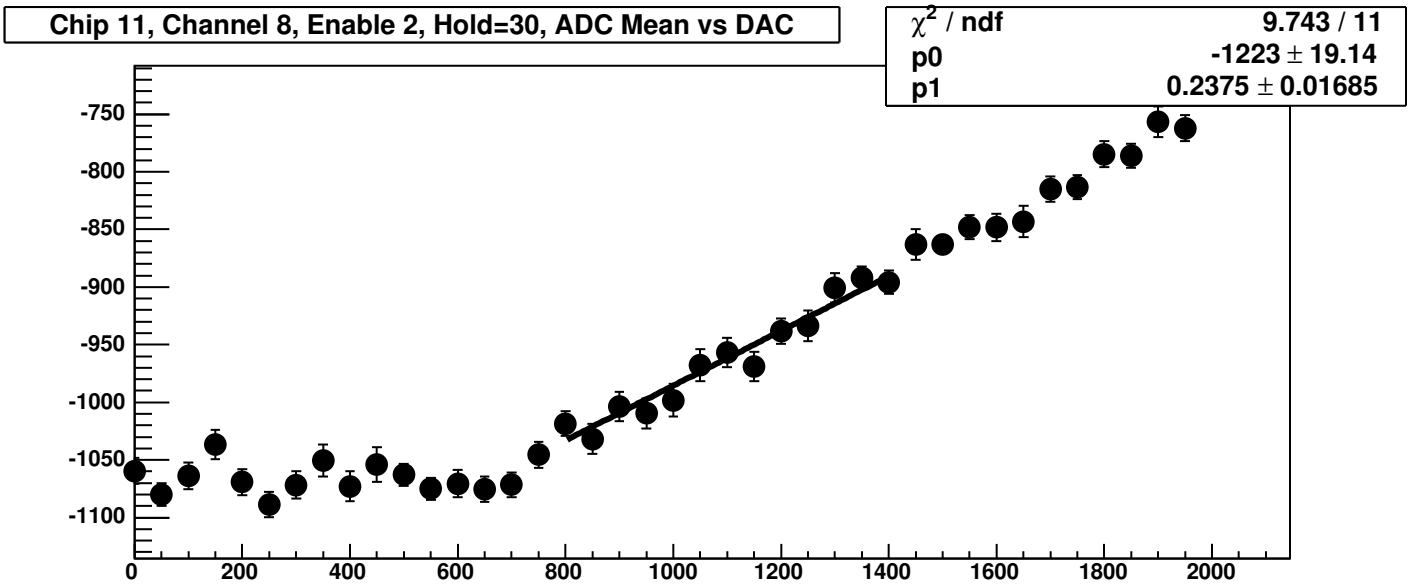

Chip 11, Channel 8, Enable 0, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

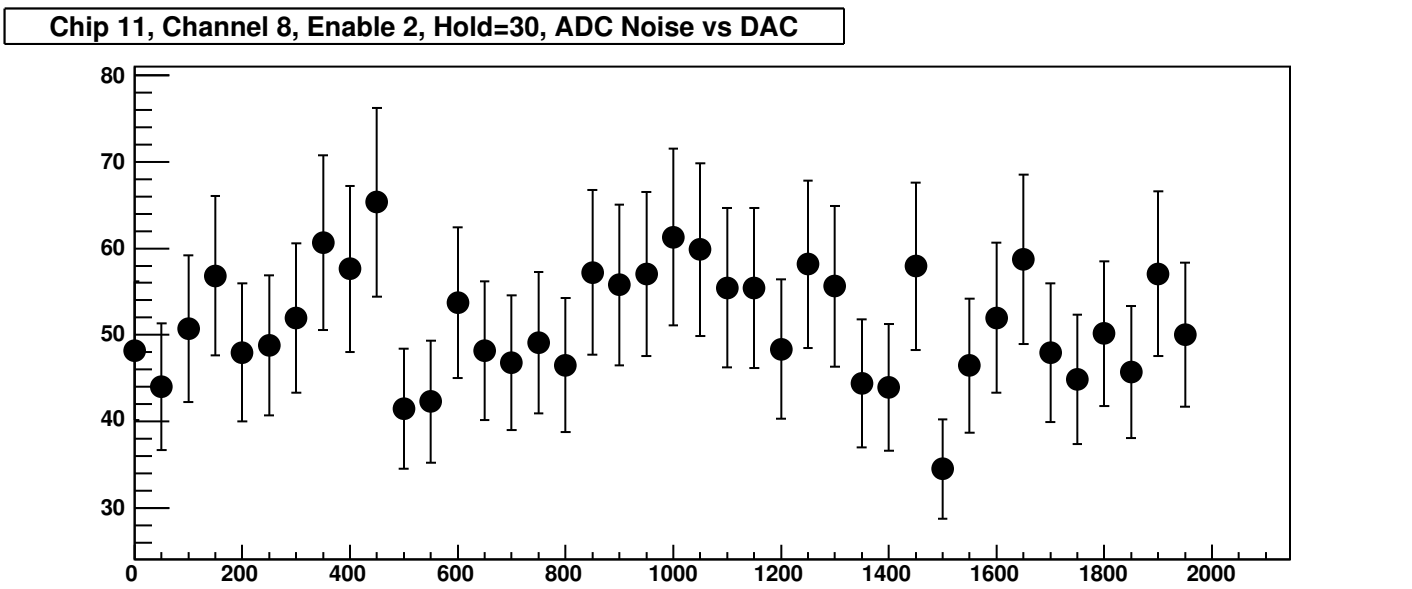

Chip 11, Channel 8, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

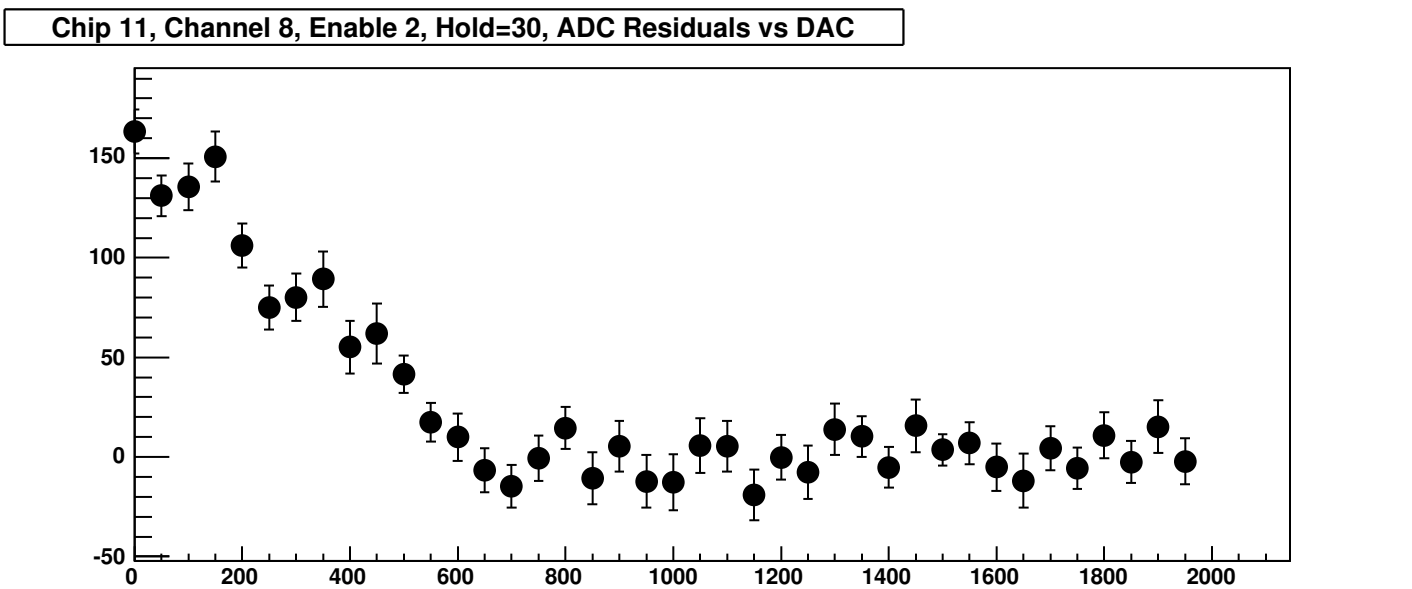

Chip 11, Channel 8, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 8.904 / 11
p0 -1267 ± 17.17
p1 0.2045 ± 0.01537
-900
-950
-1000
-1050
-1100
-1150
0
200
400
600
800
1000
1200
1400
1600
1800
2000

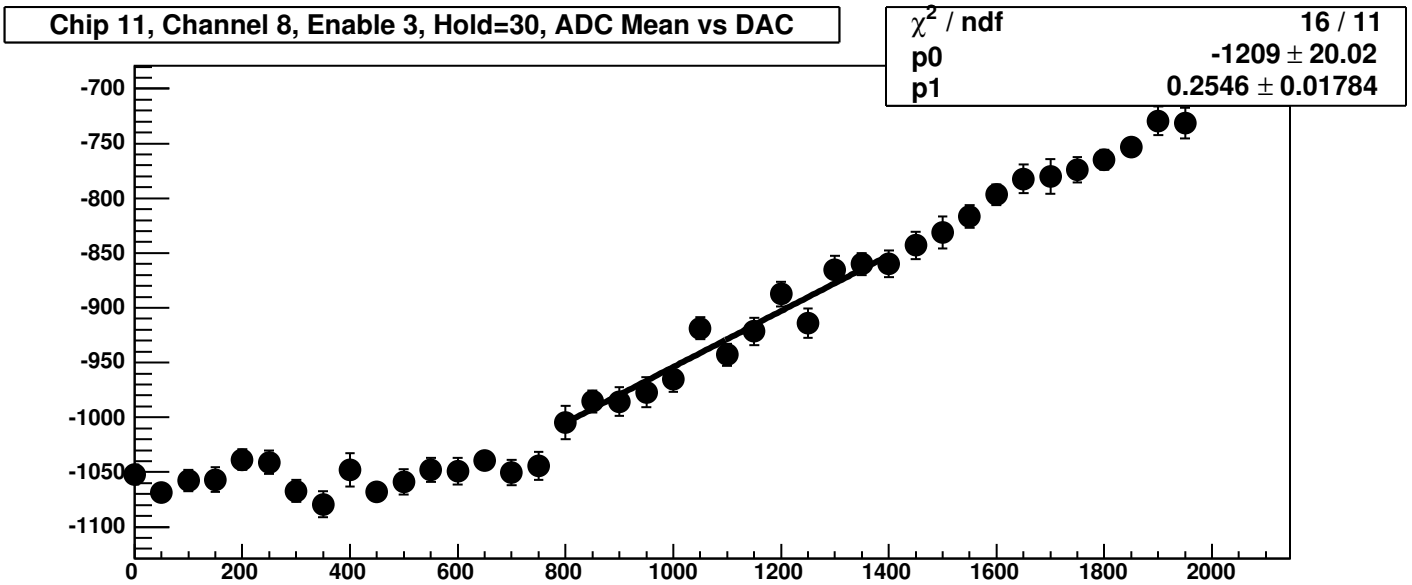

Chip 11, Channel 8, Enable 1, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

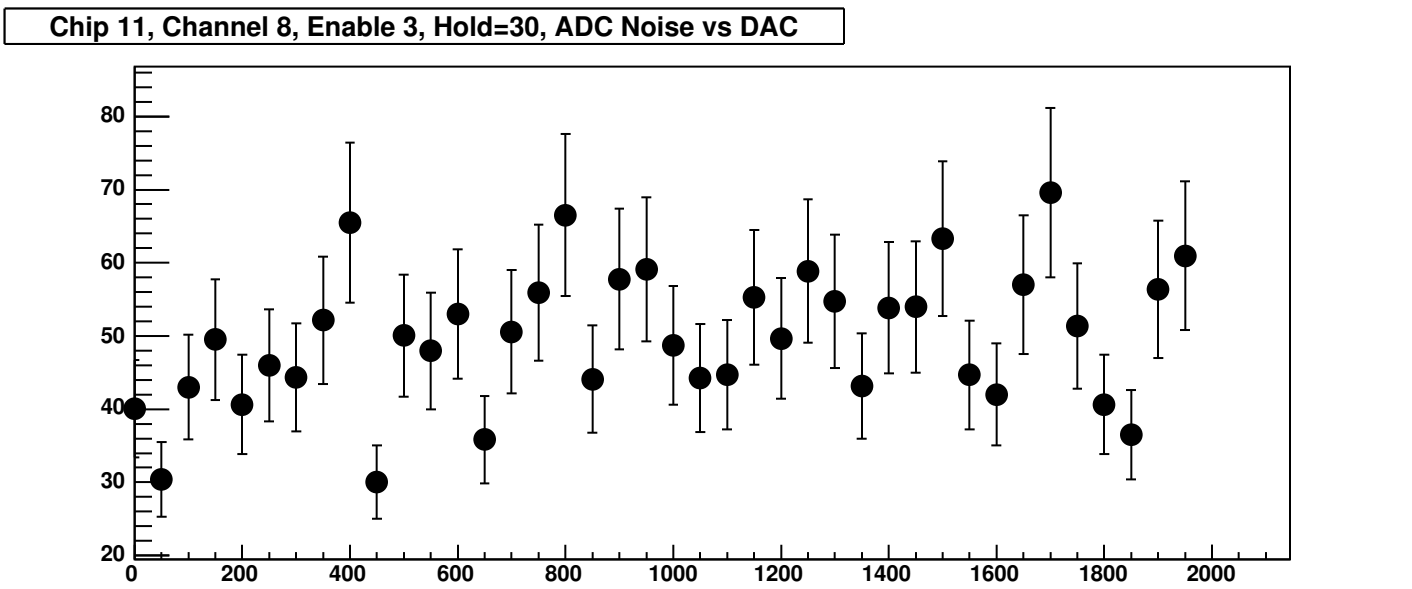

Chip 11, Channel 8, Enable 1, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

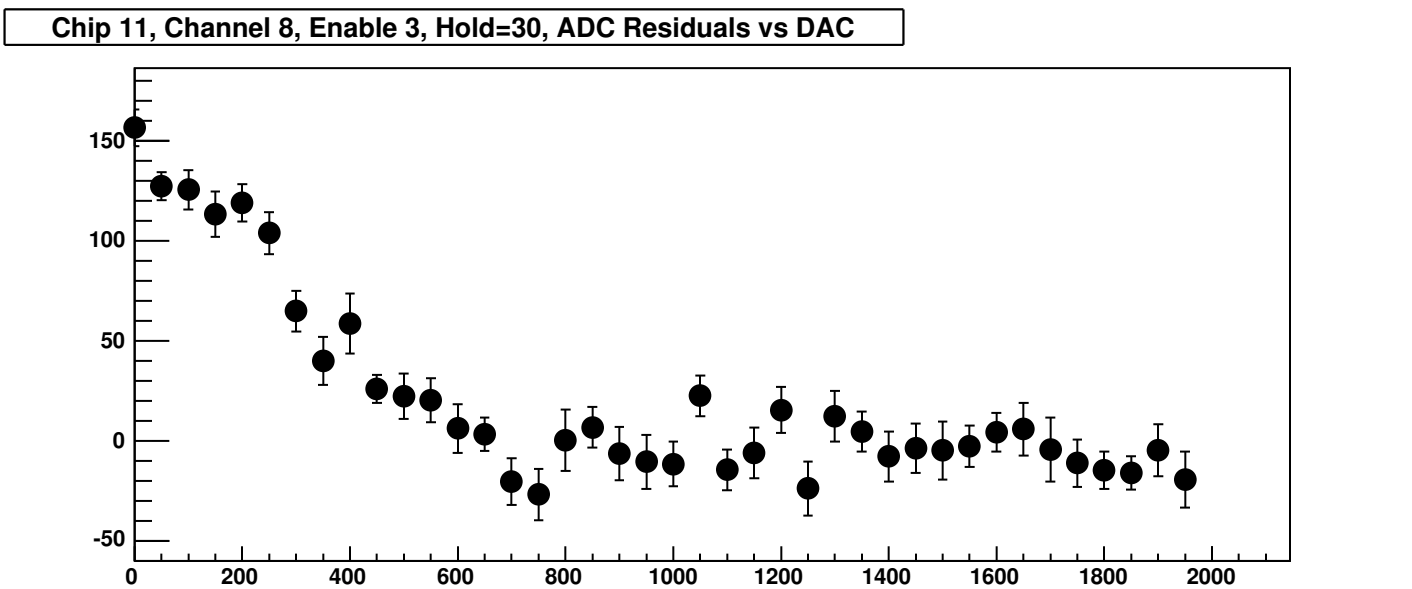

Chip 11, Channel 8, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 9.743 / 11
p0 -1223 ± 19.14
p1 0.2375 ± 0.01685
-750
-800
-850
-900
-950
-1000
-1050
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

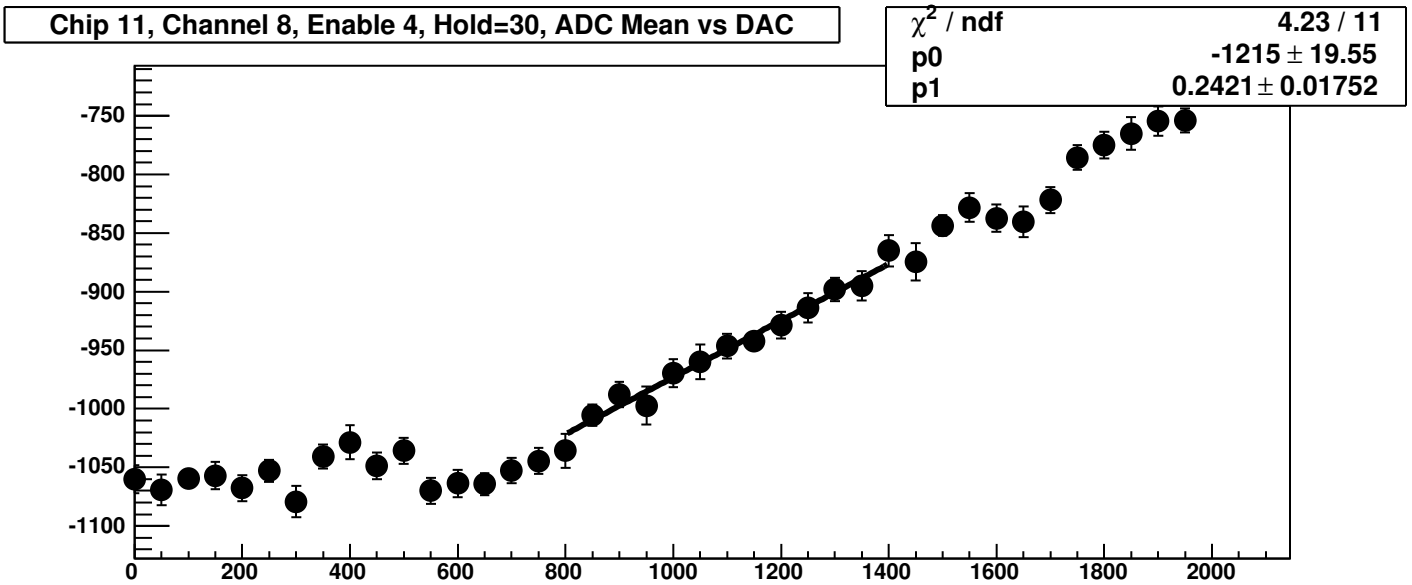

Chip 11, Channel 8, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

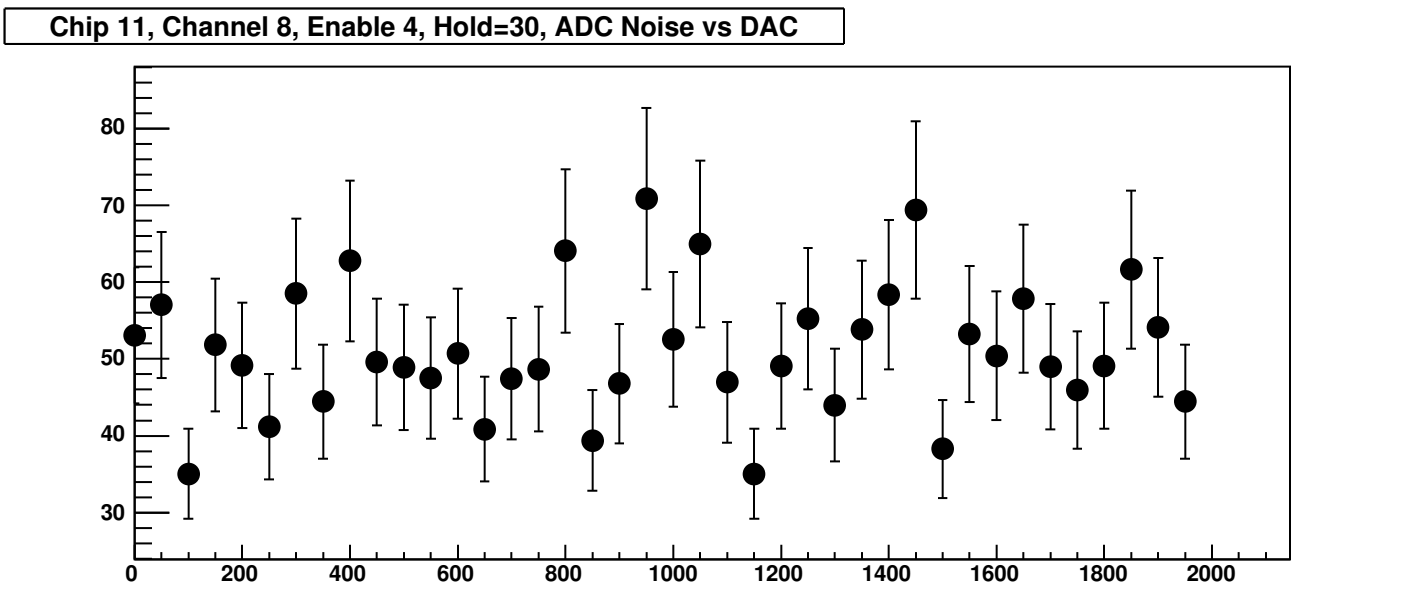

Chip 11, Channel 8, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

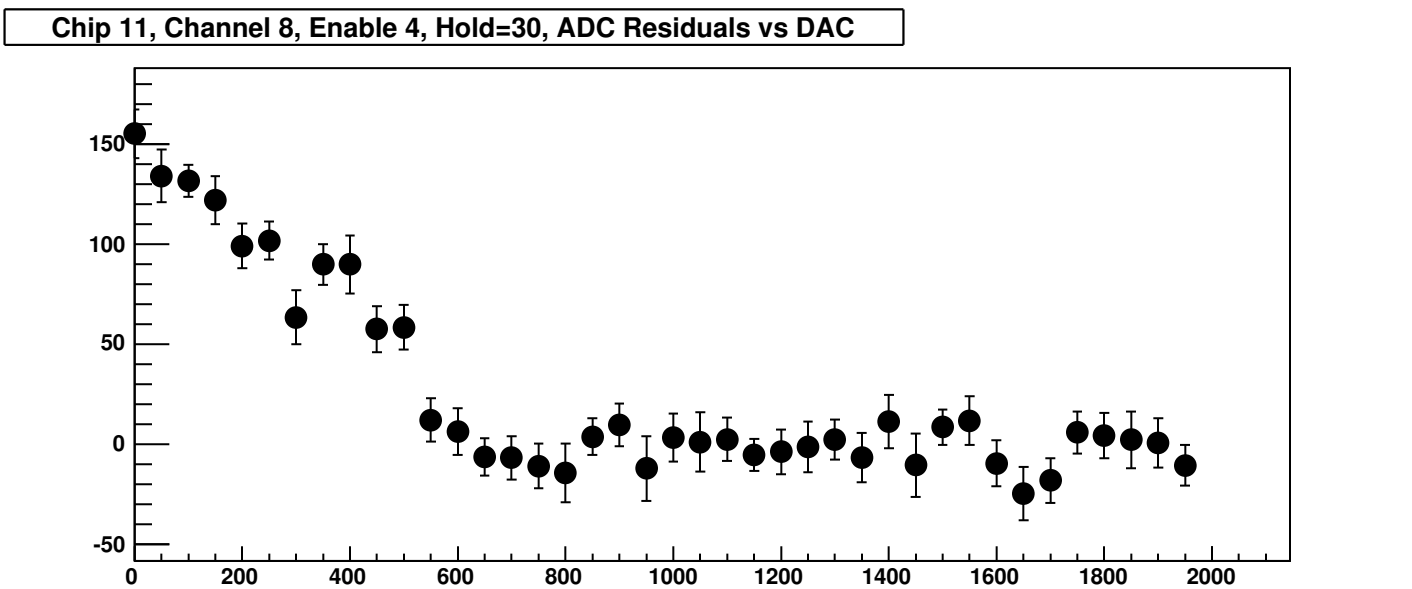

Chip 11, Channel 8, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 16 / 11
p0 -1209 ± 20.02
p1 0.2546 ± 0.01784
-700
-750
-800
-850
-900
-950
-1000
-1050
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

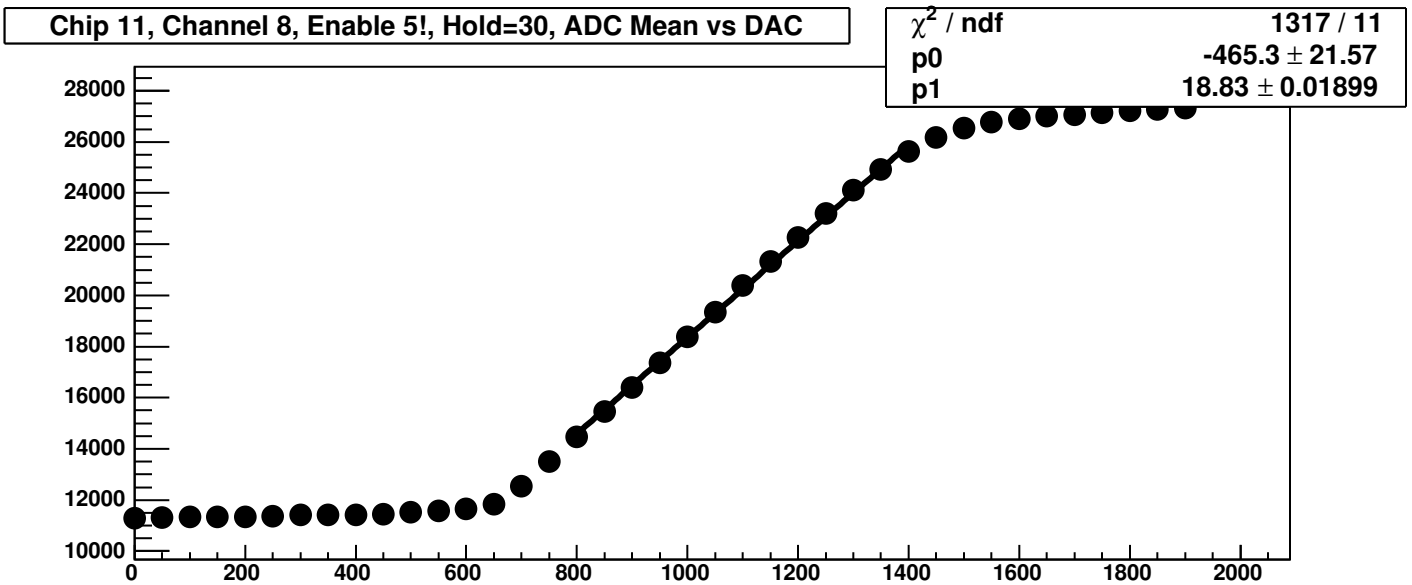

Chip 11, Channel 8, Enable 3, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

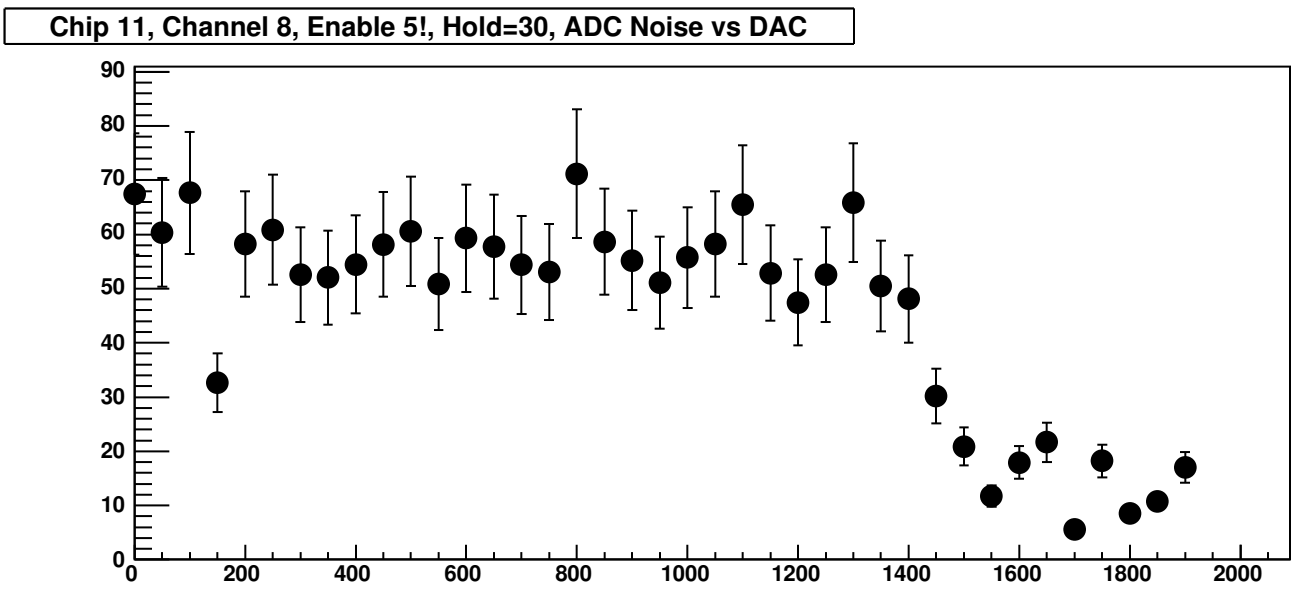

Chip 11, Channel 8, Enable 3, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

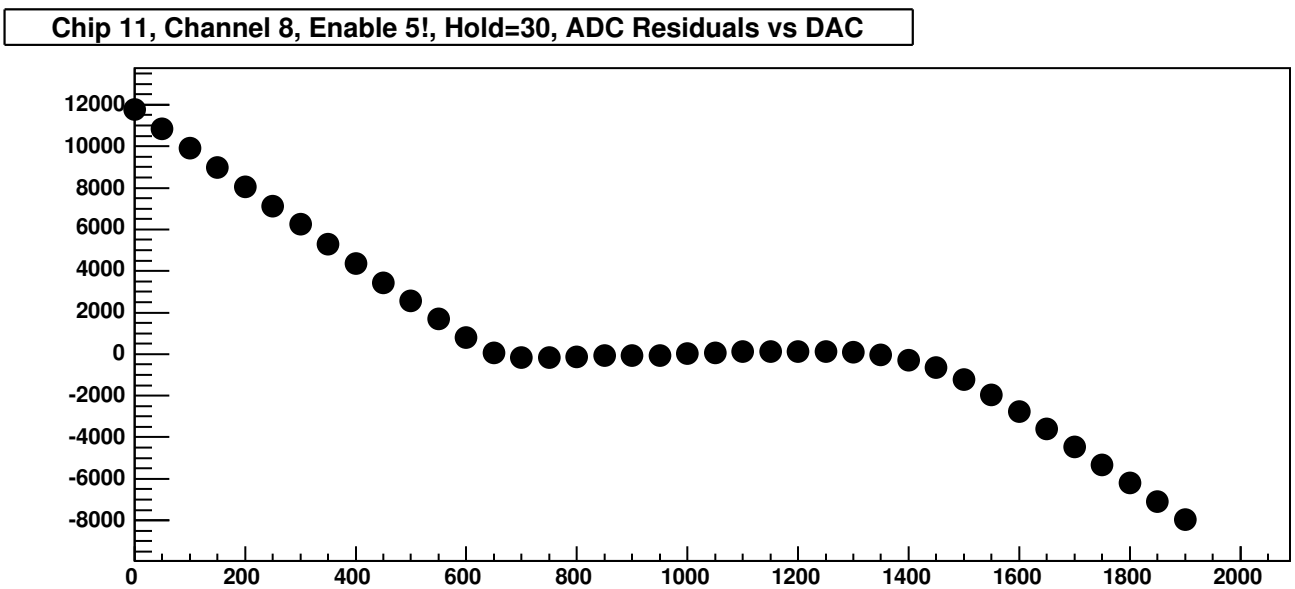

Chip 11, Channel 8, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 4.23 / 11
p0 -1215 ± 19.55
p1 0.2421 ± 0.01752
-750
-800
-850
-900
-950
-1000
-1050
-1100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

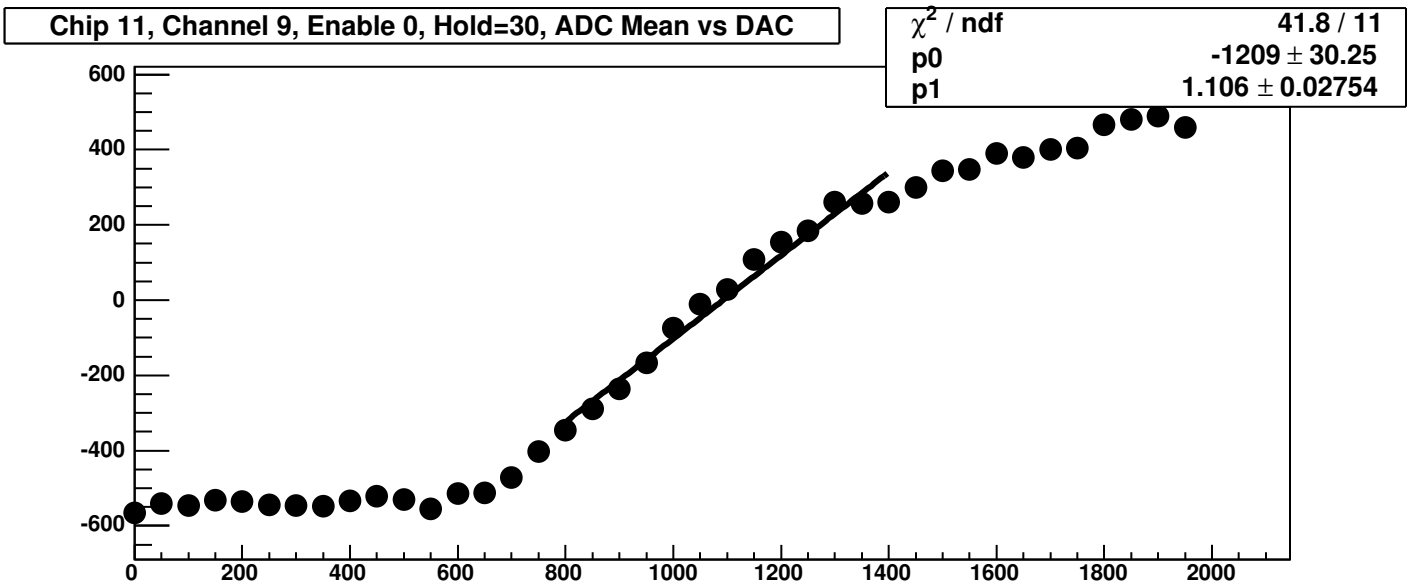

Chip 11, Channel 8, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

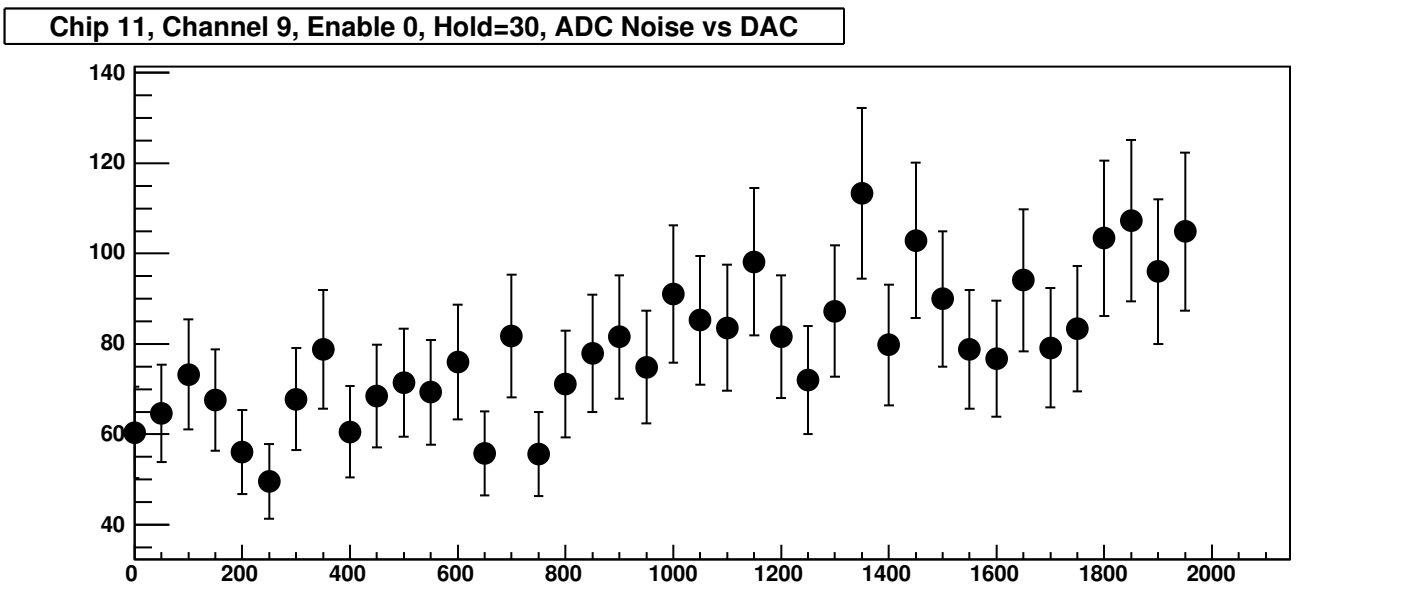

Chip 11, Channel 8, Enable 4, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

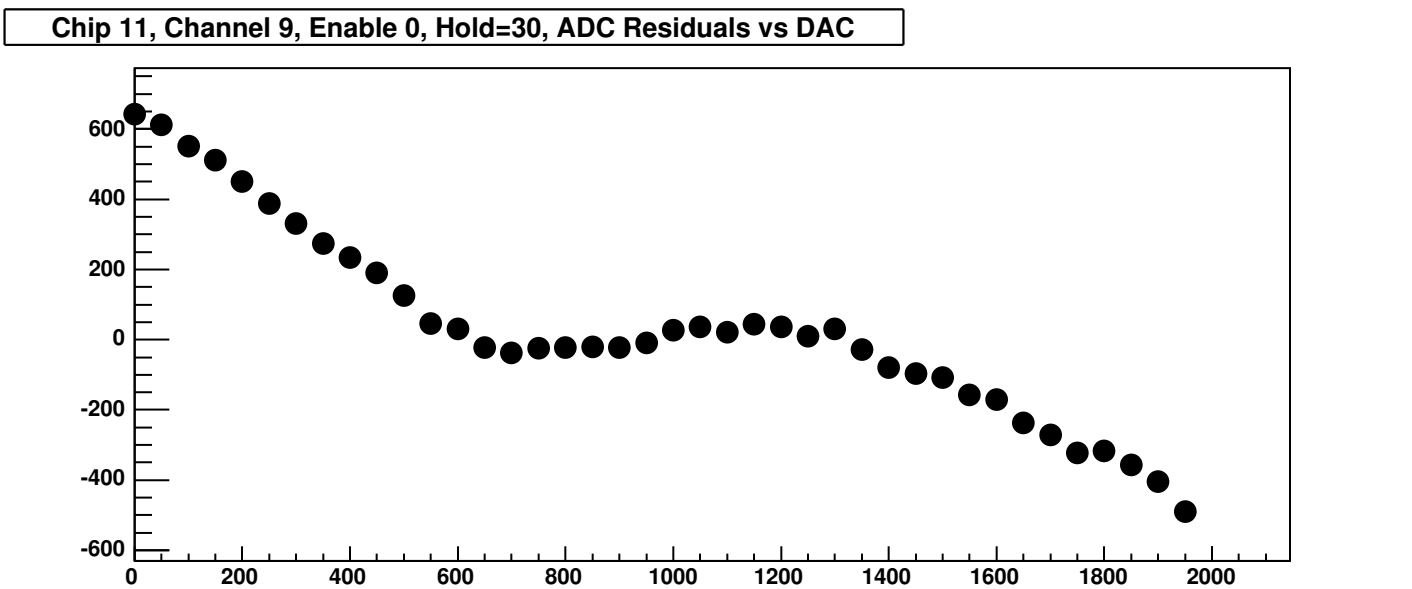

Chip 11, Channel 8, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 1317 / 11
p0 -465.3 ± 21.57
p1 18.83 ± 0.01899
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 8, Enable 5!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

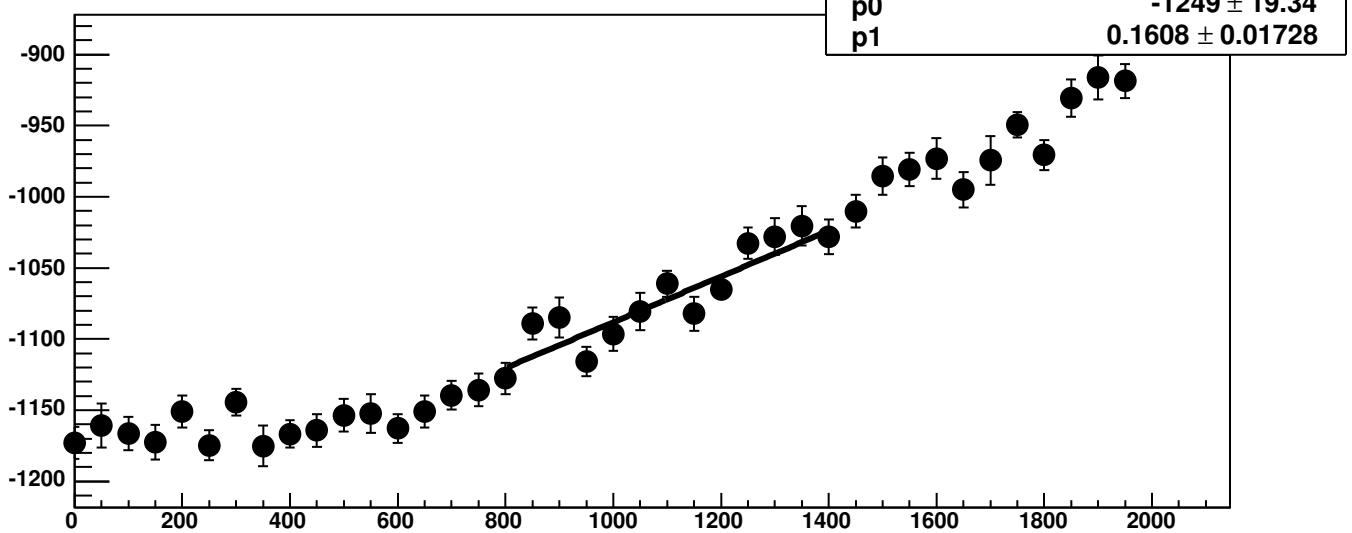

Chip 11, Channel 8, Enable 5!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 9, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 41.8 / 11
p0 -1209 ± 30.25
p1 1.106 ± 0.02754
600
400
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

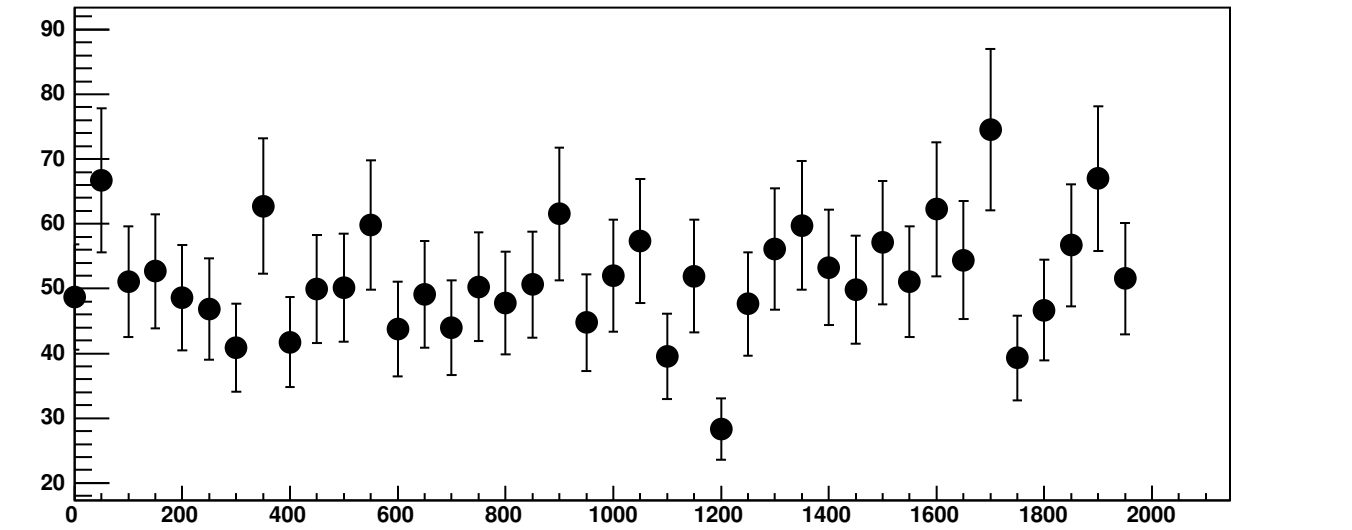

Chip 11, Channel 9, Enable 0, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 9, Enable 0, Hold=30, ADC Residuals vs DAC
600
400
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

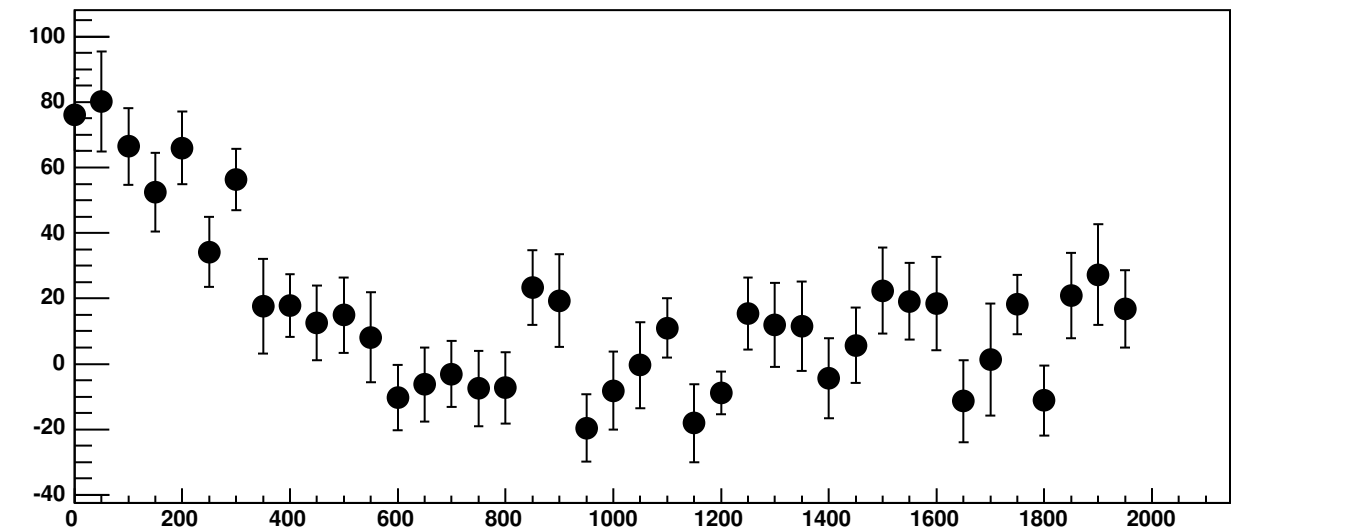
Chip 11, Channel 9, Enable 1, Hold=30, ADC Mean vs DAC



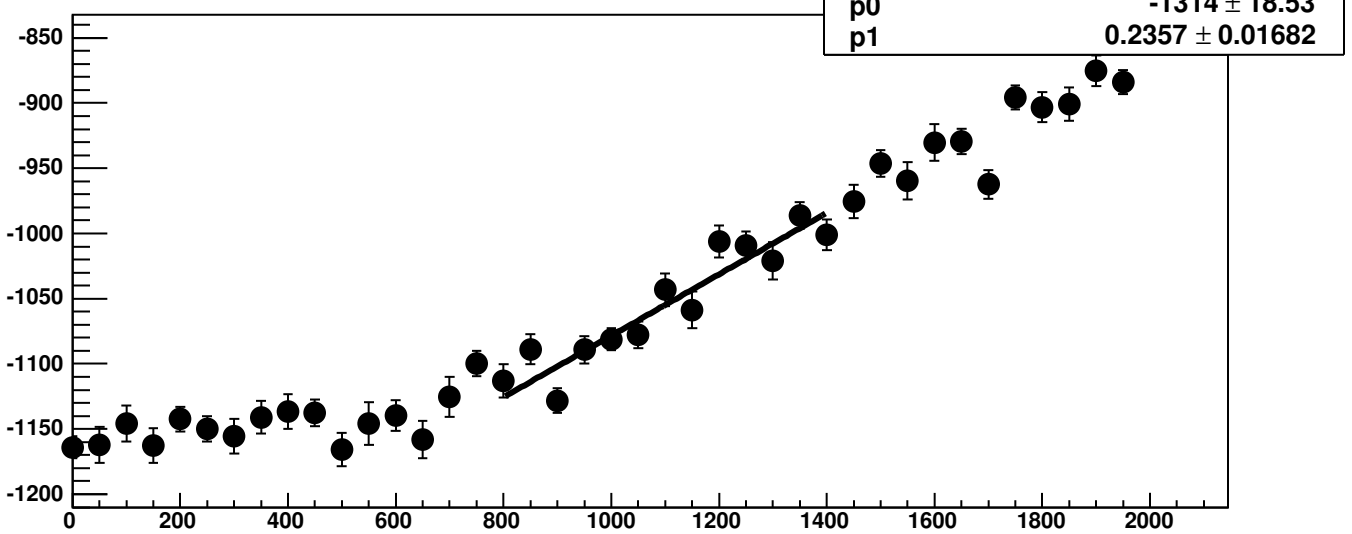
Chip 11, Channel 9, Enable 1, Hold=30, ADC Noise vs DAC



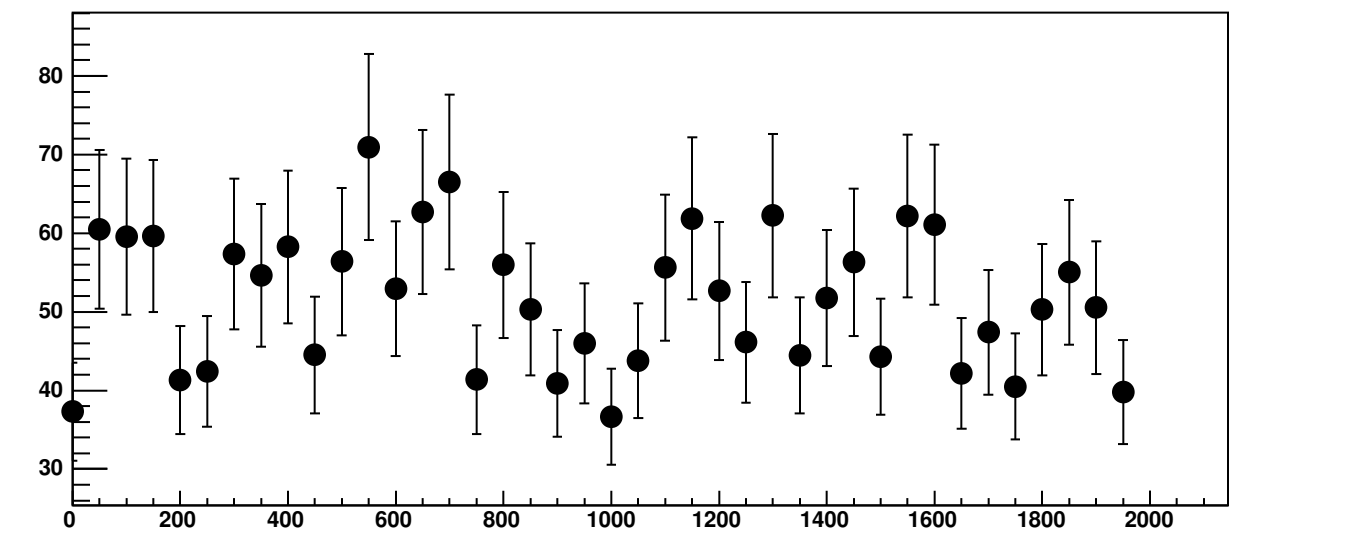
Chip 11, Channel 9, Enable 1, Hold=30, ADC Residuals vs DAC



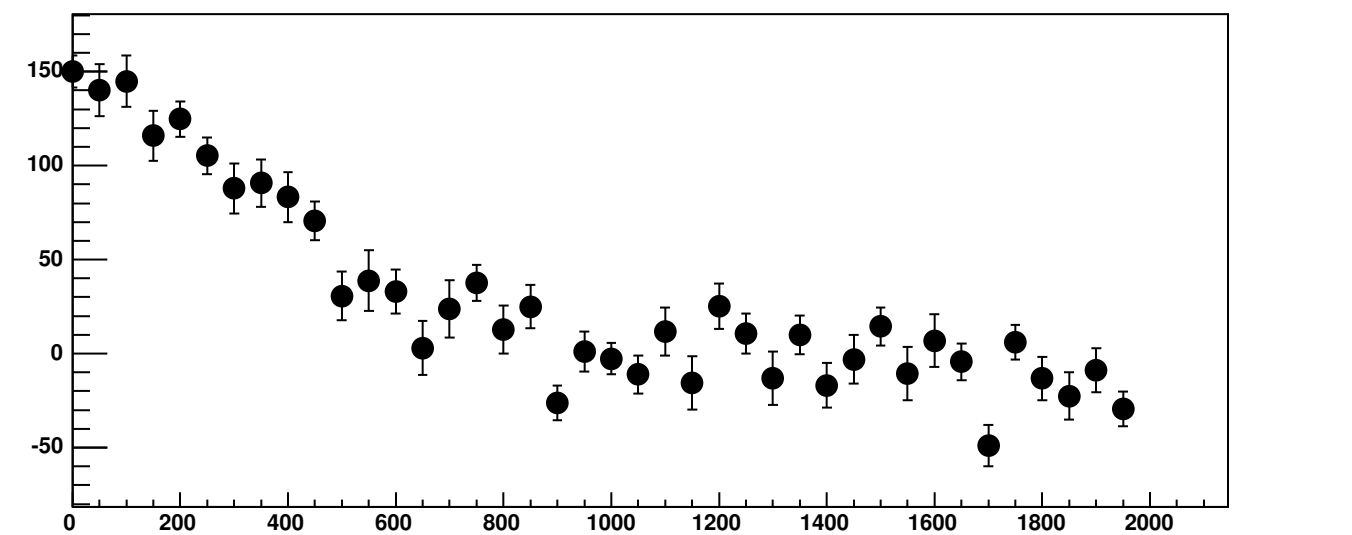
Chip 11, Channel 9, Enable 2, Hold=30, ADC Mean vs DAC

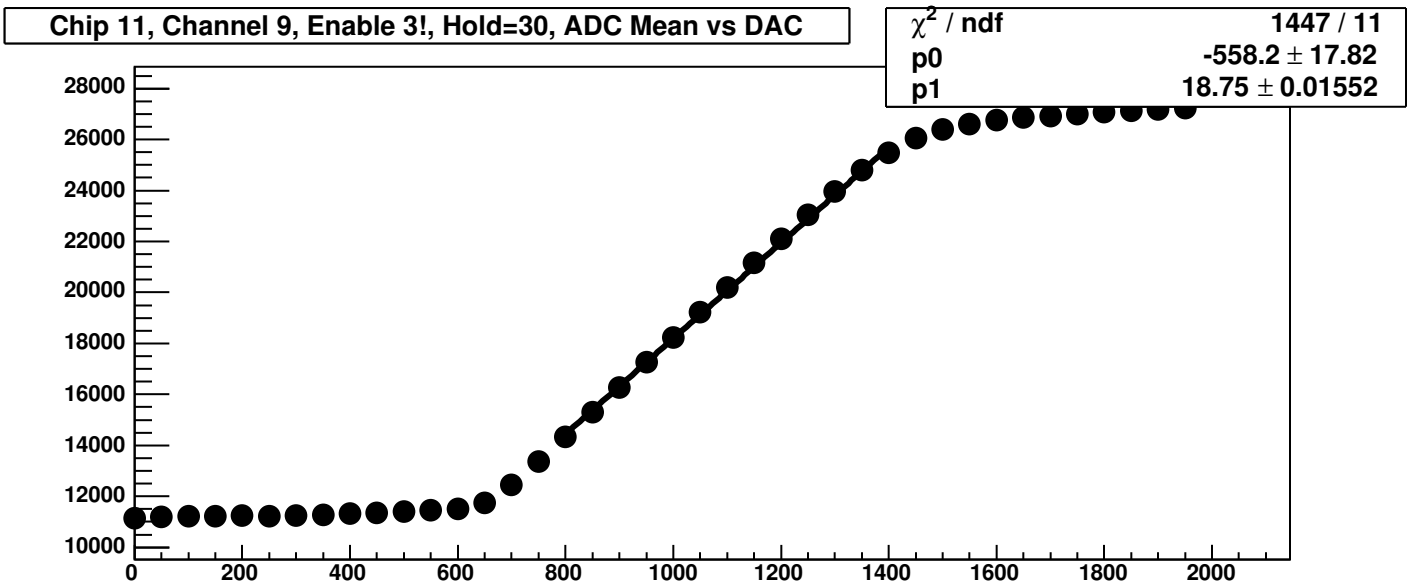


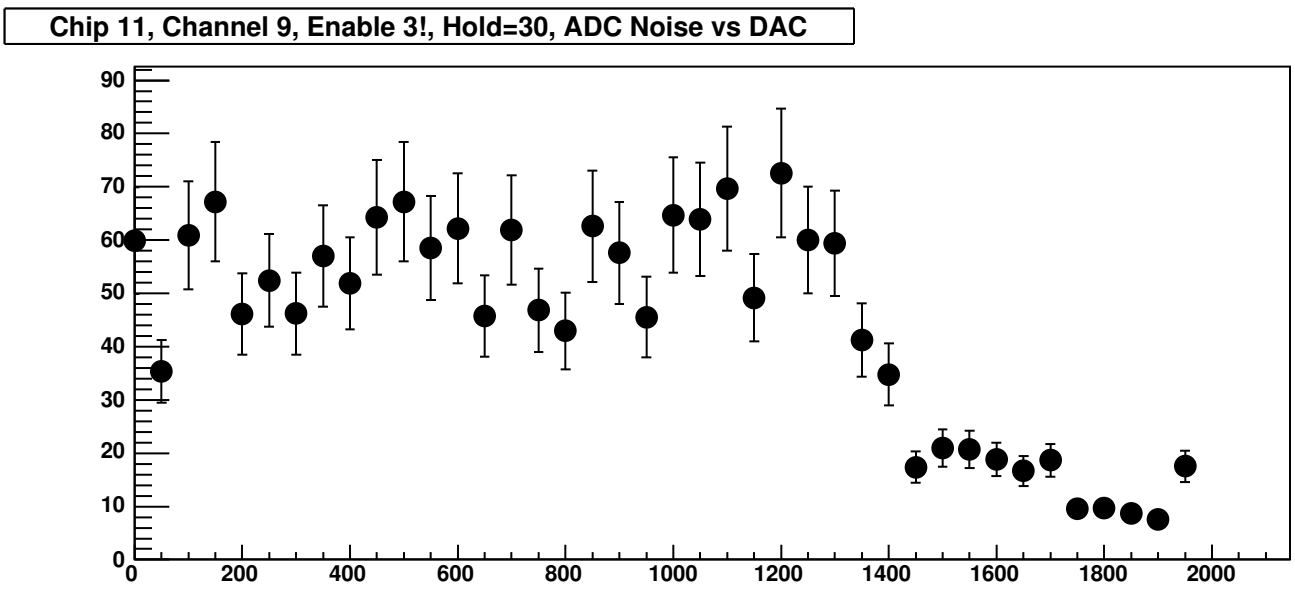
Chip 11, Channel 9, Enable 2, Hold=30, ADC Noise vs DAC

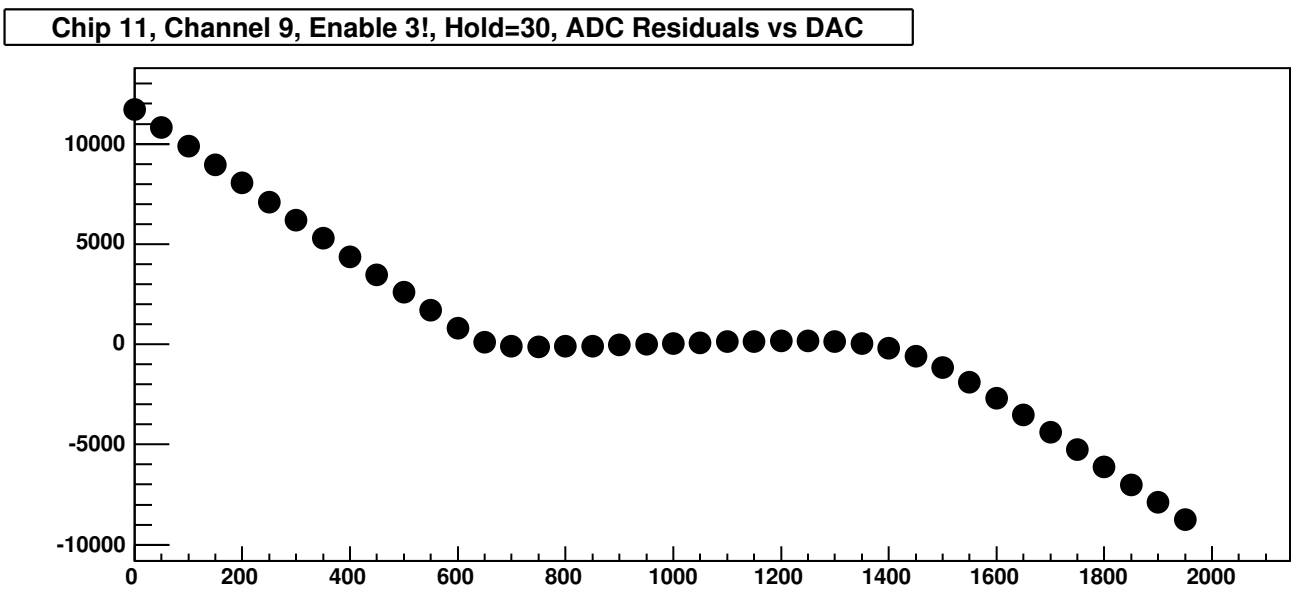


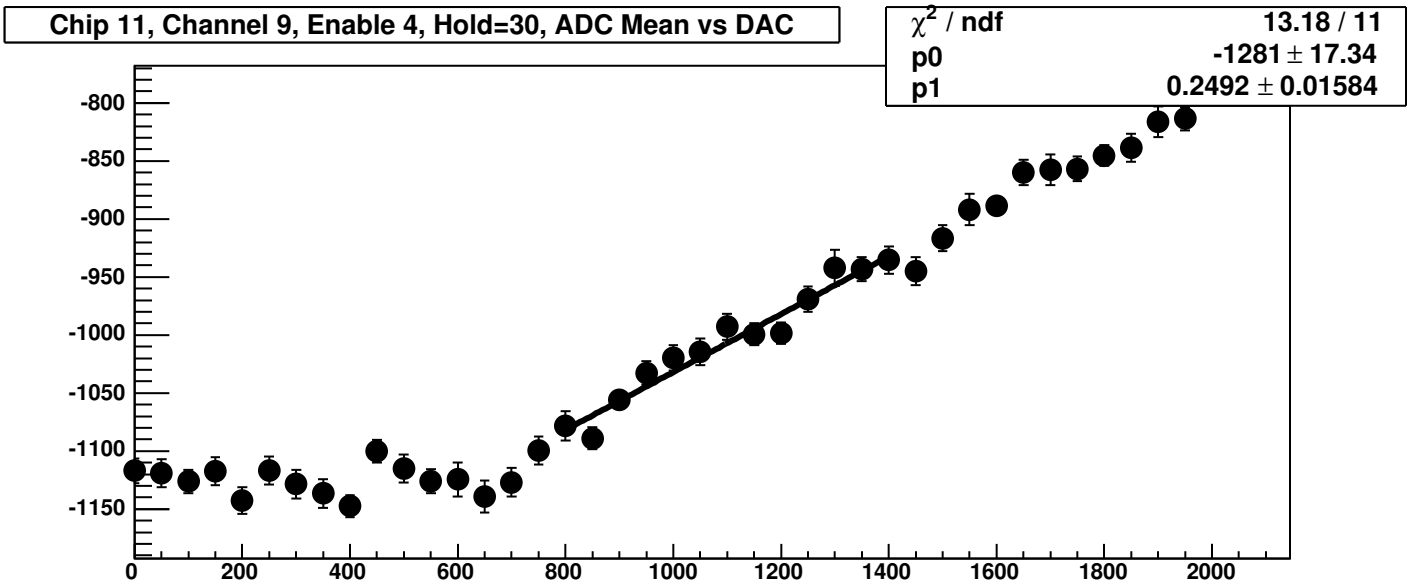
Chip 11, Channel 9, Enable 2, Hold=30, ADC Residuals vs DAC

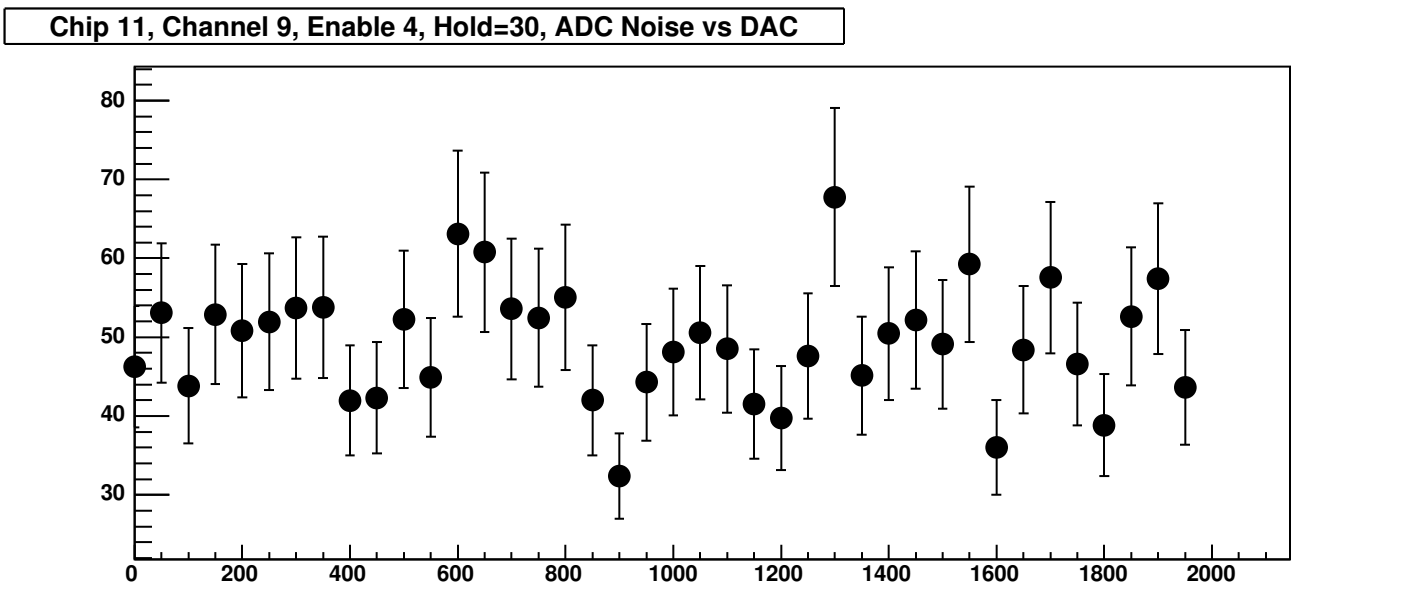


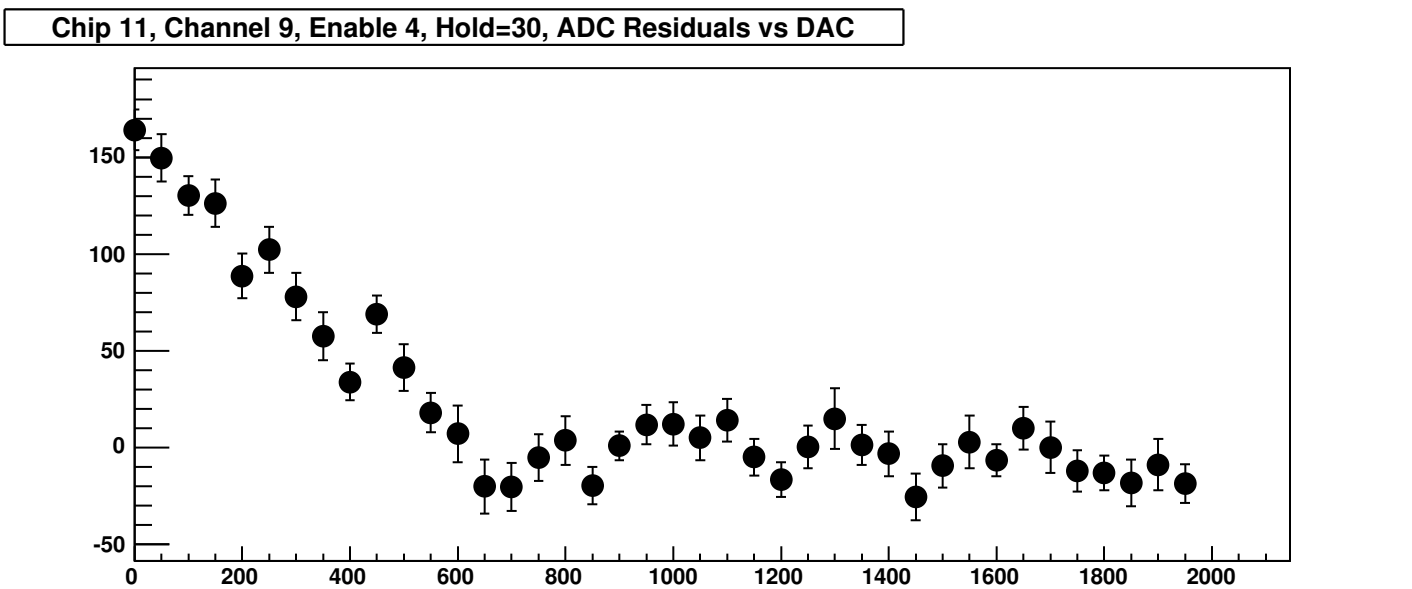

Chip 11, Channel 9, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 1447 / 11
p0 -558.2 ± 17.82
p1 18.75 ± 0.01552
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

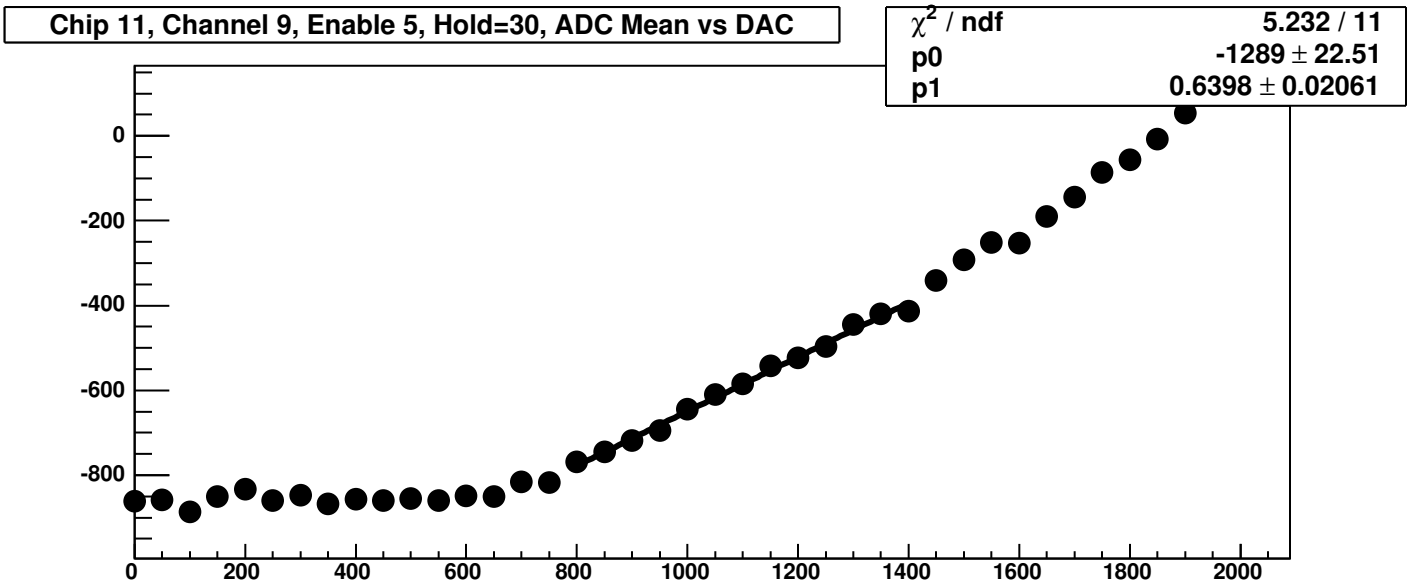

Chip 11, Channel 9, Enable 3!, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

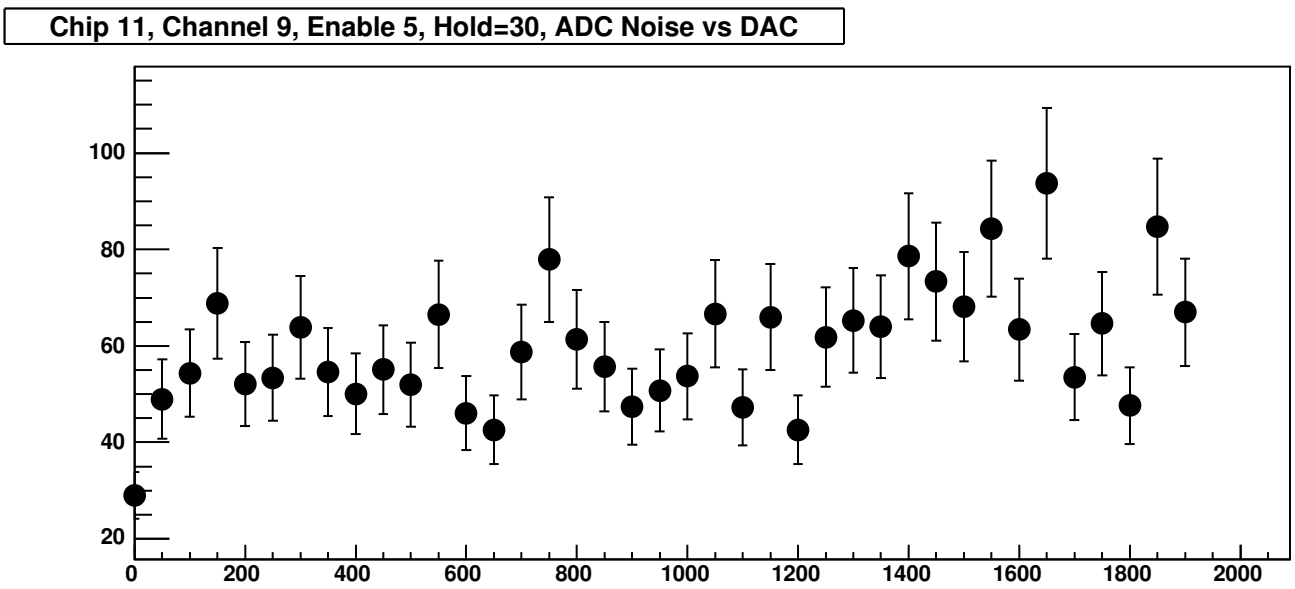

Chip 11, Channel 9, Enable 3!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

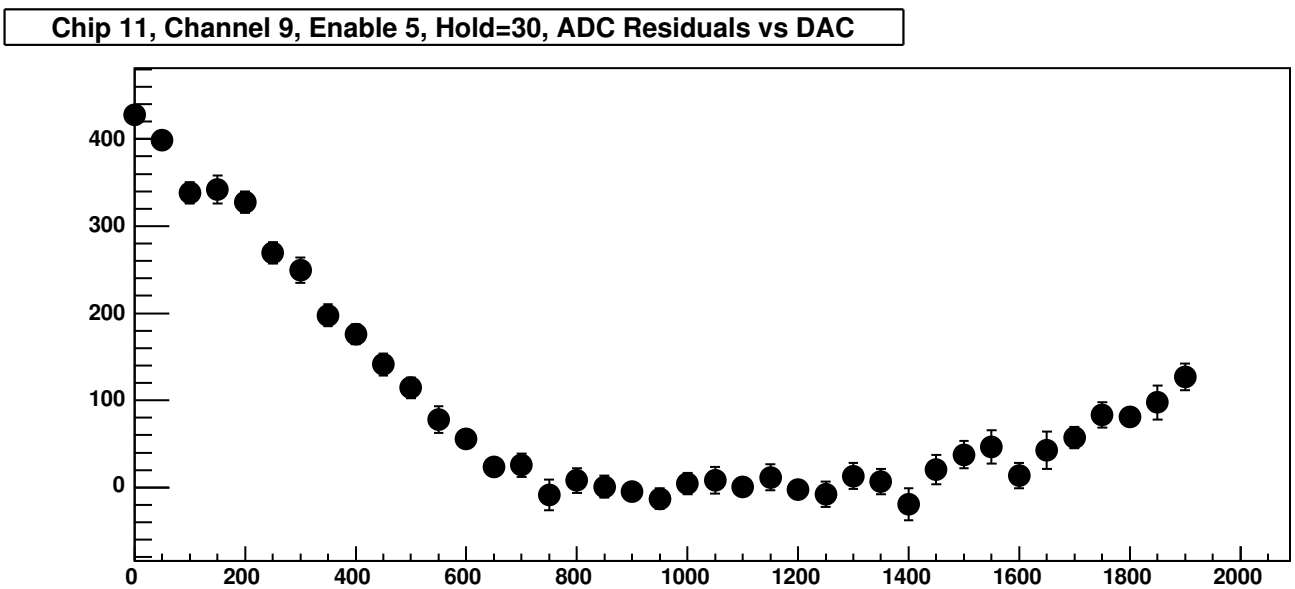

Chip 11, Channel 9, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 13.18 / 11
p0 -1281 ± 17.34
p1 0.2492 ± 0.01584
-800
-850
-900
-950
-1000
-1050
-1100
-1150
0
200
400
600
800
1000
1200
1400
1600
1800
2000

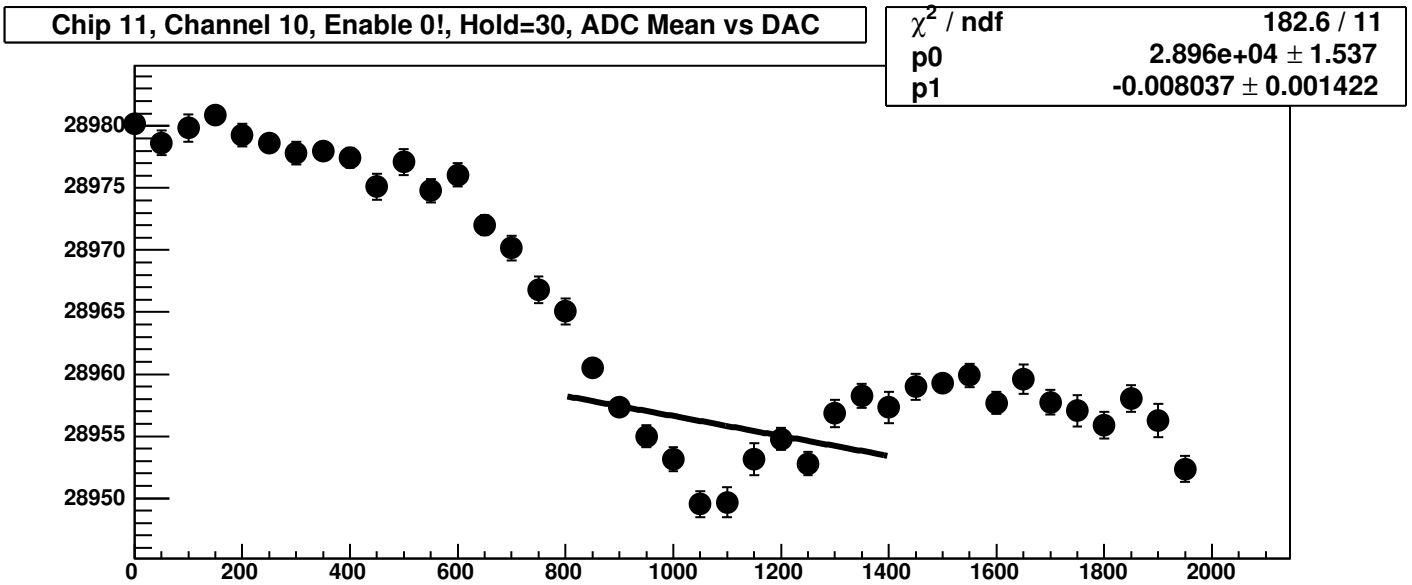

Chip 11, Channel 9, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

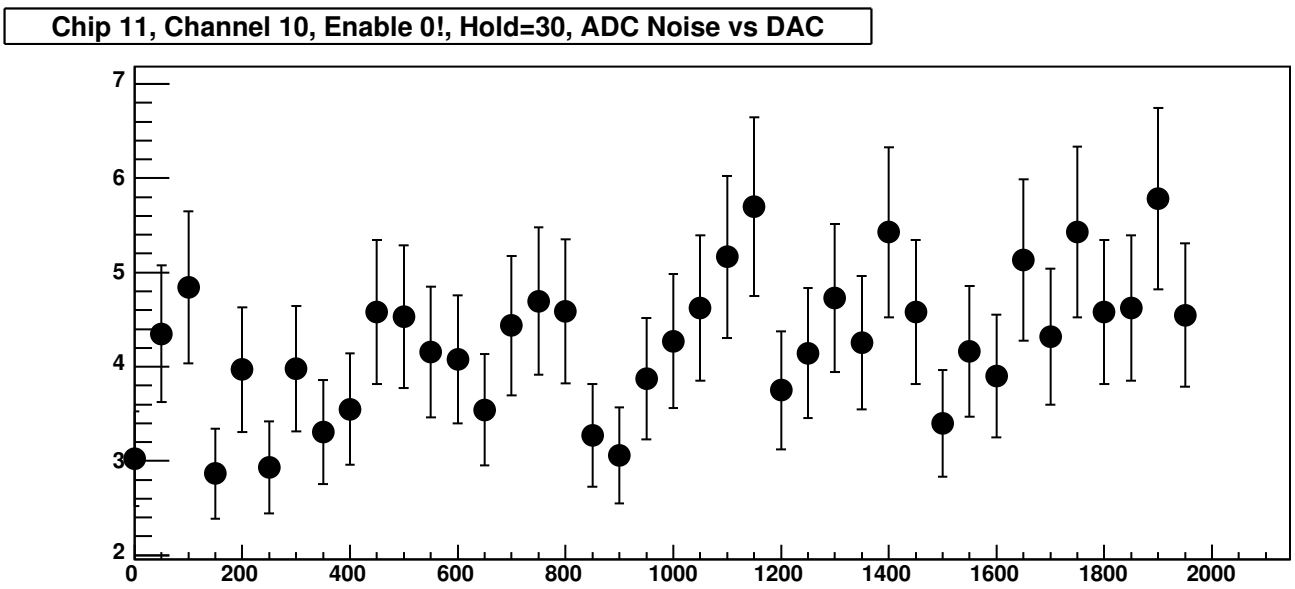

Chip 11, Channel 9, Enable 4, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

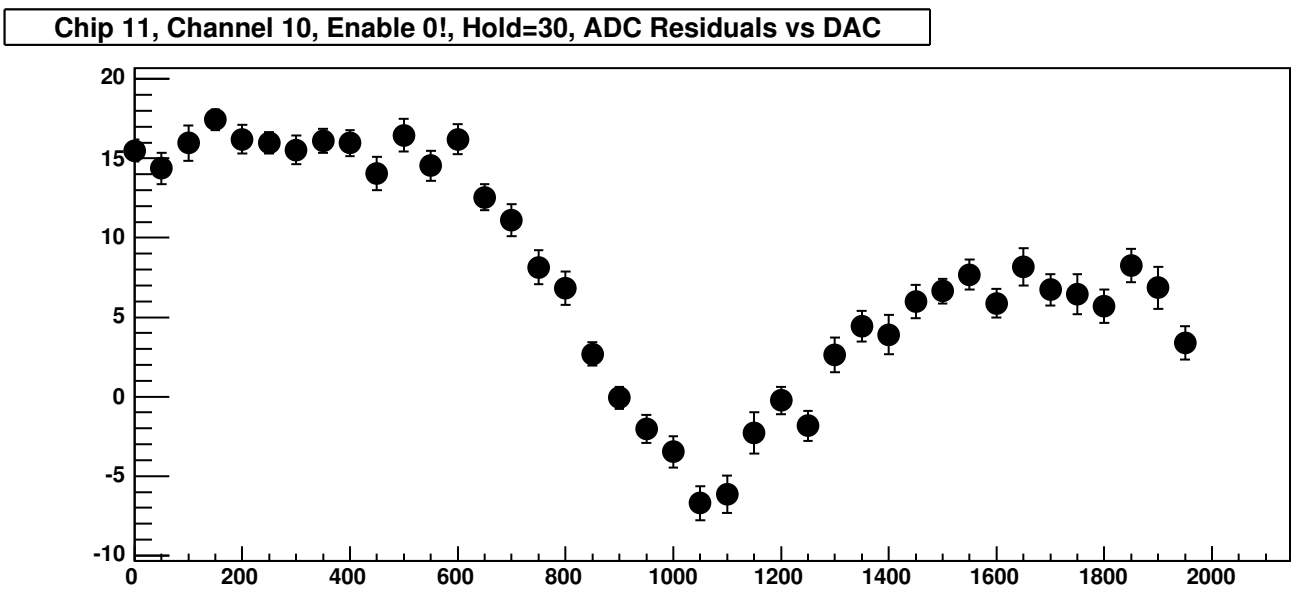

Chip 11, Channel 9, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 5.232 / 11
p0 -1289 ± 22.51
p1 0.6398 ± 0.02061
0
-200
-400
-600
-800
0 200 400 600 800 1000 1200 1400 1600 1800 2000

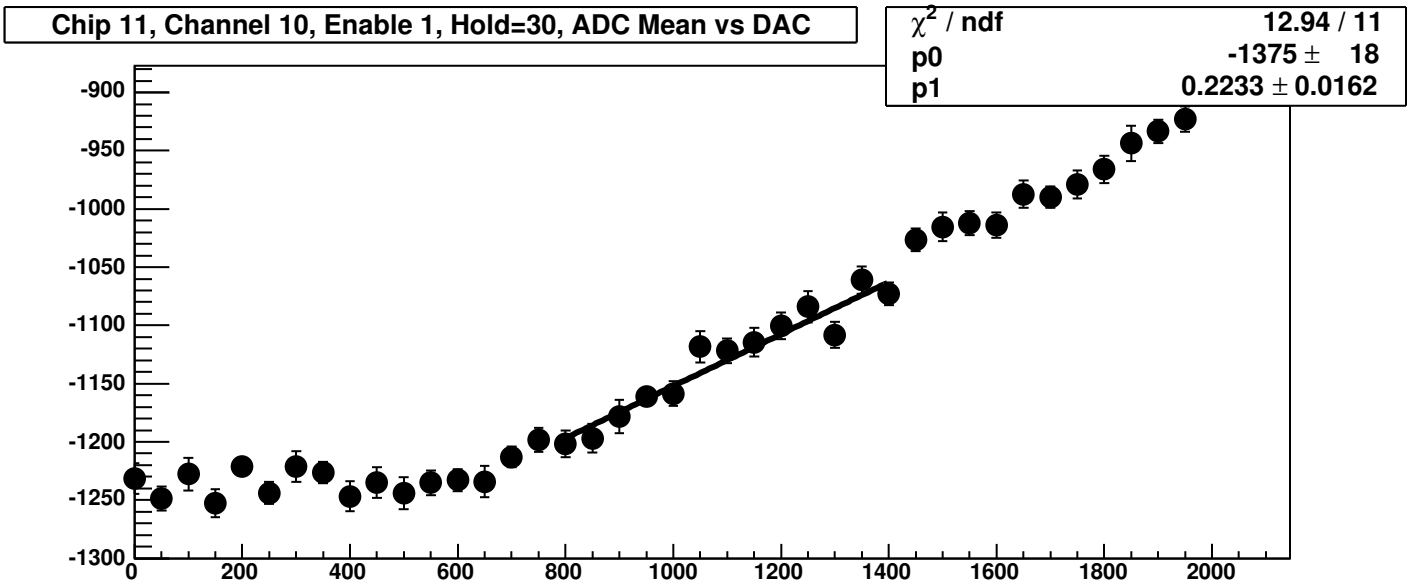

Chip 11, Channel 9, Enable 5, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0 200 400 600 800 1000 1200 1400 1600 1800 2000

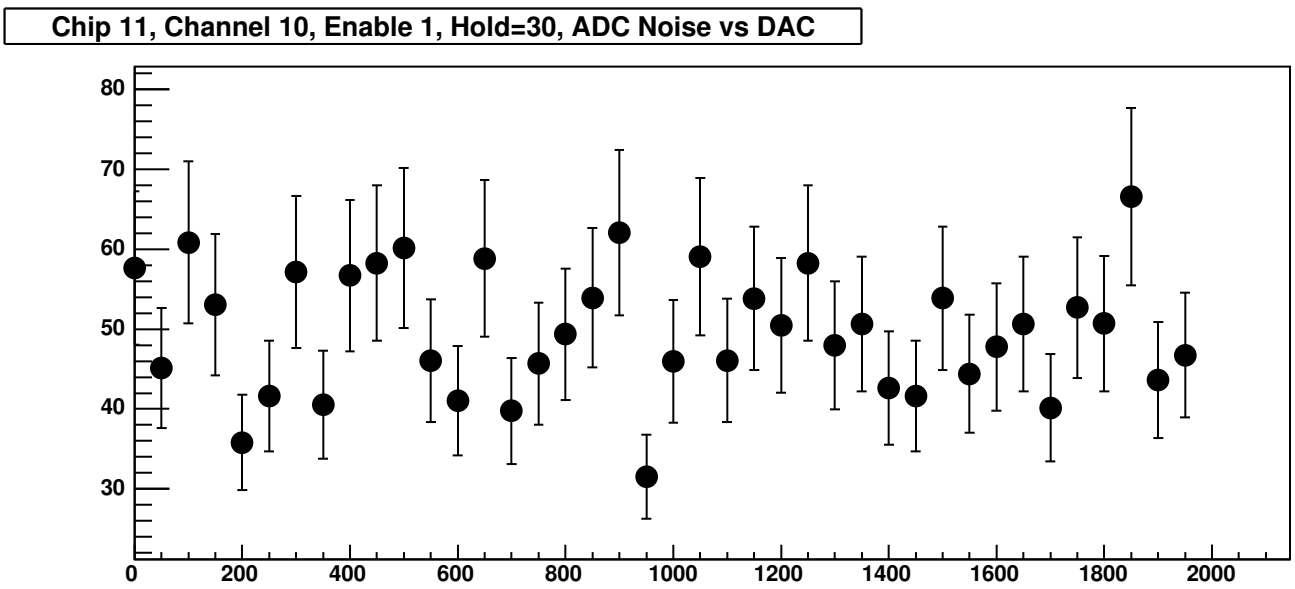

Chip 11, Channel 9, Enable 5, Hold=30, ADC Residuals vs DAC
400
300
200
100
0
0 200 400 600 800 1000 1200 1400 1600 1800 2000

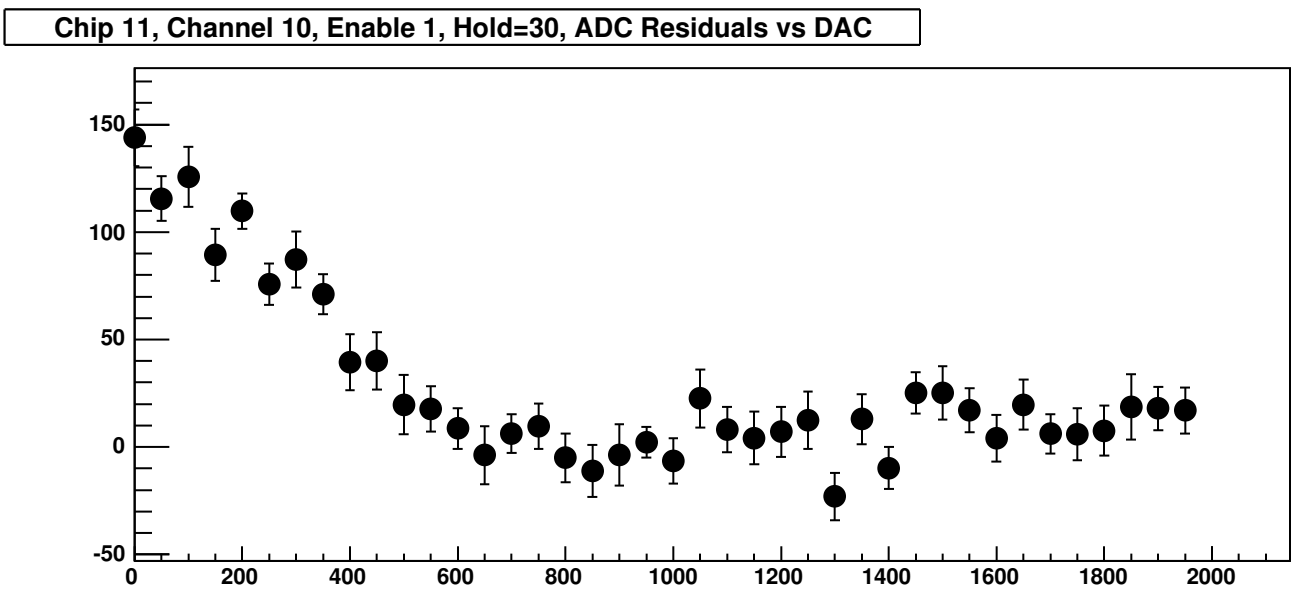

Chip 11, Channel 10, Enable 0!, Hold=30, ADC Mean vs DAC
χ² / ndf 182.6 / 11
p0 2.896e+04 ± 1.537
p1 -0.008037 ± 0.001422
28980
28975
28970
28965
28960
28955
28950
0
200
400
600
800
1000
1200
1400
1600
1800
2000

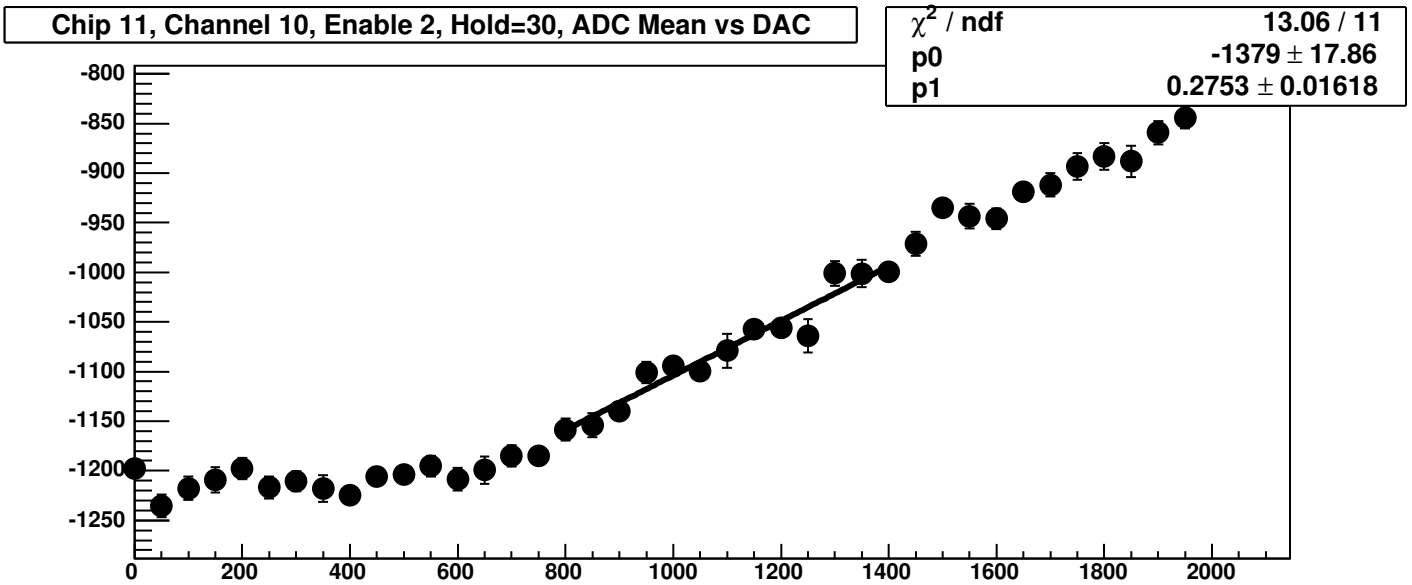

Chip 11, Channel 10, Enable 0!, Hold=30, ADC Noise vs DAC
7
6
5
4
3
2
0
200
400
600
800
1000
1200
1400
1600
1800
2000

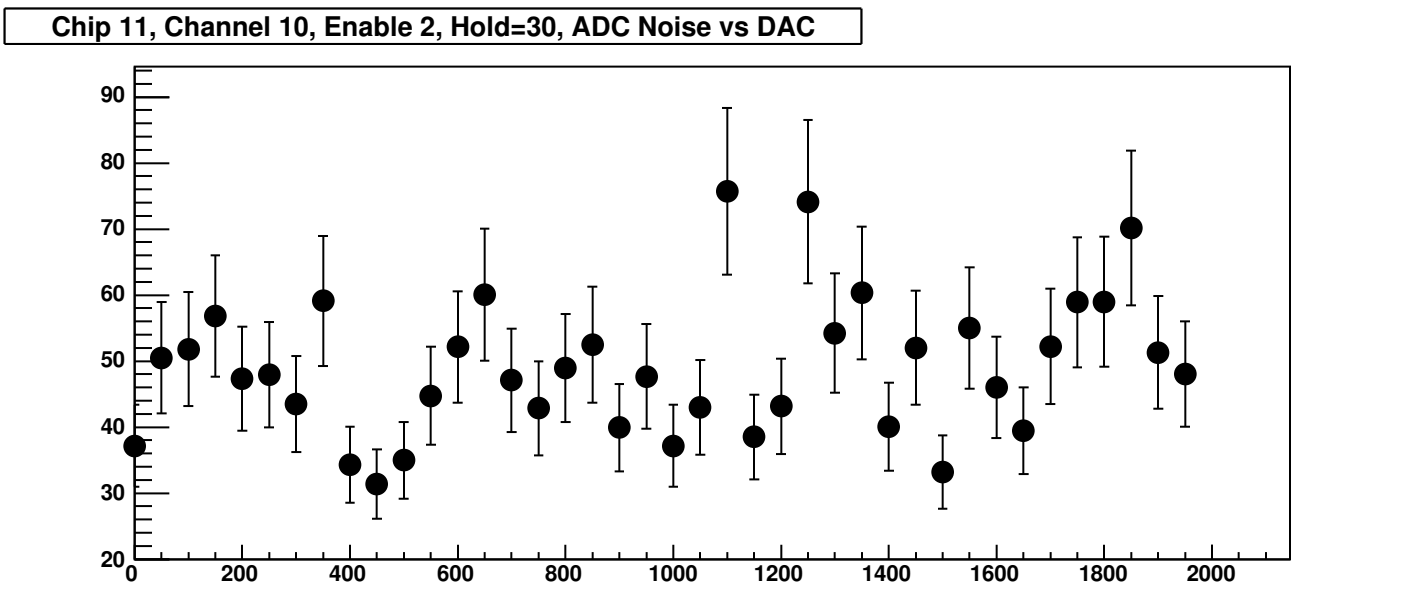

Chip 11, Channel 10, Enable 0!, Hold=30, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

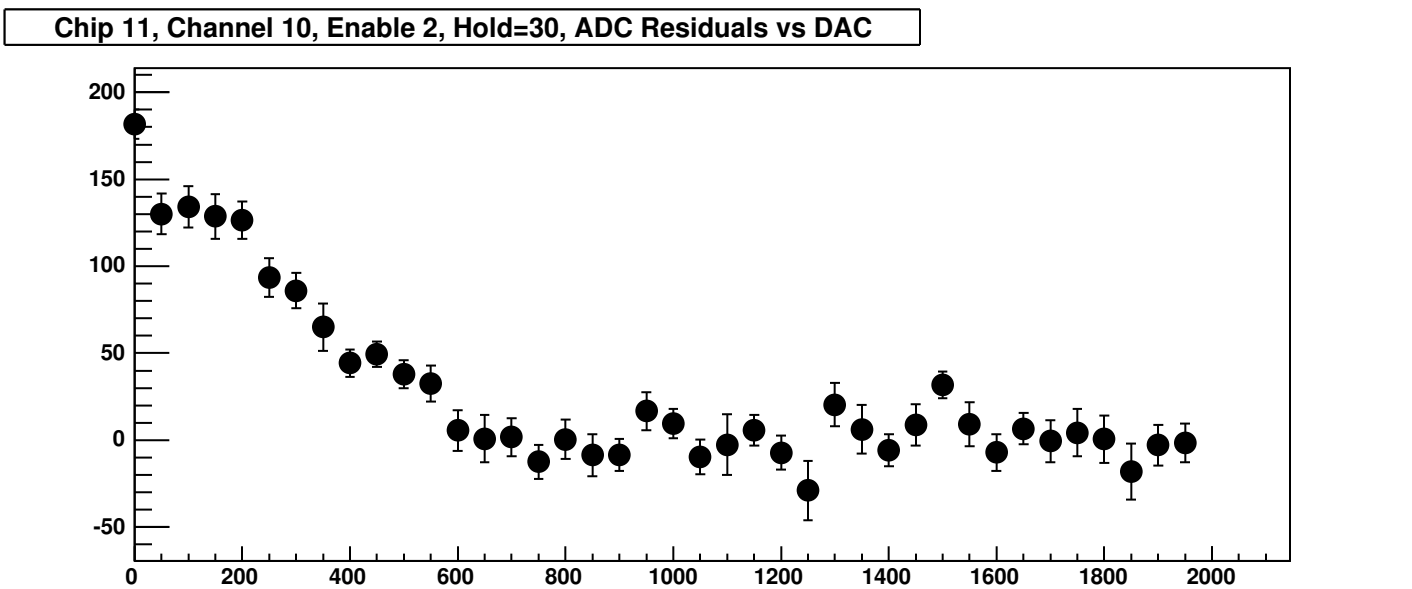

Chip 11, Channel 10, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 12.94 / 11
p0 -1375 ± 18
p1 0.2233 ± 0.0162
-900
-950
-1000
-1050
-1100
-1150
-1200
-1250
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 10, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

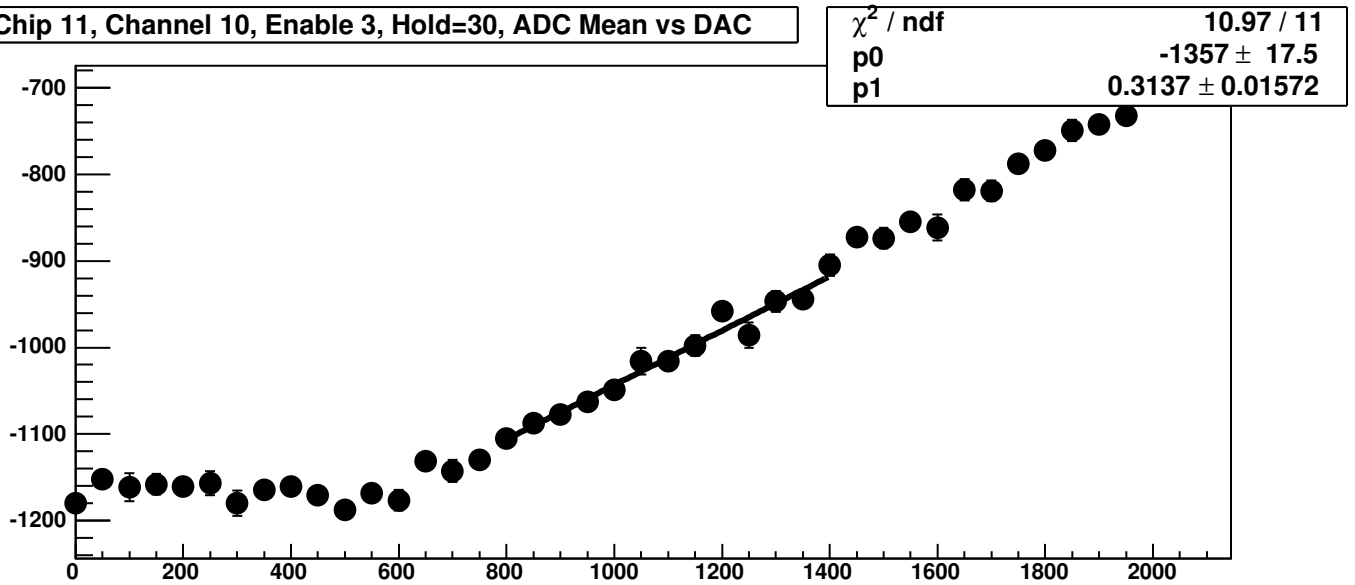

Chip 11, Channel 10, Enable 1, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 10, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 13.06 / 11
p0 -1379 ± 17.86
p1 0.2753 ± 0.01618
-800
-850
-900
-950
-1000
-1050
-1100
-1150
-1200
-1250
0
200
400
600
800
1000
1200
1400
1600
1800
2000

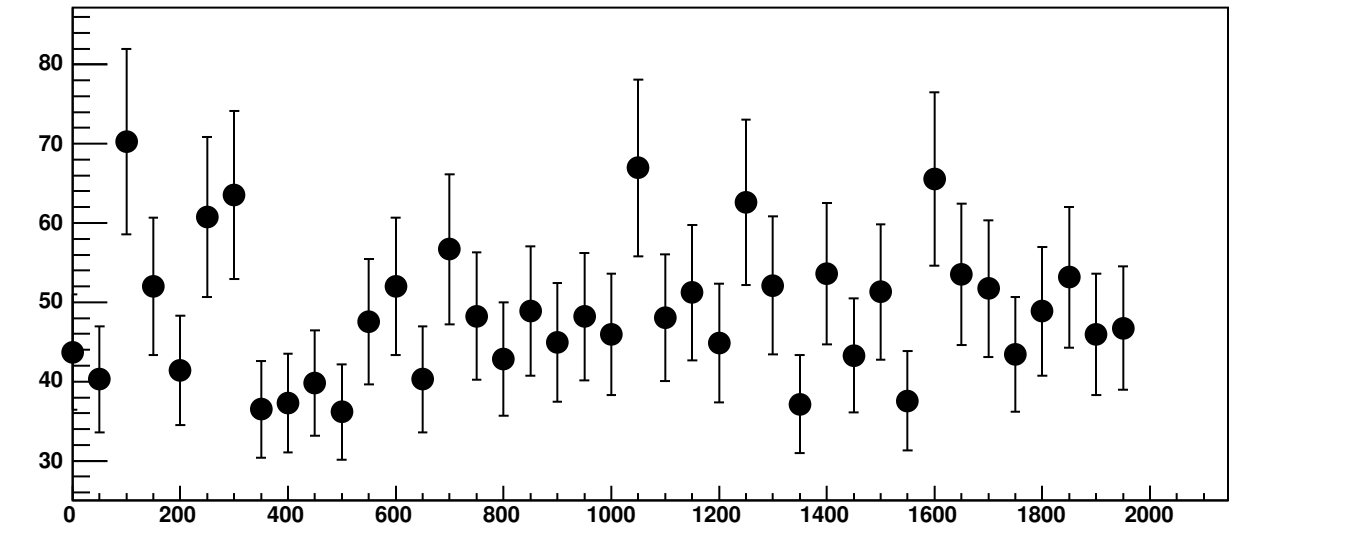

Chip 11, Channel 10, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 10, Enable 2, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

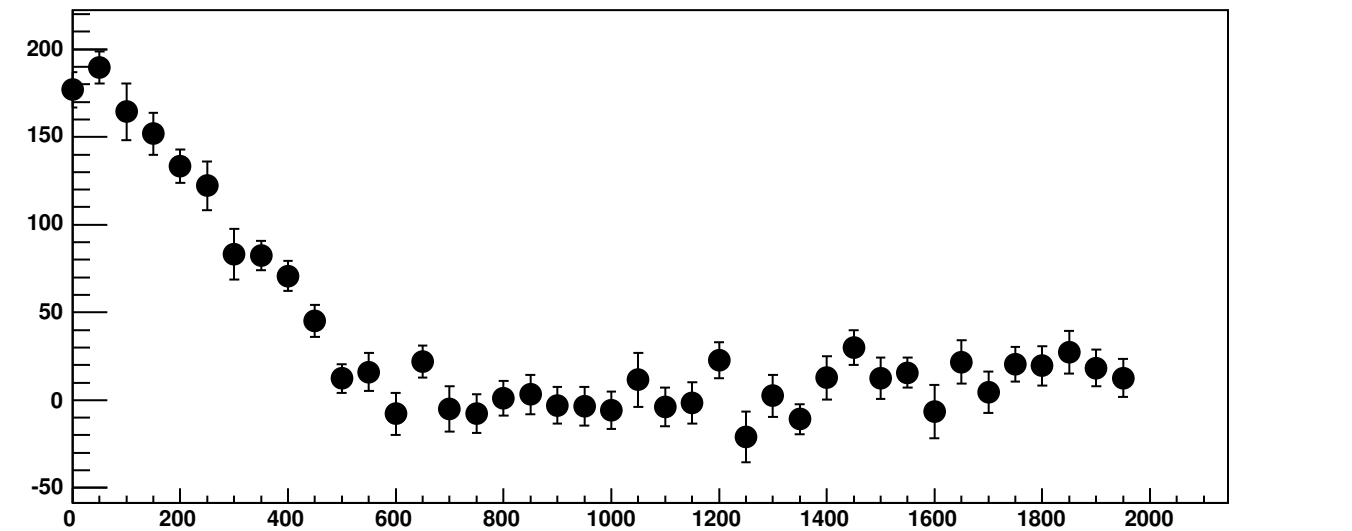
Chip 11, Channel 10, Enable 3, Hold=30, ADC Mean vs DAC

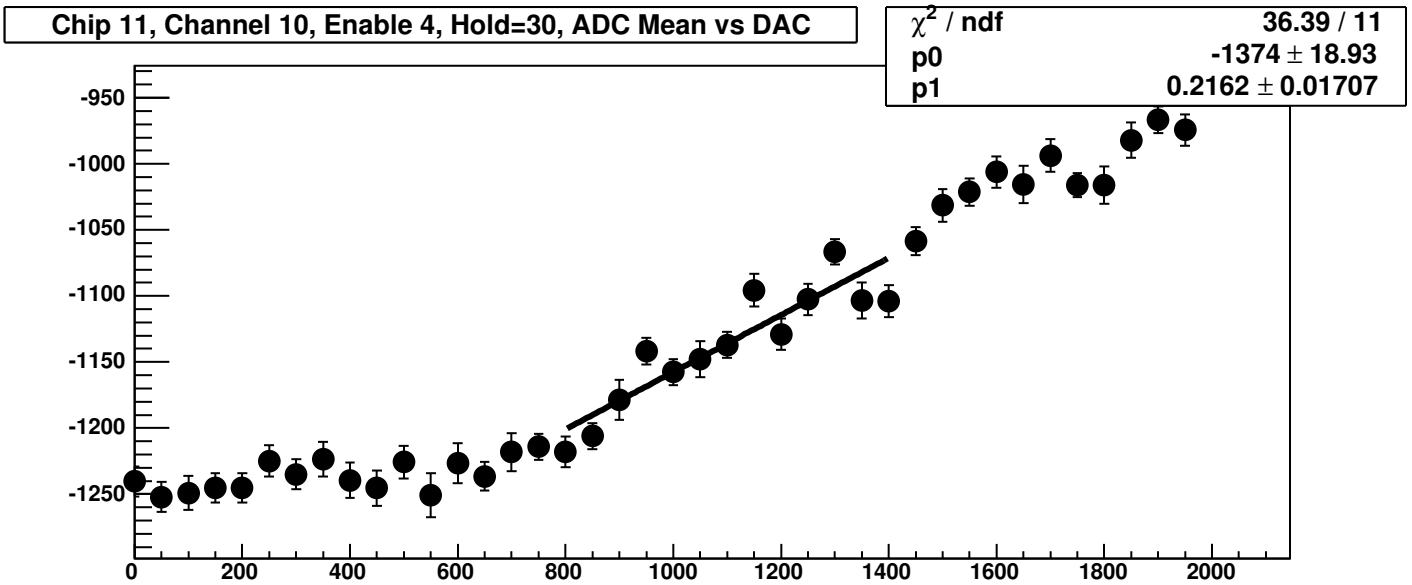


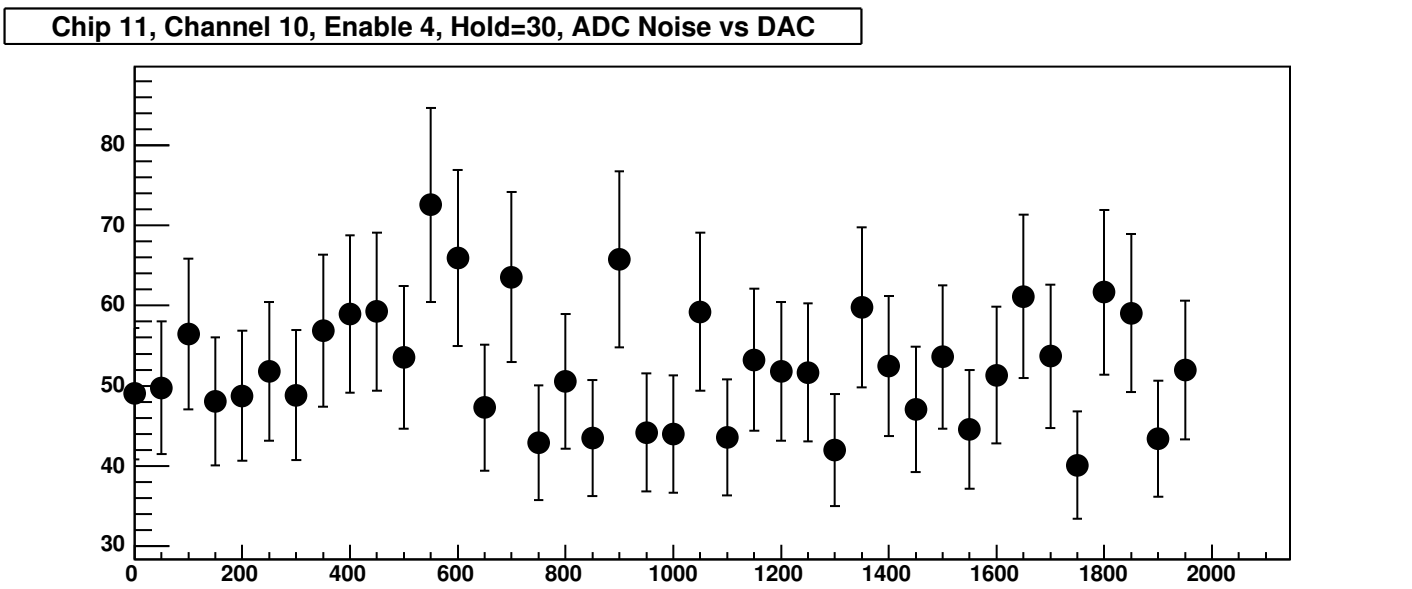
Chip 11, Channel 10, Enable 3, Hold=30, ADC Noise vs DAC

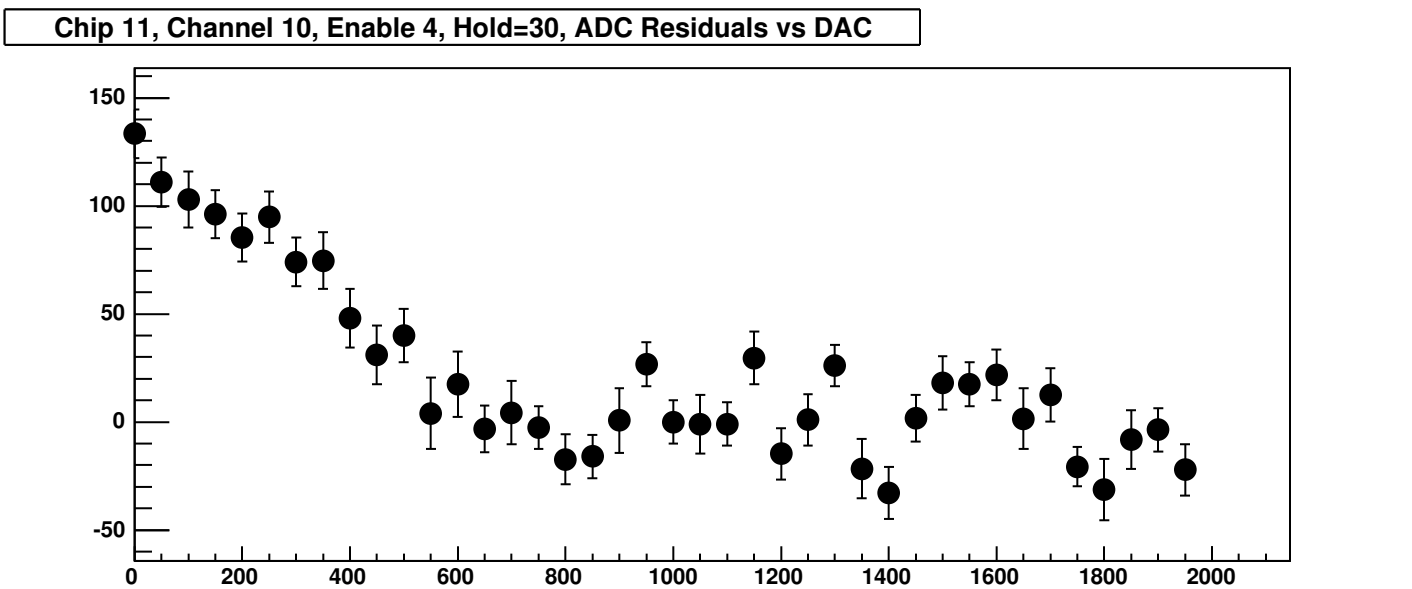


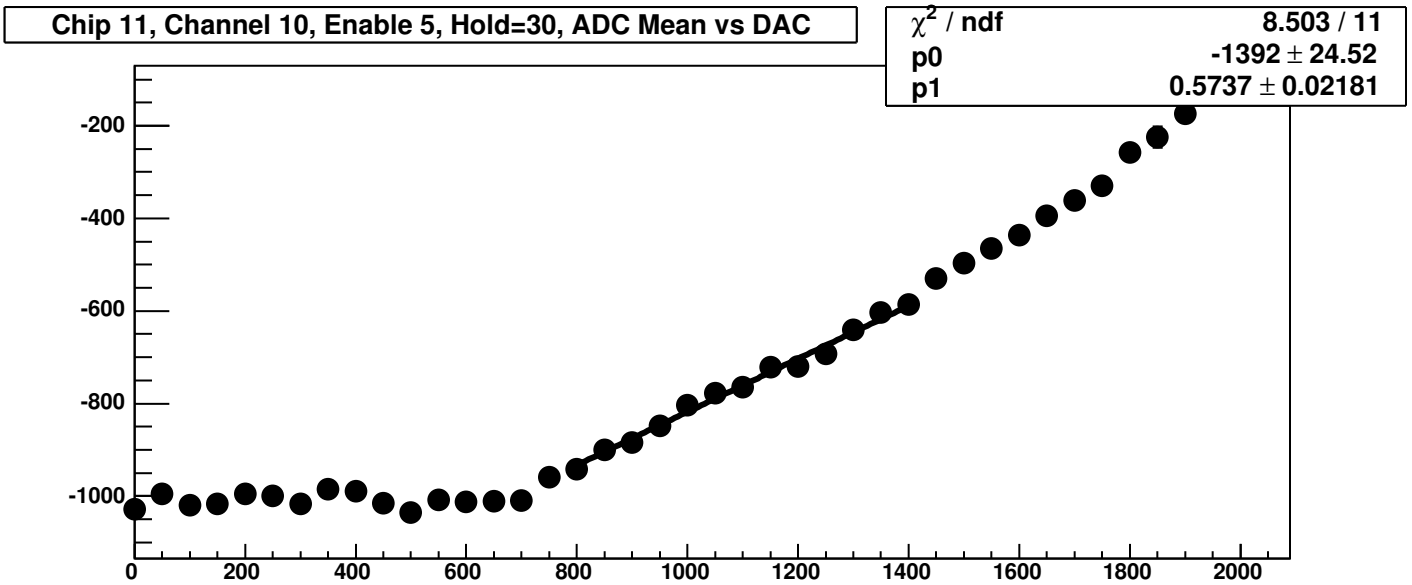
Chip 11, Channel 10, Enable 3, Hold=30, ADC Residuals vs DAC

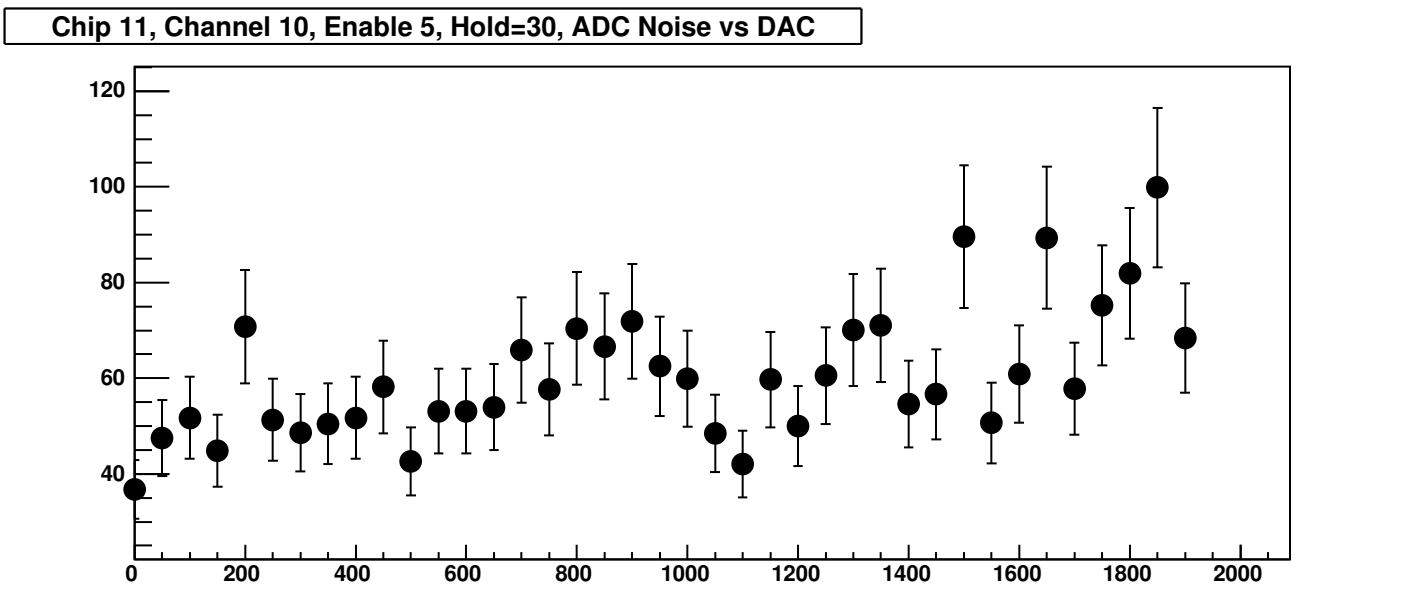


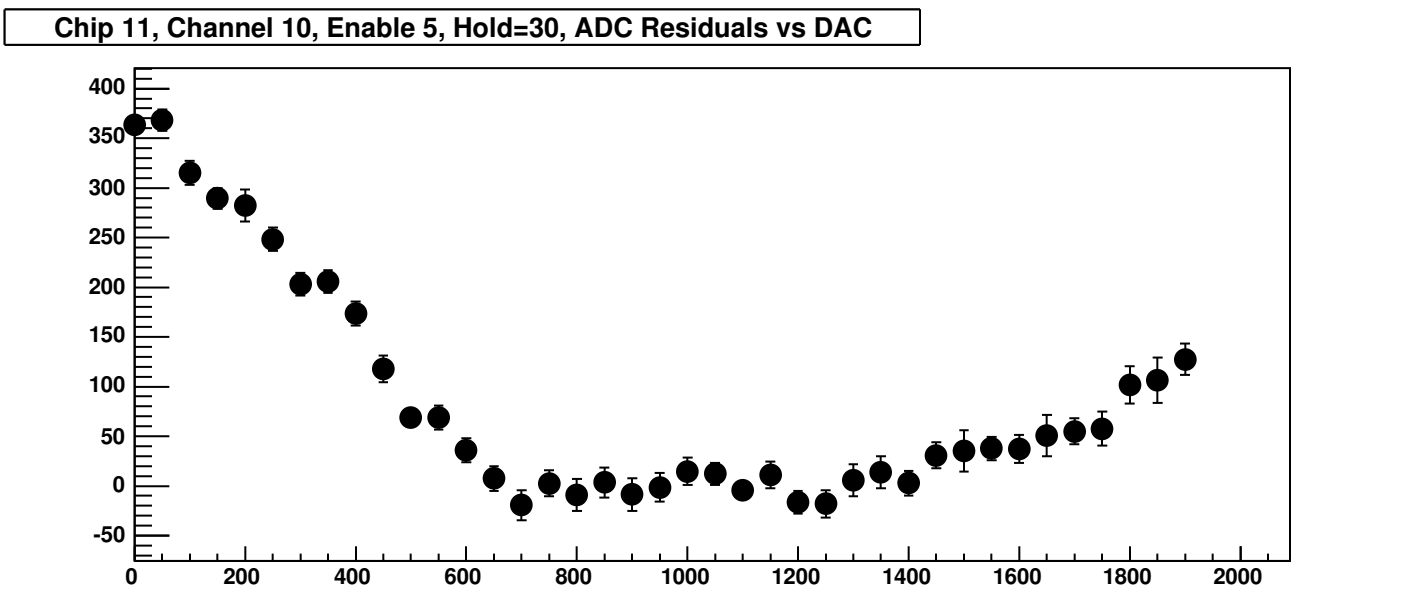

Chip 11, Channel 10, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 36.39 / 11
p0 -1374 ± 18.93
p1 0.2162 ± 0.01707
-950
-1000
-1050
-1100
-1150
-1200
-1250
0
200
400
600
800
1000
1200
1400
1600
1800
2000

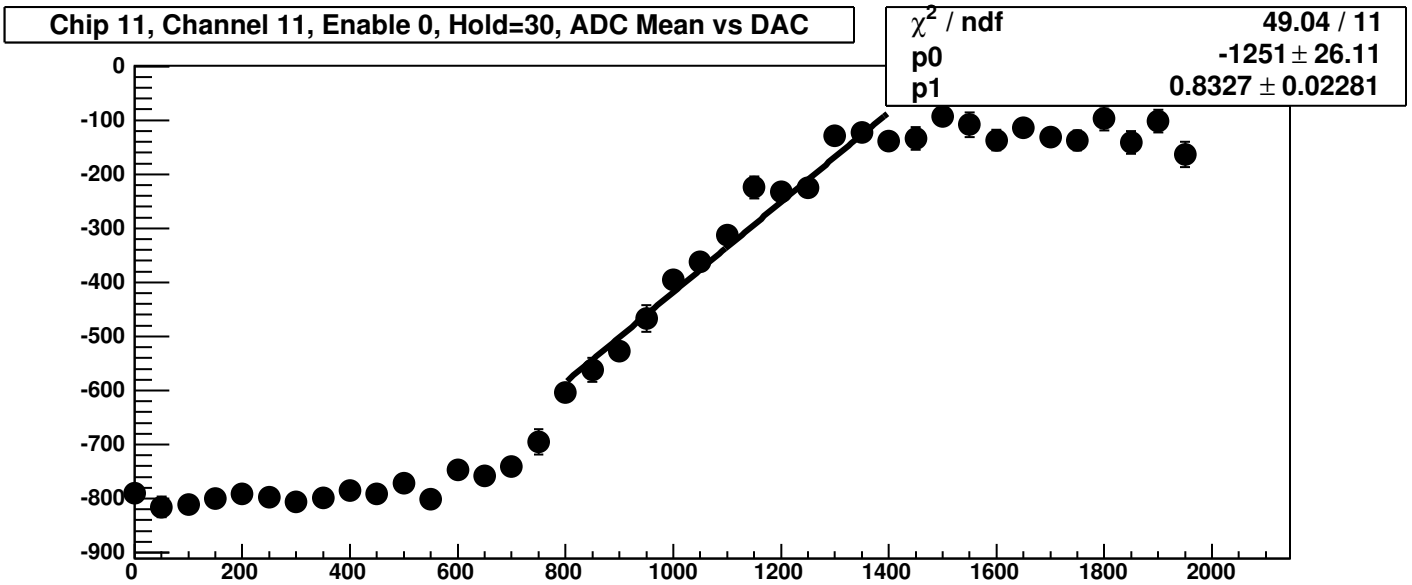

Chip 11, Channel 10, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

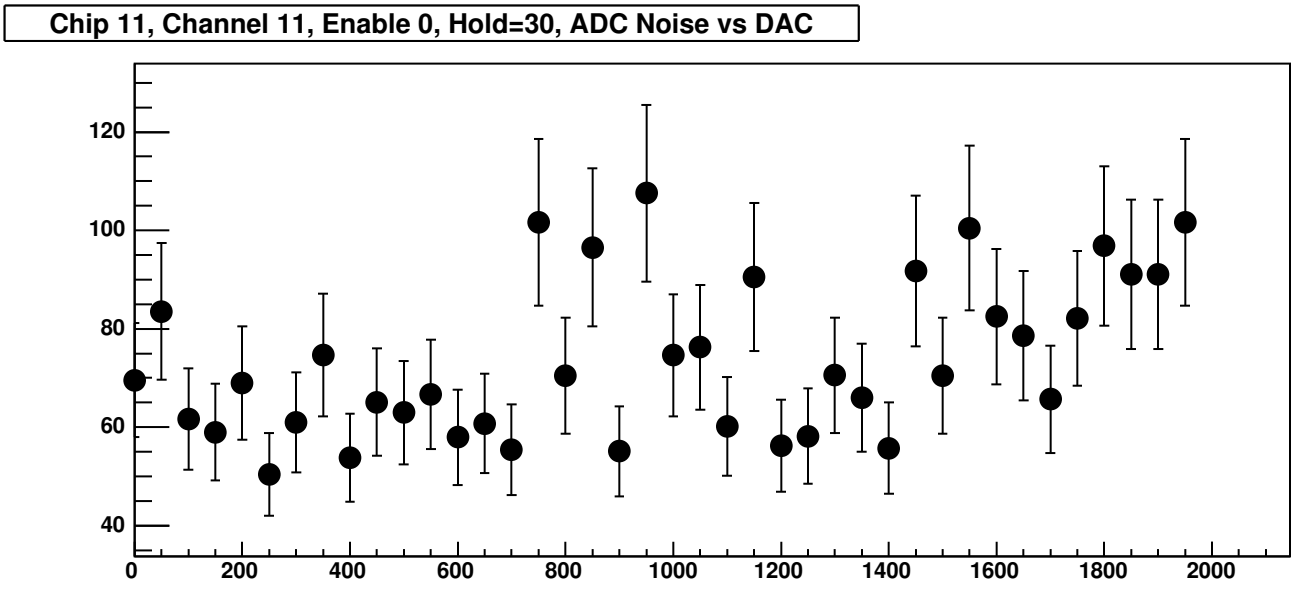

Chip 11, Channel 10, Enable 4, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

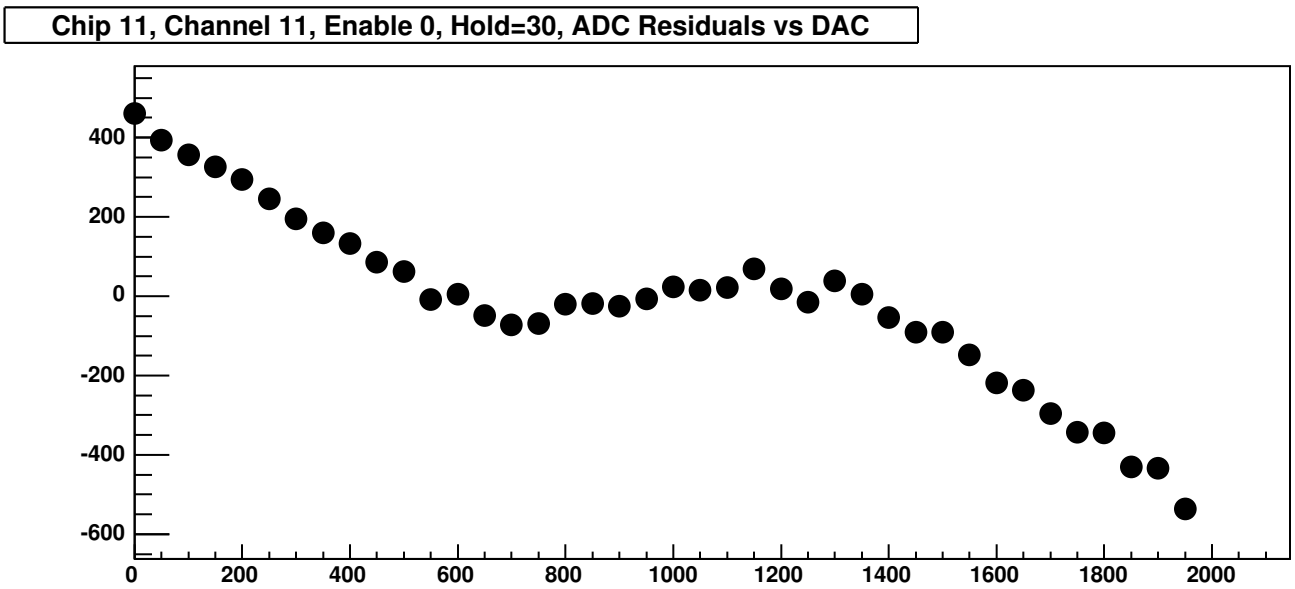

Chip 11, Channel 10, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 8.503 / 11
p0 -1392 ± 24.52
p1 0.5737 ± 0.02181
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

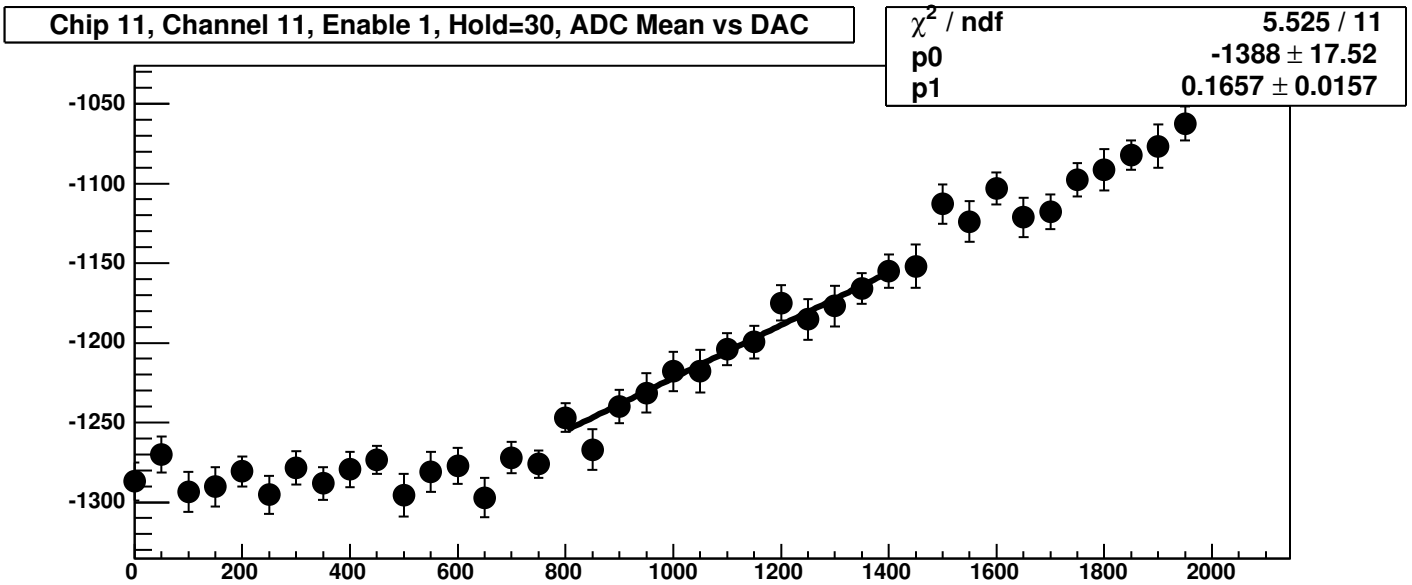

Chip 11, Channel 10, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

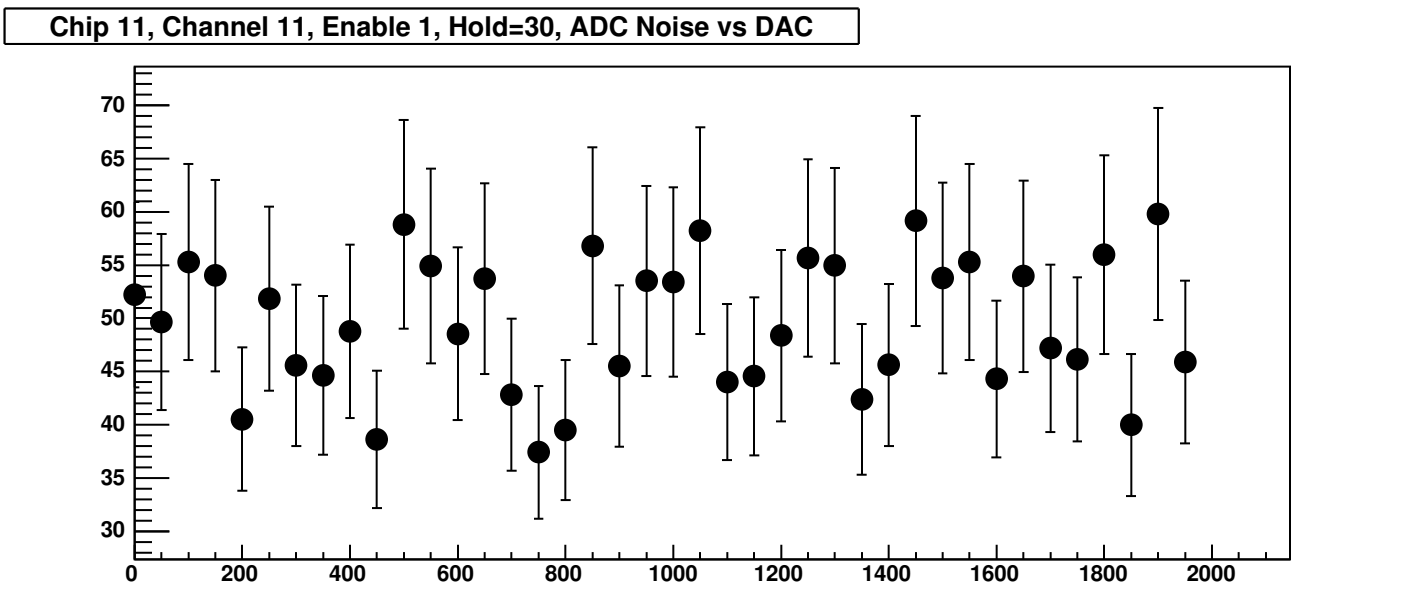

Chip 11, Channel 10, Enable 5, Hold=30, ADC Residuals vs DAC
400
350
300
250
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

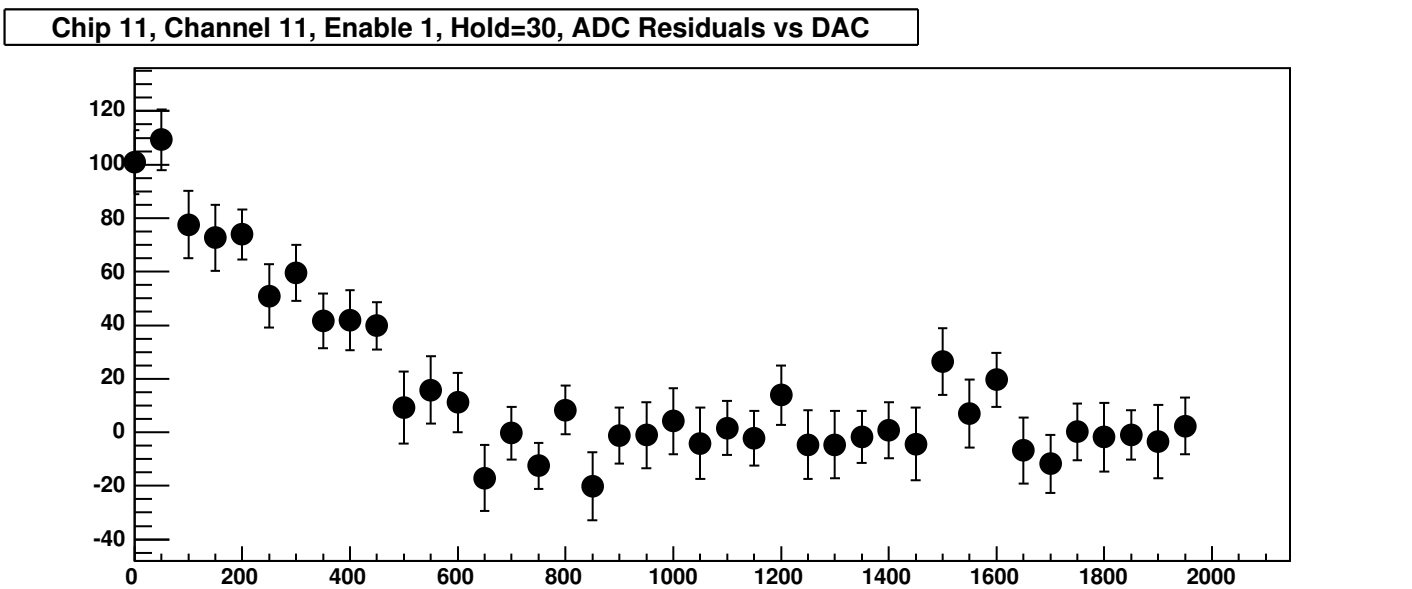

Chip 11, Channel 11, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 49.04 / 11
p0 -1251 ± 26.11
p1 0.8327 ± 0.02281
0
-100
-200
-300
-400
-500
-600
-700
-800
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

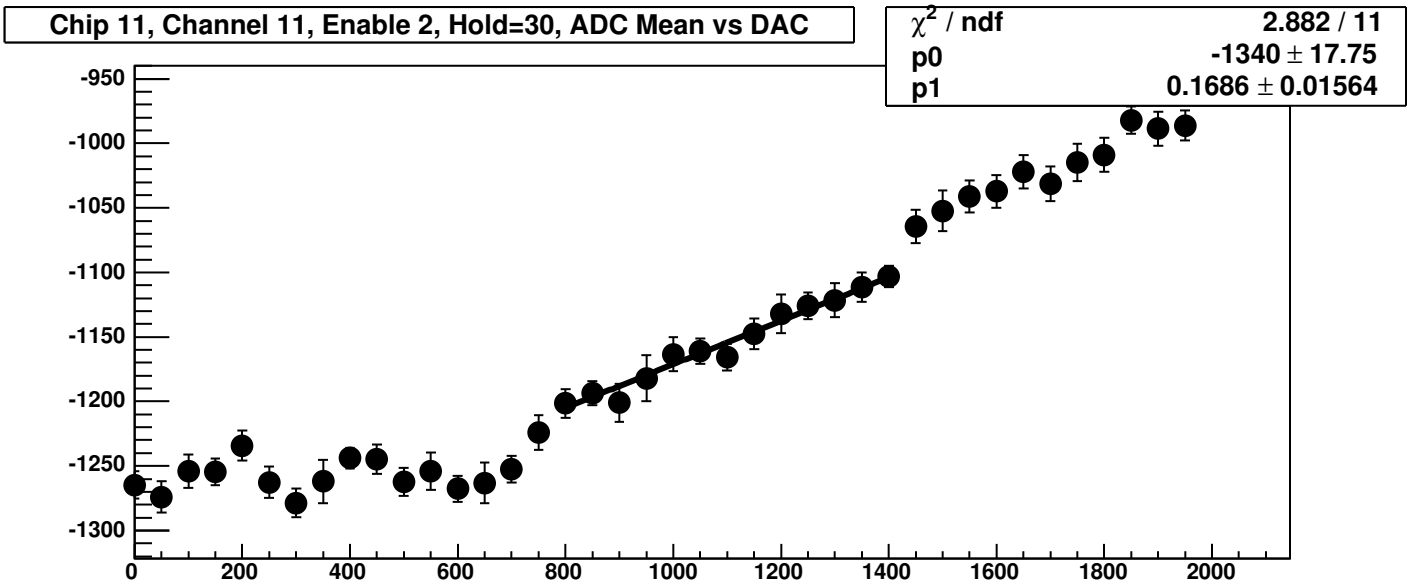

Chip 11, Channel 11, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

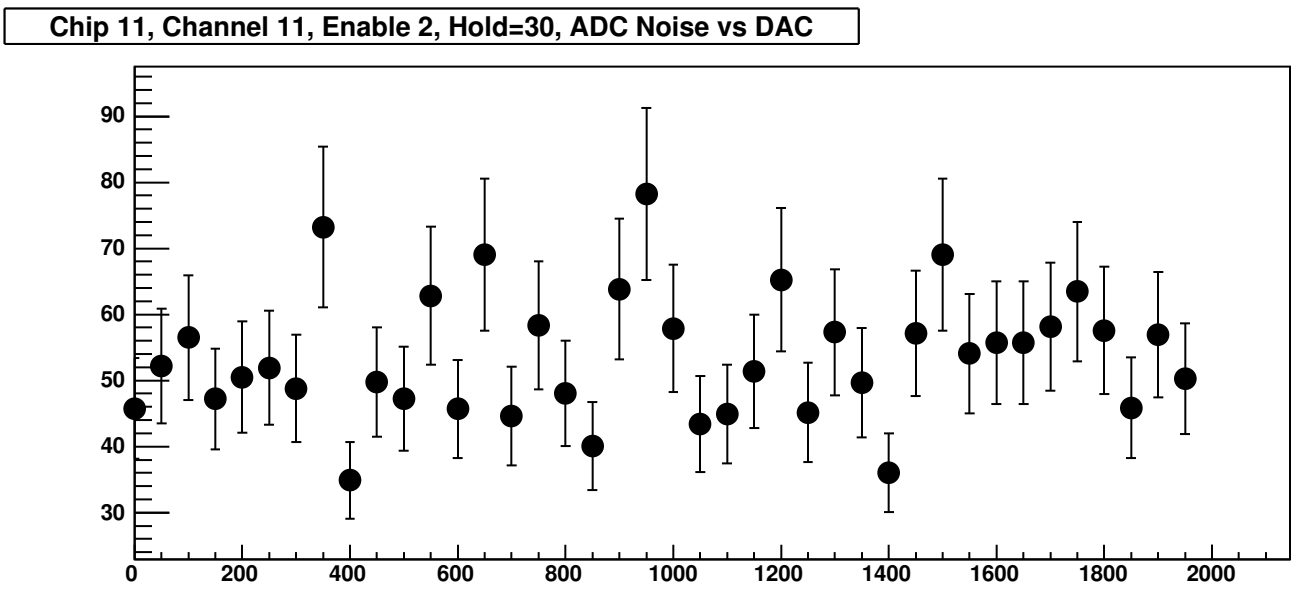

Chip 11, Channel 11, Enable 0, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
0
200
400
600
800
1000
1200
1400
1600
1800
2000

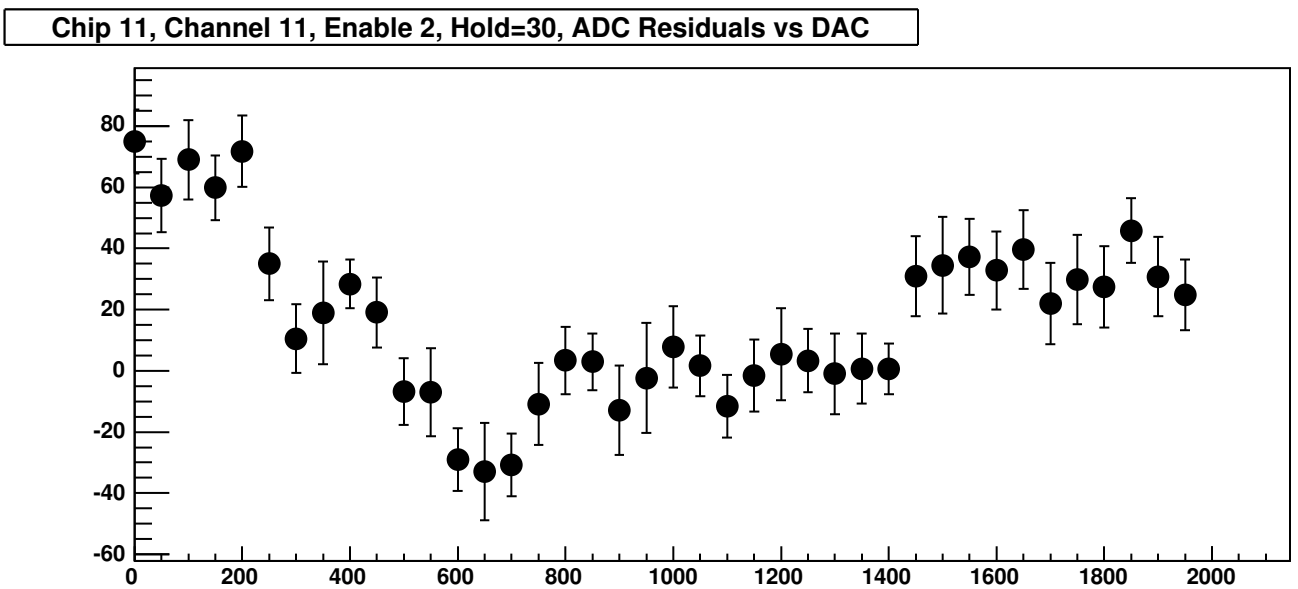

Chip 11, Channel 11, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 5.525 / 11
p0 -1388 ± 17.52
p1 0.1657 ± 0.0157
-1050
-1100
-1150
-1200
-1250
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

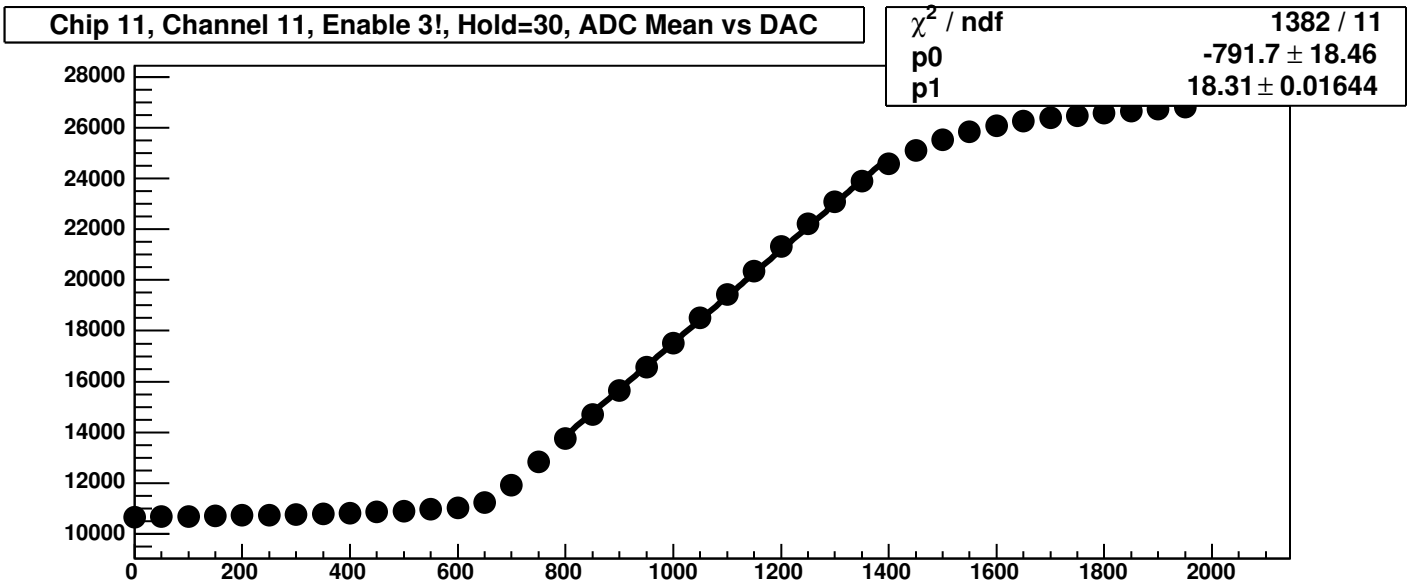

Chip 11, Channel 11, Enable 1, Hold=30, ADC Noise vs DAC
70
65
60
55
50
45
40
35
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

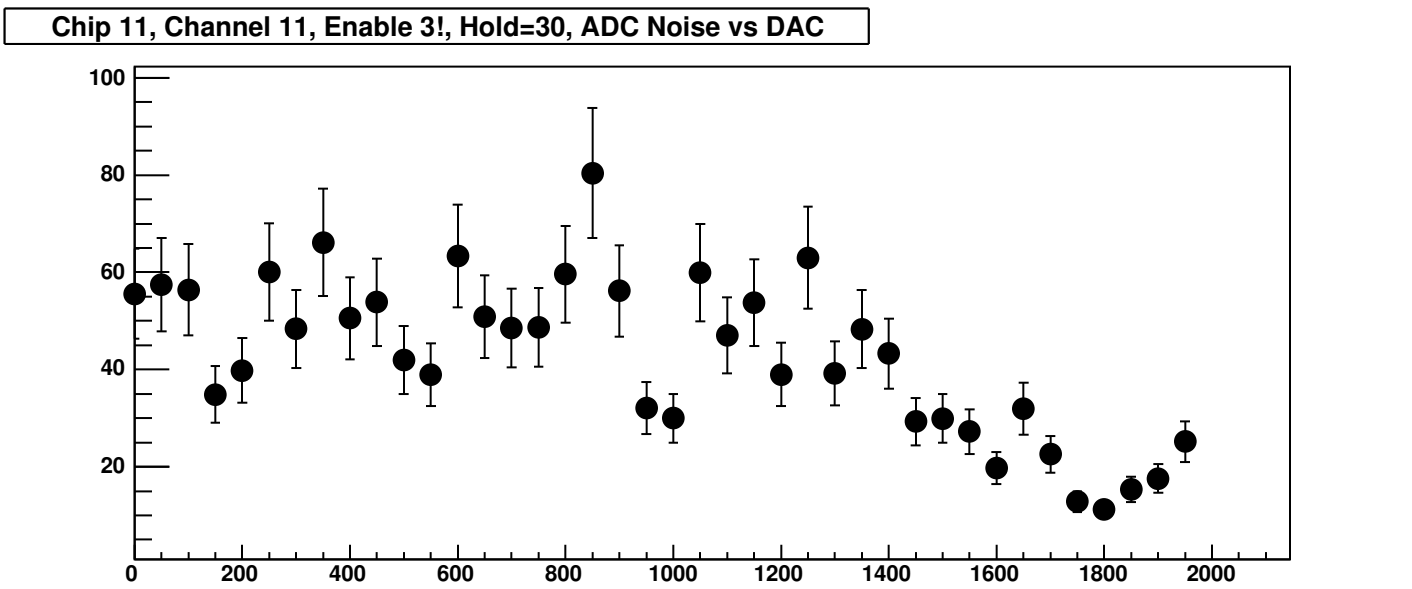

Chip 11, Channel 11, Enable 1, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

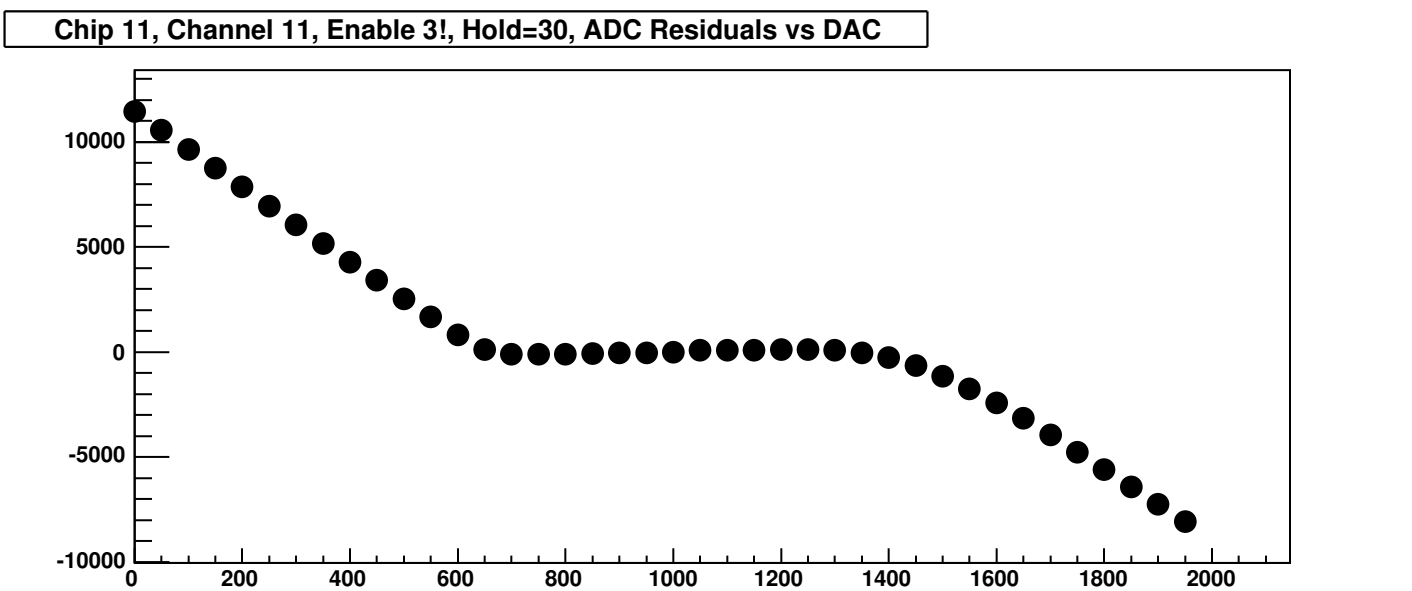

Chip 11, Channel 11, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 2.882 / 11
p0 -1340 ± 17.75
p1 0.1686 ± 0.01564
-950
-1000
-1050
-1100
-1150
-1200
-1250
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

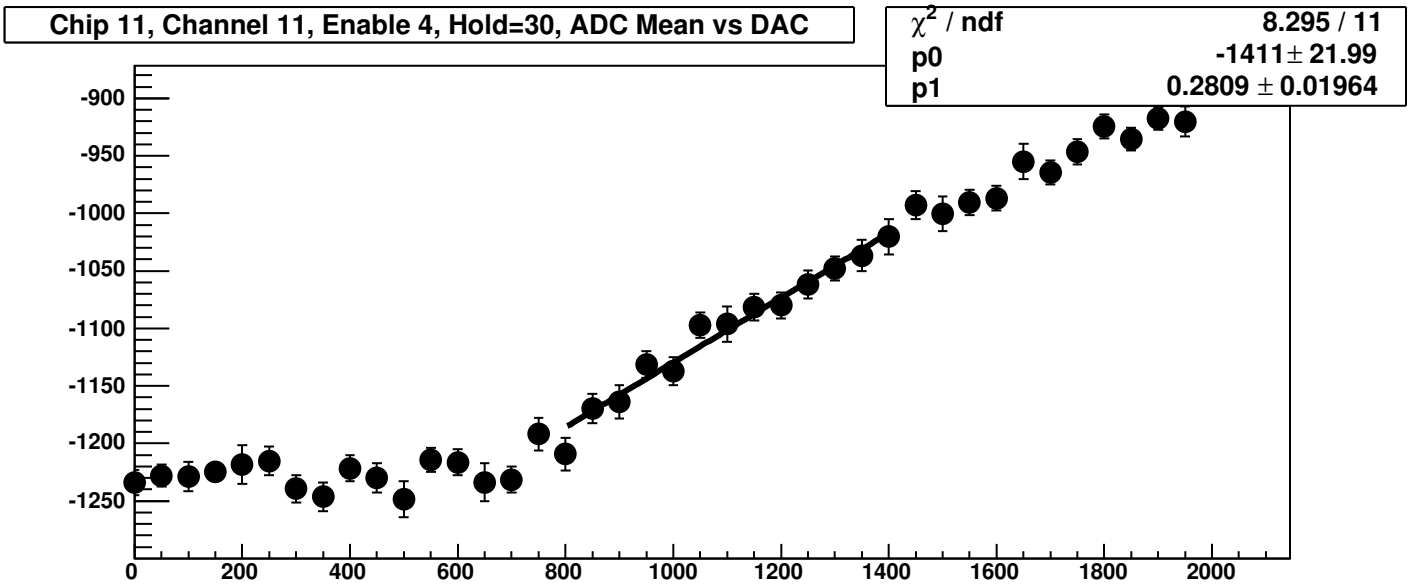

Chip 11, Channel 11, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

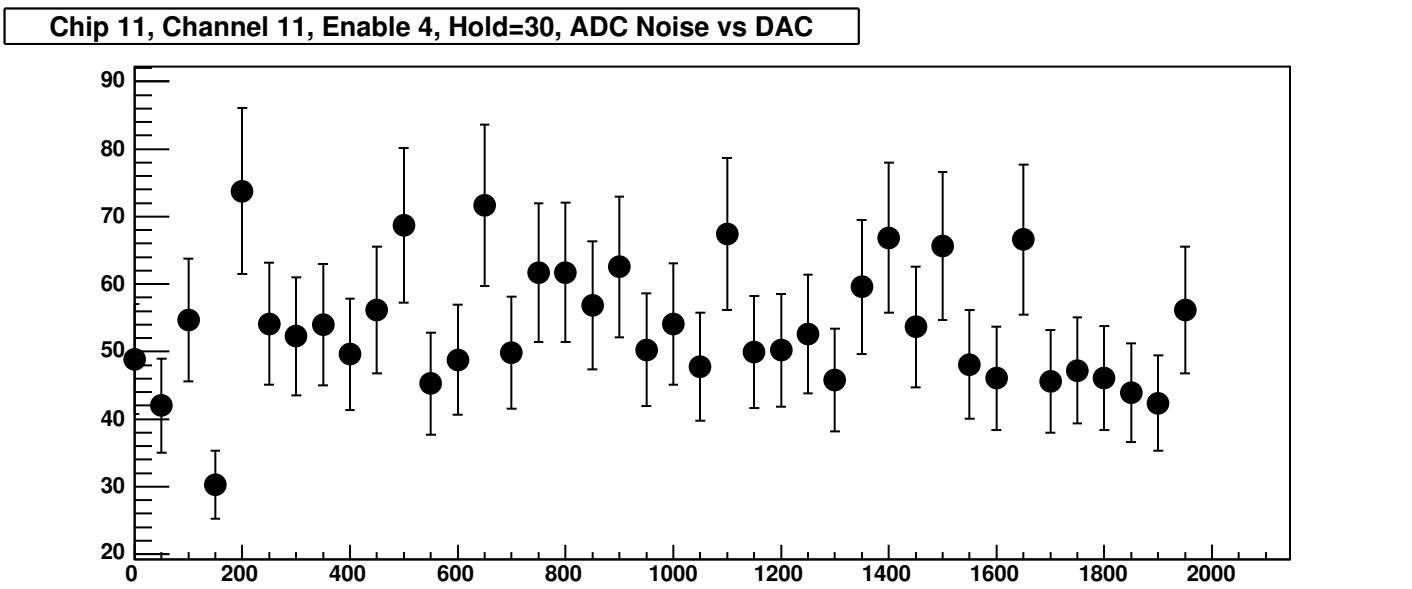

Chip 11, Channel 11, Enable 2, Hold=30, ADC Residuals vs DAC
80
60
40
20
0
-20
-40
-60
0
200
400
600
800
1000
1200
1400
1600
1800
2000

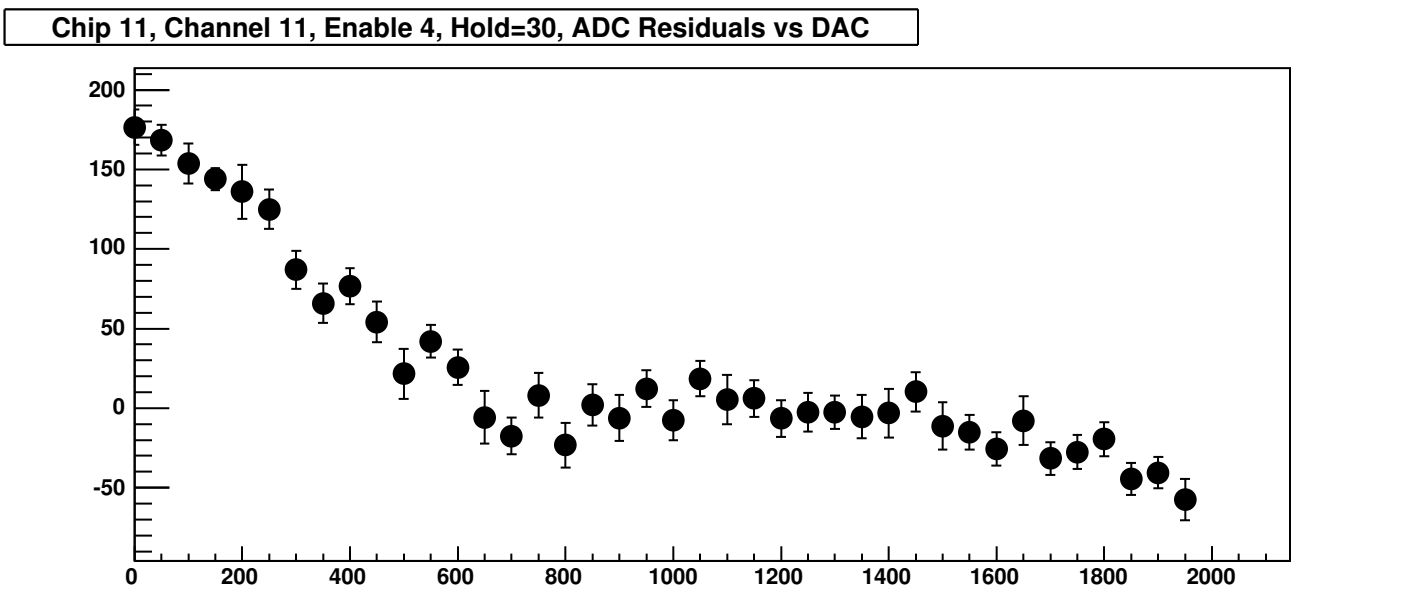

Chip 11, Channel 11, Enable 3!, Hold=30, ADC Mean vs DAC
χ² / ndf 1382 / 11
p0 -791.7 ± 18.46
p1 18.31 ± 0.01644
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

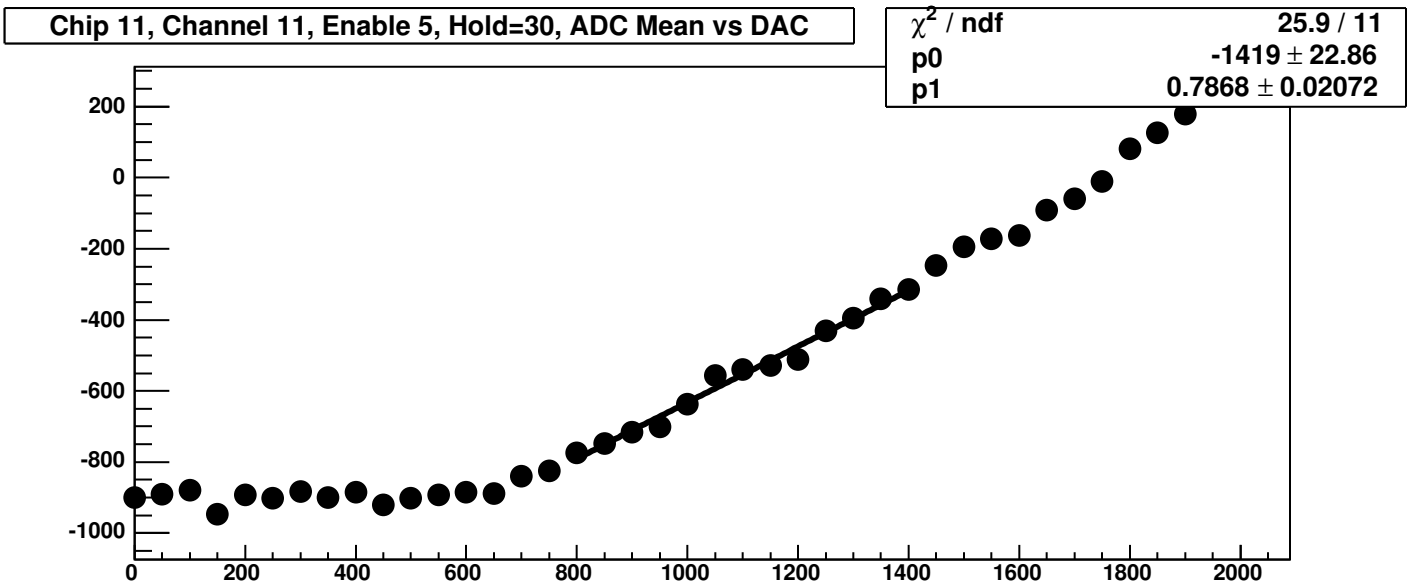

Chip 11, Channel 11, Enable 3!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

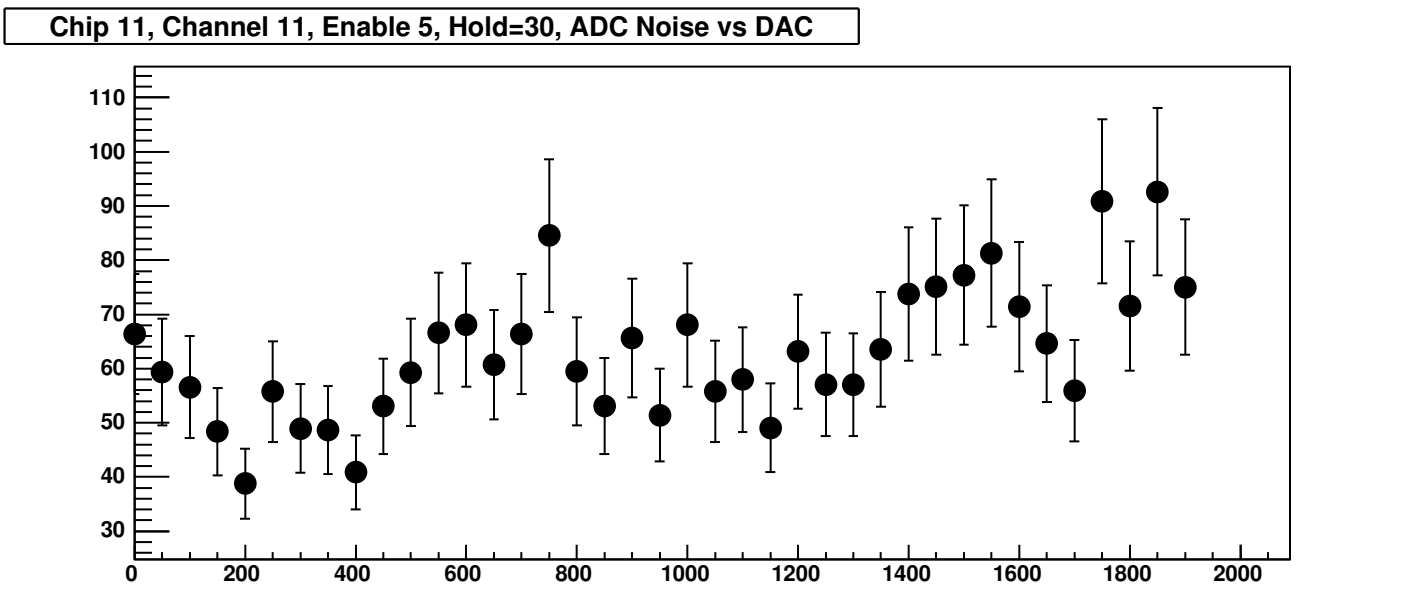

Chip 11, Channel 11, Enable 3!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

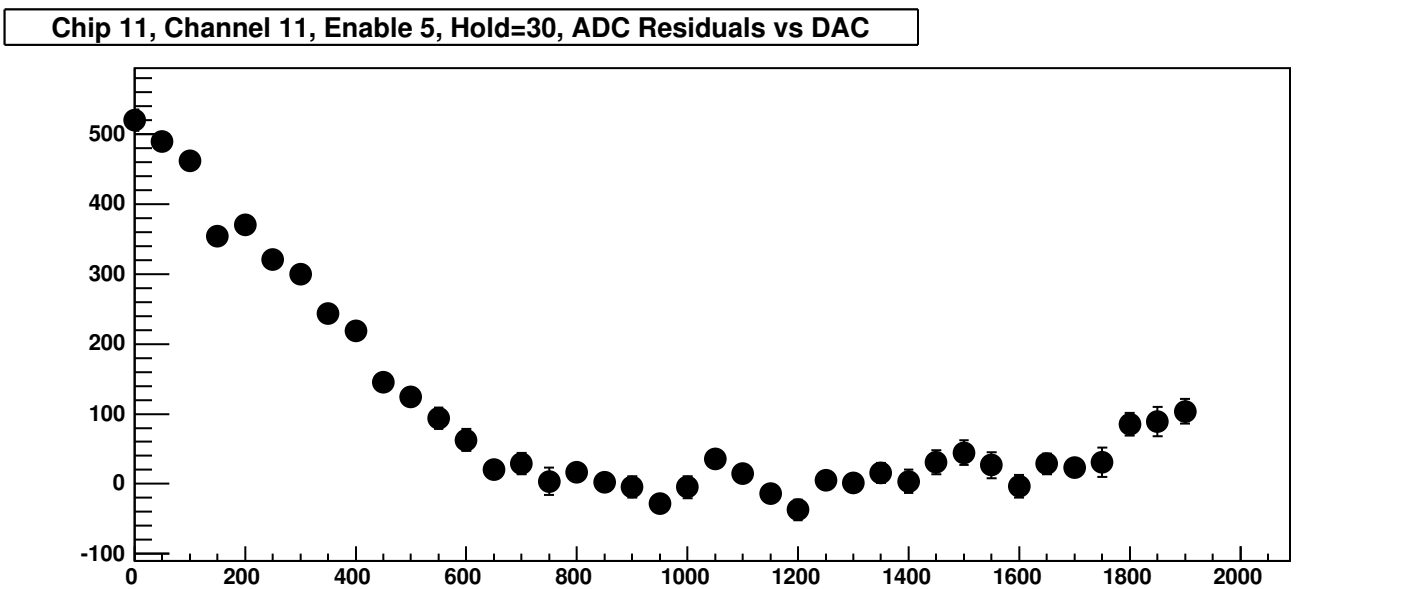

Chip 11, Channel 11, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 8.295 / 11
p0 -1411 ± 21.99
p1 0.2809 ± 0.01964
-900
-950
-1000
-1050
-1100
-1150
-1200
-1250
0
200
400
600
800
1000
1200
1400
1600
1800
2000

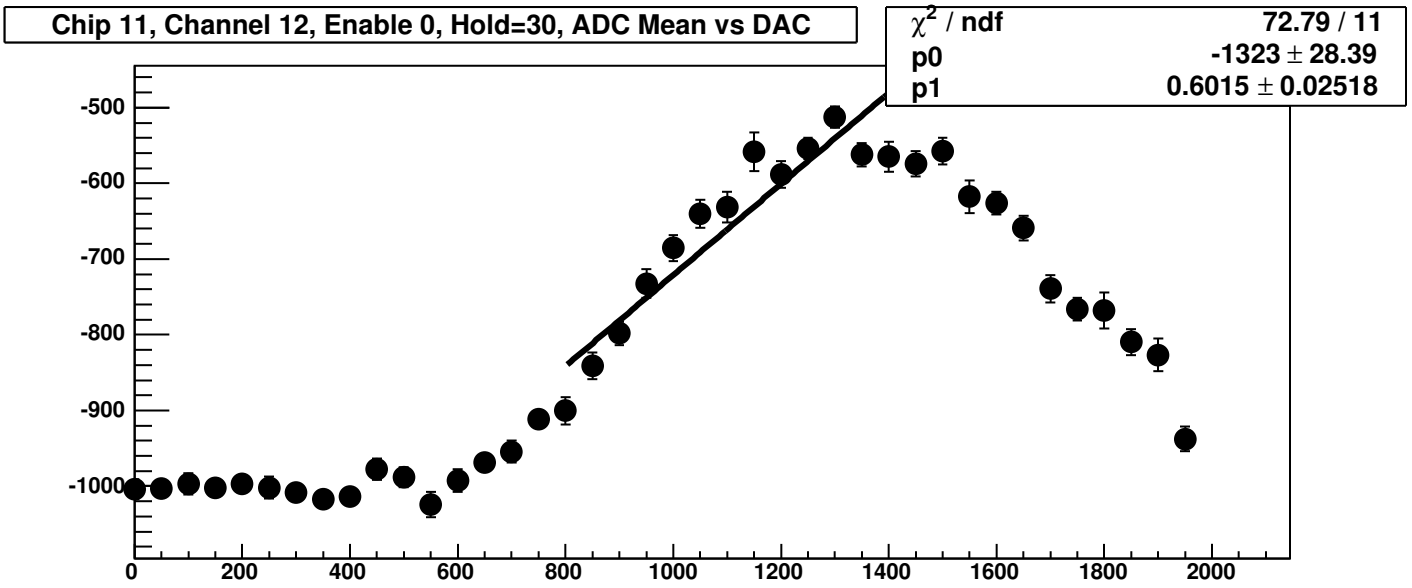

Chip 11, Channel 11, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

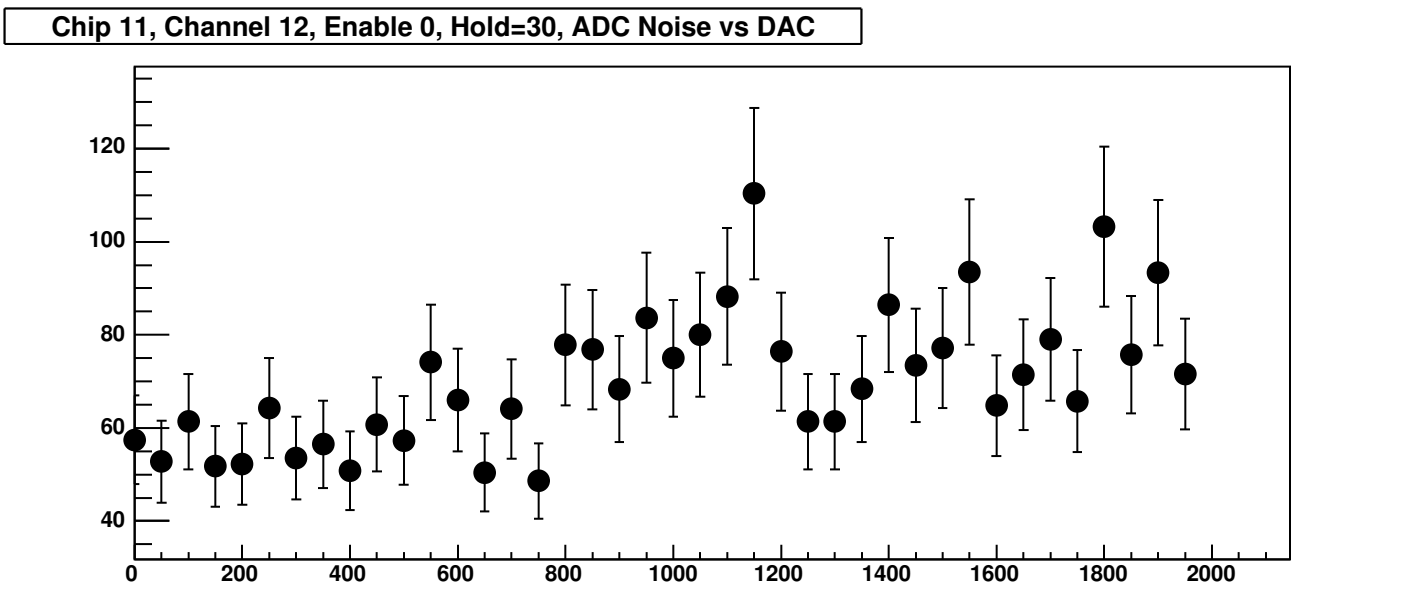

Chip 11, Channel 11, Enable 4, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

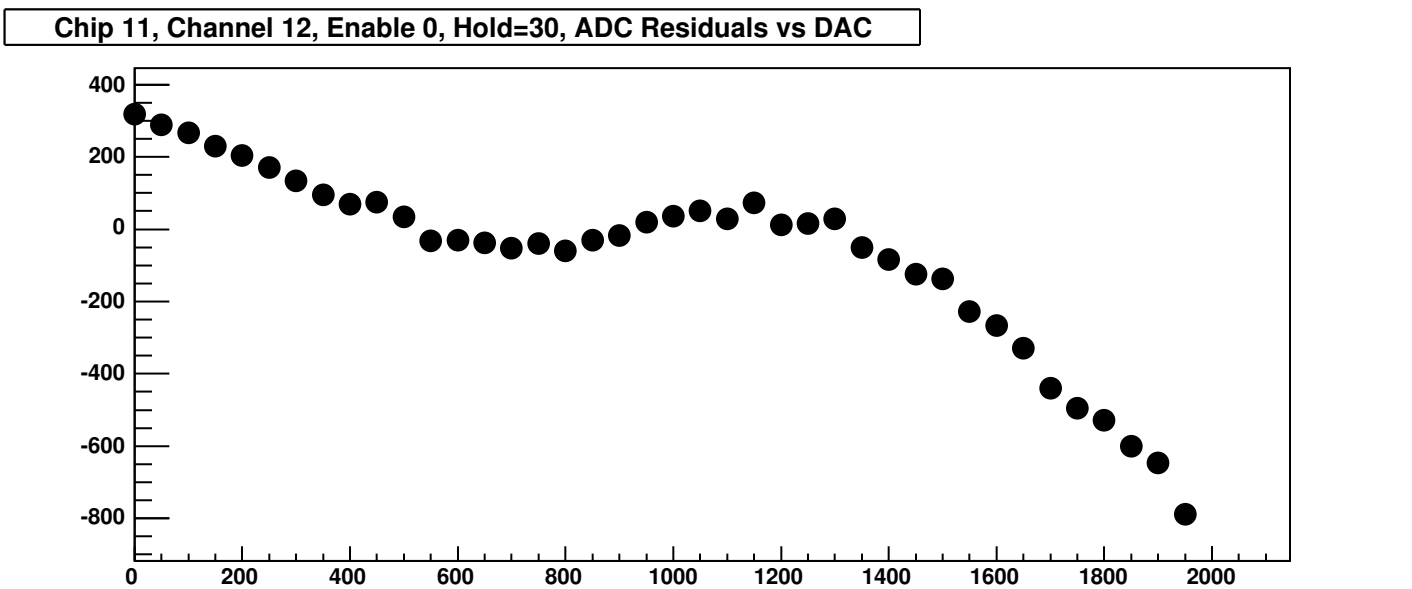

Chip 11, Channel 11, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 25.9 / 11
p0 -1419 ± 22.86
p1 0.7868 ± 0.02072
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 11, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

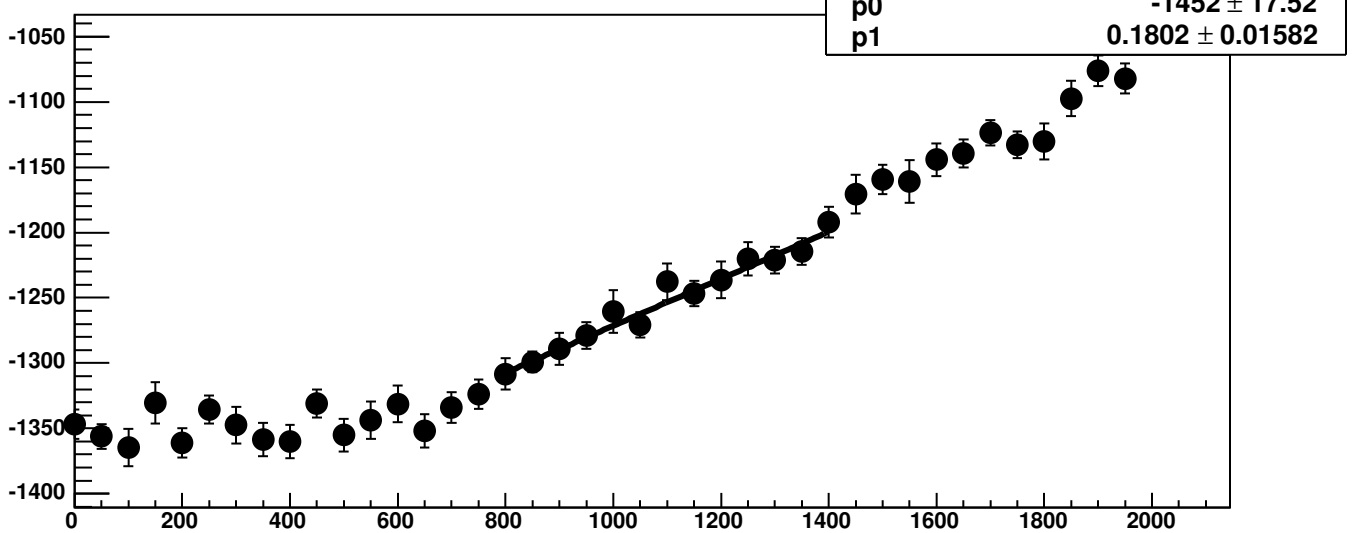

Chip 11, Channel 11, Enable 5, Hold=30, ADC Residuals vs DAC
500
400
300
200
100
0
-100
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 12, Enable 0, Hold=30, ADC Mean vs DAC
χ^2 / ndf 72.79 / 11
p0 -1323 ± 28.39
p1 0.6015 ± 0.02518
-500
-600
-700
-800
-900
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

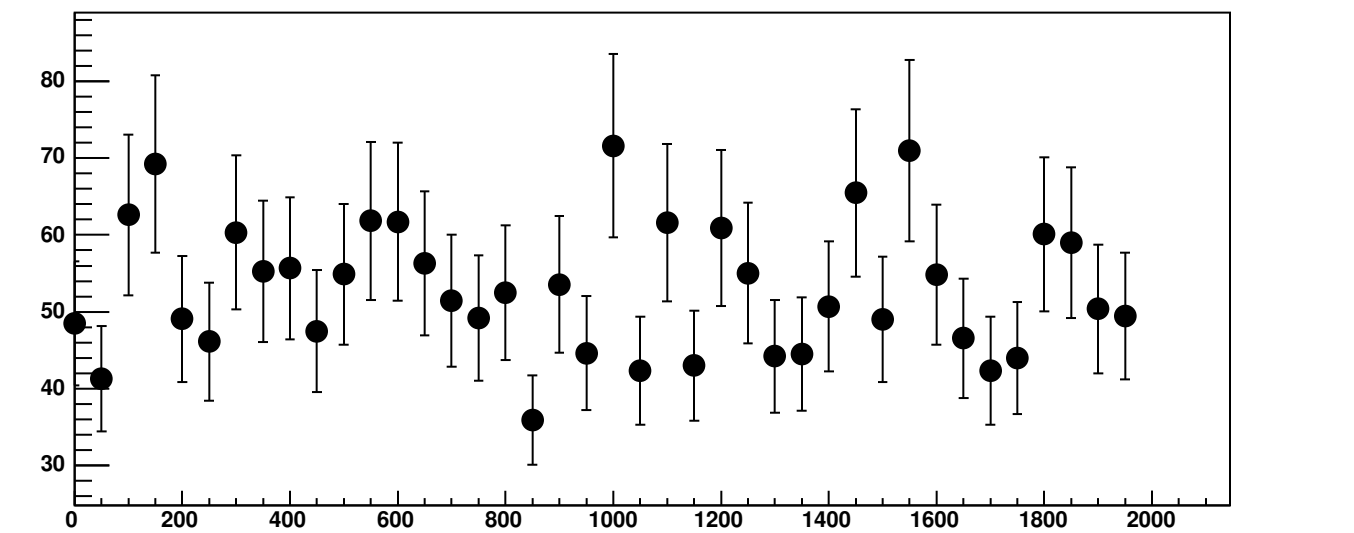

Chip 11, Channel 12, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 12, Enable 0, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

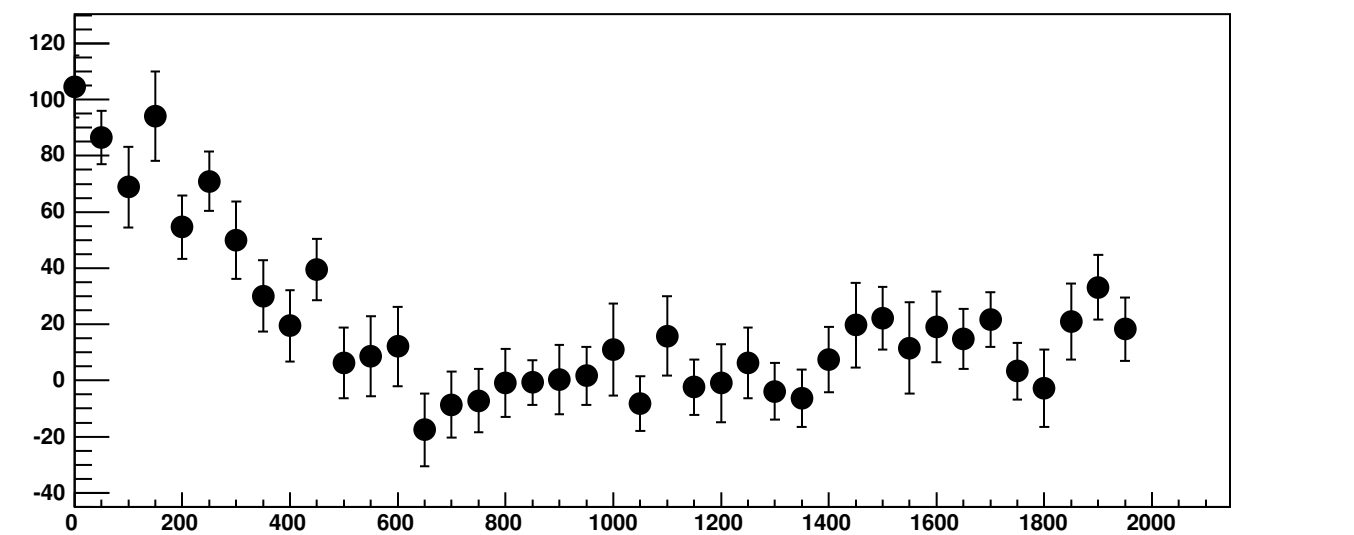
Chip 11, Channel 12, Enable 1, Hold=30, ADC Mean vs DAC

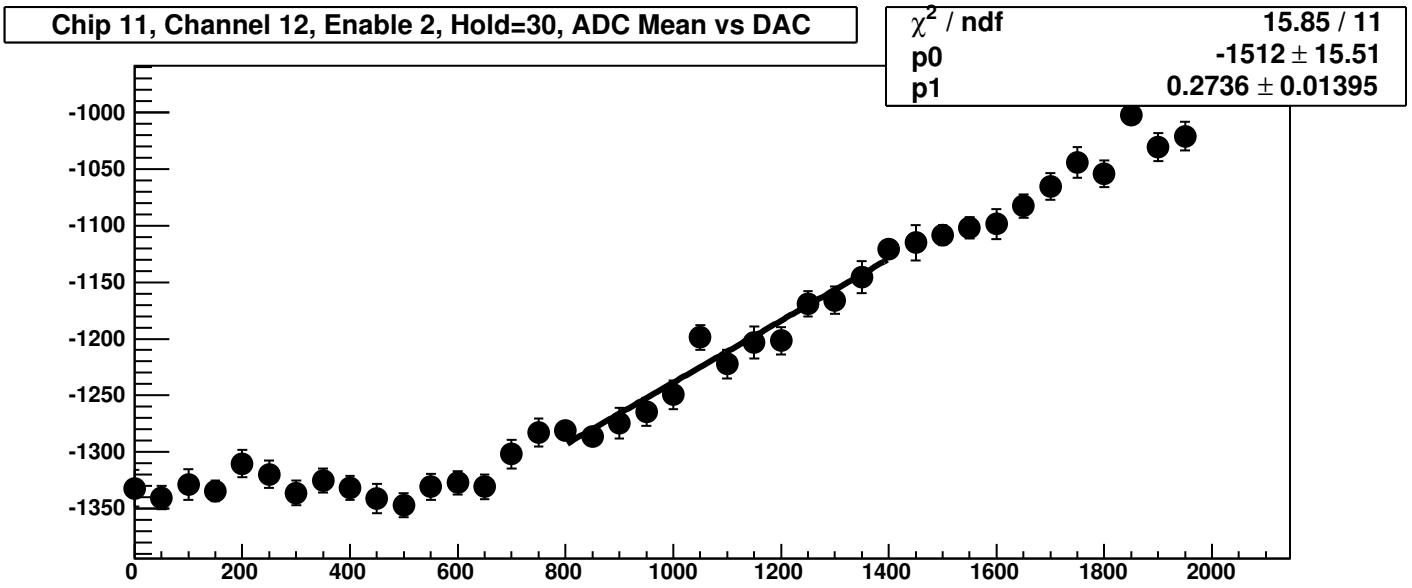


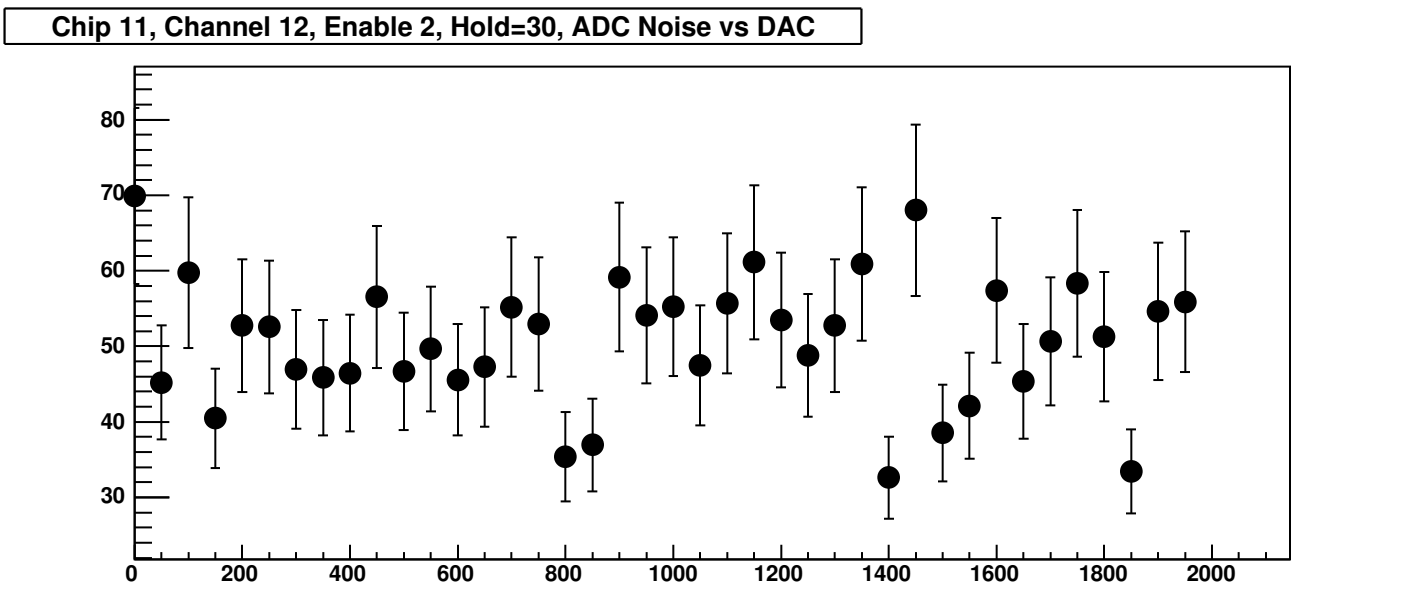
Chip 11, Channel 12, Enable 1, Hold=30, ADC Noise vs DAC

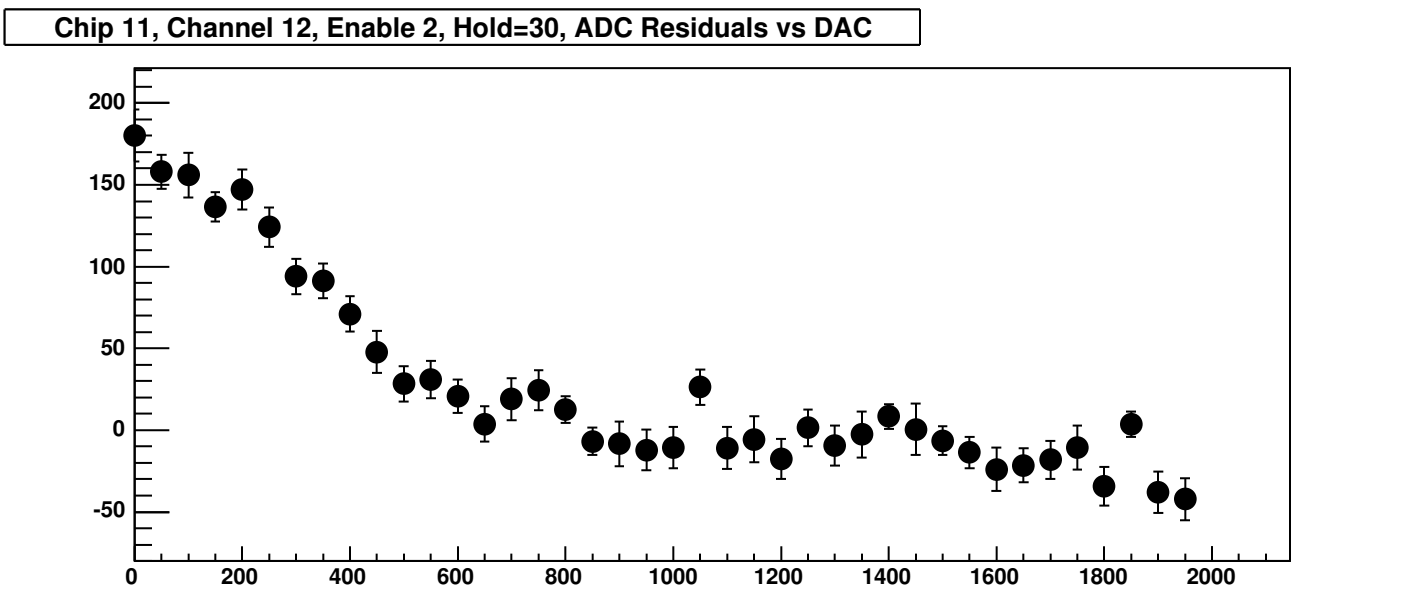


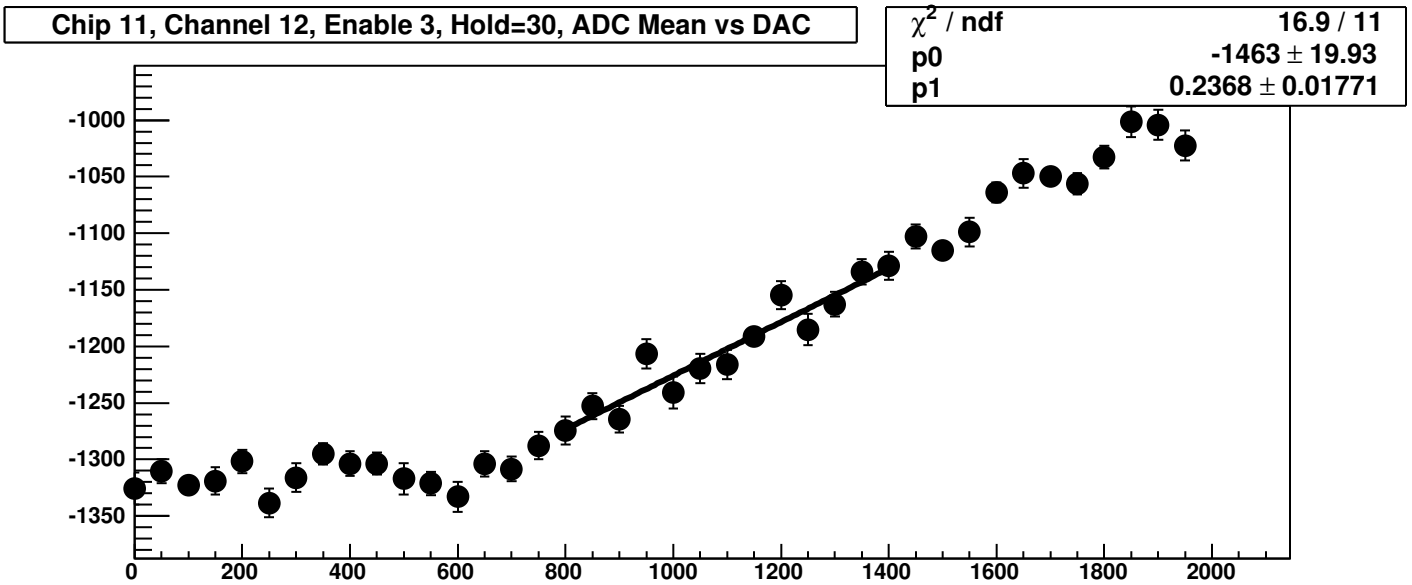
Chip 11, Channel 12, Enable 1, Hold=30, ADC Residuals vs DAC

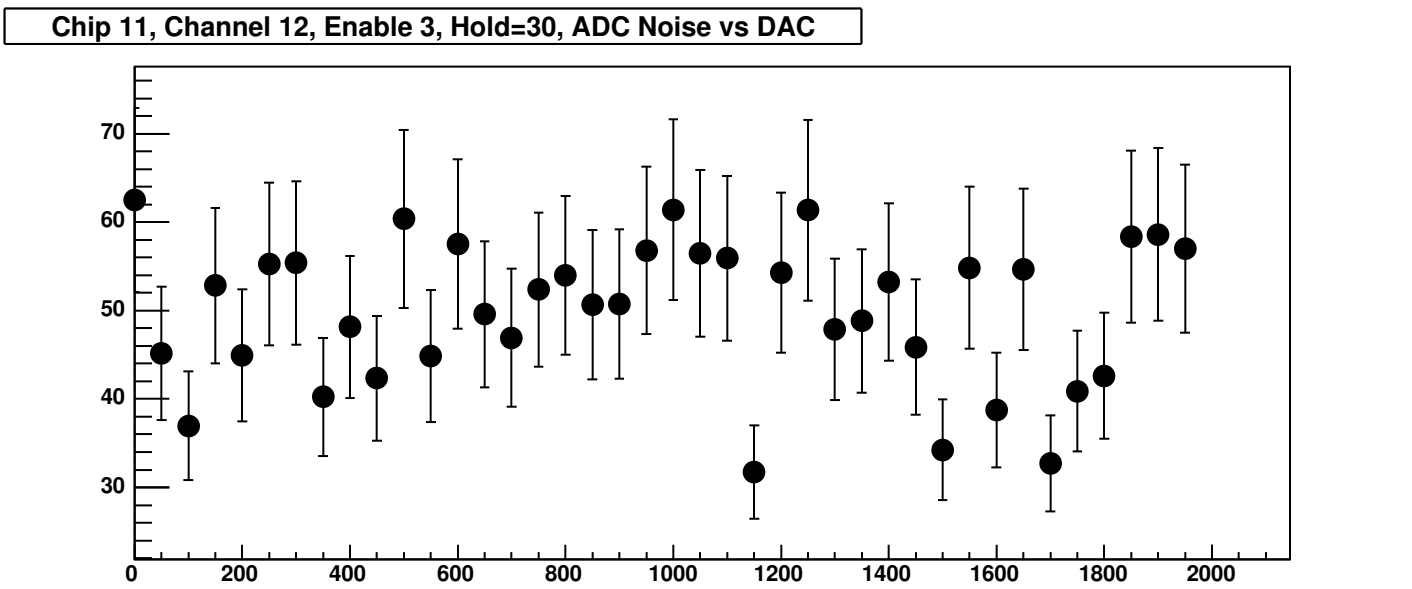


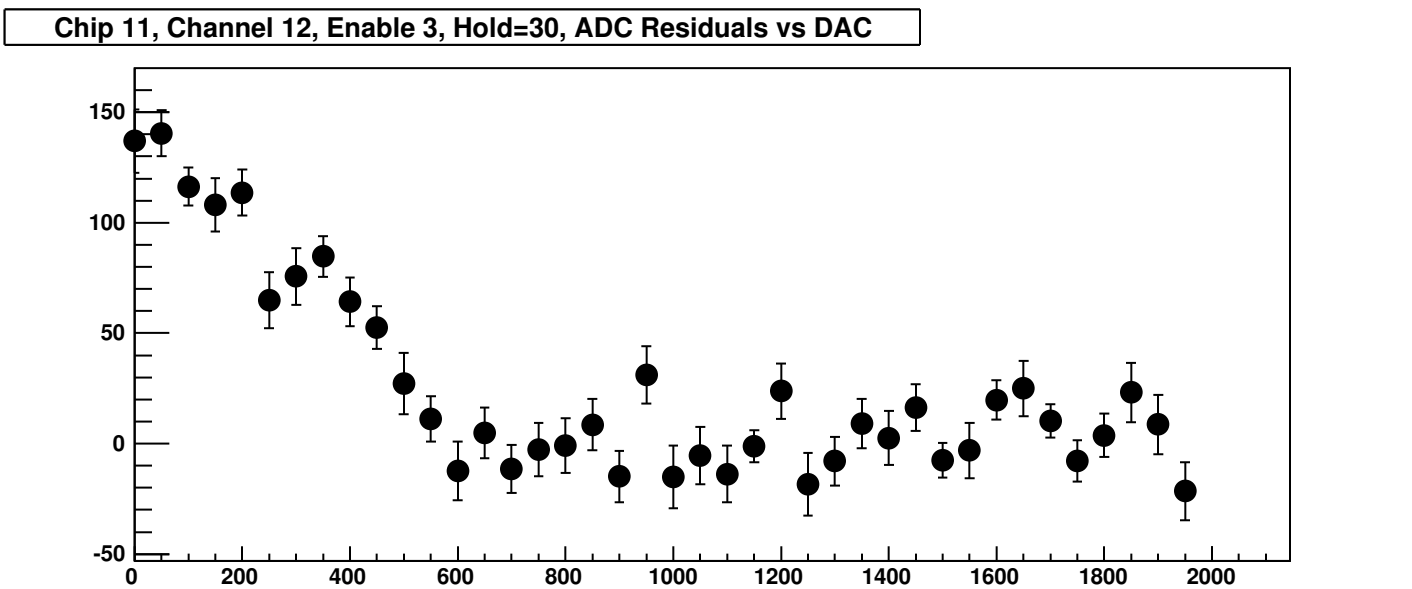

Chip 11, Channel 12, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 15.85 / 11
p0 -1512 ± 15.51
p1 0.2736 ± 0.01395
-1000
-1050
-1100
-1150
-1200
-1250
-1300
-1350
0
200
400
600
800
1000
1200
1400
1600
1800
2000

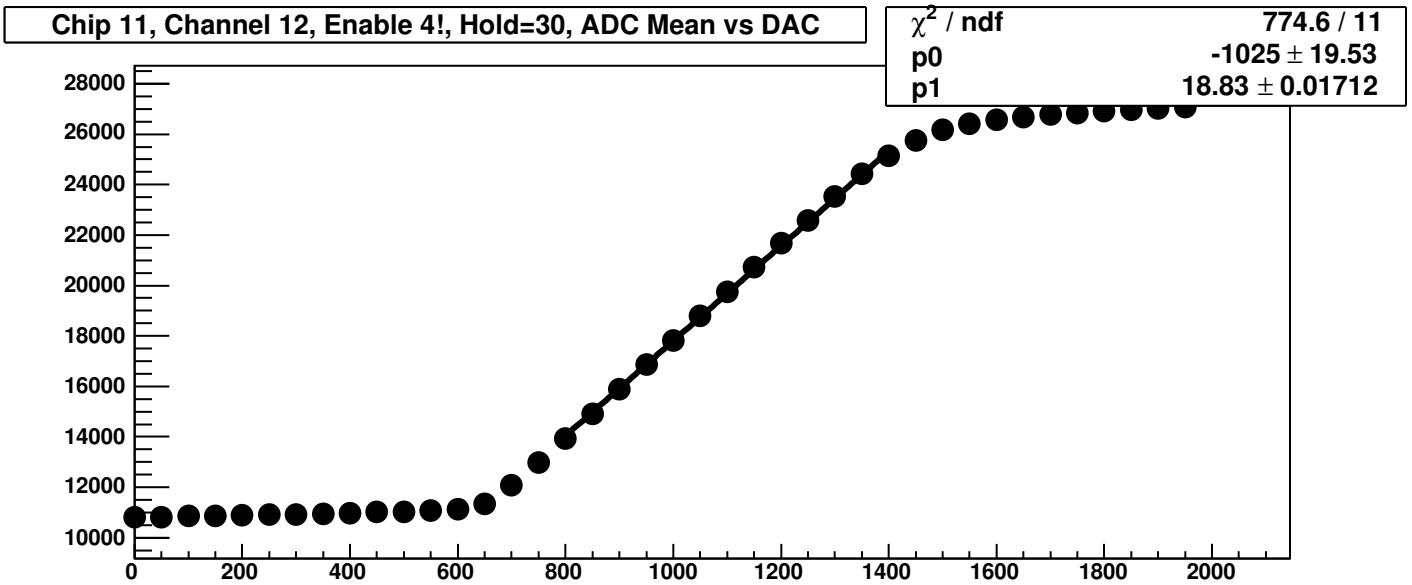

Chip 11, Channel 12, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

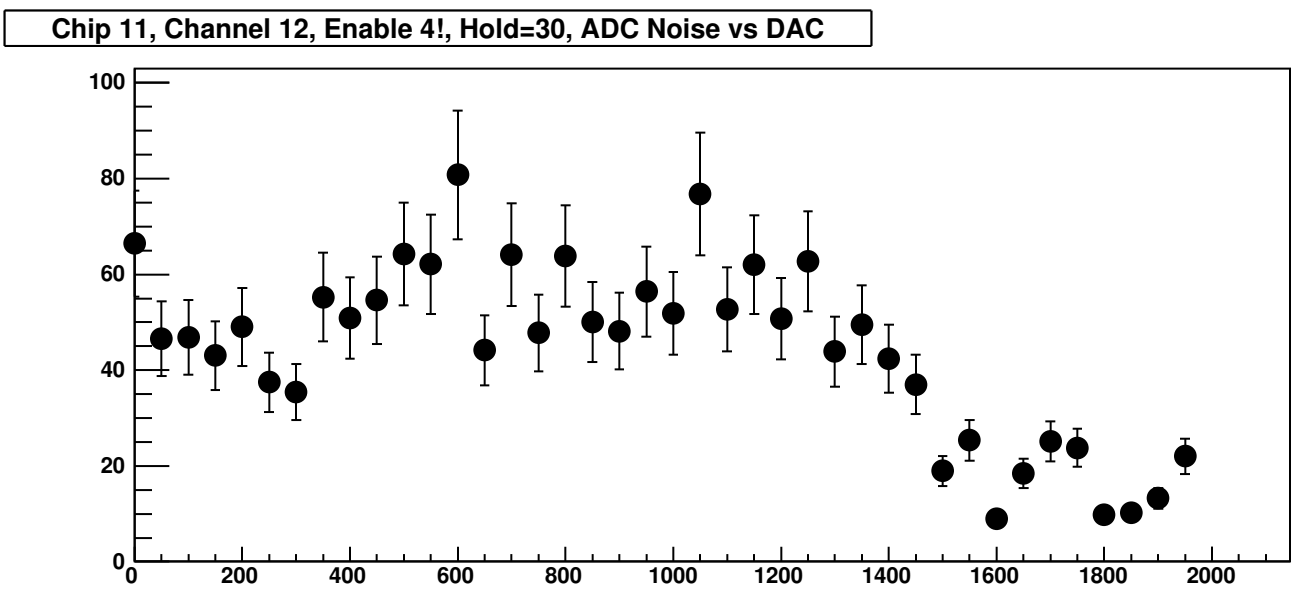

Chip 11, Channel 12, Enable 2, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

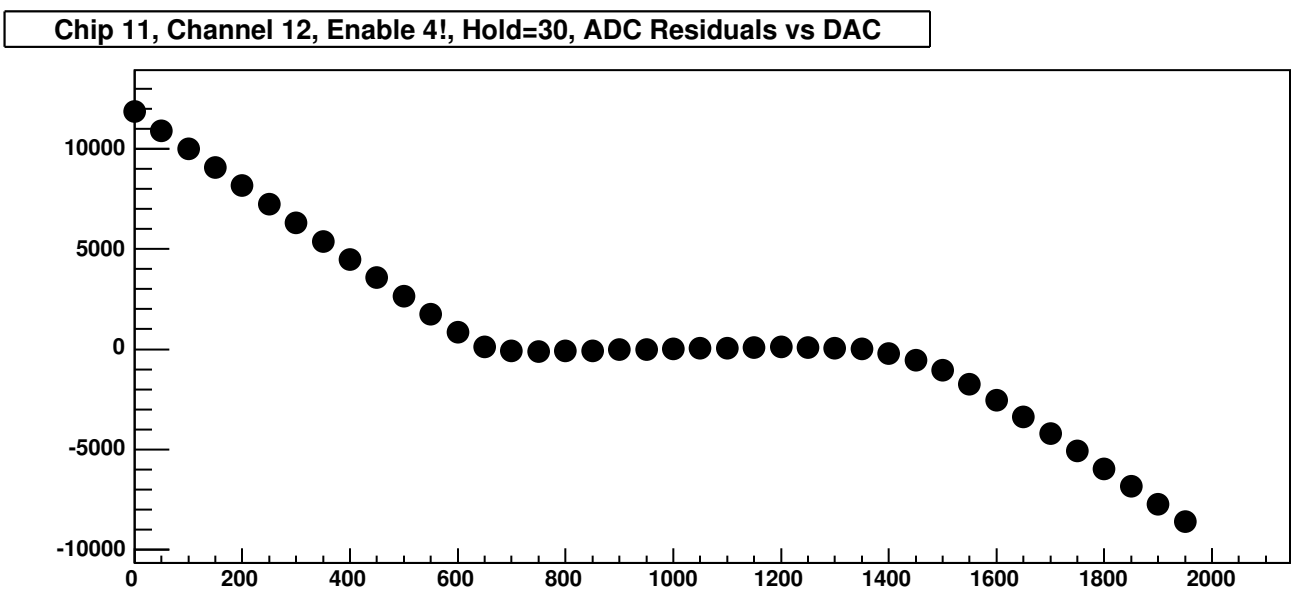

Chip 11, Channel 12, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 16.9 / 11
p0 -1463 ± 19.93
p1 0.2368 ± 0.01771
-1000
-1050
-1100
-1150
-1200
-1250
-1300
-1350
0
200
400
600
800
1000
1200
1400
1600
1800
2000

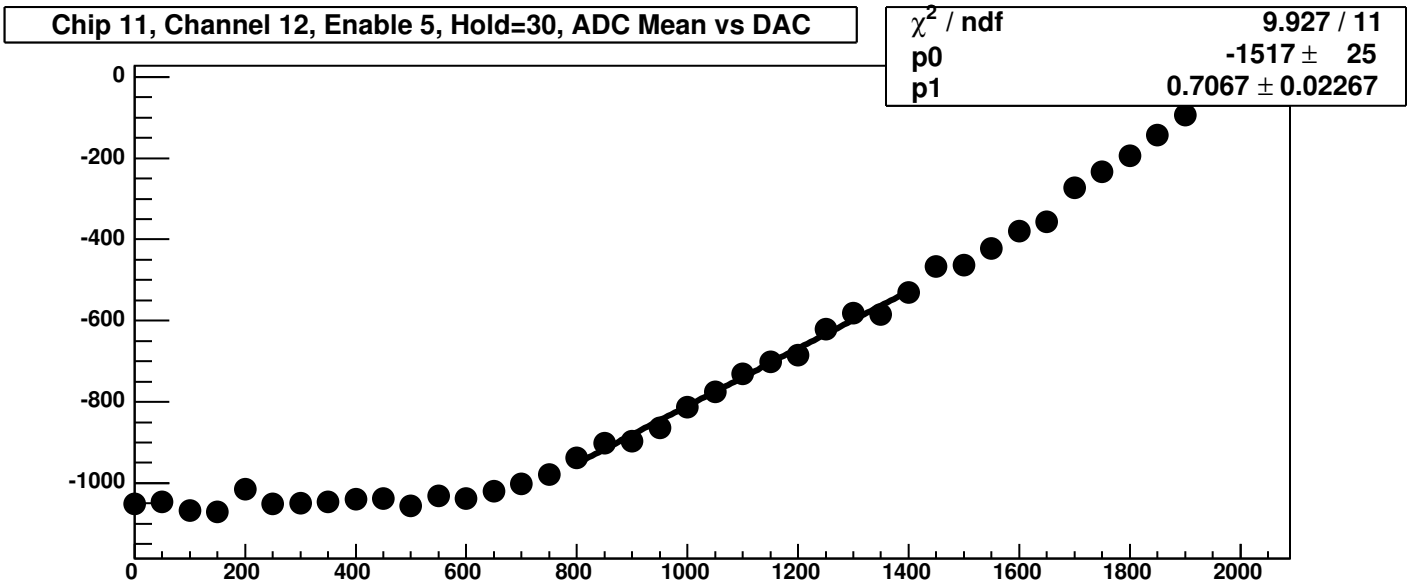

Chip 11, Channel 12, Enable 3, Hold=30, ADC Noise vs DAC
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

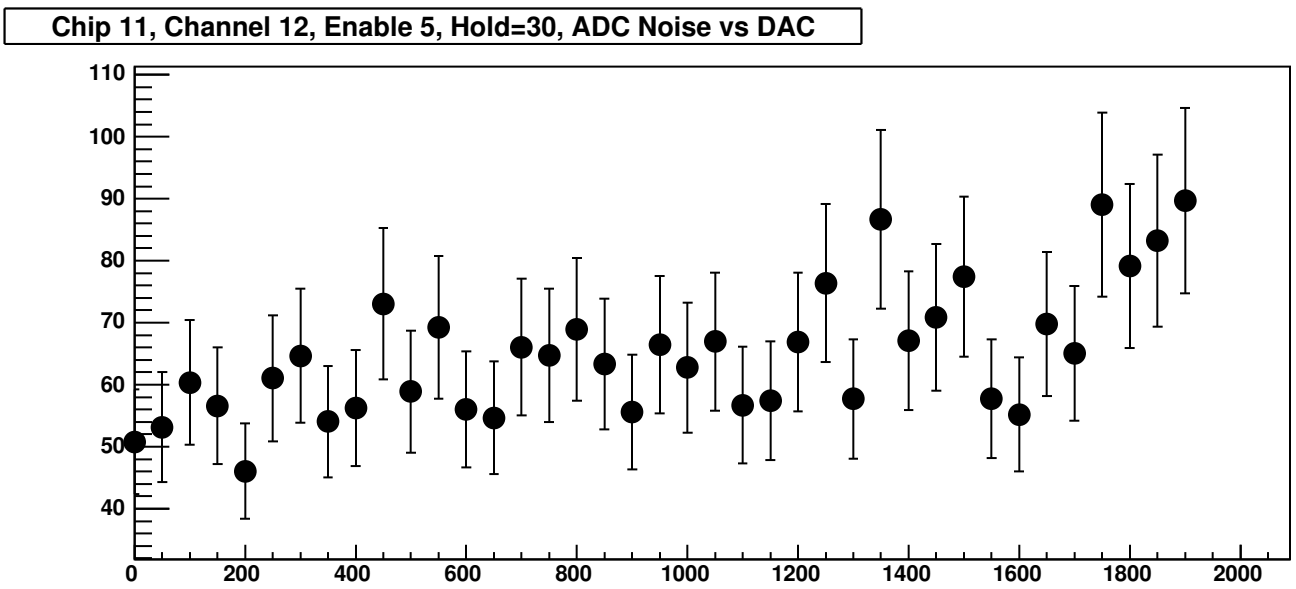

Chip 11, Channel 12, Enable 3, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

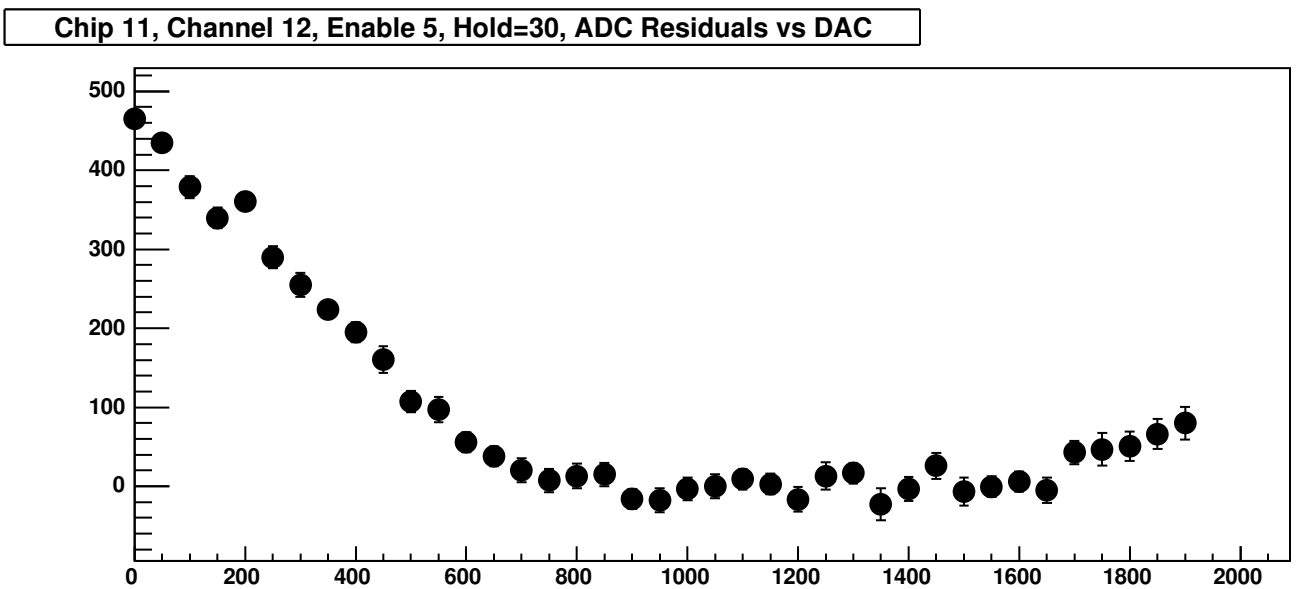

Chip 11, Channel 12, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 774.6 / 11
p0 -1025 ± 19.53
p1 18.83 ± 0.01712
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

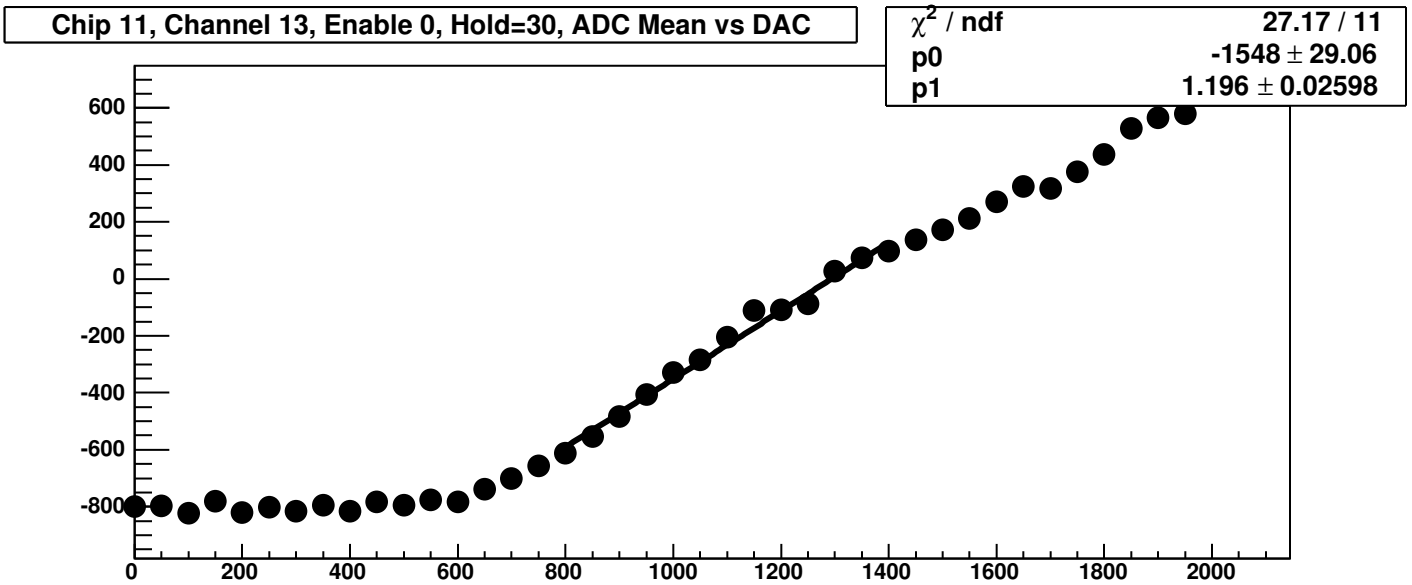

Chip 11, Channel 12, Enable 4!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

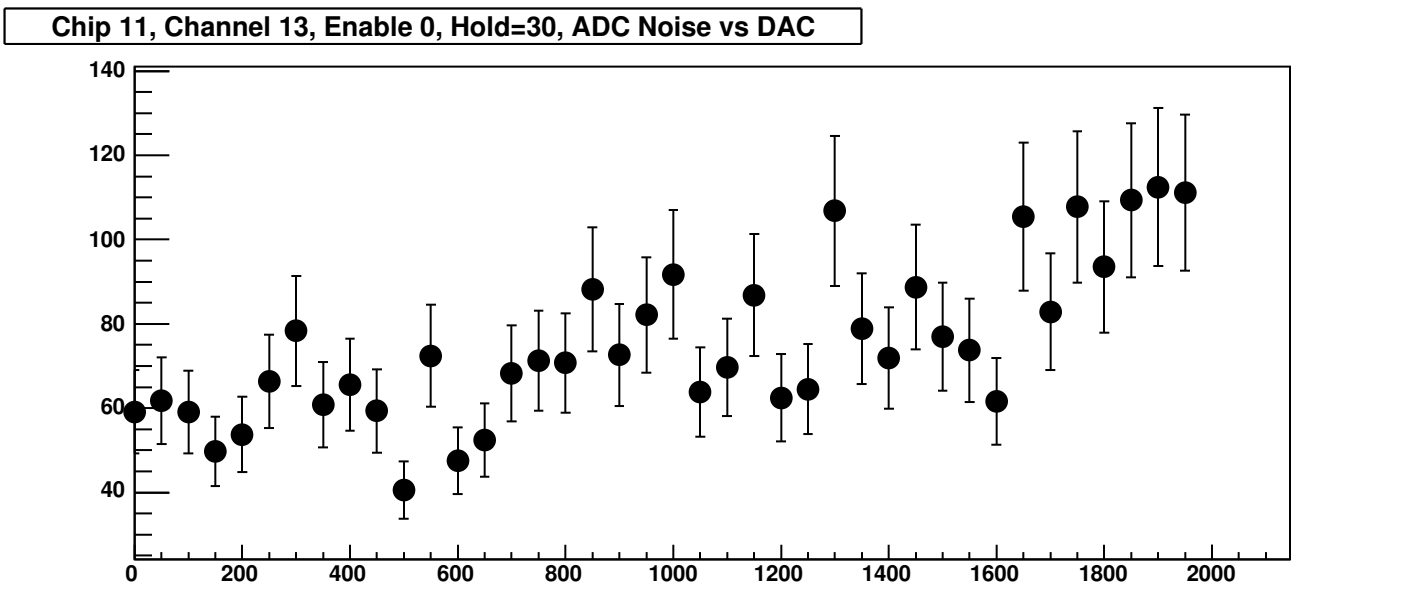

Chip 11, Channel 12, Enable 4!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

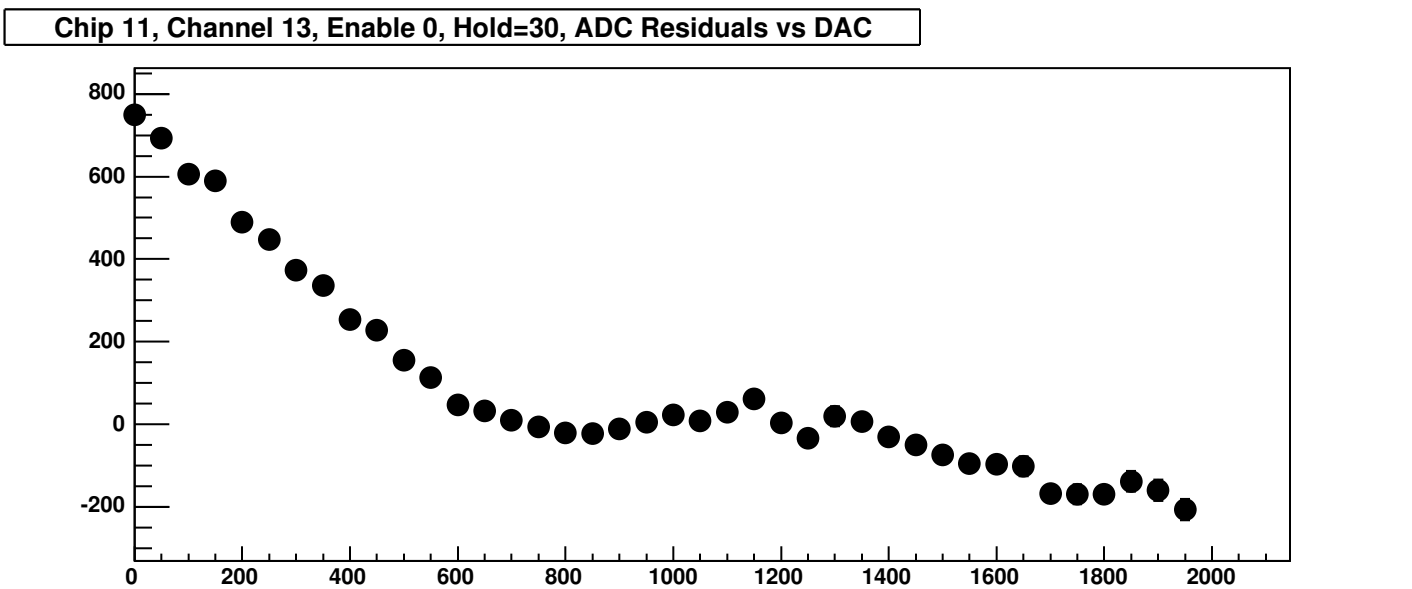

Chip 11, Channel 12, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 9.927 / 11
p0 -1517 ± 25
p1 0.7067 ± 0.02267
0
-200
-400
-600
-800
-1000
0 200 400 600 800 1000 1200 1400 1600 1800 2000


Chip 11, Channel 12, Enable 5, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
0 200 400 600 800 1000 1200 1400 1600 1800 2000

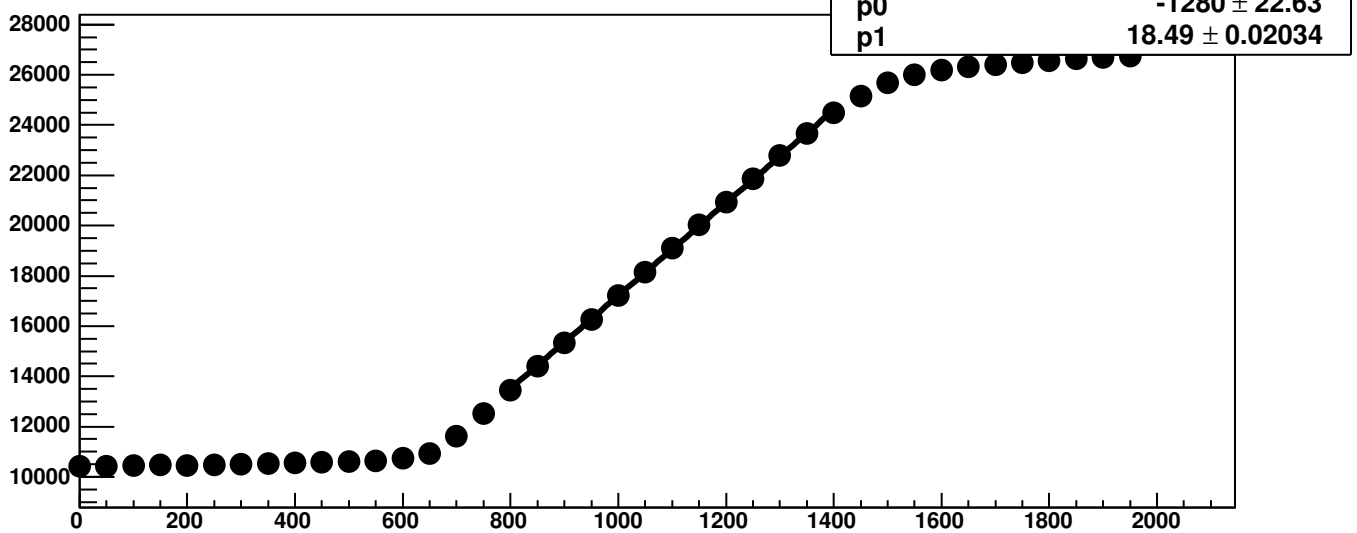

Chip 11, Channel 12, Enable 5, Hold=30, ADC Residuals vs DAC
500
400
300
200
100
0
0 200 400 600 800 1000 1200 1400 1600 1800 2000


Chip 11, Channel 13, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 27.17 / 11
p0 -1548 ± 29.06
p1 1.196 ± 0.02598
600
400
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

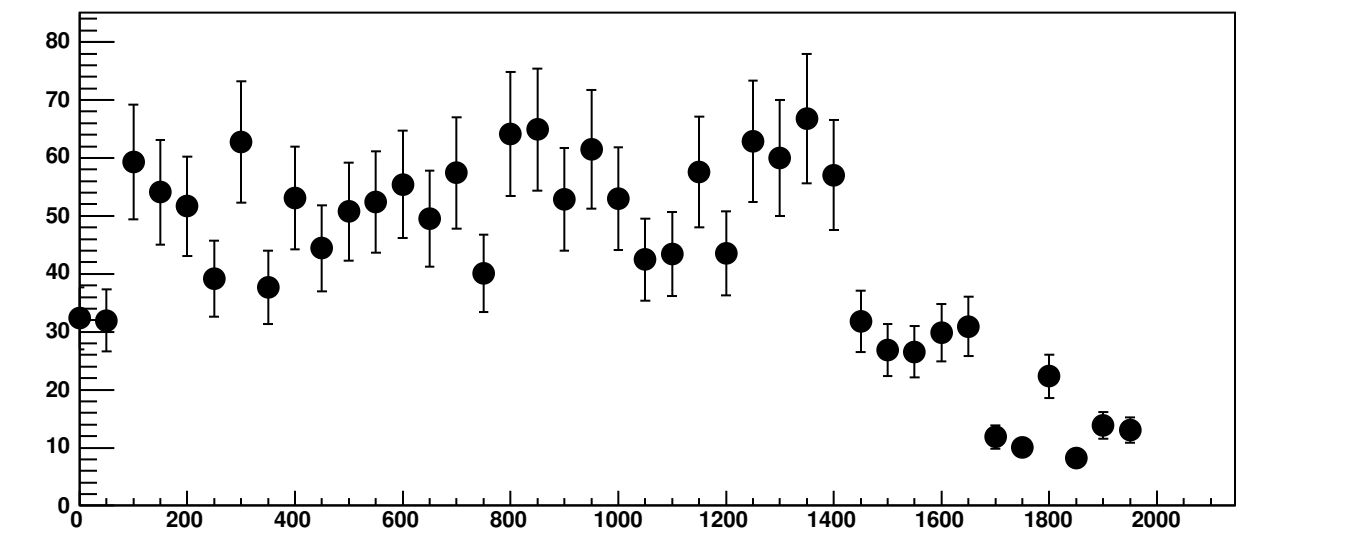

Chip 11, Channel 13, Enable 0, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 13, Enable 0, Hold=30, ADC Residuals vs DAC
800
600
400
200
0
-200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

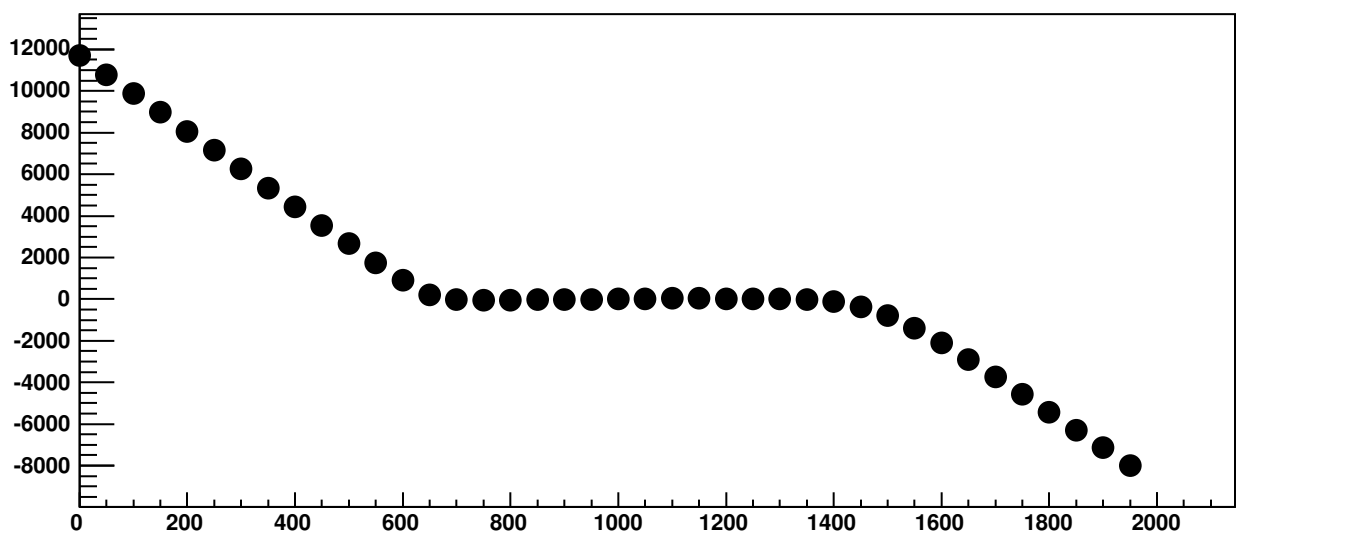
Chip 11, Channel 13, Enable 1!, Hold=30, ADC Mean vs DAC

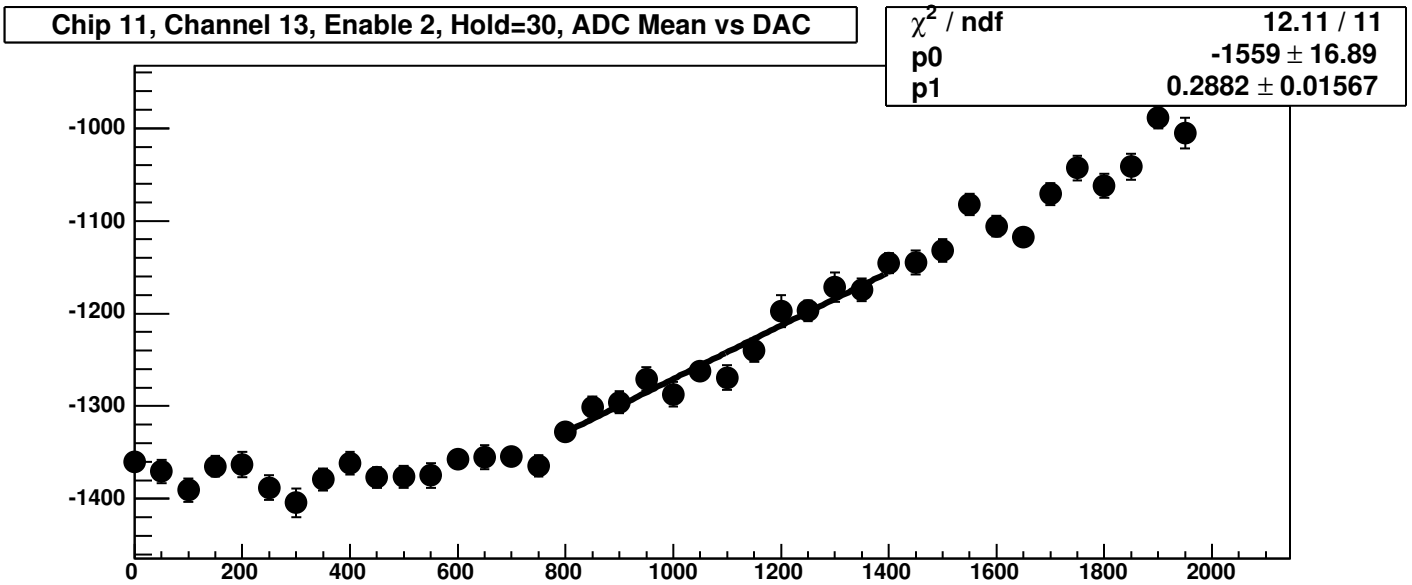


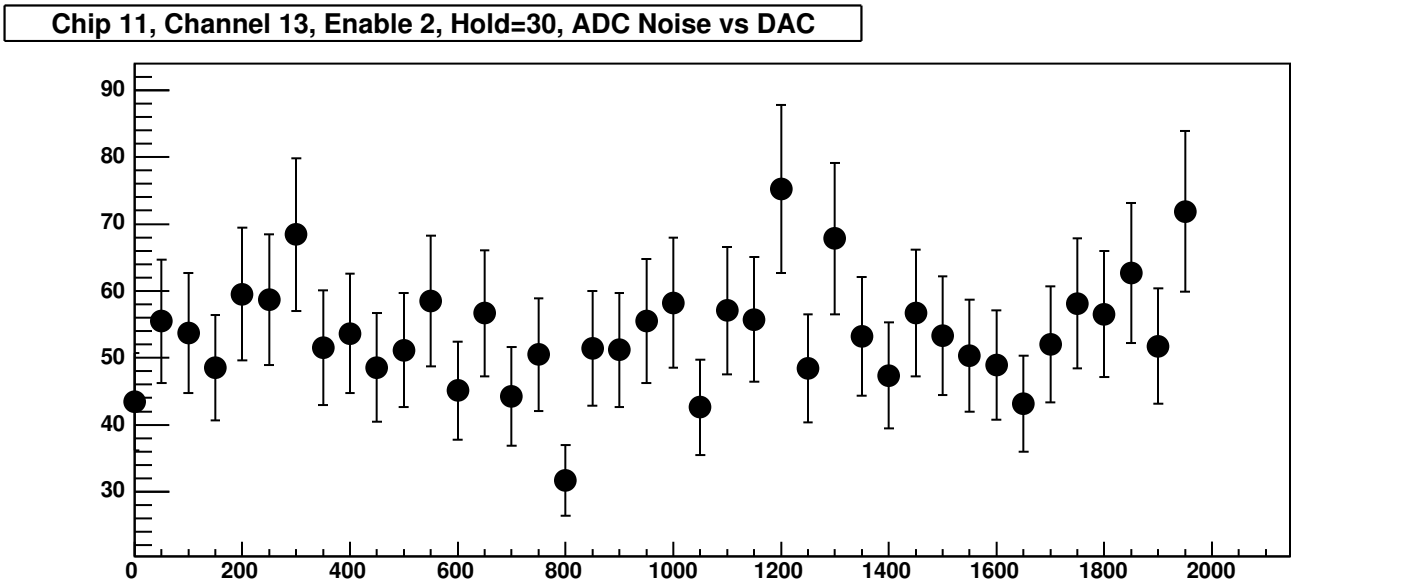
Chip 11, Channel 13, Enable 1!, Hold=30, ADC Noise vs DAC

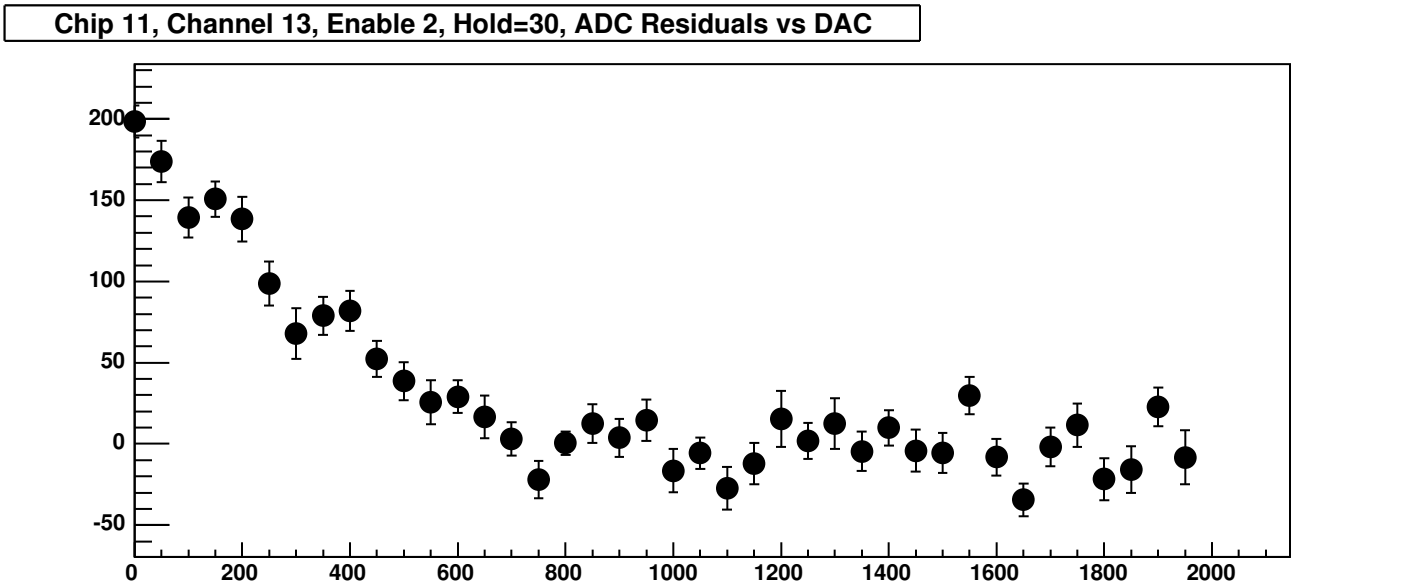


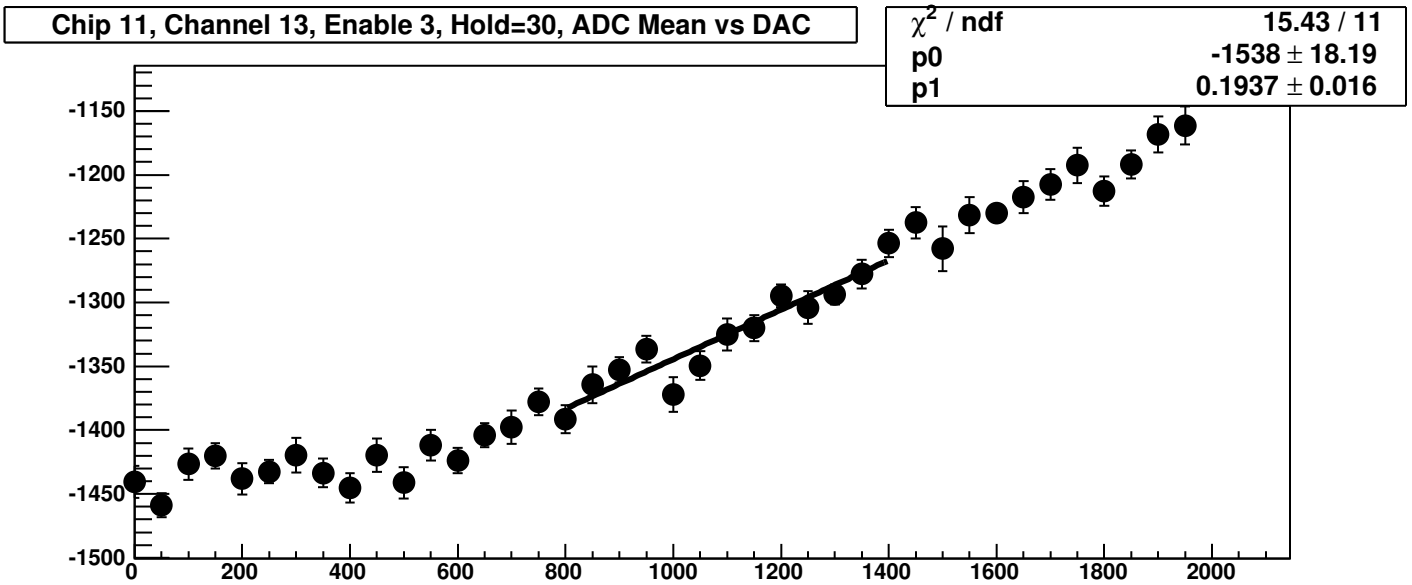
Chip 11, Channel 13, Enable 1!, Hold=30, ADC Residuals vs DAC

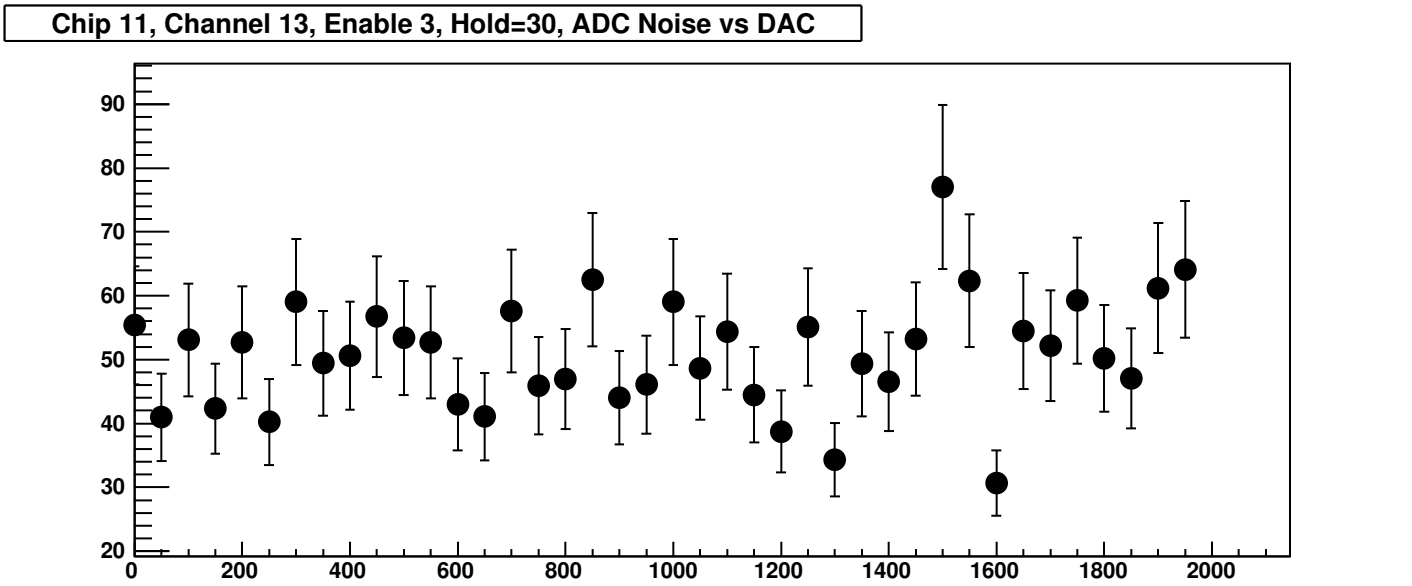


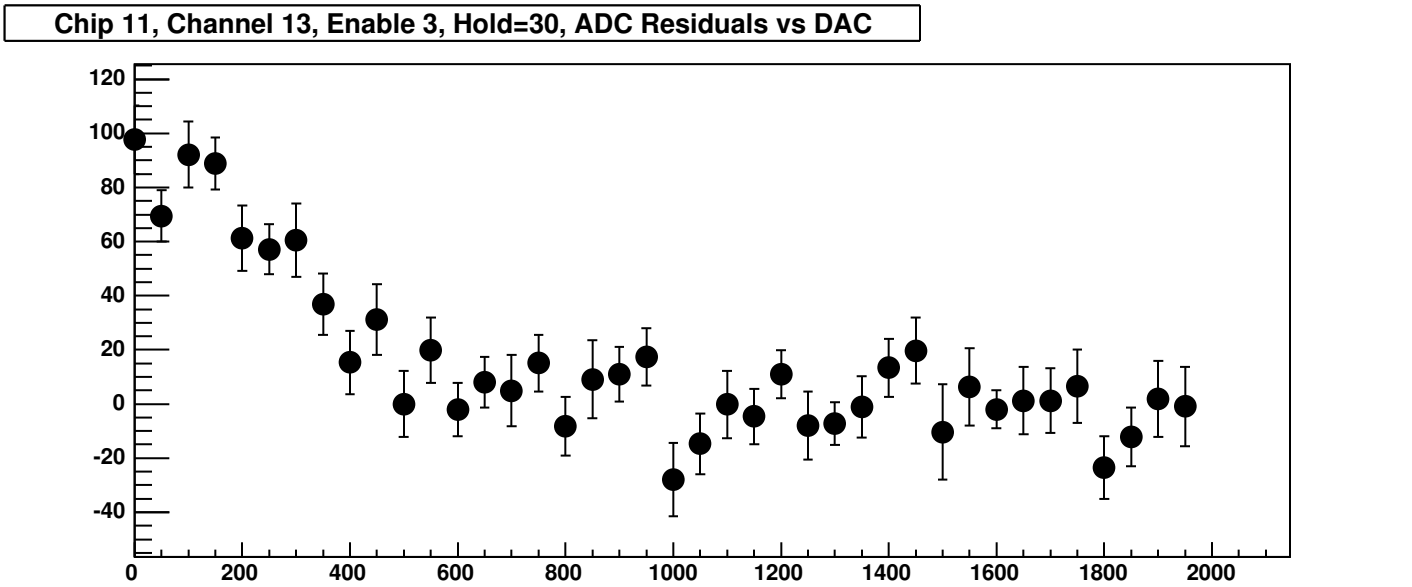

Chip 11, Channel 13, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 12.11 / 11
p0 -1559 ± 16.89
p1 0.2882 ± 0.01567
-1000
-1100
-1200
-1300
-1400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

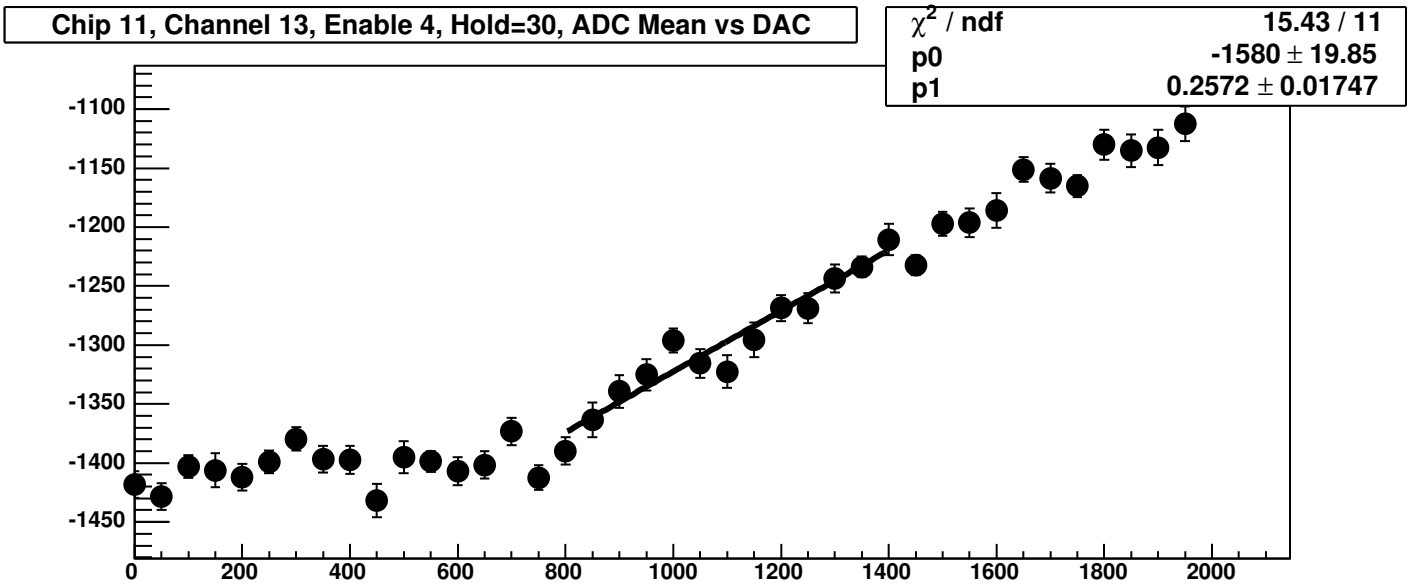

Chip 11, Channel 13, Enable 2, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

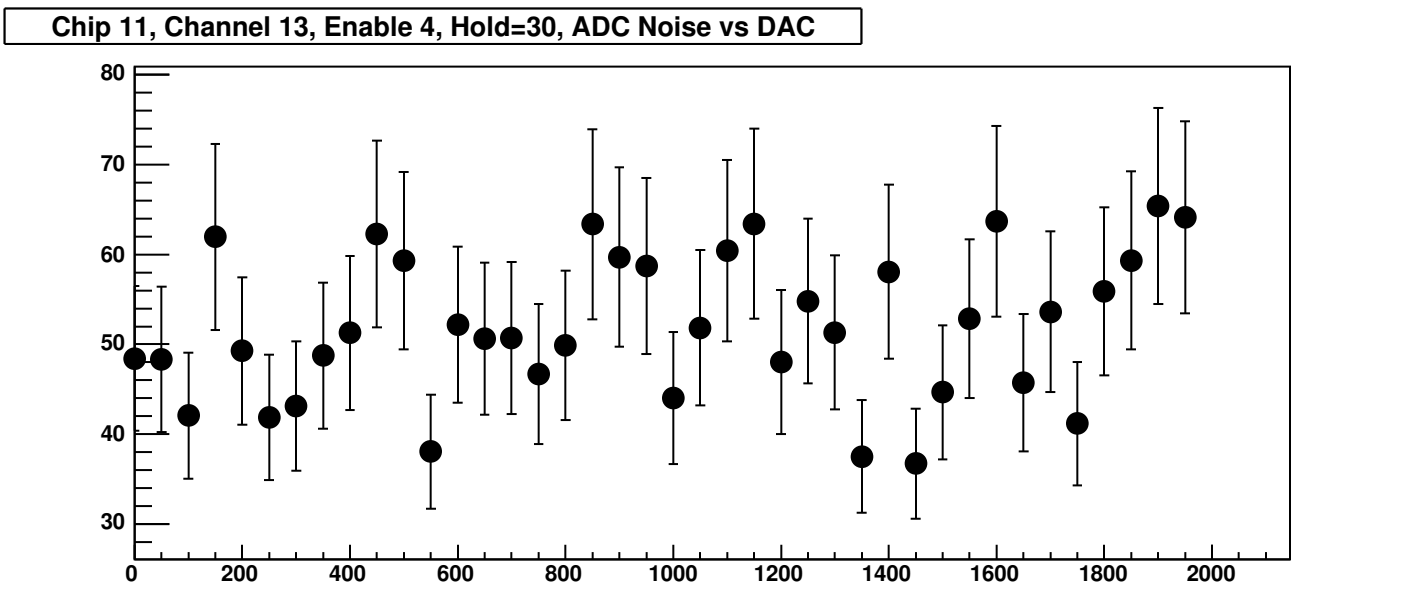

Chip 11, Channel 13, Enable 2, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

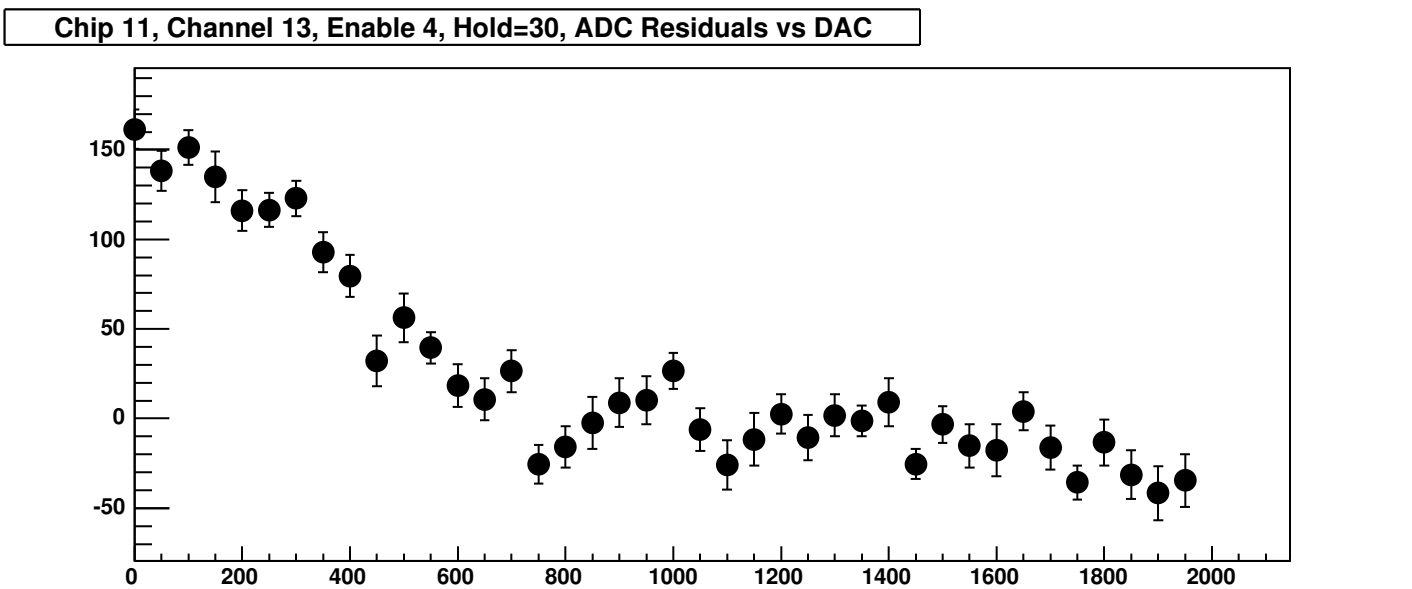

Chip 11, Channel 13, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 15.43 / 11
p0 -1538 ± 18.19
p1 0.1937 ± 0.016
-1150
-1200
-1250
-1300
-1350
-1400
-1450
-1500
0
200
400
600
800
1000
1200
1400
1600
1800
2000

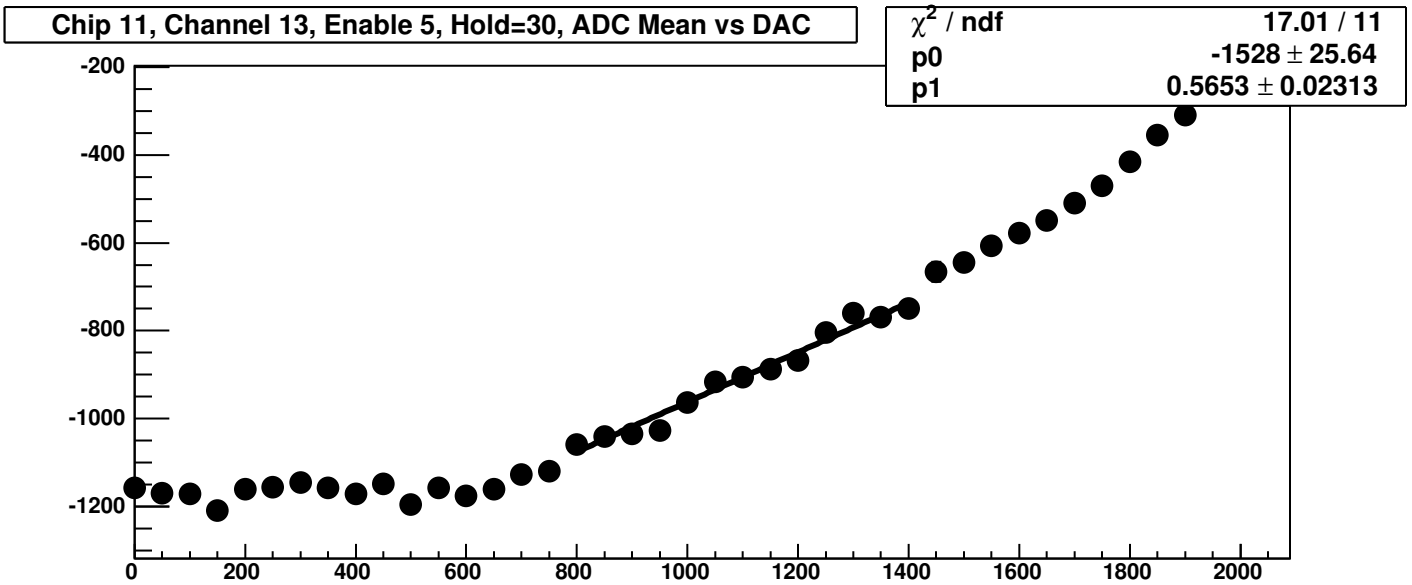

Chip 11, Channel 13, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

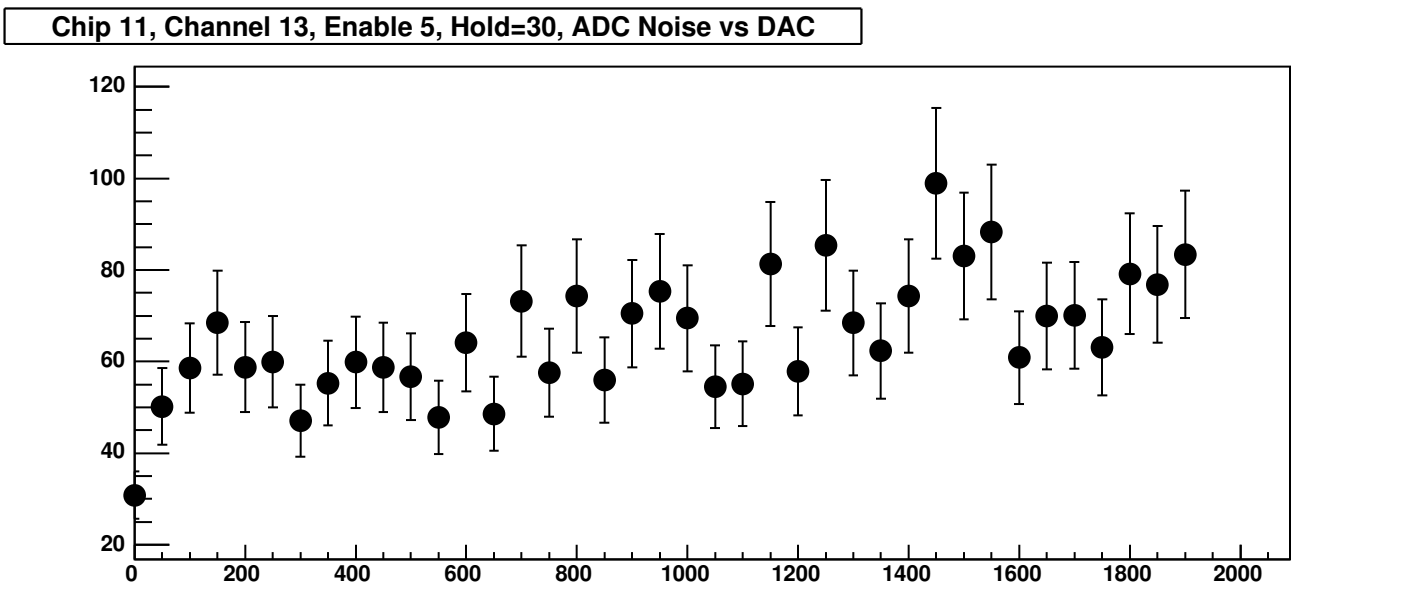

Chip 11, Channel 13, Enable 3, Hold=30, ADC Residuals vs DAC
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

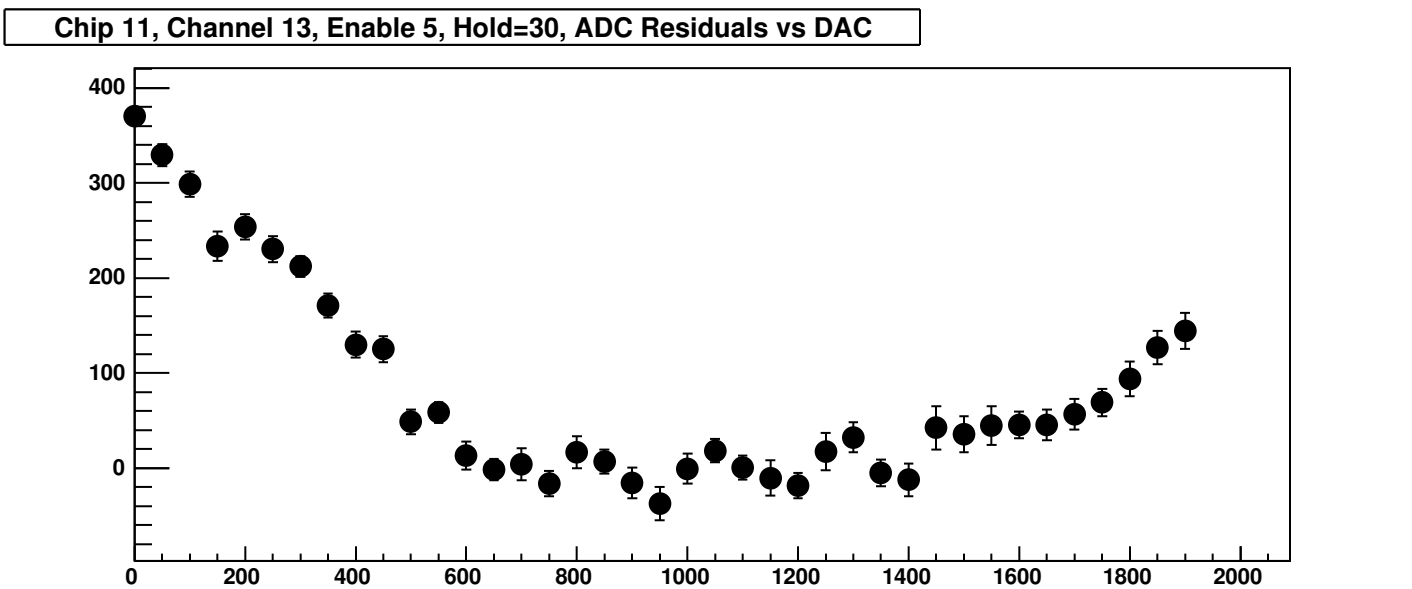

Chip 11, Channel 13, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 15.43 / 11
p0 -1580 ± 19.85
p1 0.2572 ± 0.01747
-1100
-1150
-1200
-1250
-1300
-1350
-1400
-1450
0
200
400
600
800
1000
1200
1400
1600
1800
2000

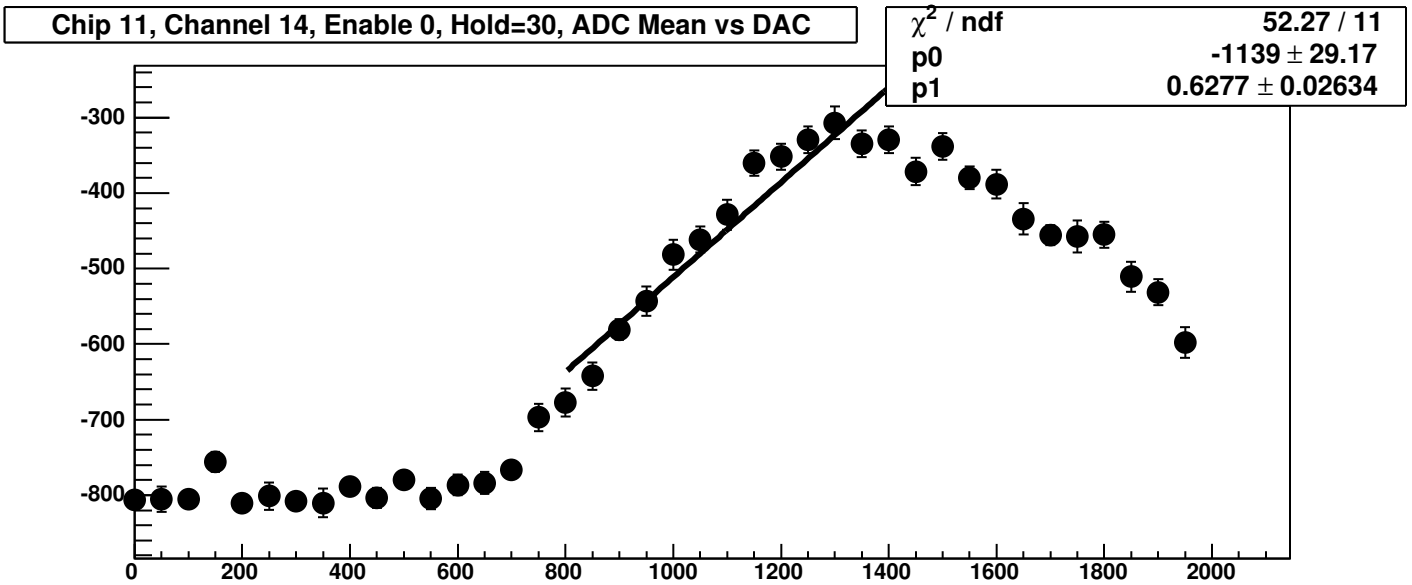

Chip 11, Channel 13, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

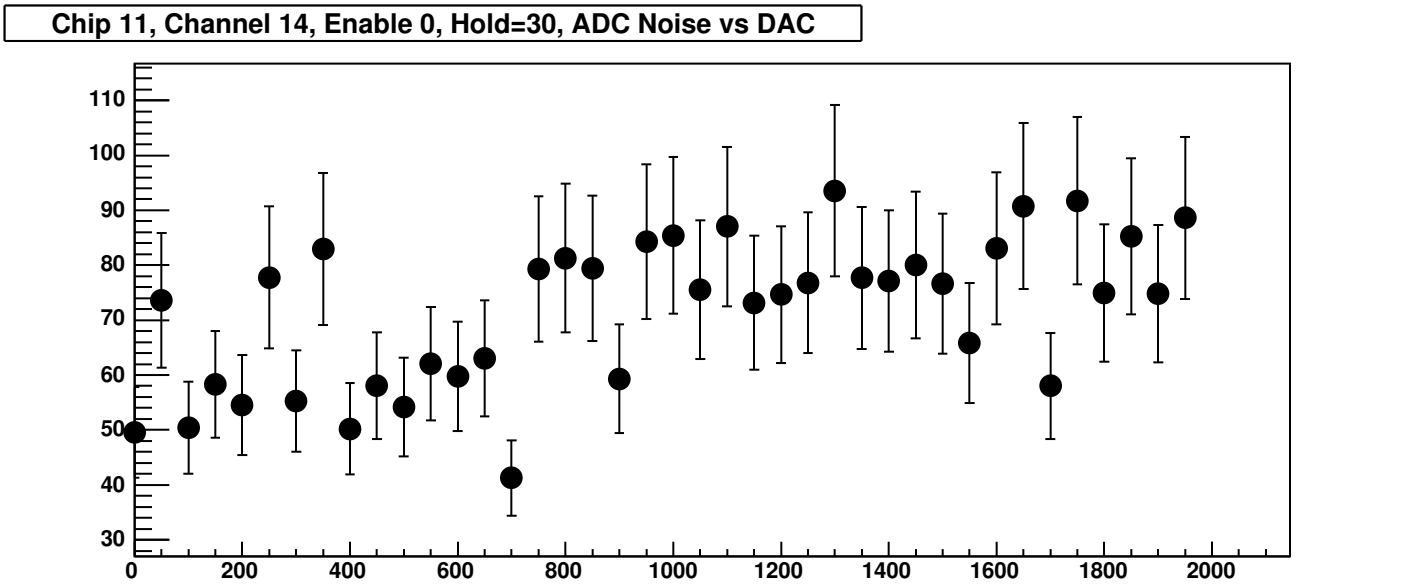

Chip 11, Channel 13, Enable 4, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

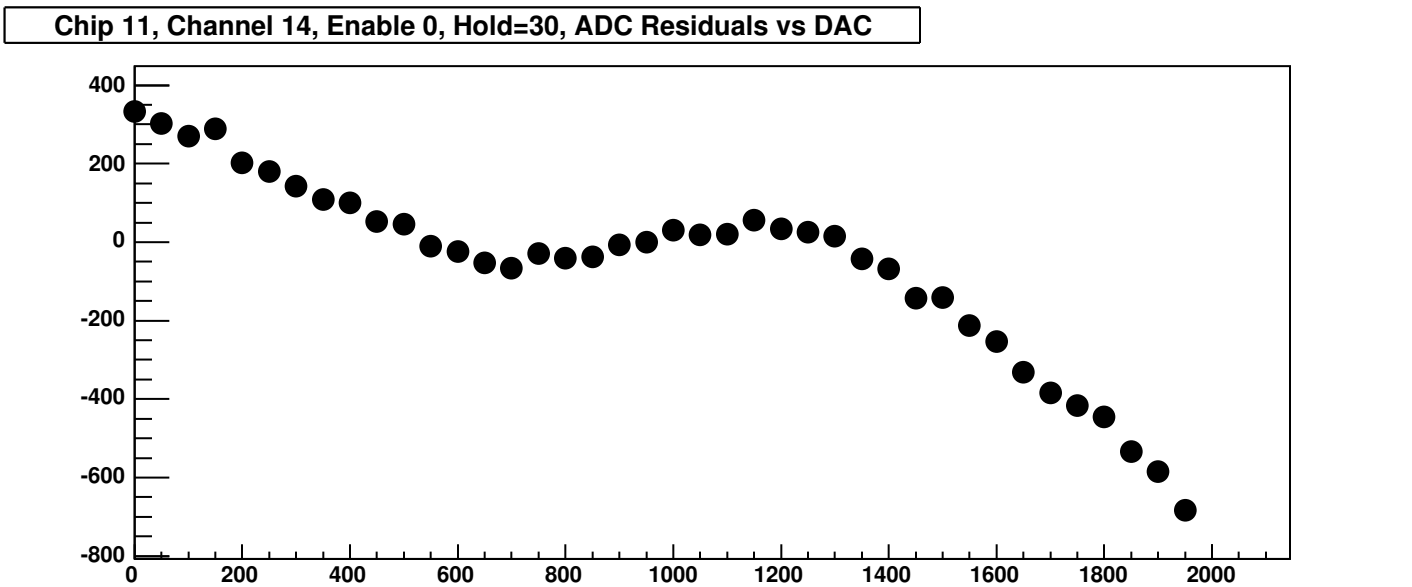

Chip 11, Channel 13, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 17.01 / 11
p0 -1528 ± 25.64
p1 0.5653 ± 0.02313
-200
-400
-600
-800
-1000
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 13, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

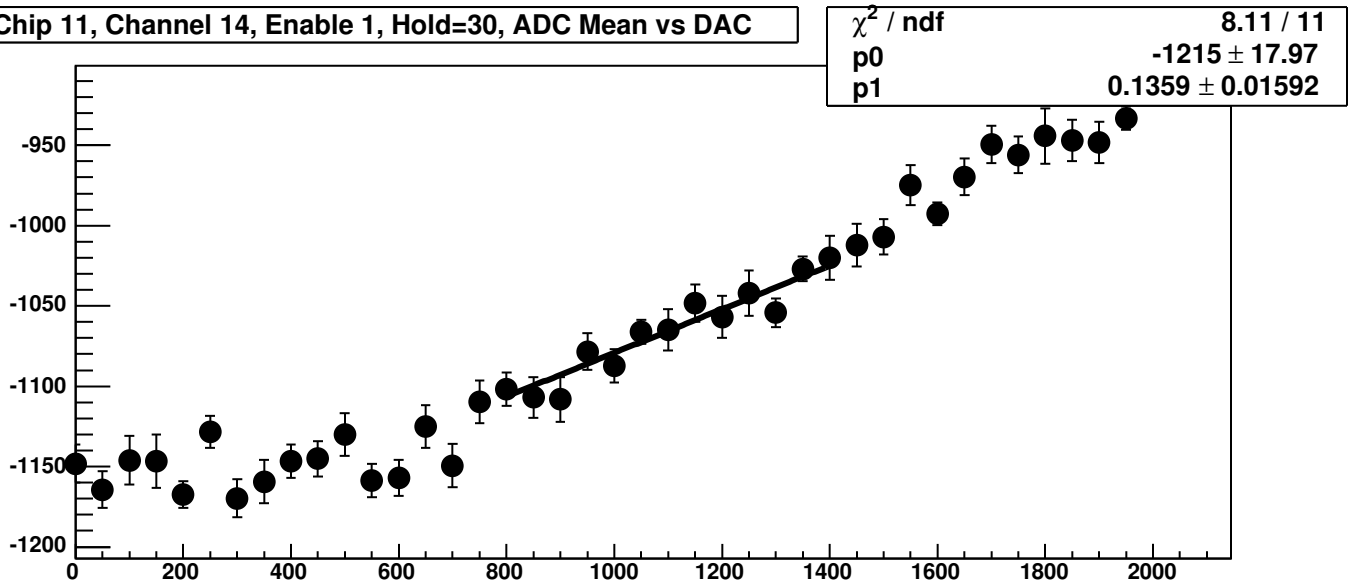

Chip 11, Channel 13, Enable 5, Hold=30, ADC Residuals vs DAC
400
300
200
100
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 14, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 52.27 / 11
p0 -1139 ± 29.17
p1 0.6277 ± 0.02634
-300
-400
-500
-600
-700
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

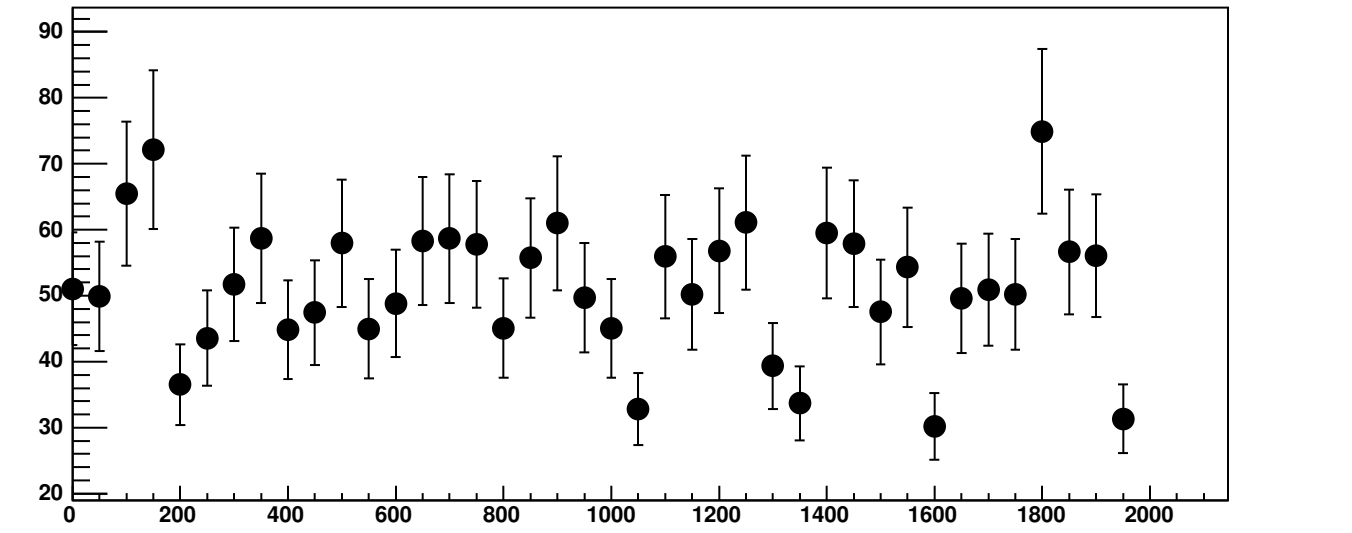

Chip 11, Channel 14, Enable 0, Hold=30, ADC Noise vs DAC
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 14, Enable 0, Hold=30, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

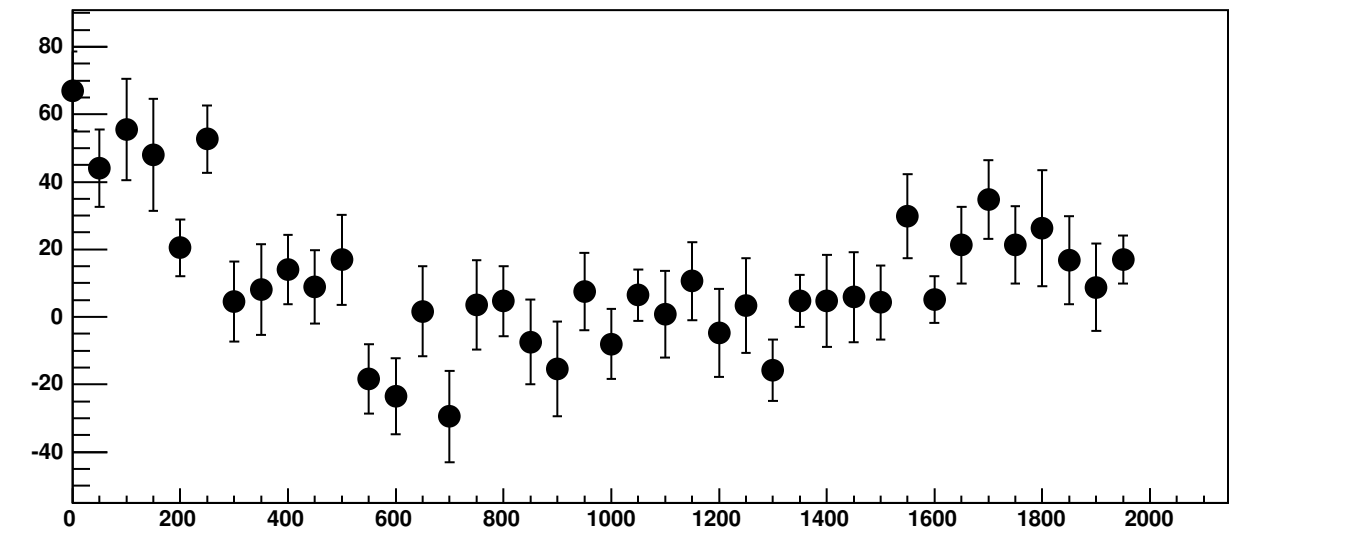
Chip 11, Channel 14, Enable 1, Hold=30, ADC Mean vs DAC



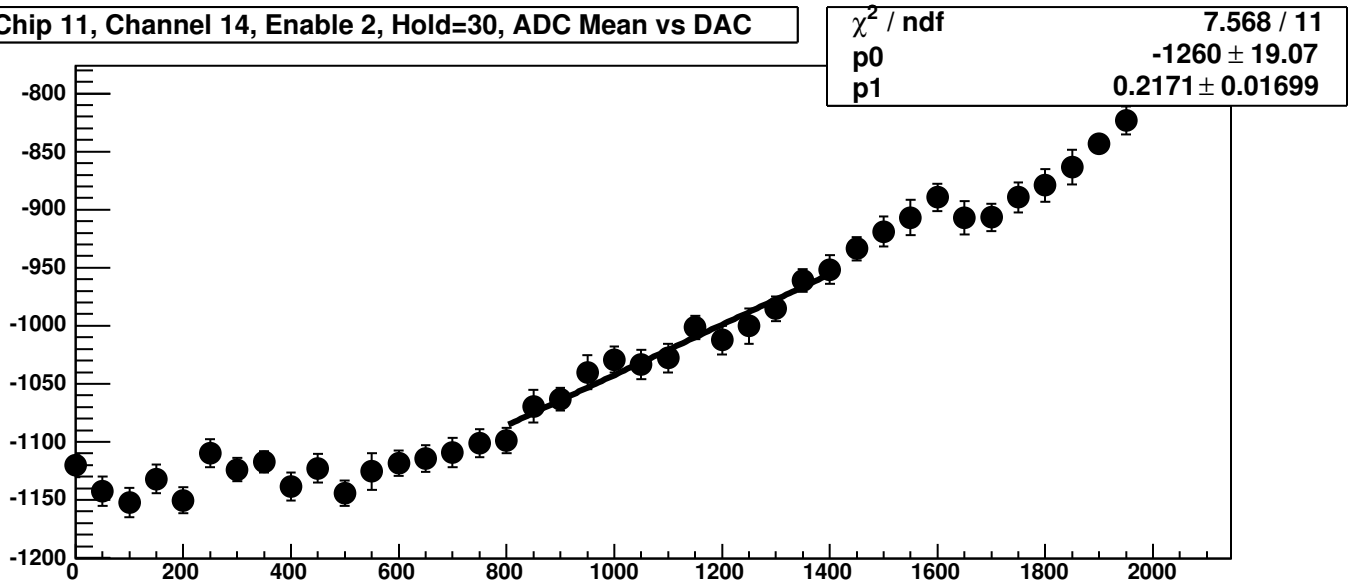
Chip 11, Channel 14, Enable 1, Hold=30, ADC Noise vs DAC



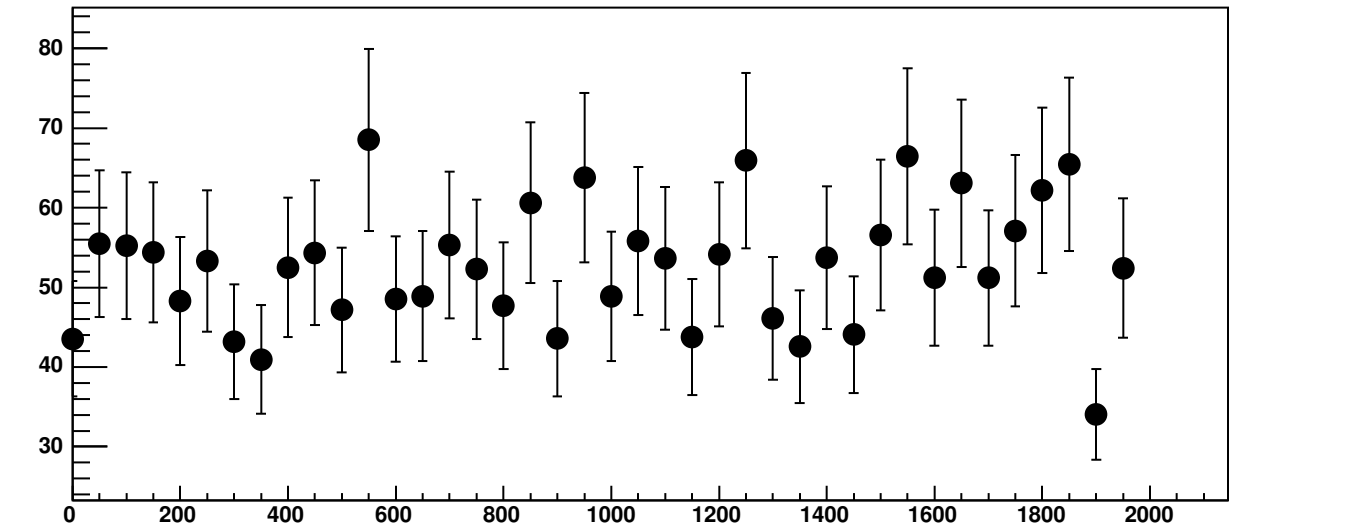
Chip 11, Channel 14, Enable 1, Hold=30, ADC Residuals vs DAC



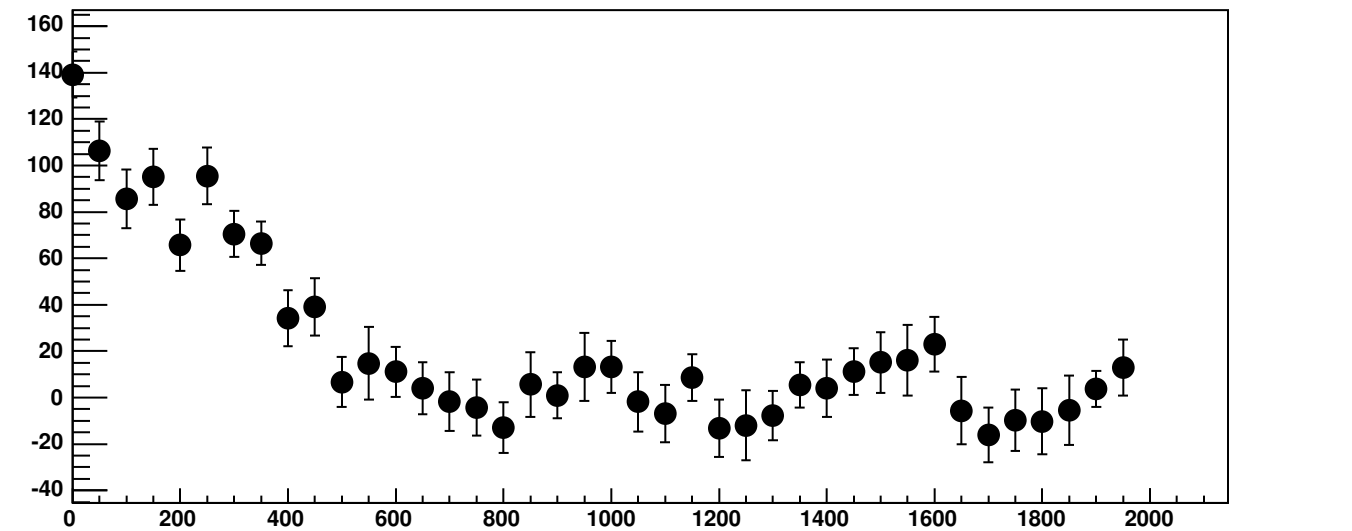
Chip 11, Channel 14, Enable 2, Hold=30, ADC Mean vs DAC

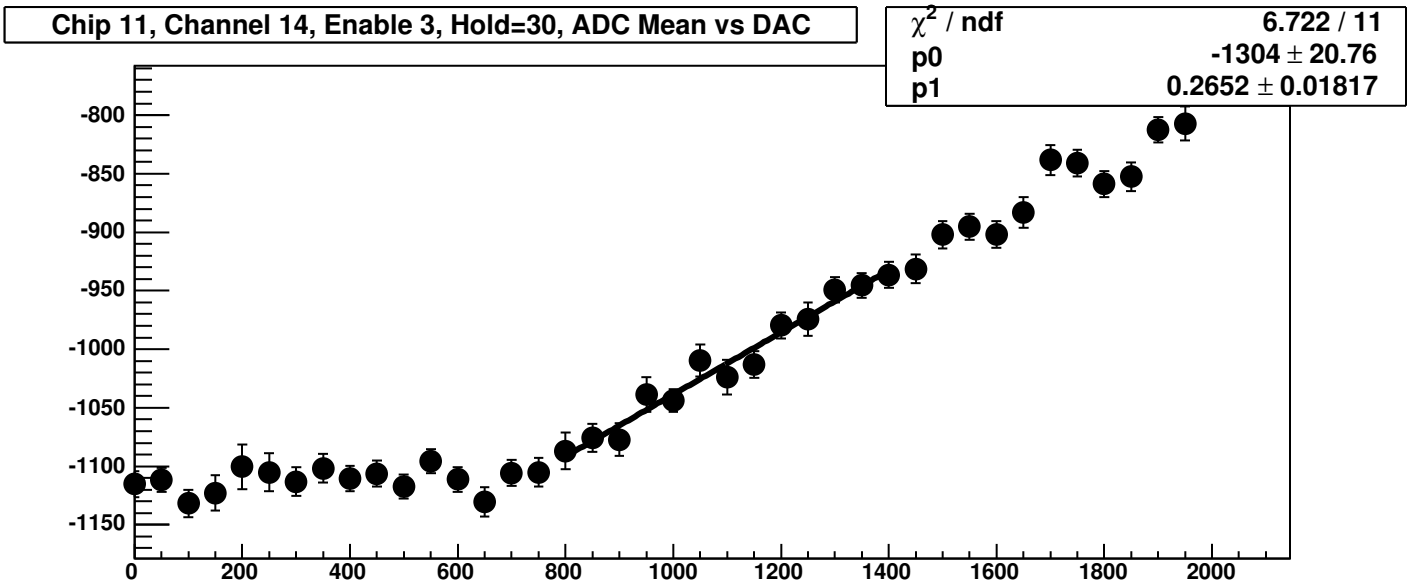


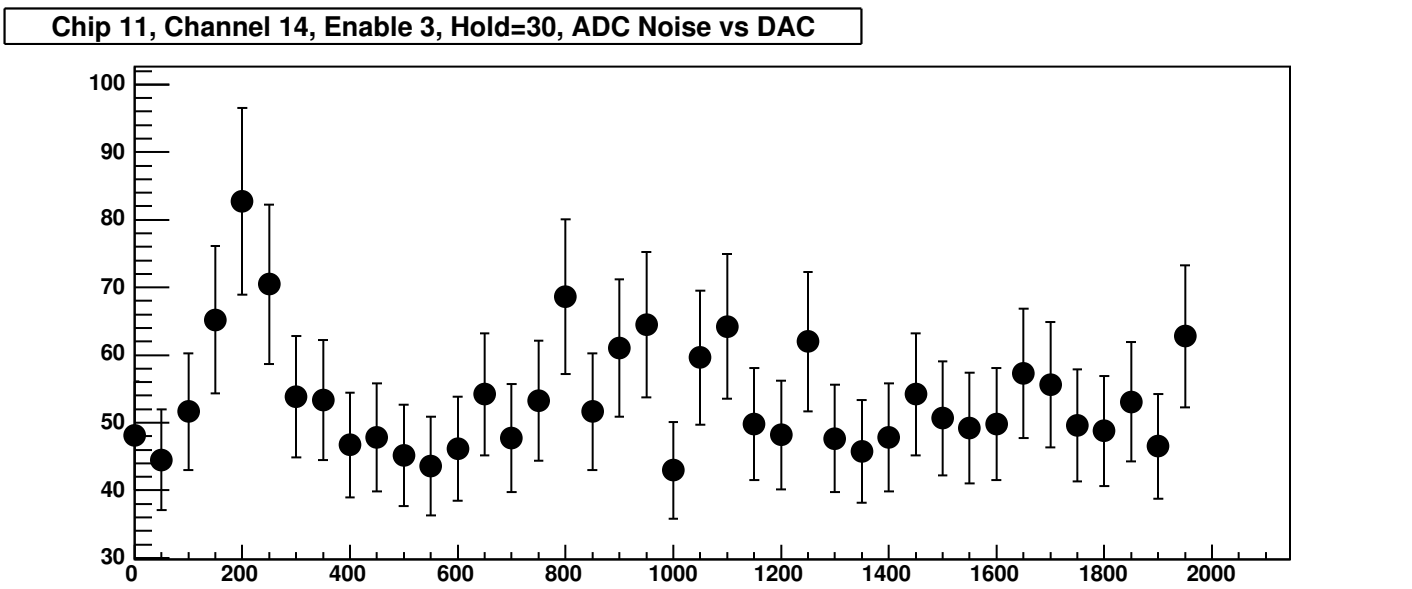
Chip 11, Channel 14, Enable 2, Hold=30, ADC Noise vs DAC

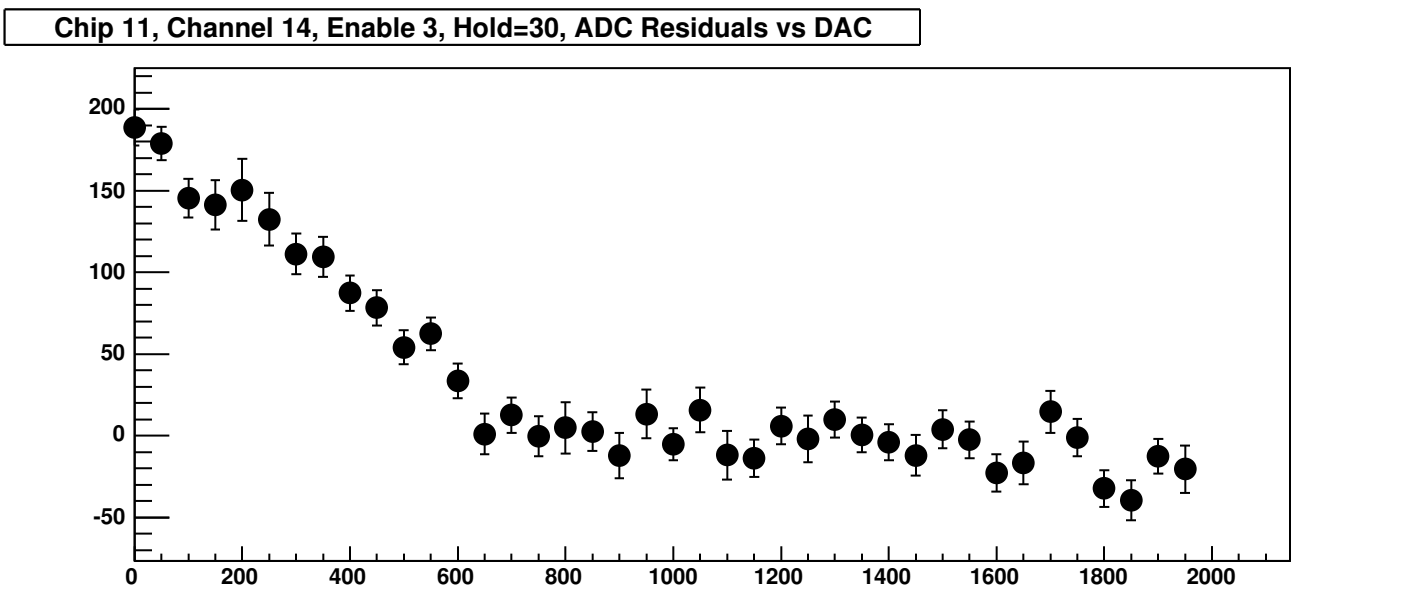


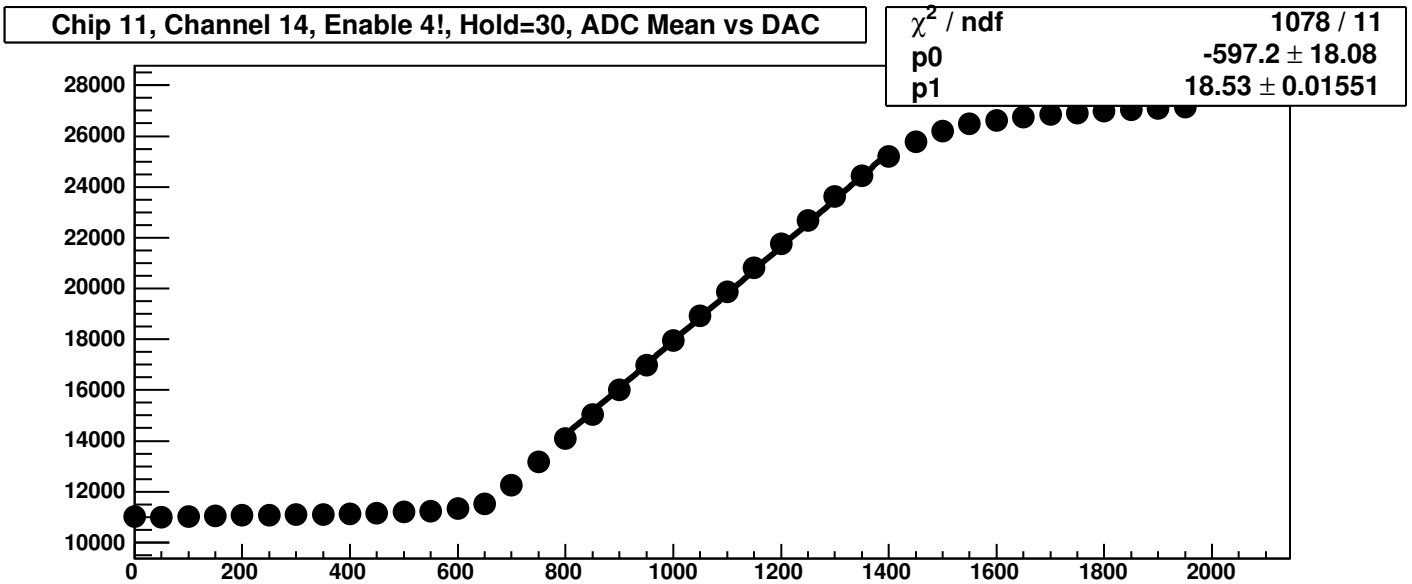
Chip 11, Channel 14, Enable 2, Hold=30, ADC Residuals vs DAC

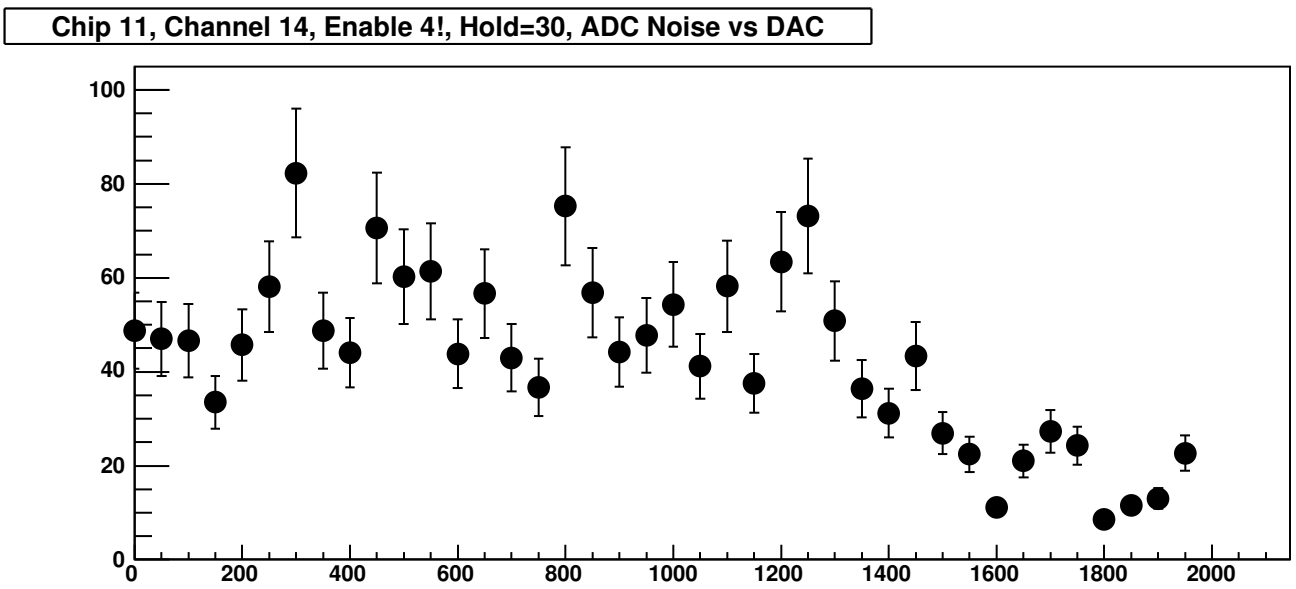


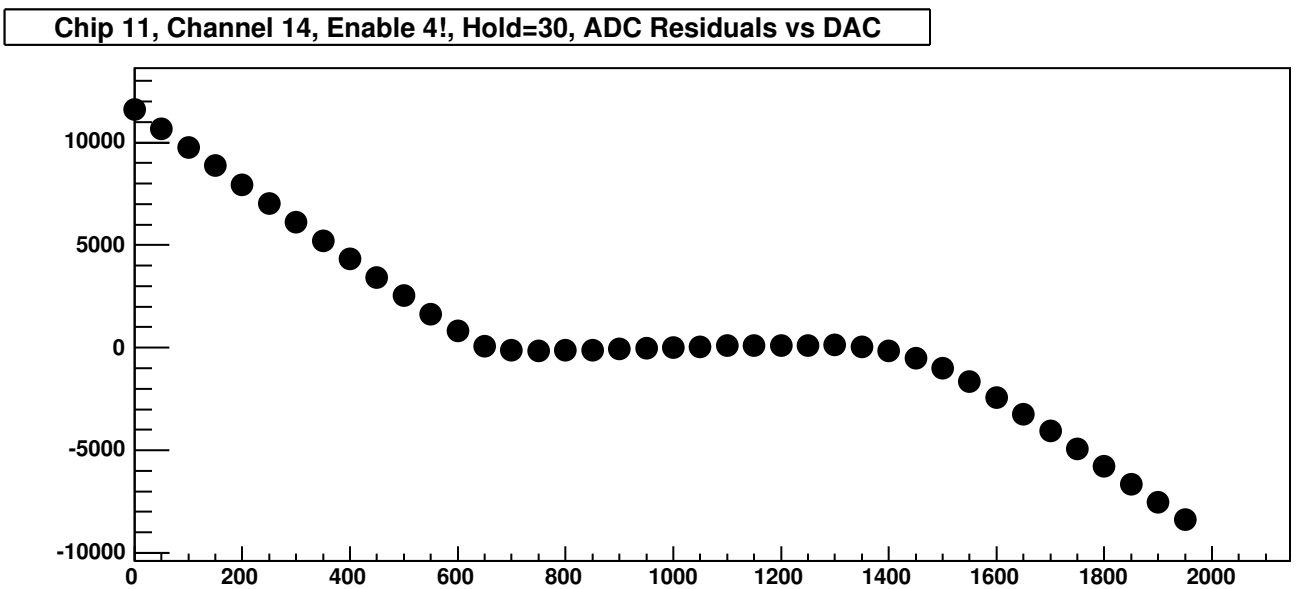

Chip 11, Channel 14, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 6.722 / 11
p0 -1304 ± 20.76
p1 0.2652 ± 0.01817
-800
-850
-900
-950
-1000
-1050
-1100
-1150
0
200
400
600
800
1000
1200
1400
1600
1800
2000

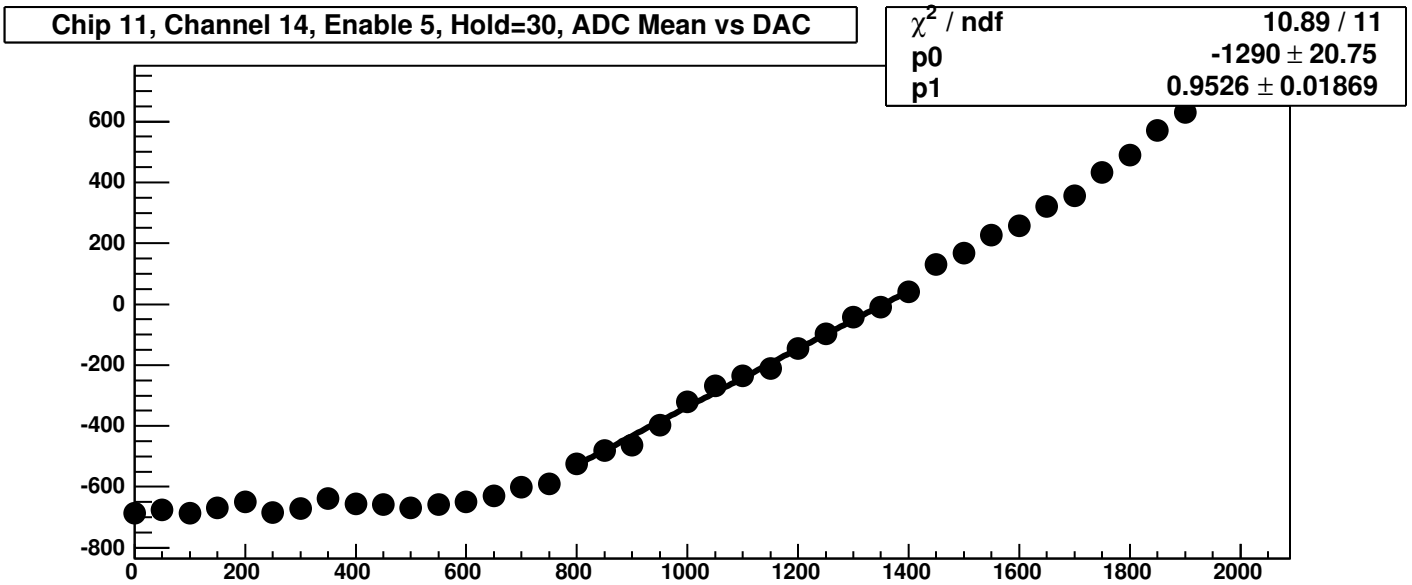

Chip 11, Channel 14, Enable 3, Hold=30, ADC Noise vs DAC
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

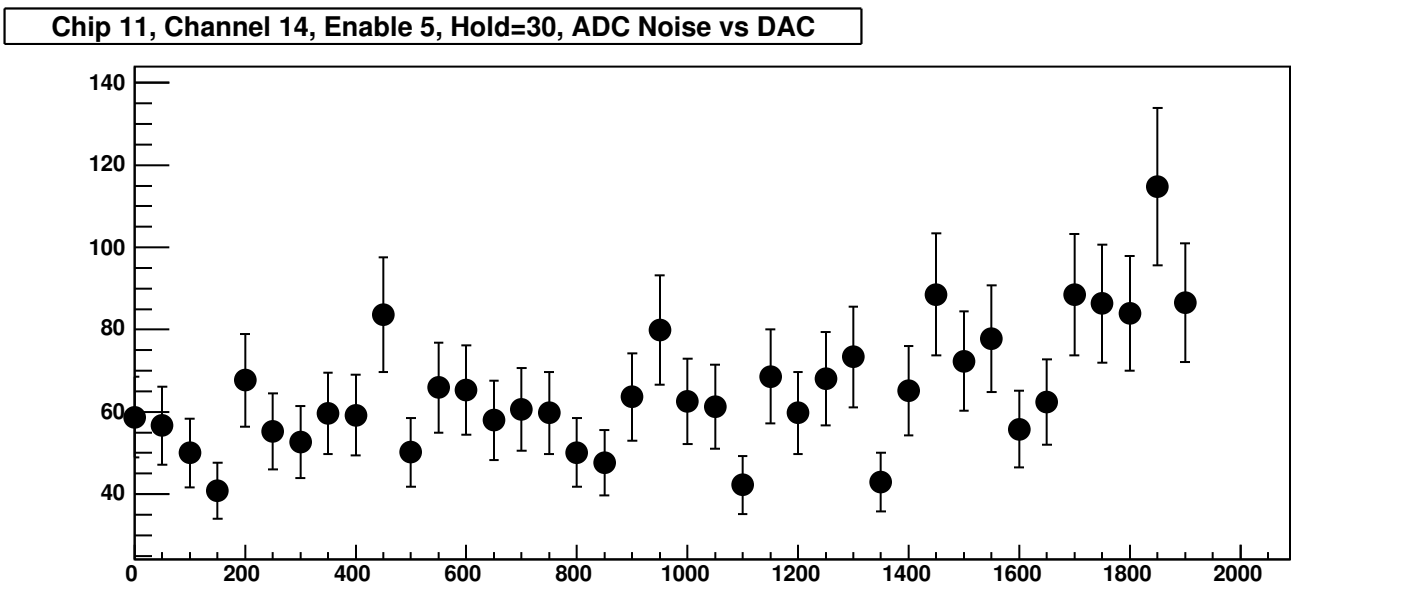

Chip 11, Channel 14, Enable 3, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

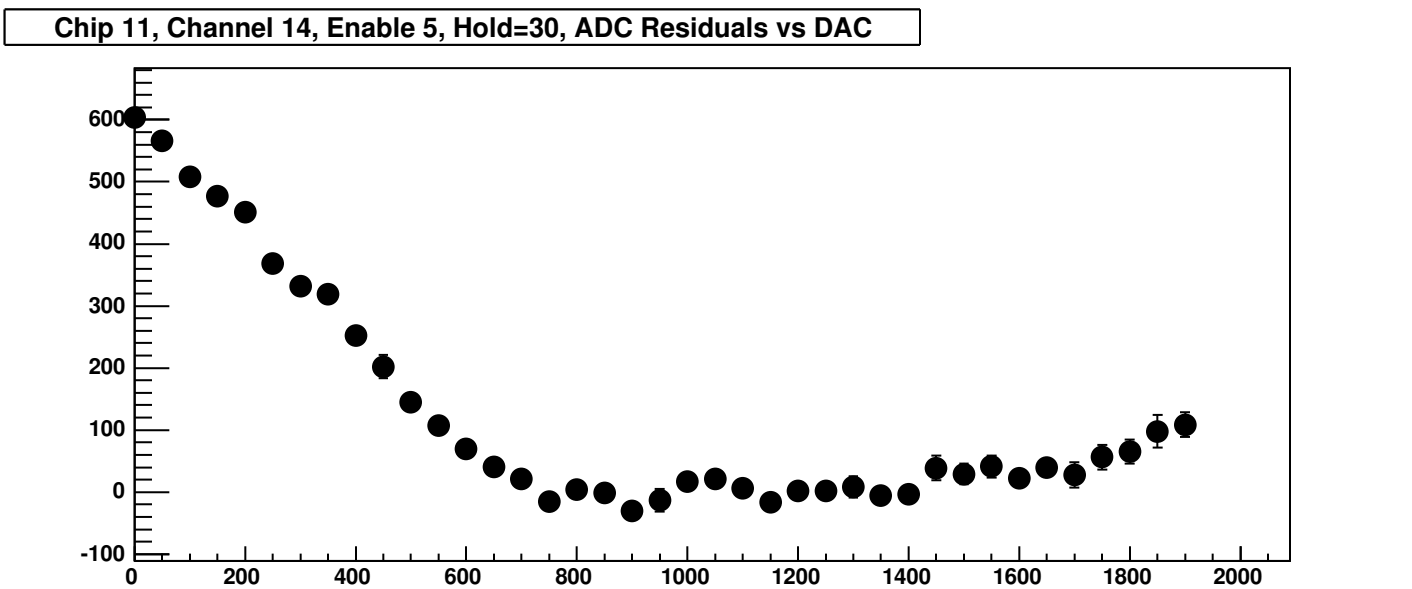

Chip 11, Channel 14, Enable 4!, Hold=30, ADC Mean vs DAC
χ² / ndf 1078 / 11
p0 -597.2 ± 18.08
p1 18.53 ± 0.01551
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

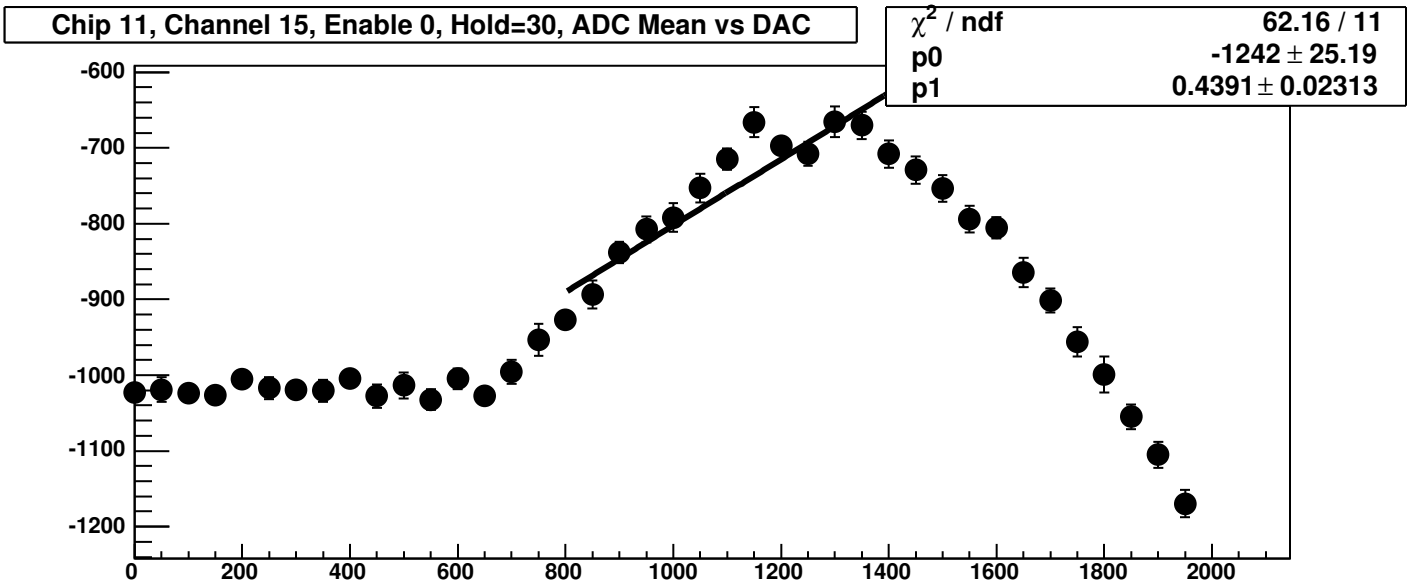

Chip 11, Channel 14, Enable 4!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

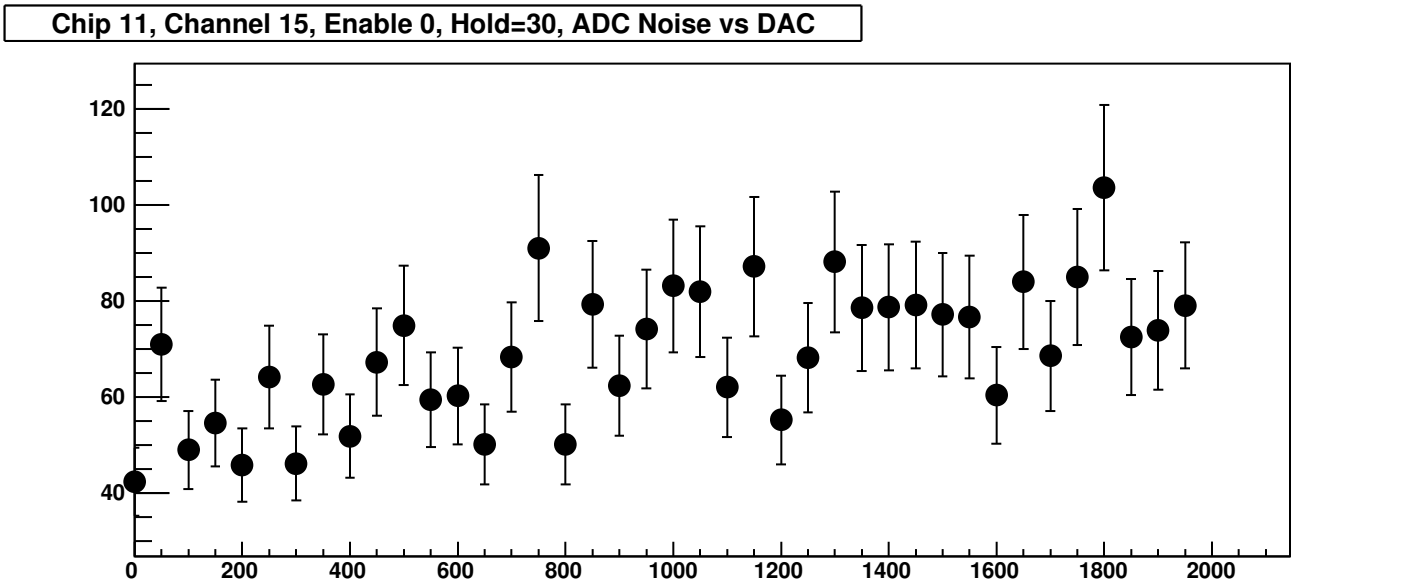

Chip 11, Channel 14, Enable 4!, Hold=30, ADC Residuals vs DAC
10000
5000
0
-5000
-10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

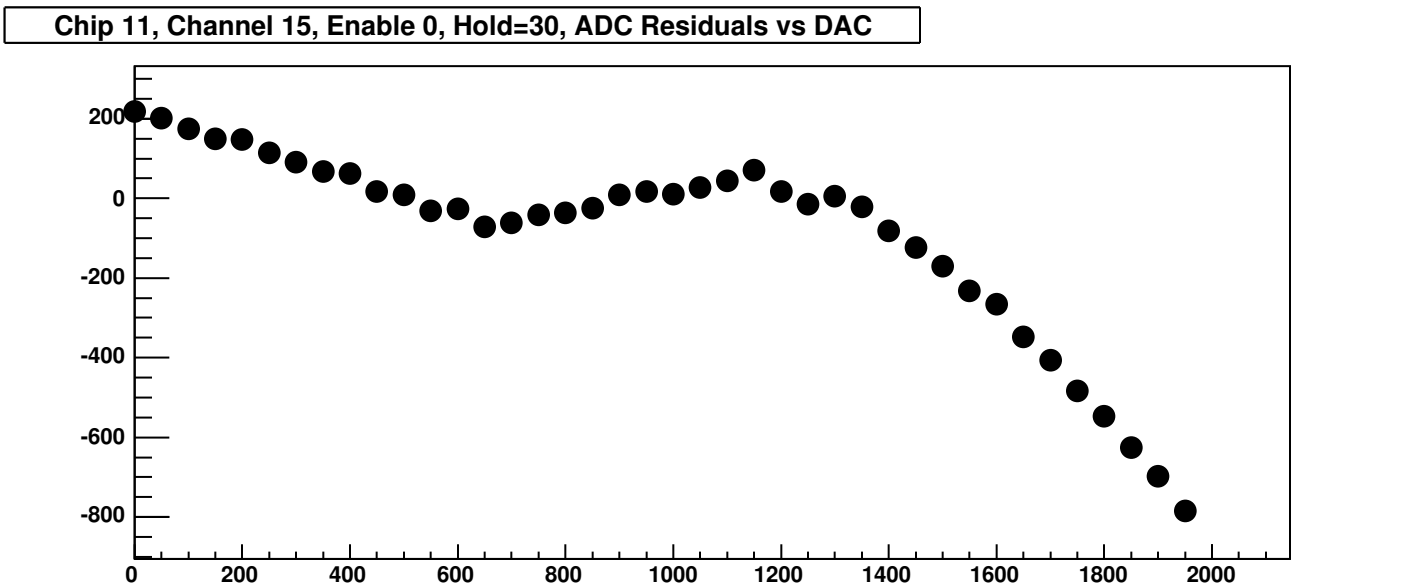

Chip 11, Channel 14, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 10.89 / 11
p0 -1290 ± 20.75
p1 0.9526 ± 0.01869
600
400
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

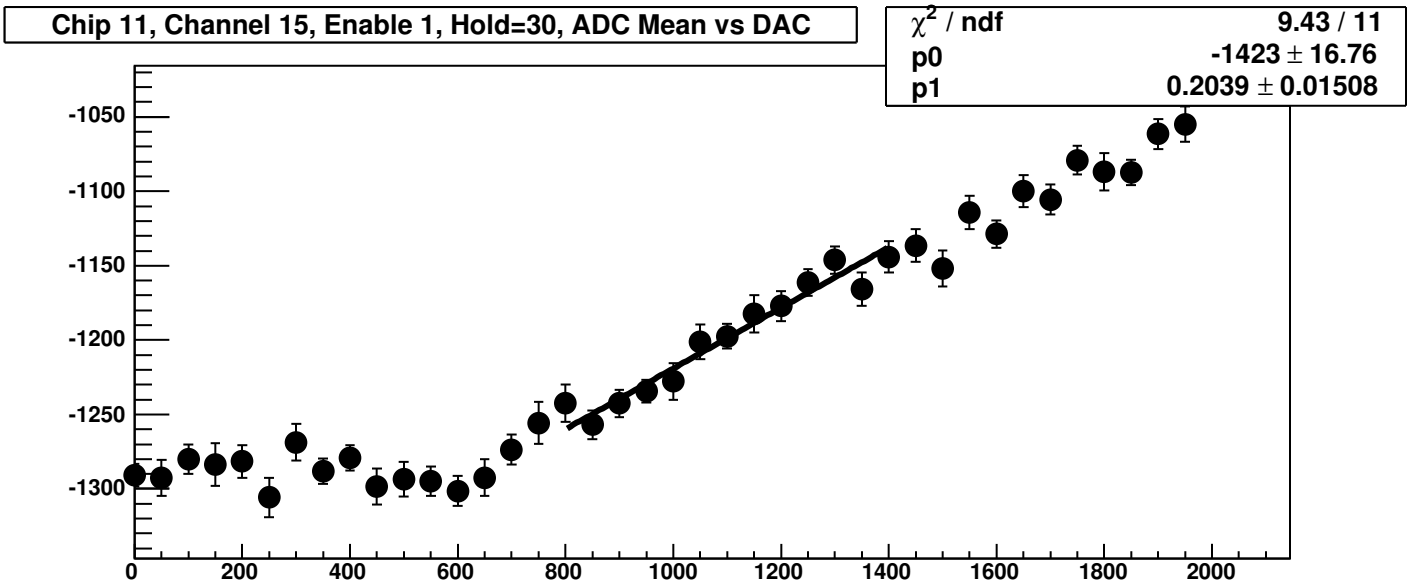

Chip 11, Channel 14, Enable 5, Hold=30, ADC Noise vs DAC
140
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

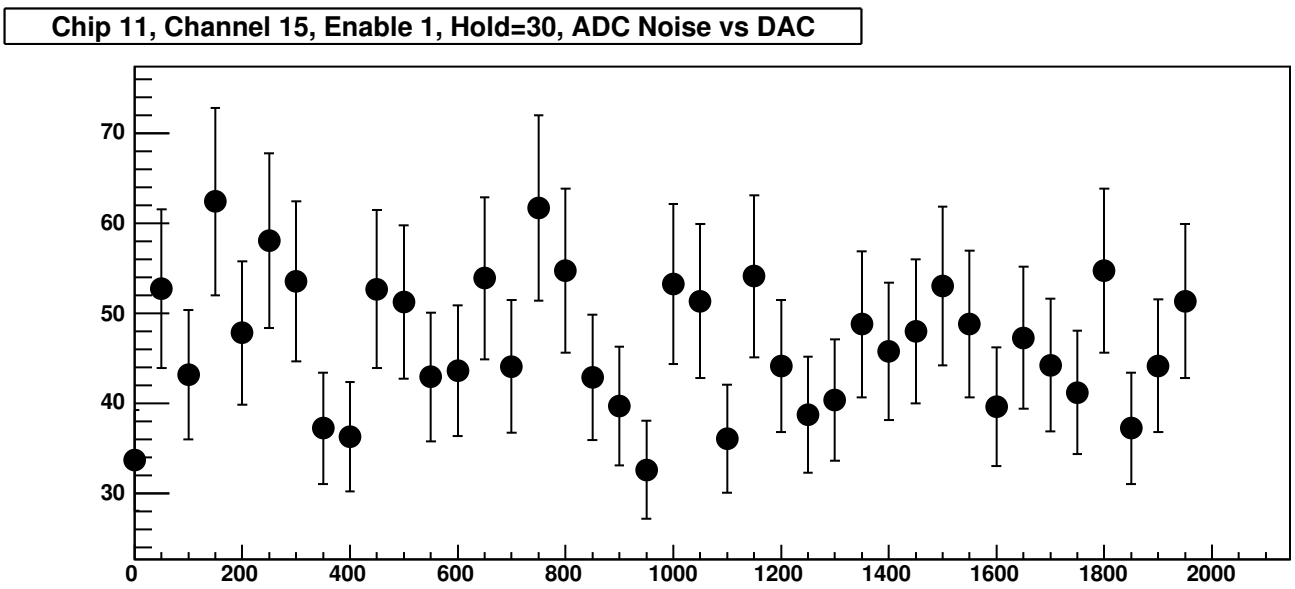

Chip 11, Channel 14, Enable 5, Hold=30, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
0
200
400
600
800
1000
1200
1400
1600
1800
2000

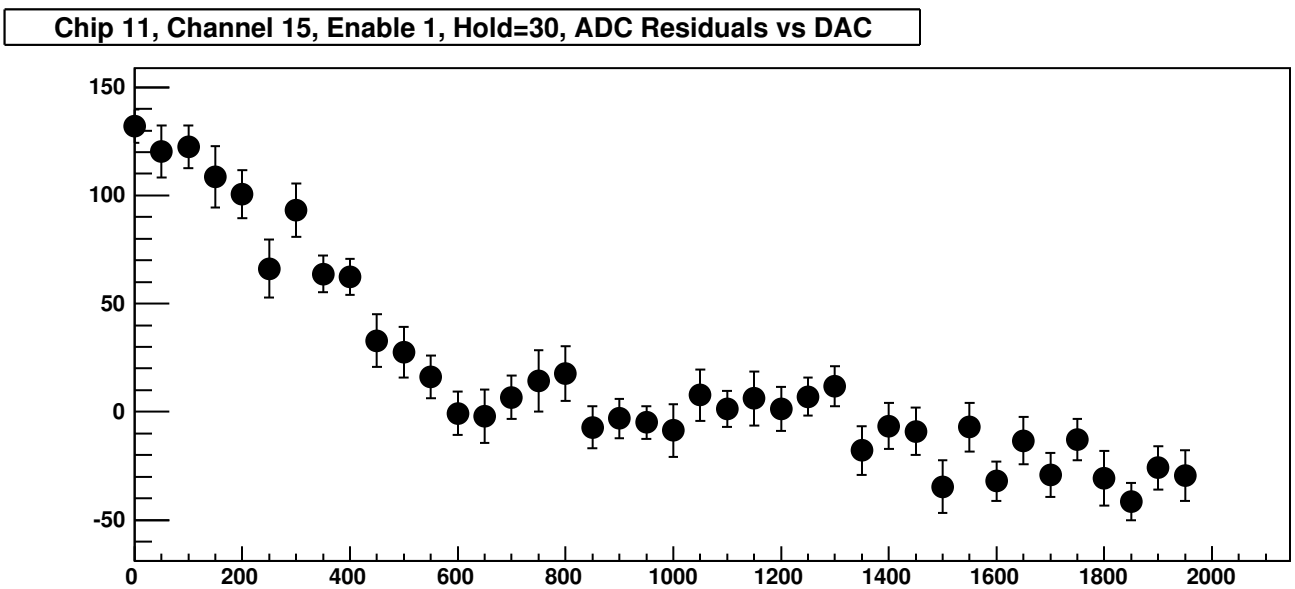

Chip 11, Channel 15, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 62.16 / 11
p0 -1242 ± 25.19
p1 0.4391 ± 0.02313
-600
-700
-800
-900
-1000
-1100
-1200
0
200
400
600
800
1000
1200
1400
1600
1800
2000

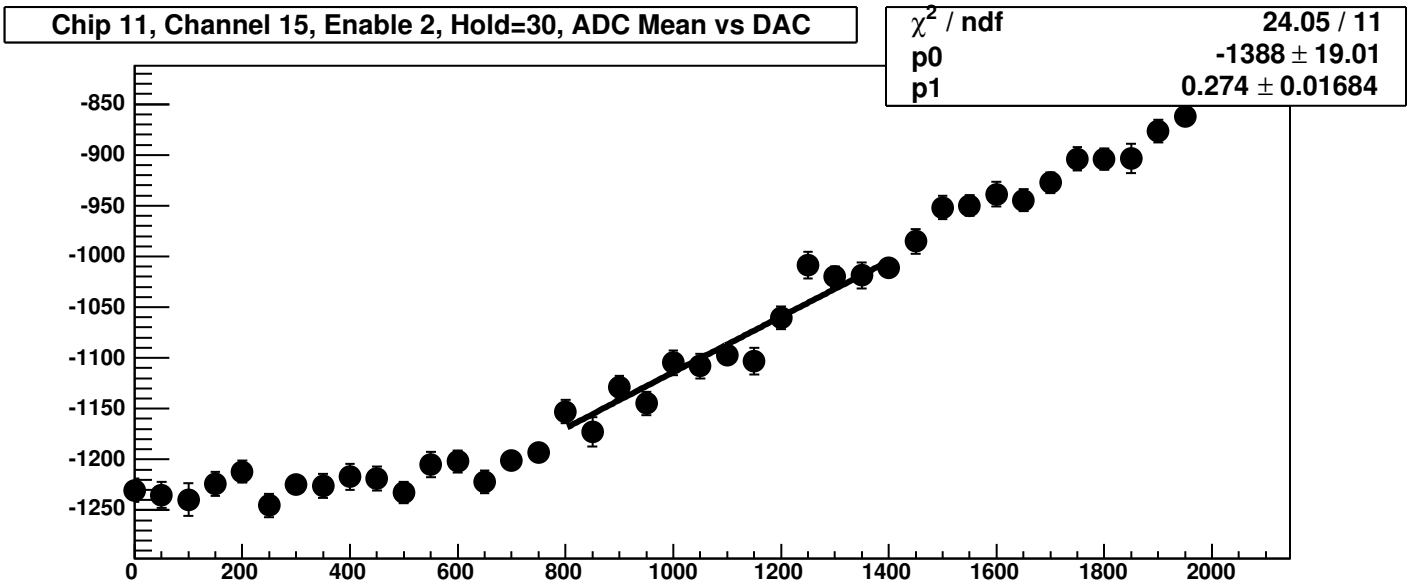

Chip 11, Channel 15, Enable 0, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

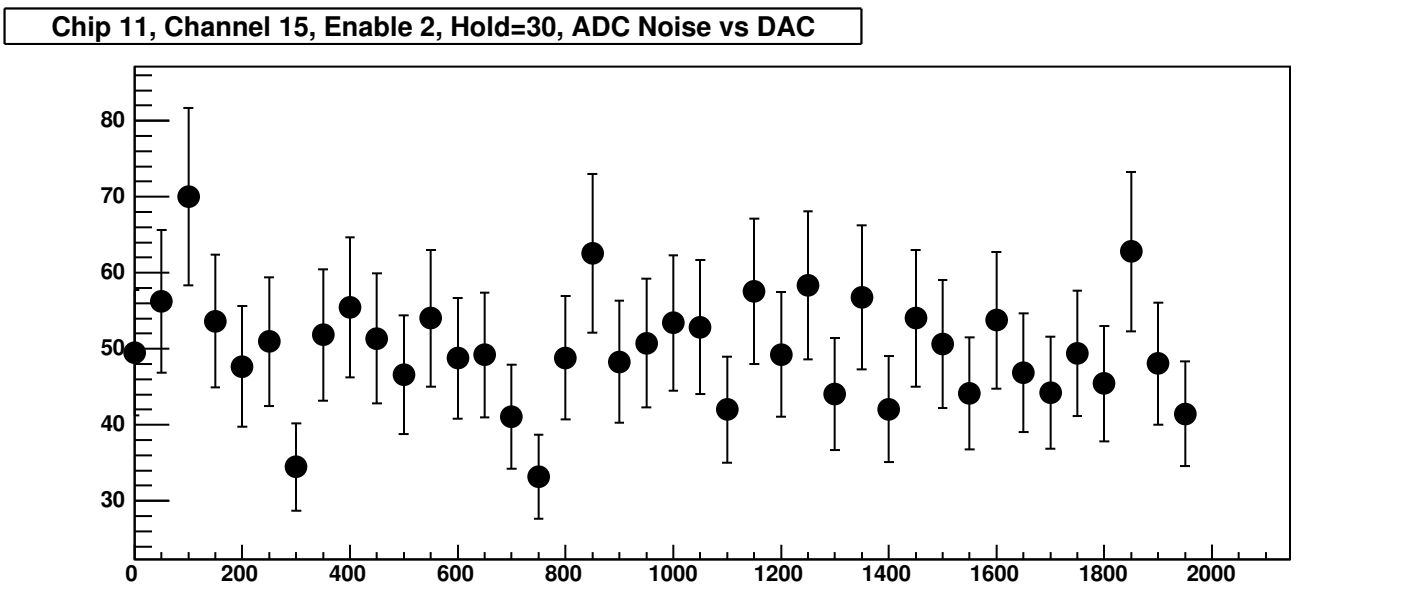

Chip 11, Channel 15, Enable 0, Hold=30, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
0
200
400
600
800
1000
1200
1400
1600
1800
2000

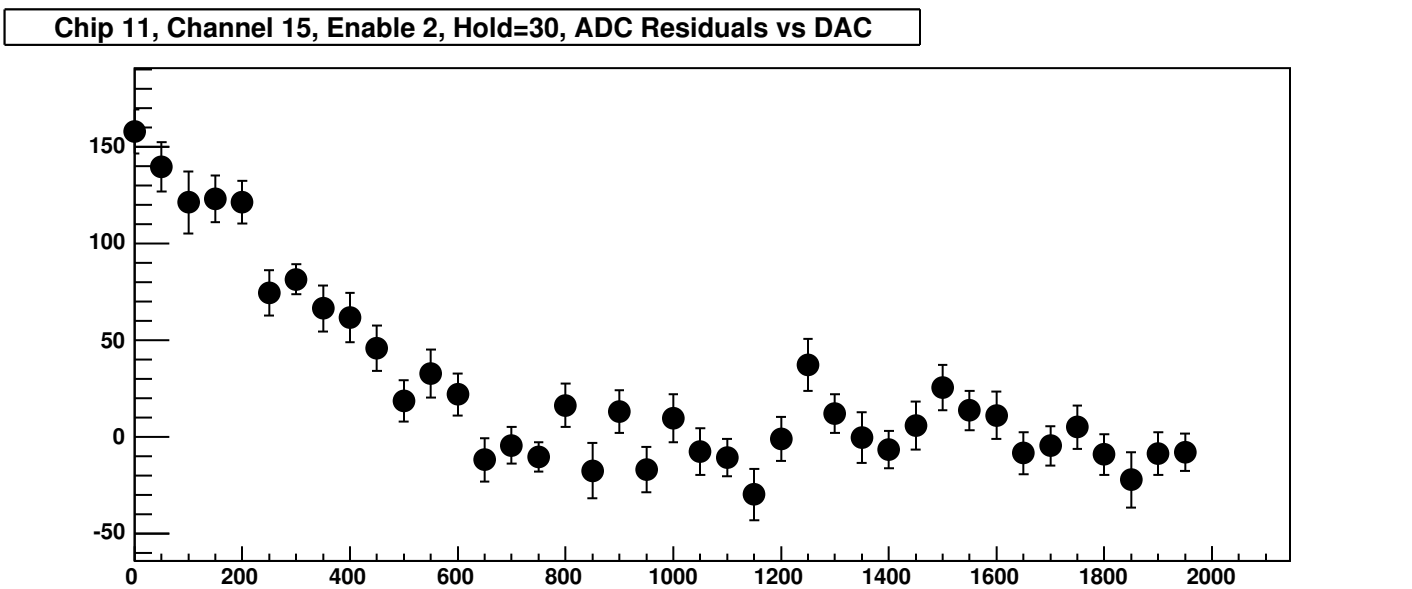

Chip 11, Channel 15, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 9.43 / 11
p0 -1423 ± 16.76
p1 0.2039 ± 0.01508
-1050
-1100
-1150
-1200
-1250
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

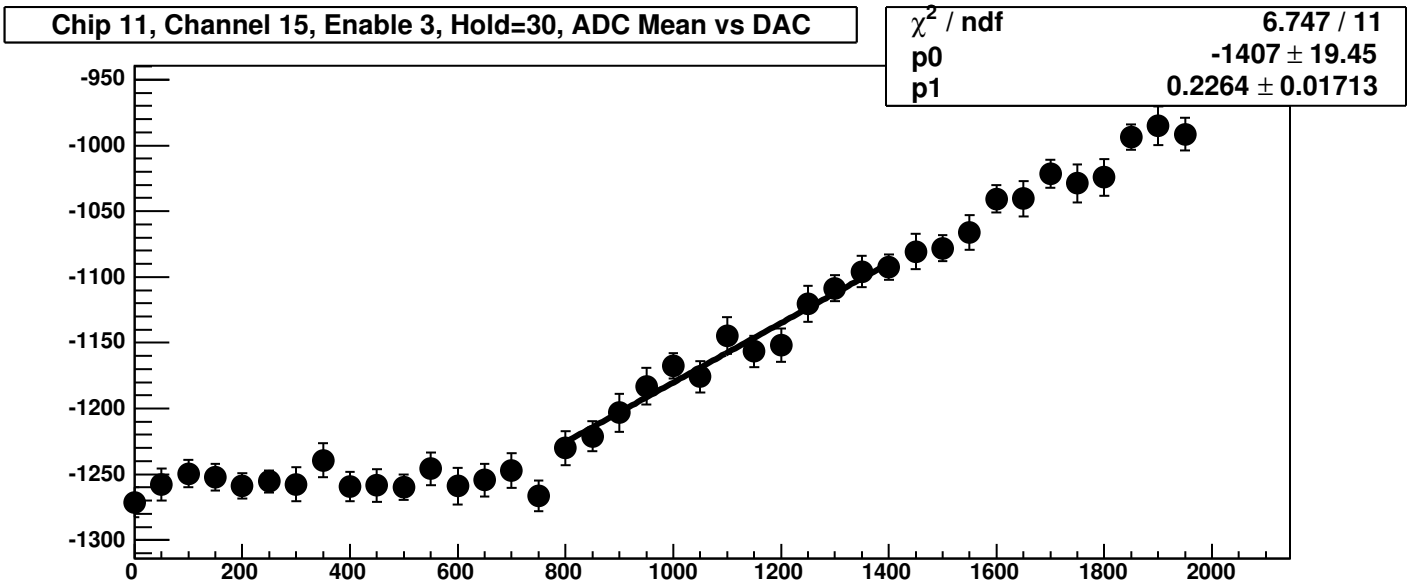

Chip 11, Channel 15, Enable 1, Hold=30, ADC Noise vs DAC
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

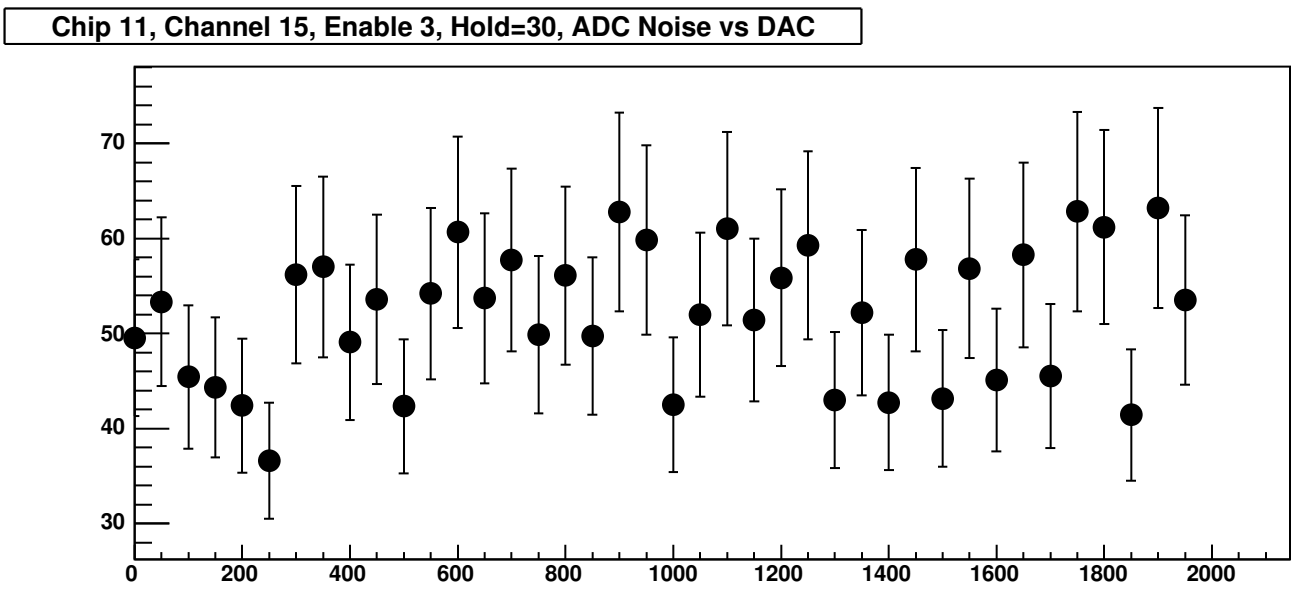

Chip 11, Channel 15, Enable 1, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

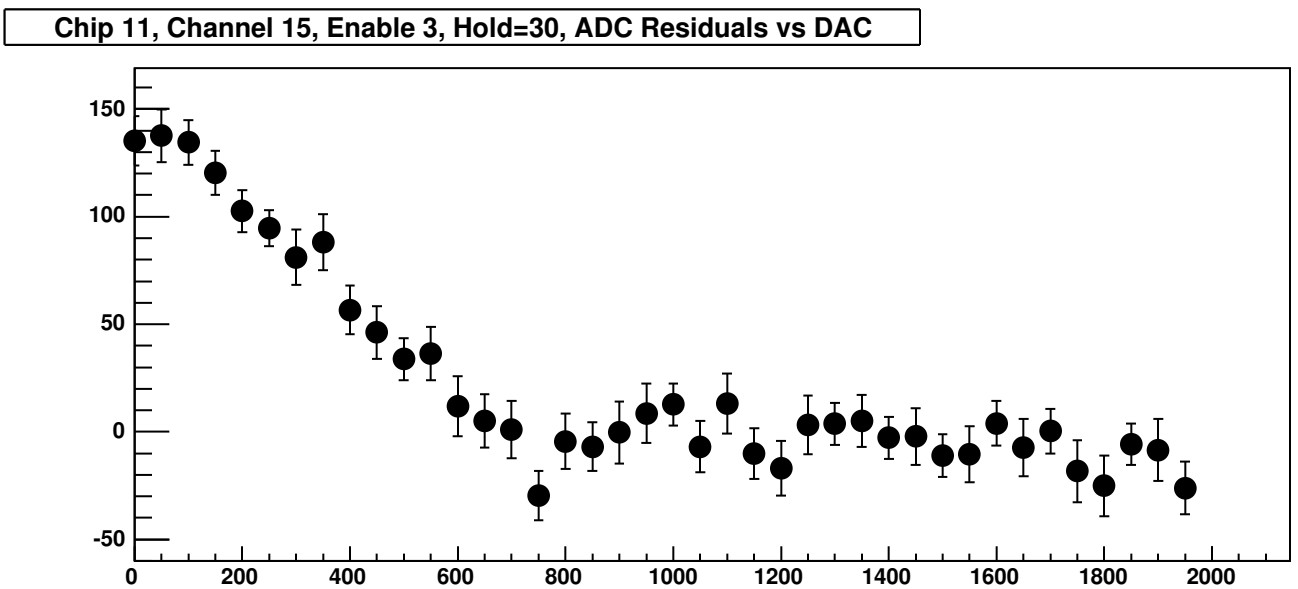

Chip 11, Channel 15, Enable 2, Hold=30, ADC Mean vs DAC
χ² / ndf 24.05 / 11
p0 -1388 ± 19.01
p1 0.274 ± 0.01684
-850
-900
-950
-1000
-1050
-1100
-1150
-1200
-1250
0
200
400
600
800
1000
1200
1400
1600
1800
2000

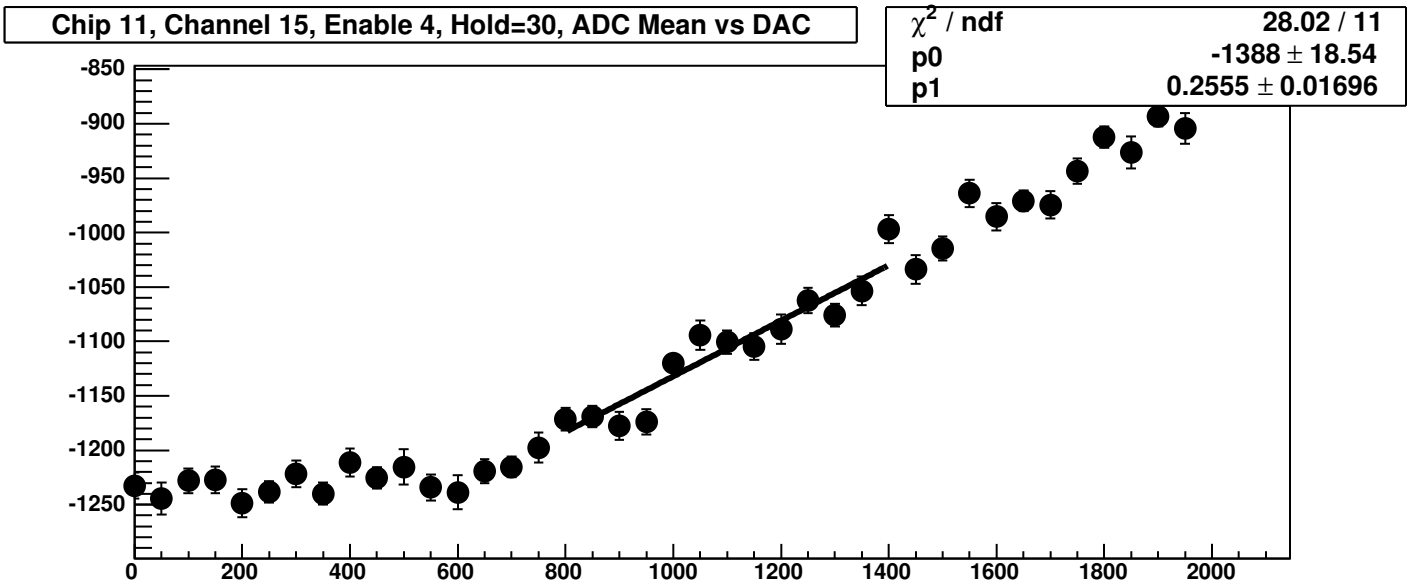

Chip 11, Channel 15, Enable 2, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

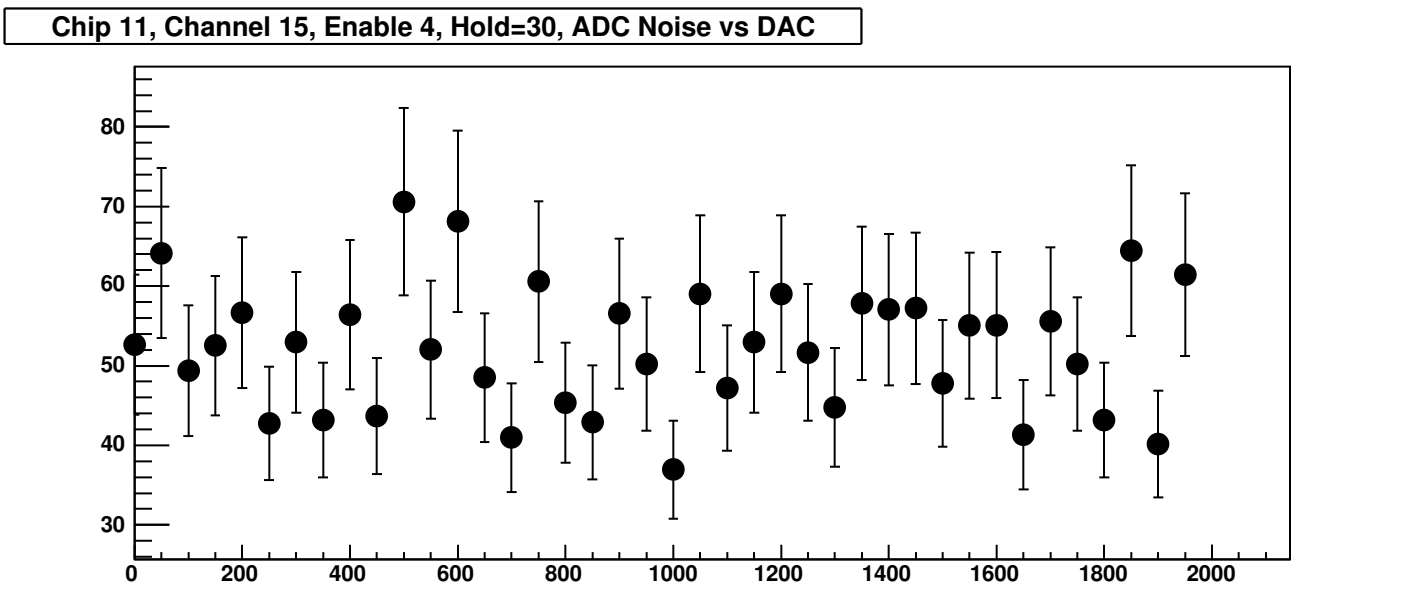

Chip 11, Channel 15, Enable 2, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

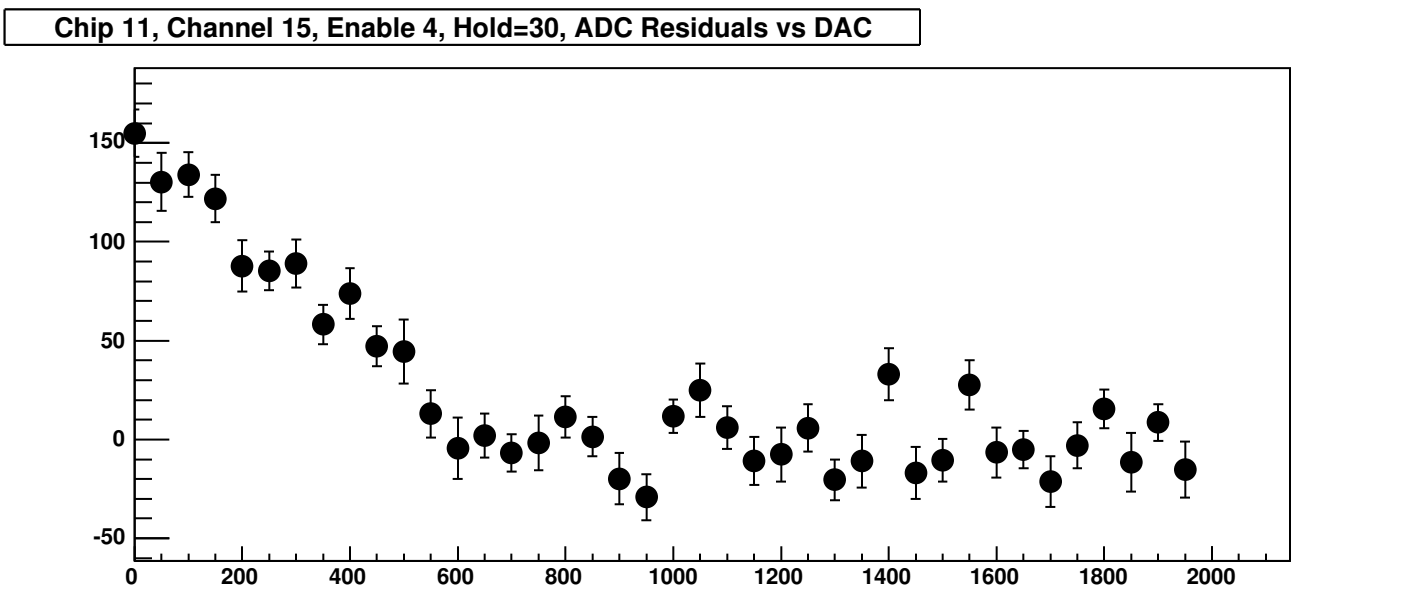

Chip 11, Channel 15, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 6.747 / 11
p0 -1407 ± 19.45
p1 0.2264 ± 0.01713
-950
-1000
-1050
-1100
-1150
-1200
-1250
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

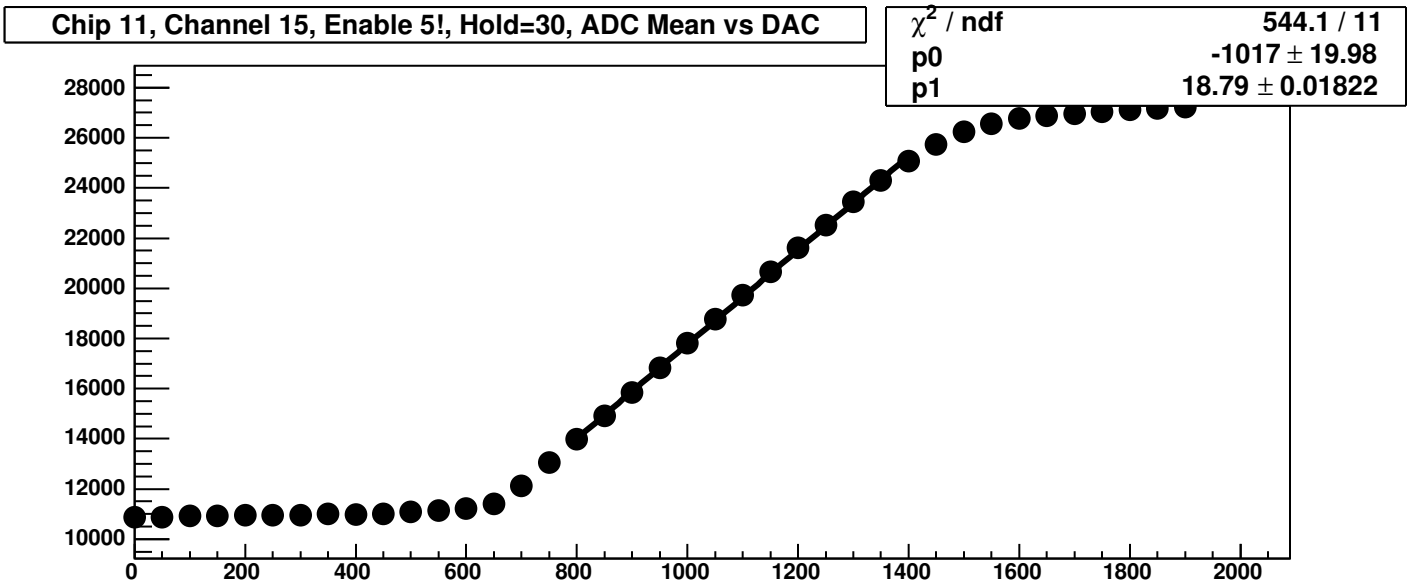

Chip 11, Channel 15, Enable 3, Hold=30, ADC Noise vs DAC
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

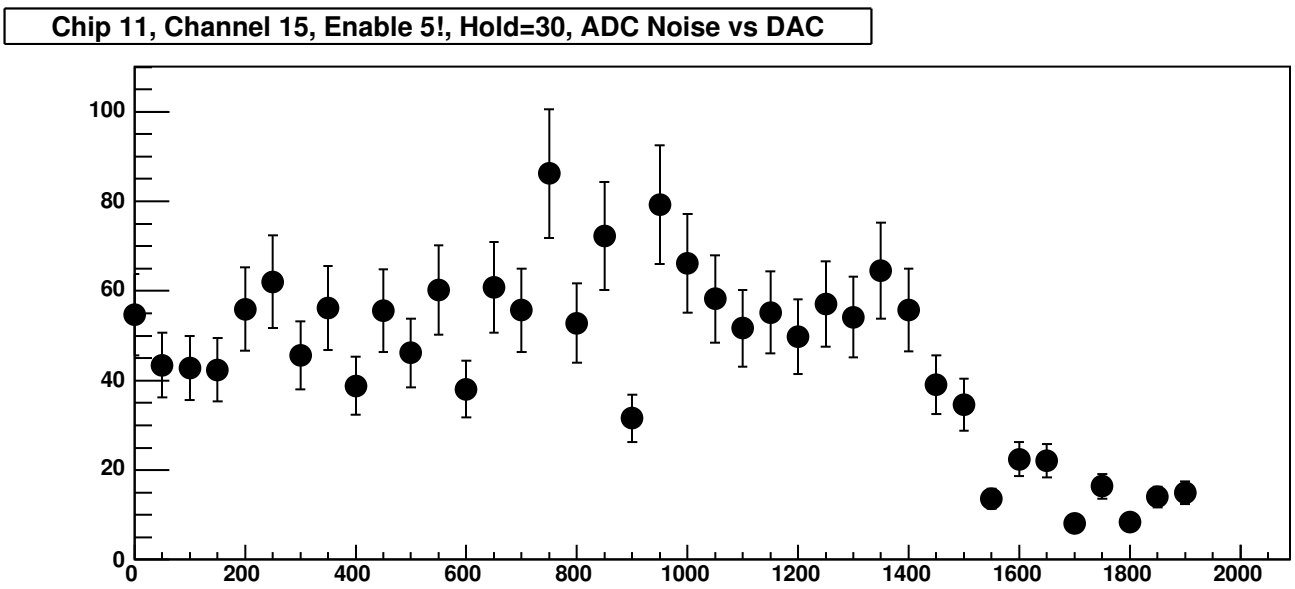

Chip 11, Channel 15, Enable 3, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

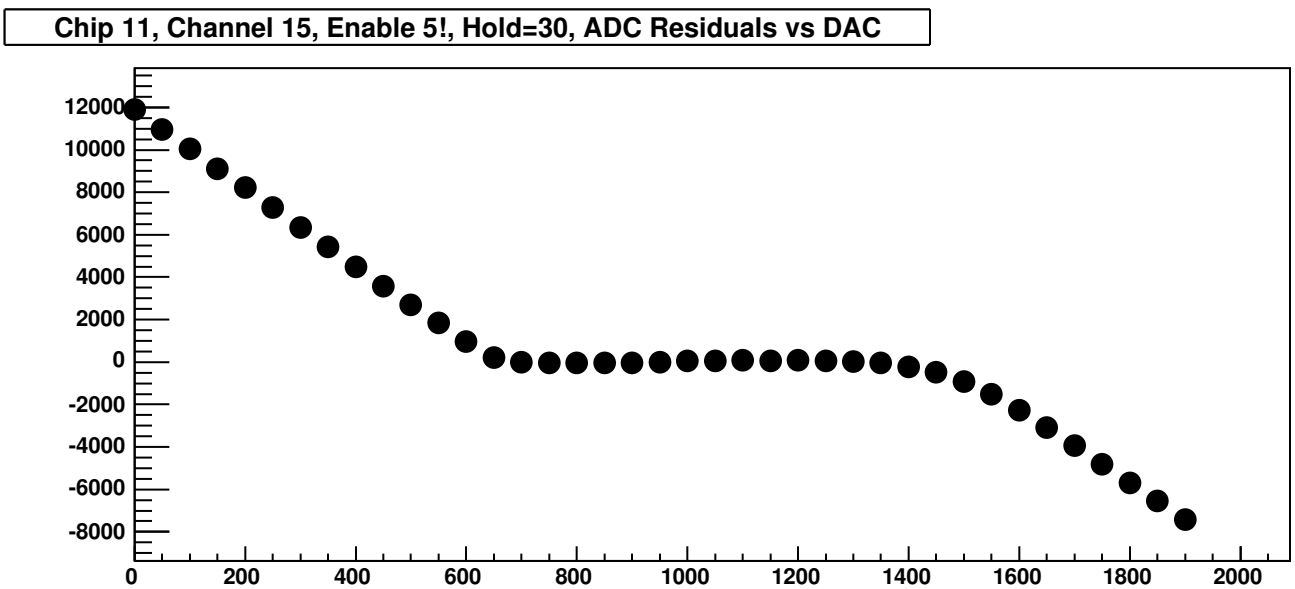

Chip 11, Channel 15, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 28.02 / 11
p0 -1388 ± 18.54
p1 0.2555 ± 0.01696
-850
-900
-950
-1000
-1050
-1100
-1150
-1200
-1250
0
200
400
600
800
1000
1200
1400
1600
1800
2000

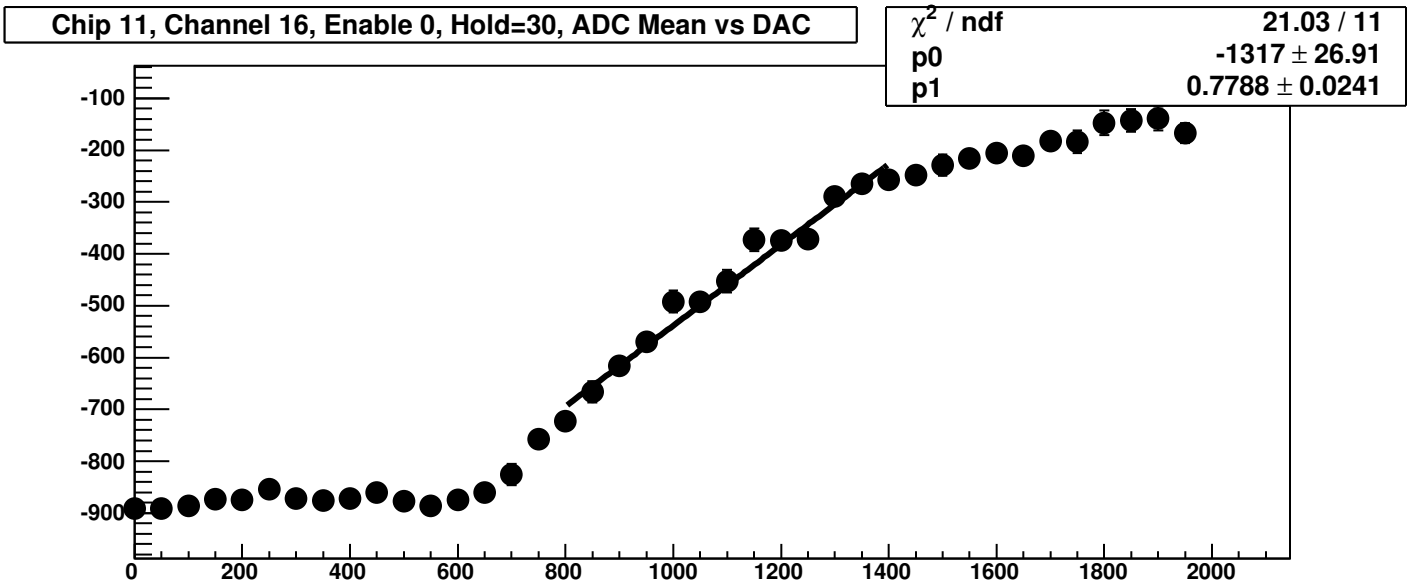

Chip 11, Channel 15, Enable 4, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

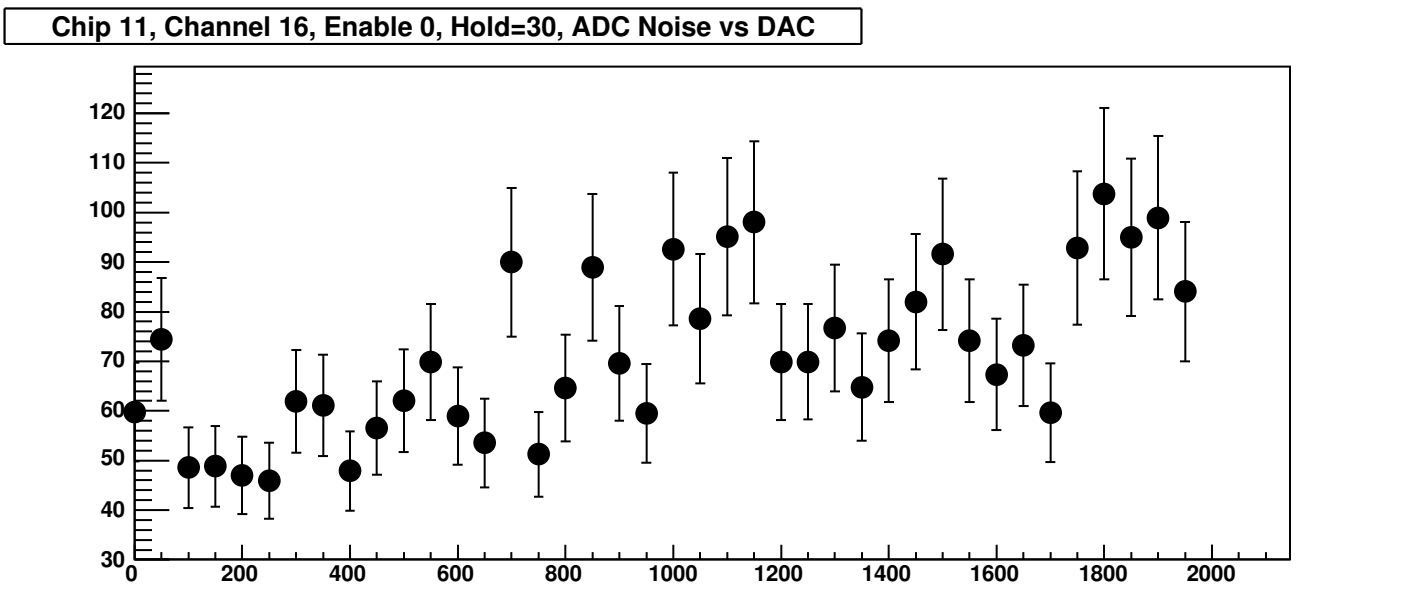

Chip 11, Channel 15, Enable 4, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

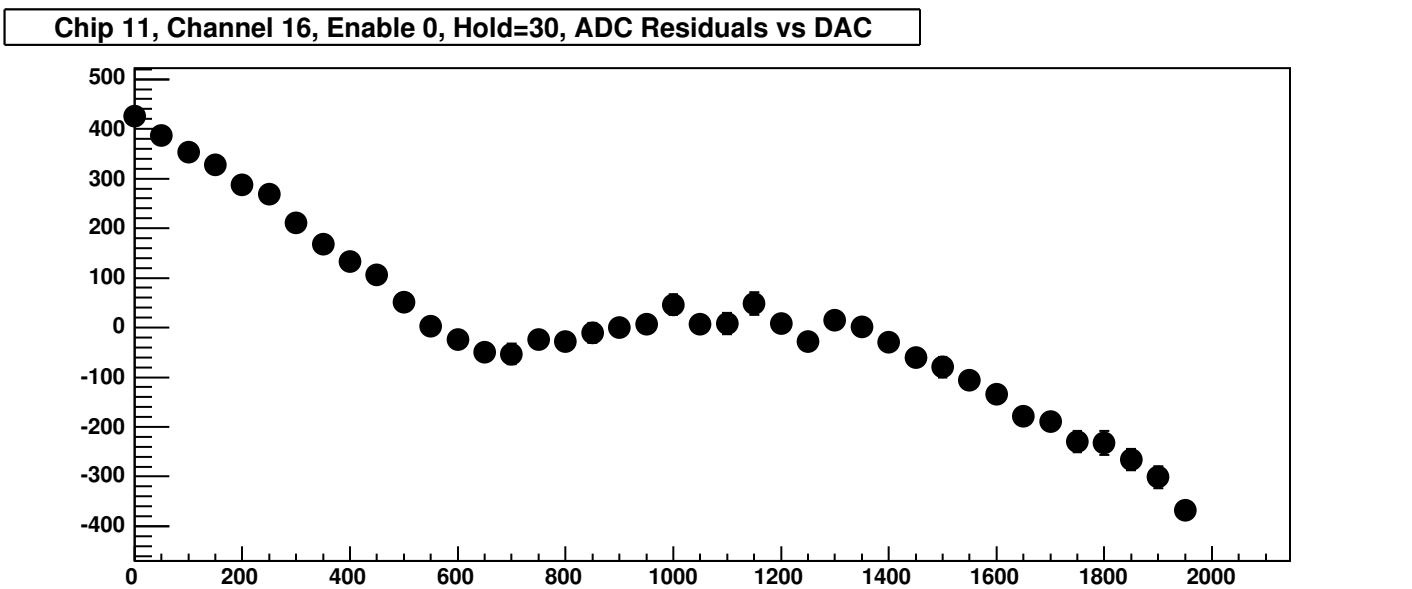

Chip 11, Channel 15, Enable 5!, Hold=30, ADC Mean vs DAC
χ² / ndf 544.1 / 11
p0 -1017 ± 19.98
p1 18.79 ± 0.01822
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

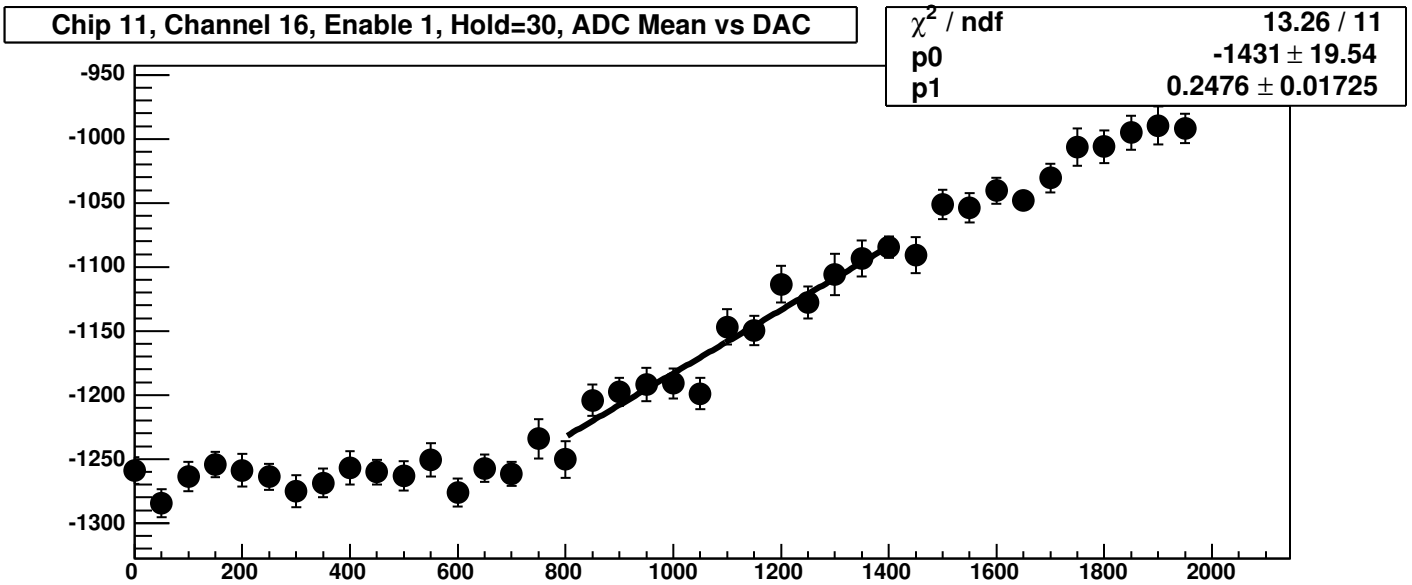

Chip 11, Channel 15, Enable 5!, Hold=30, ADC Noise vs DAC
100
80
60
40
20
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

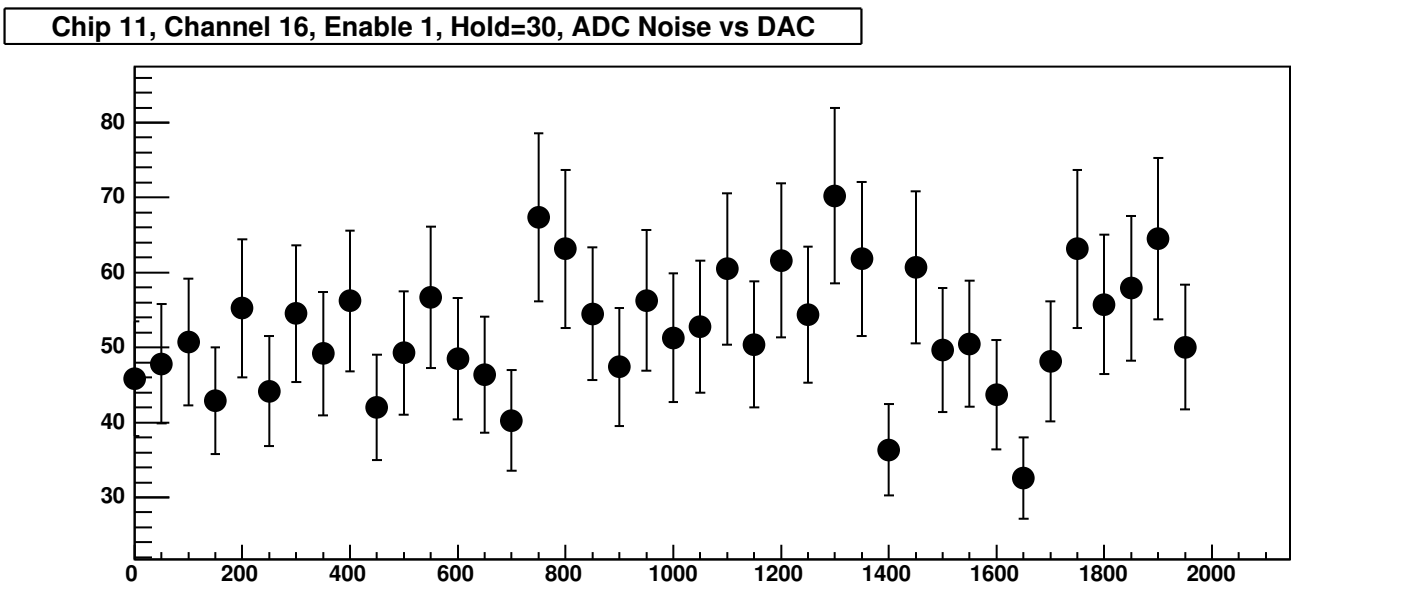

Chip 11, Channel 15, Enable 5!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

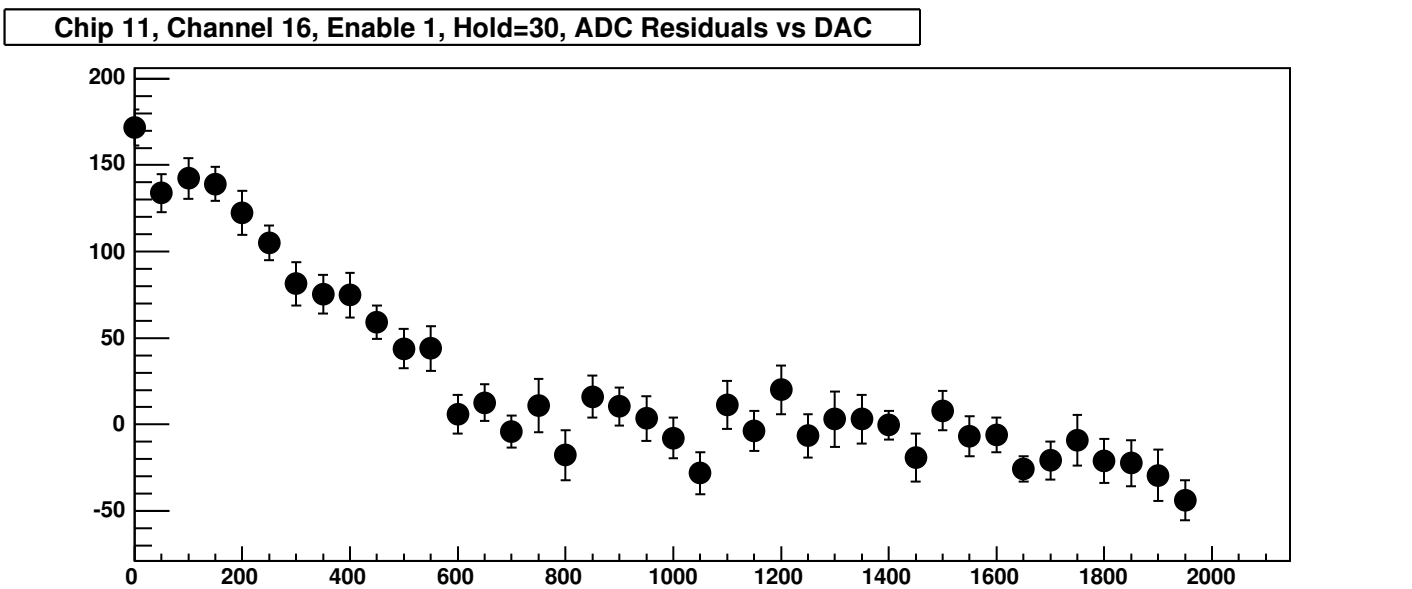

Chip 11, Channel 16, Enable 0, Hold=30, ADC Mean vs DAC
χ^2 / ndf 21.03 / 11
p0 -1317 ± 26.91
p1 0.7788 ± 0.0241
-100
-200
-300
-400
-500
-600
-700
-800
-900
0
200
400
600
800
1000
1200
1400
1600
1800
2000

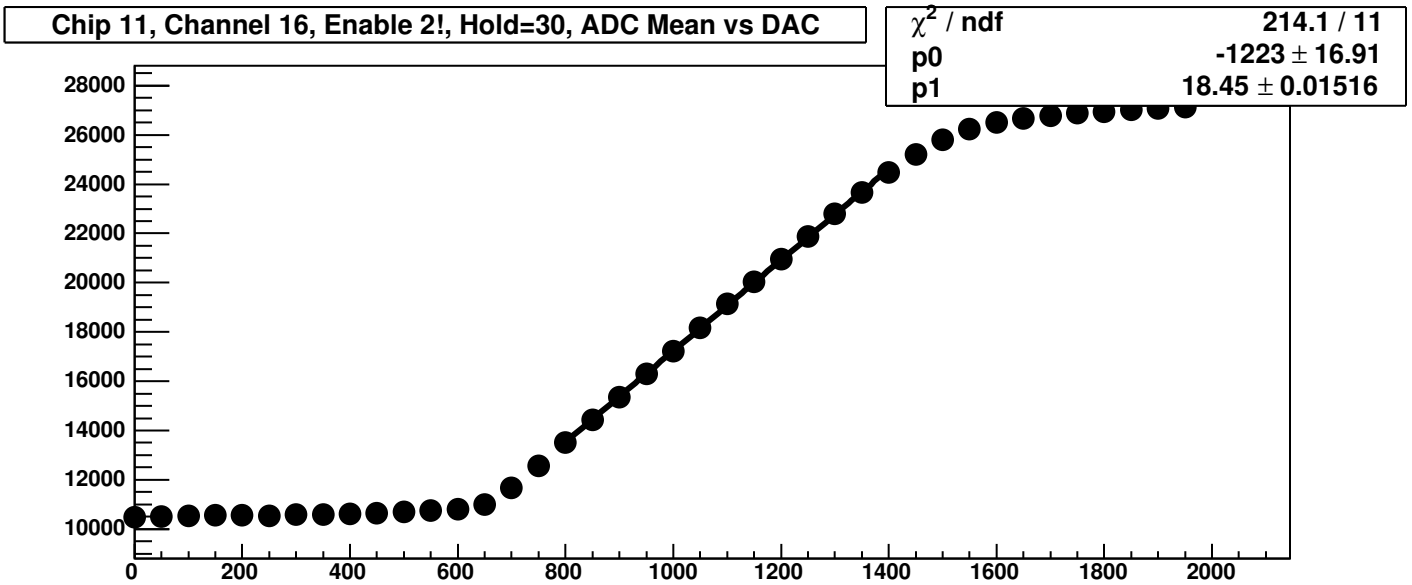

Chip 11, Channel 16, Enable 0, Hold=30, ADC Noise vs DAC
120
110
100
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

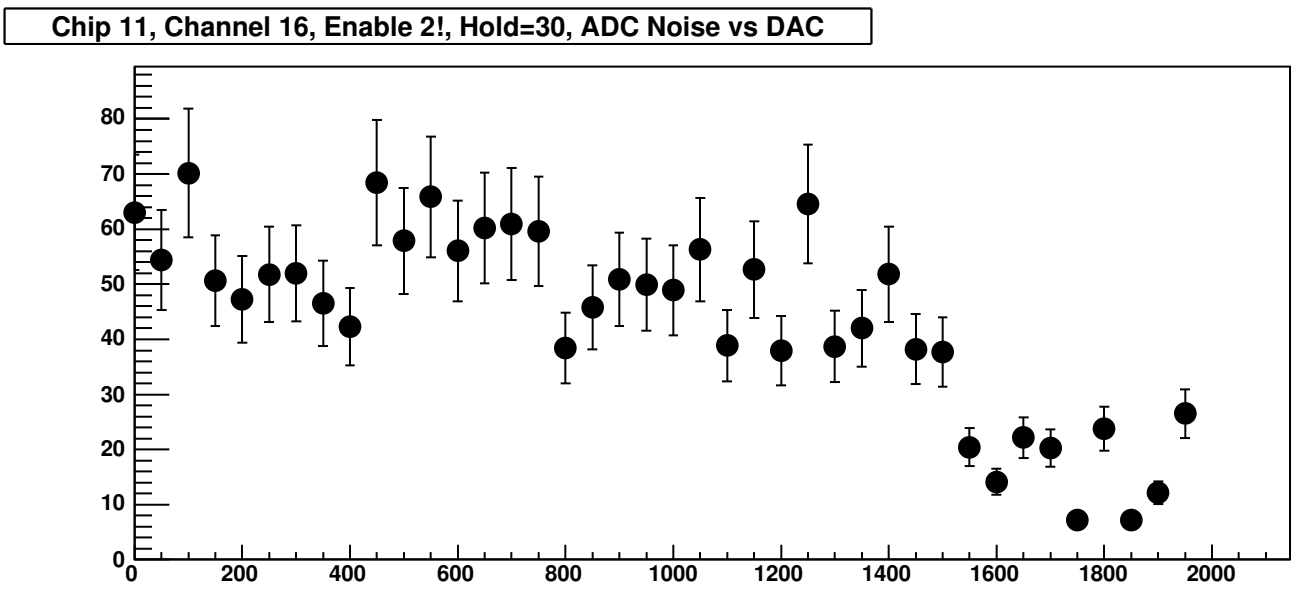

Chip 11, Channel 16, Enable 0, Hold=30, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
-300
-400
0
200
400
600
800
1000
1200
1400
1600
1800
2000

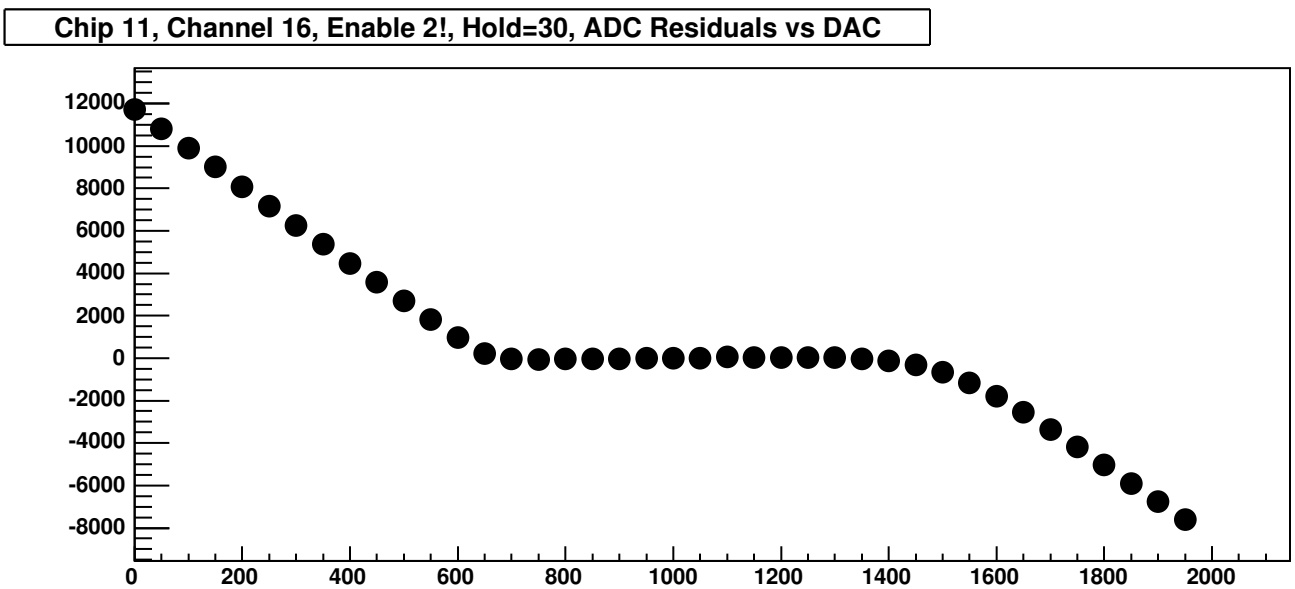

Chip 11, Channel 16, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 13.26 / 11
p0 -1431 ± 19.54
p1 0.2476 ± 0.01725
-950
-1000
-1050
-1100
-1150
-1200
-1250
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

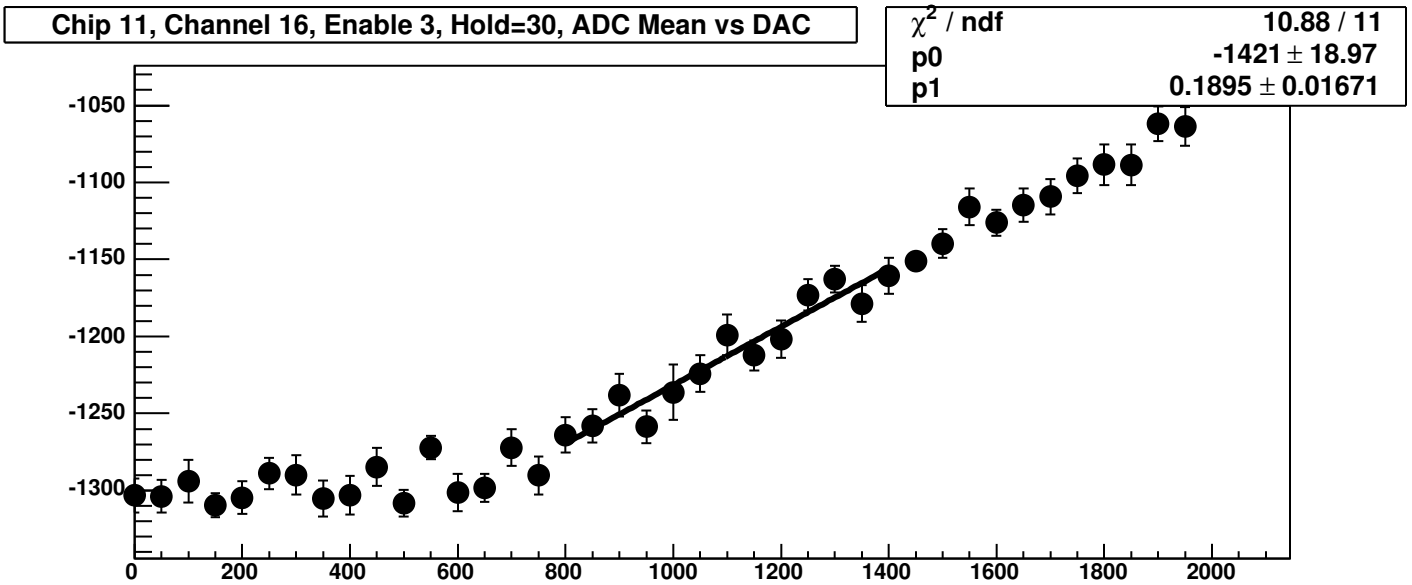

Chip 11, Channel 16, Enable 1, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

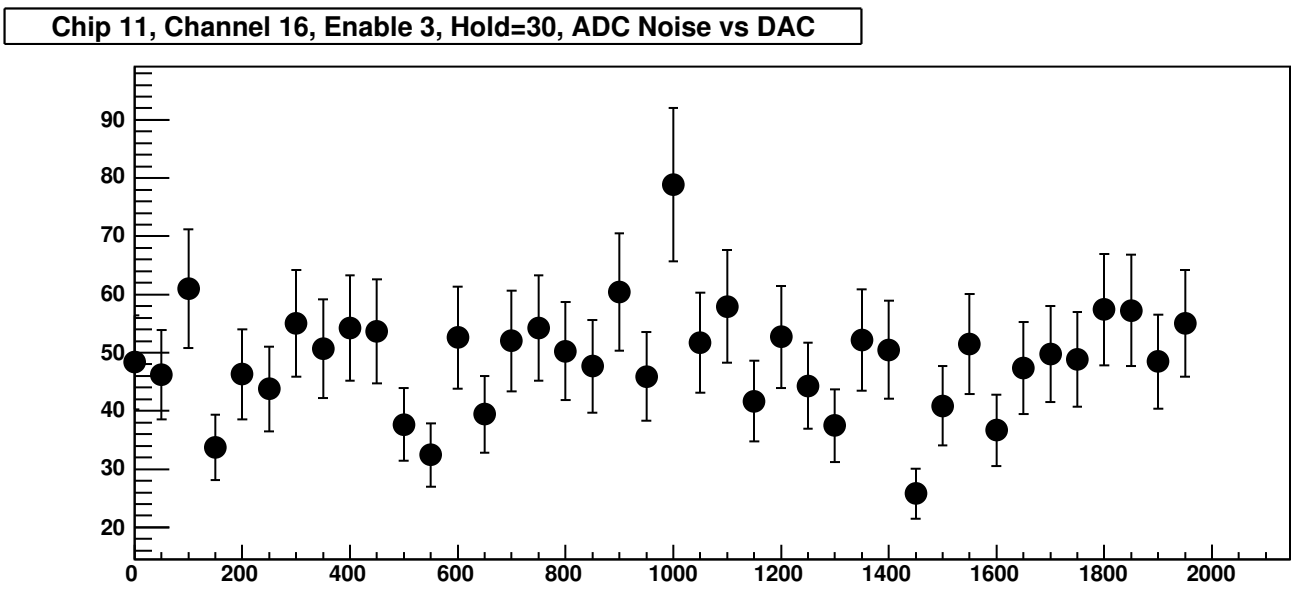

Chip 11, Channel 16, Enable 1, Hold=30, ADC Residuals vs DAC
200
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

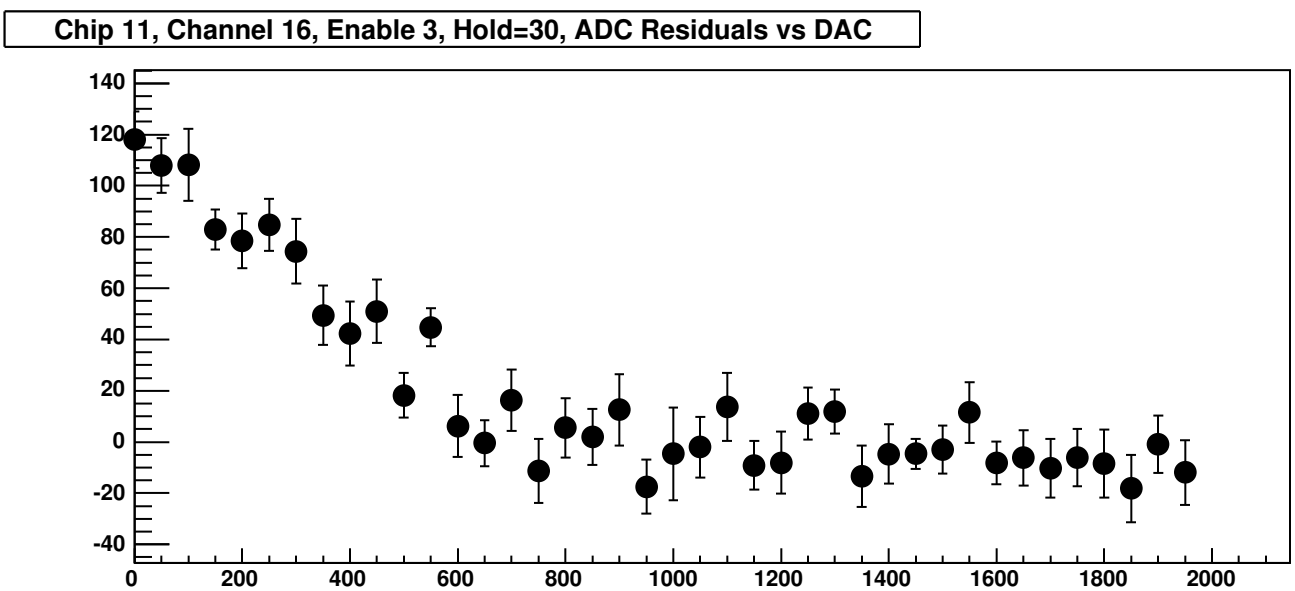

Chip 11, Channel 16, Enable 2!, Hold=30, ADC Mean vs DAC
χ² / ndf 214.1 / 11
p0 -1223 ± 16.91
p1 18.45 ± 0.01516
28000
26000
24000
22000
20000
18000
16000
14000
12000
10000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

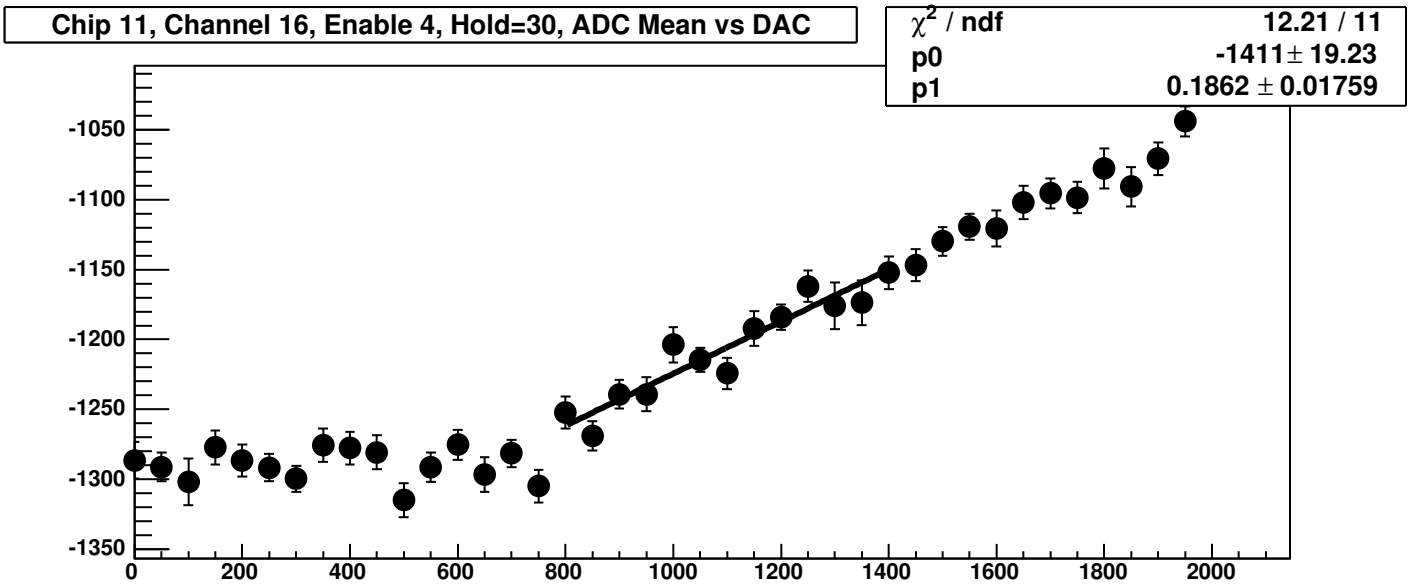

Chip 11, Channel 16, Enable 2!, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

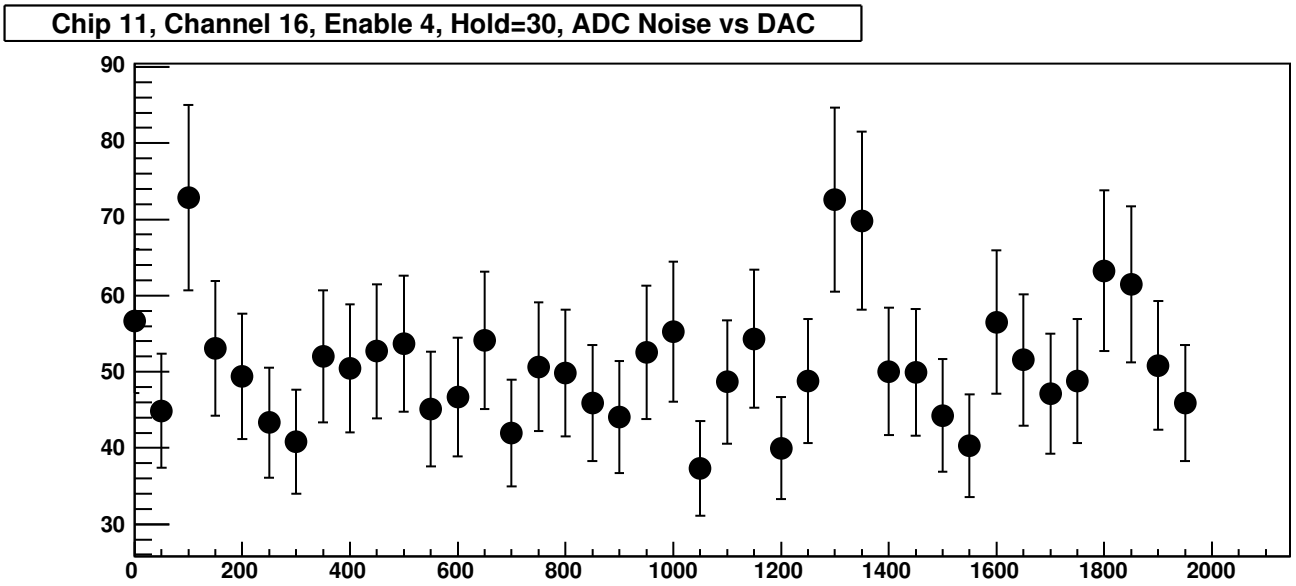

Chip 11, Channel 16, Enable 2!, Hold=30, ADC Residuals vs DAC
12000
10000
8000
6000
4000
2000
0
-2000
-4000
-6000
-8000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

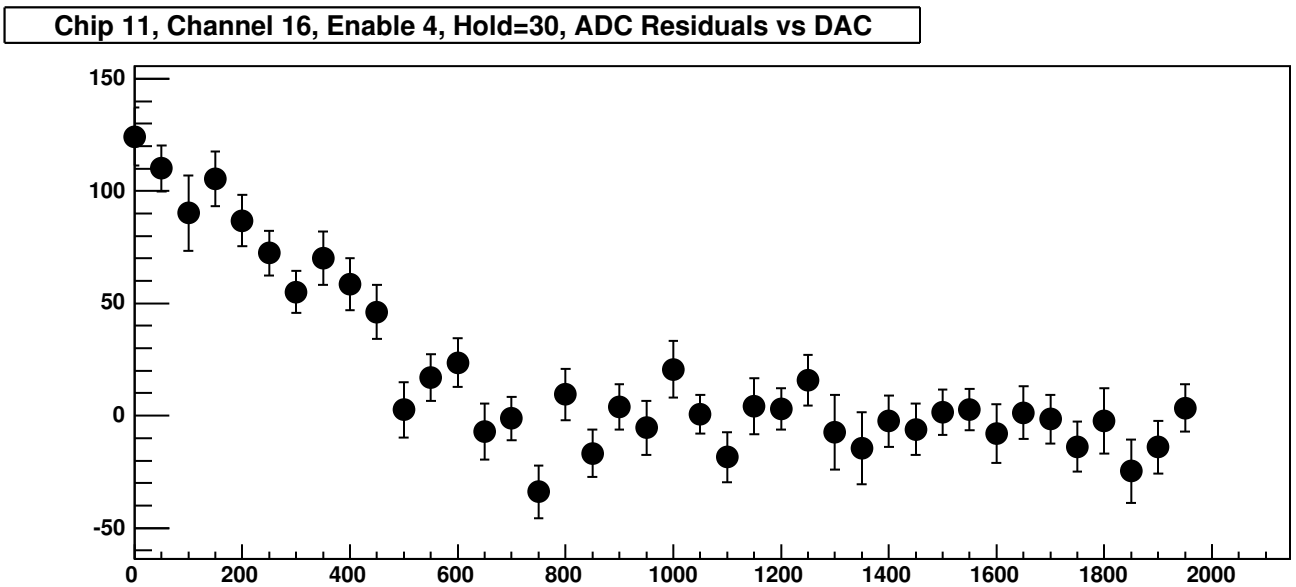

Chip 11, Channel 16, Enable 3, Hold=30, ADC Mean vs DAC
χ² / ndf 10.88 / 11
p0 -1421 ± 18.97
p1 0.1895 ± 0.01671
-1050
-1100
-1150
-1200
-1250
-1300
0
200
400
600
800
1000
1200
1400
1600
1800
2000

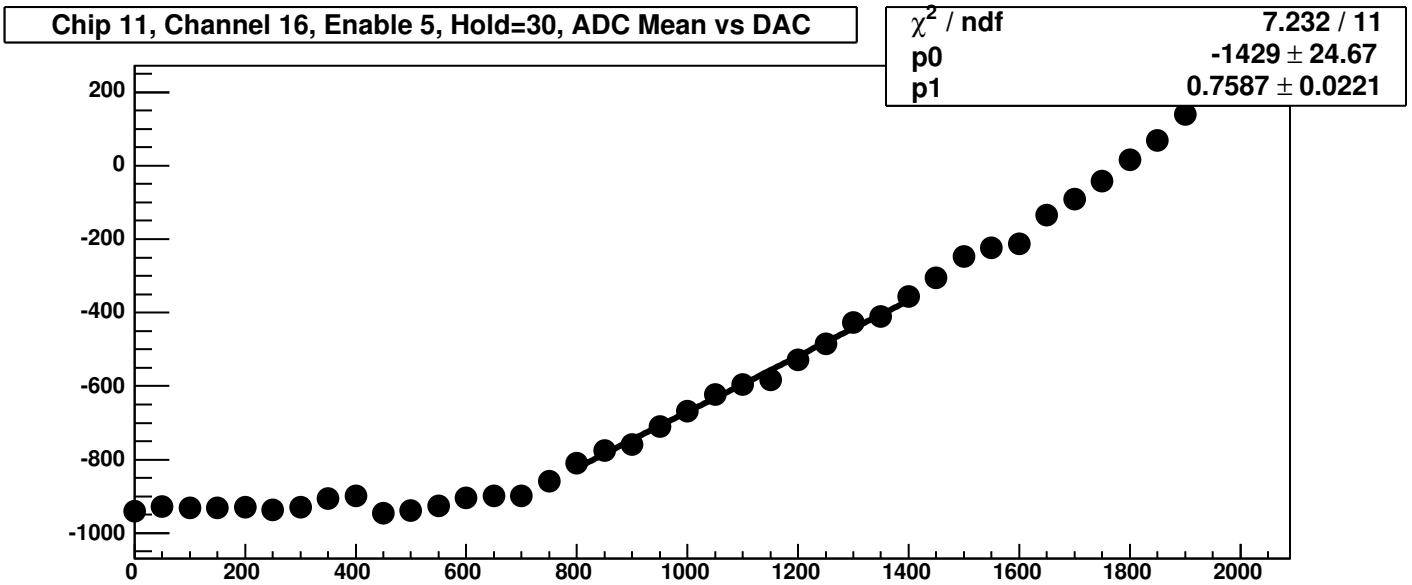

Chip 11, Channel 16, Enable 3, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
20
0
200
400
600
800
1000
1200
1400
1600
1800
2000

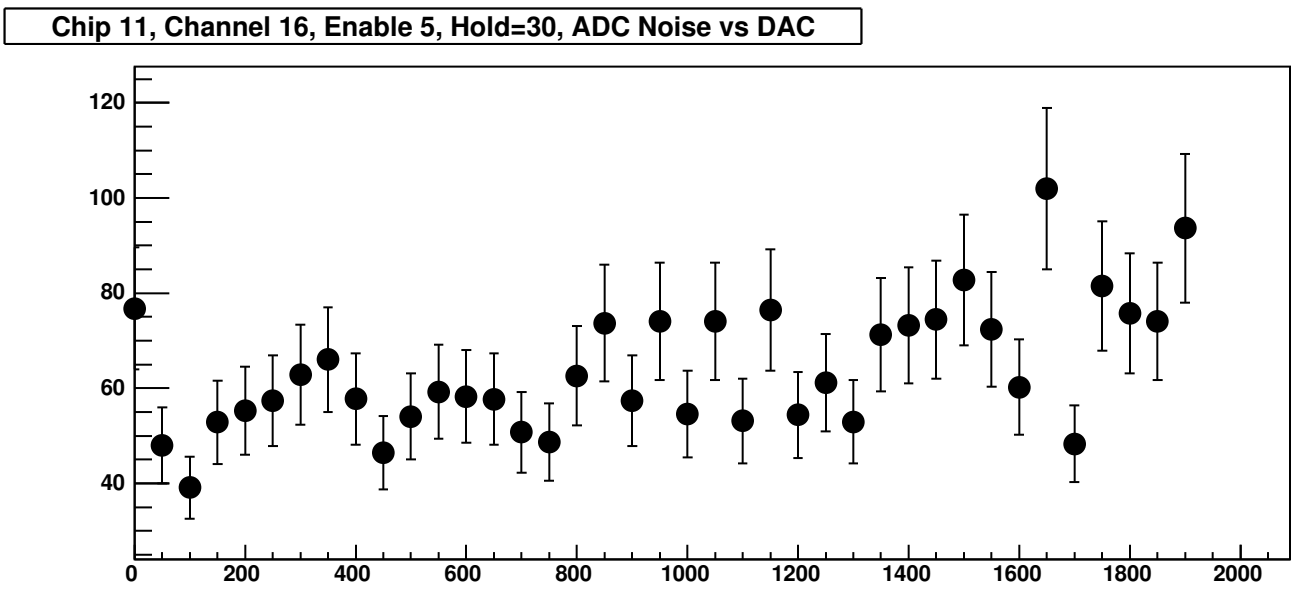

Chip 11, Channel 16, Enable 3, Hold=30, ADC Residuals vs DAC
140
120
100
80
60
40
20
0
-20
-40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

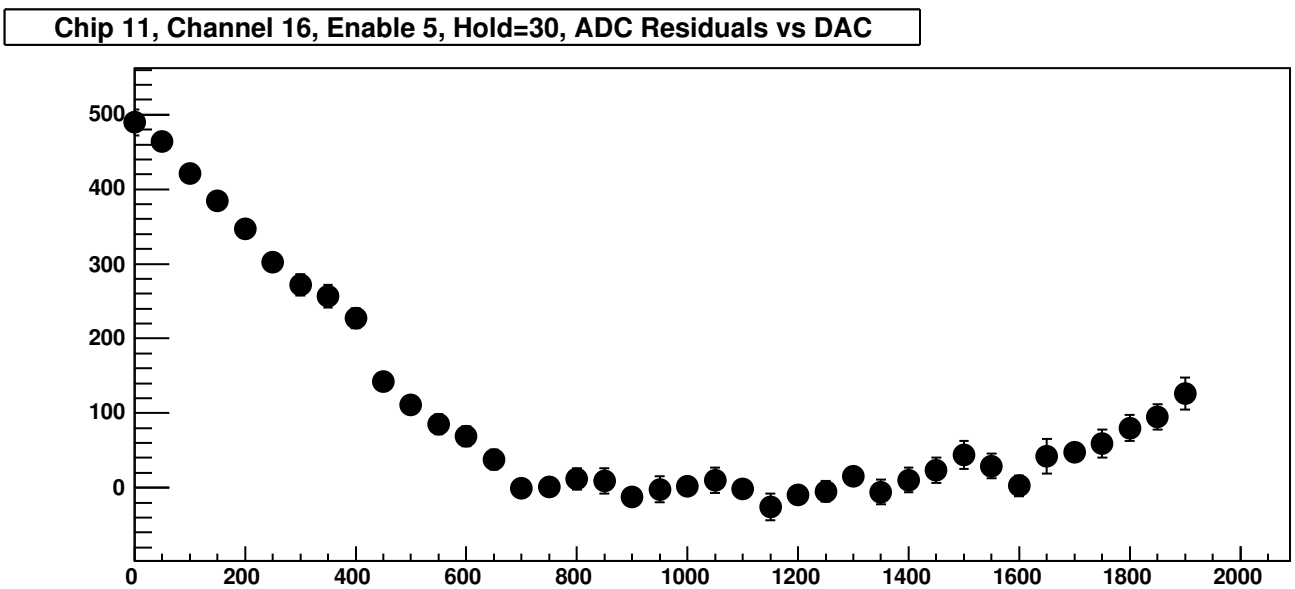

Chip 11, Channel 16, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 12.21 / 11
p0 -1411 ± 19.23
p1 0.1862 ± 0.01759
-1050
-1100
-1150
-1200
-1250
-1300
-1350
0
200
400
600
800
1000
1200
1400
1600
1800
2000

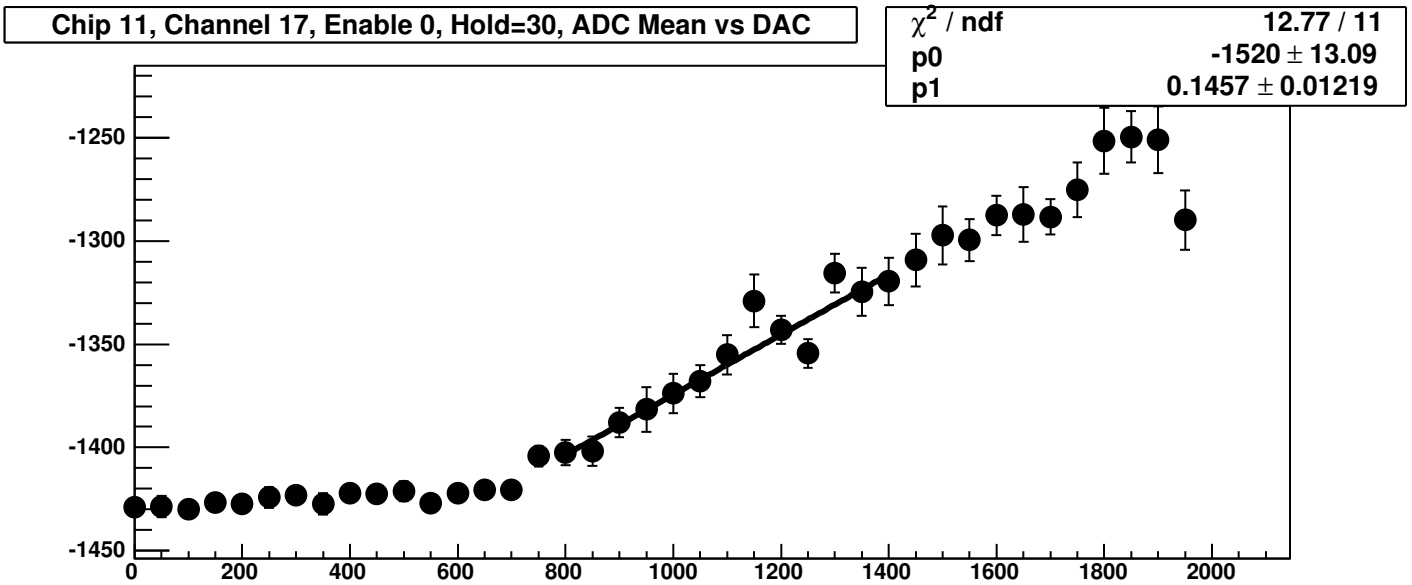

Chip 11, Channel 16, Enable 4, Hold=30, ADC Noise vs DAC
90
80
70
60
50
40
30
0
200
400
600
800
1000
1200
1400
1600
1800
2000

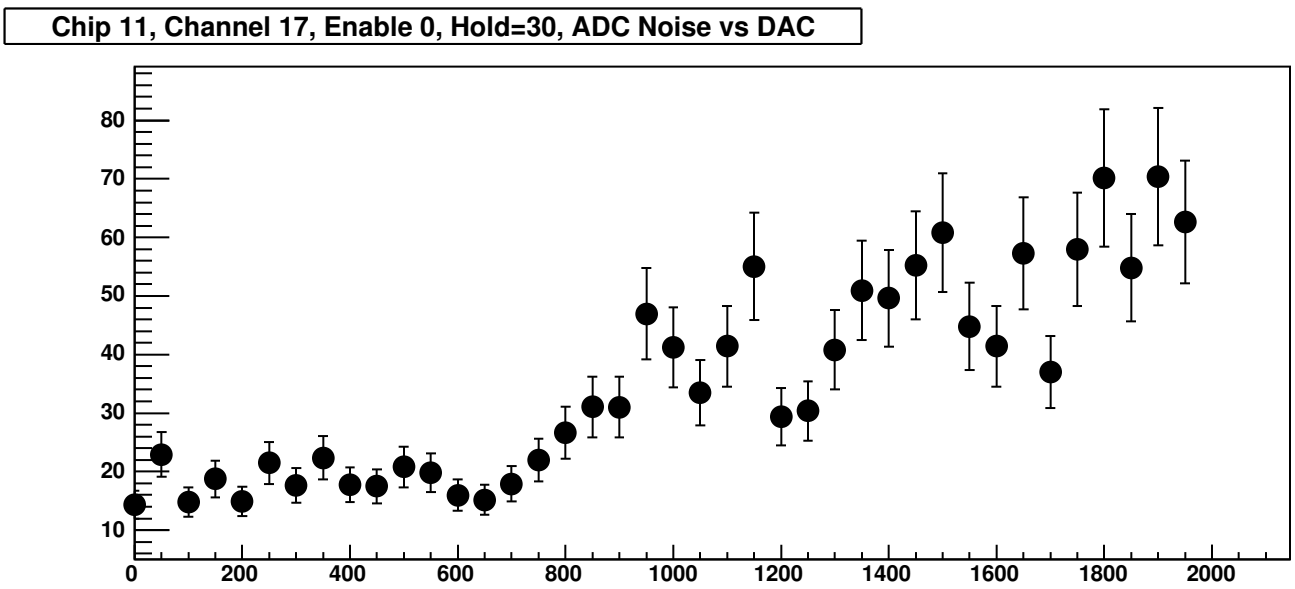

Chip 11, Channel 16, Enable 4, Hold=30, ADC Residuals vs DAC
150
100
50
0
-50
0
200
400
600
800
1000
1200
1400
1600
1800
2000

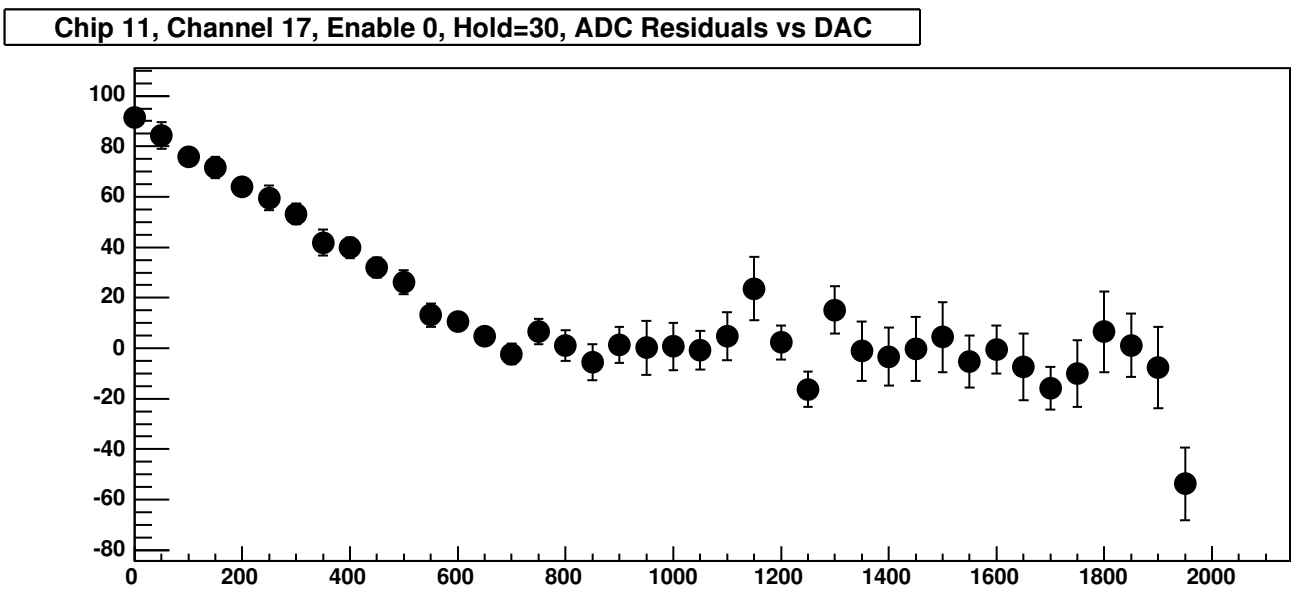

Chip 11, Channel 16, Enable 5, Hold=30, ADC Mean vs DAC
χ² / ndf 7.232 / 11
p0 -1429 ± 24.67
p1 0.7587 ± 0.0221
200
0
-200
-400
-600
-800
-1000
0
200
400
600
800
1000
1200
1400
1600
1800
2000

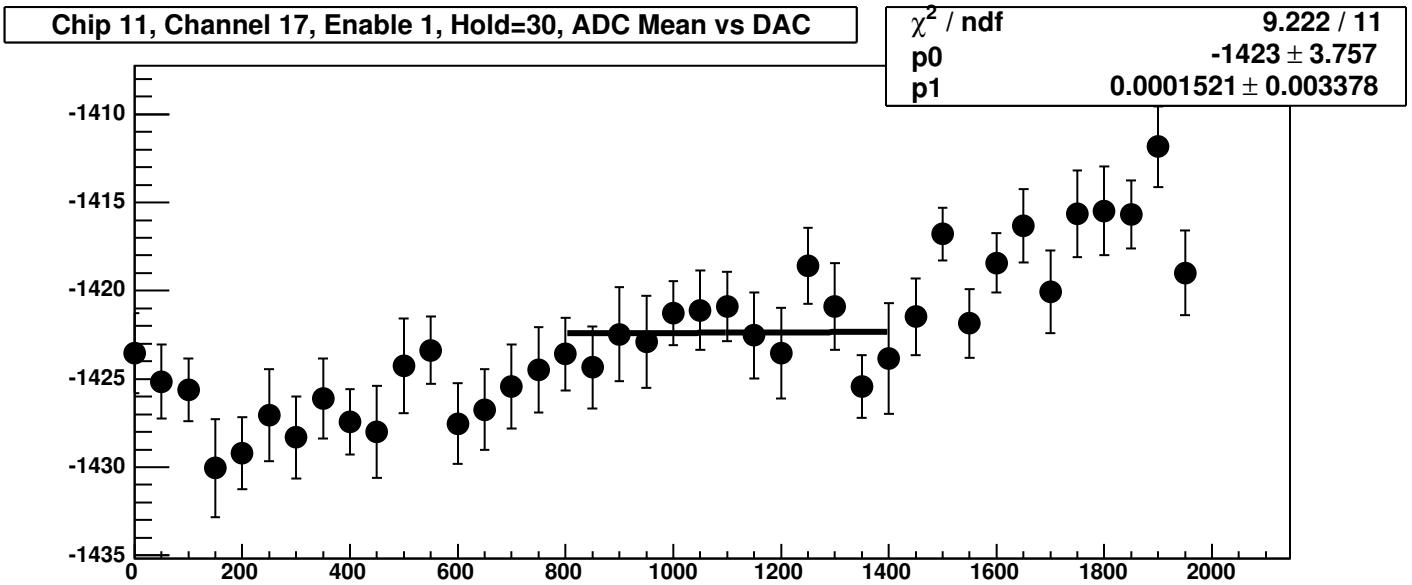

Chip 11, Channel 16, Enable 5, Hold=30, ADC Noise vs DAC
120
100
80
60
40
0
200
400
600
800
1000
1200
1400
1600
1800
2000

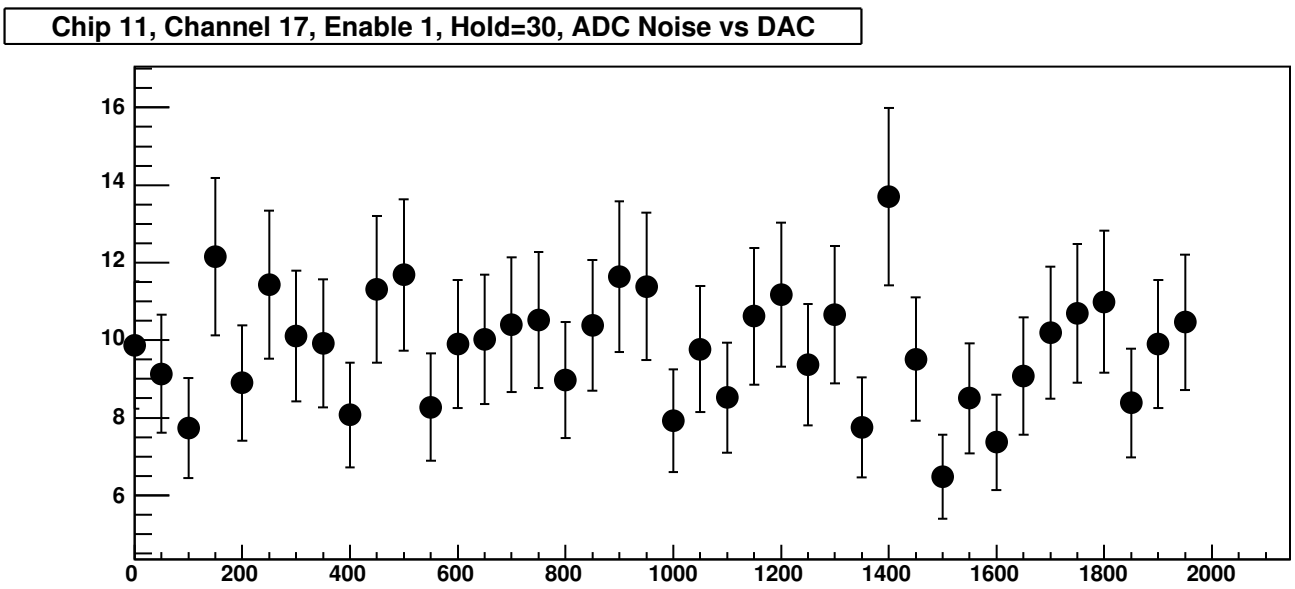

Chip 11, Channel 16, Enable 5, Hold=30, ADC Residuals vs DAC
500
400
300
200
100
0
0
200
400
600
800
1000
1200
1400
1600
1800
2000

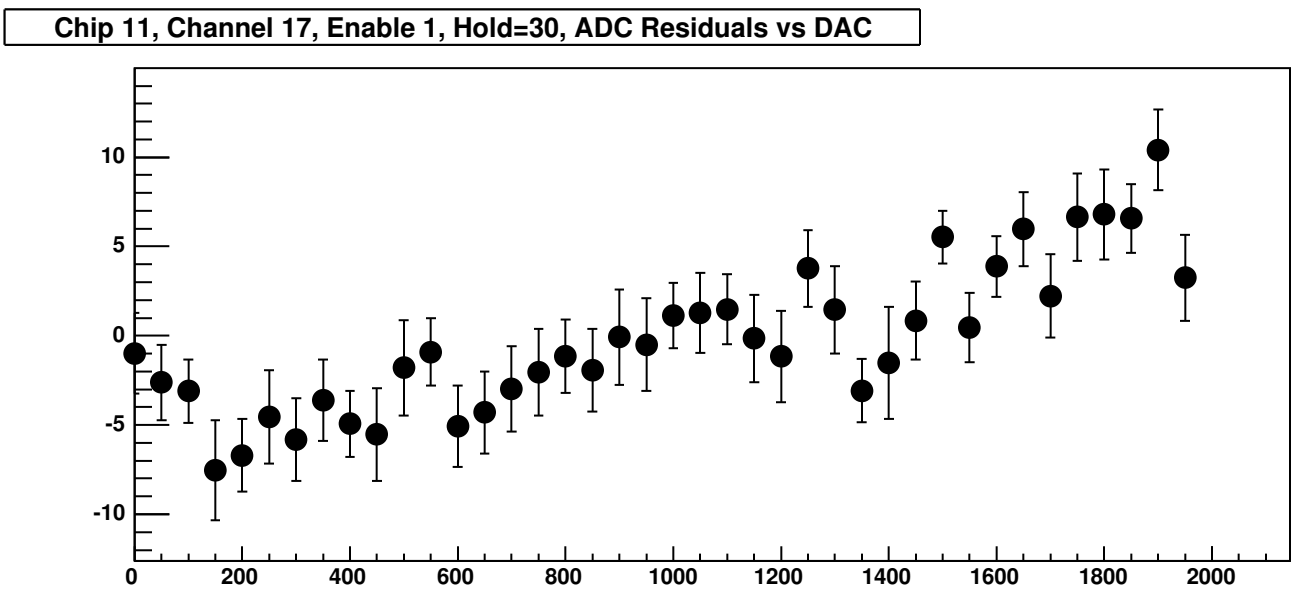

Chip 11, Channel 17, Enable 0, Hold=30, ADC Mean vs DAC
χ² / ndf 12.77 / 11
p0 -1520 ± 13.09
p1 0.1457 ± 0.01219
-1250
-1300
-1350
-1400
-1450
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 17, Enable 0, Hold=30, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

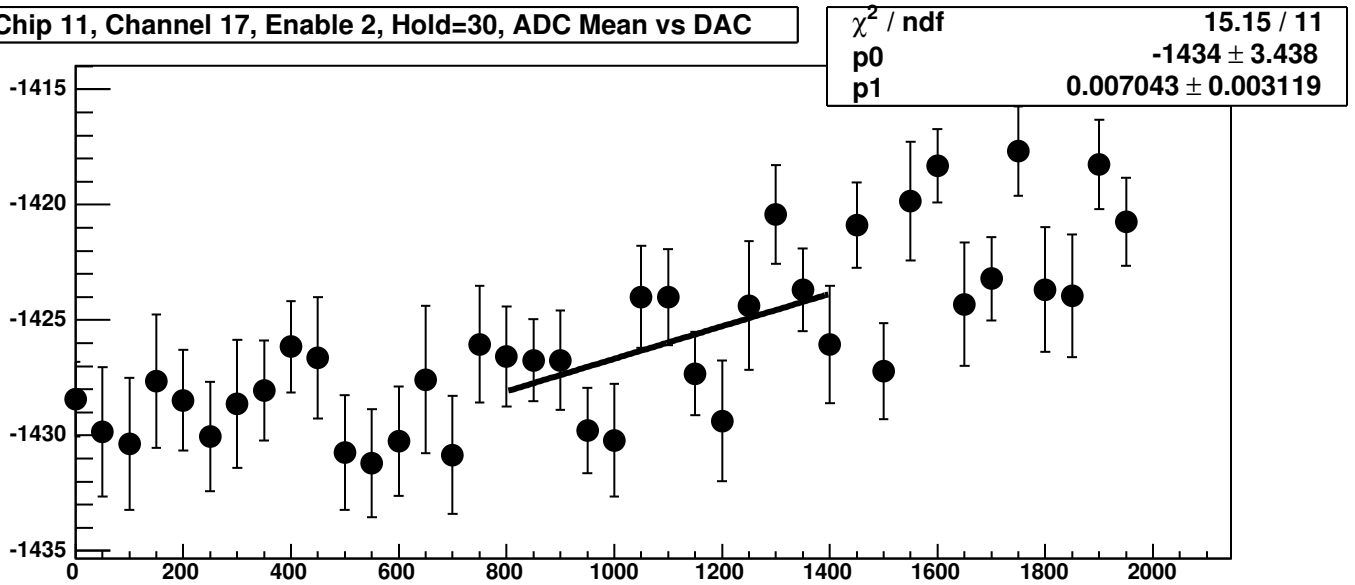

Chip 11, Channel 17, Enable 0, Hold=30, ADC Residuals vs DAC
100
80
60
40
20
0
-20
-40
-60
-80
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 17, Enable 1, Hold=30, ADC Mean vs DAC
χ² / ndf 9.222 / 11
p0 -1423 ± 3.757
p1 0.0001521 ± 0.003378
-1410
-1415
-1420
-1425
-1430
-1435
0
200
400
600
800
1000
1200
1400
1600
1800
2000

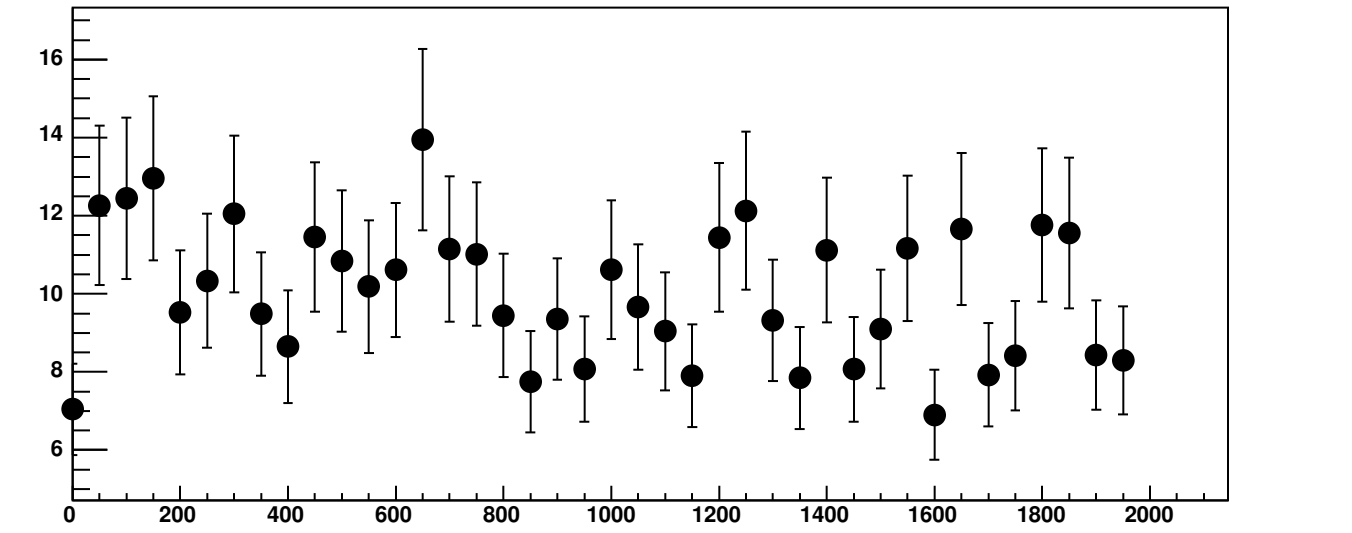

Chip 11, Channel 17, Enable 1, Hold=30, ADC Noise vs DAC
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 17, Enable 1, Hold=30, ADC Residuals vs DAC
10
5
0
-5
-10
0
200
400
600
800
1000
1200
1400
1600
1800
2000

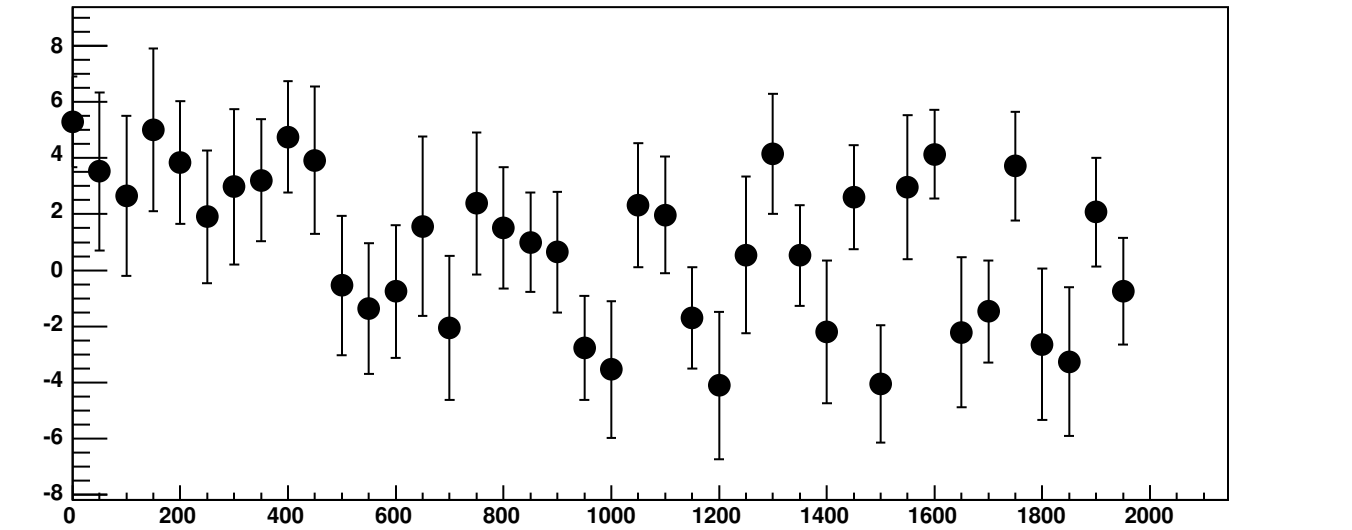
Chip 11, Channel 17, Enable 2, Hold=30, ADC Mean vs DAC



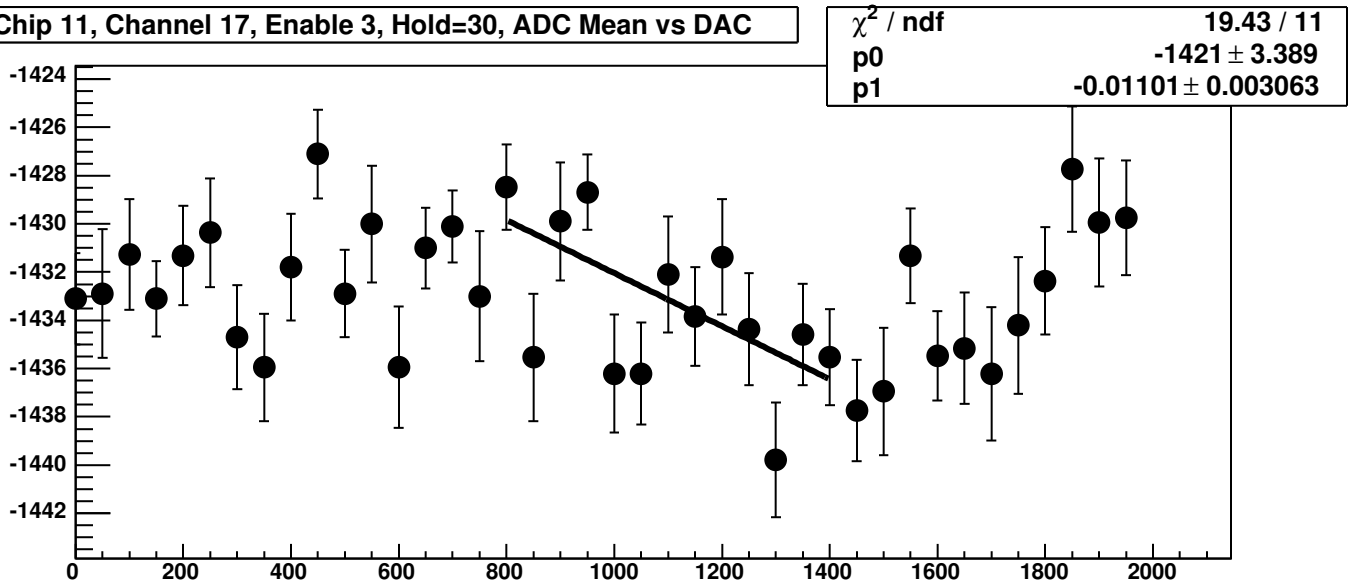
Chip 11, Channel 17, Enable 2, Hold=30, ADC Noise vs DAC



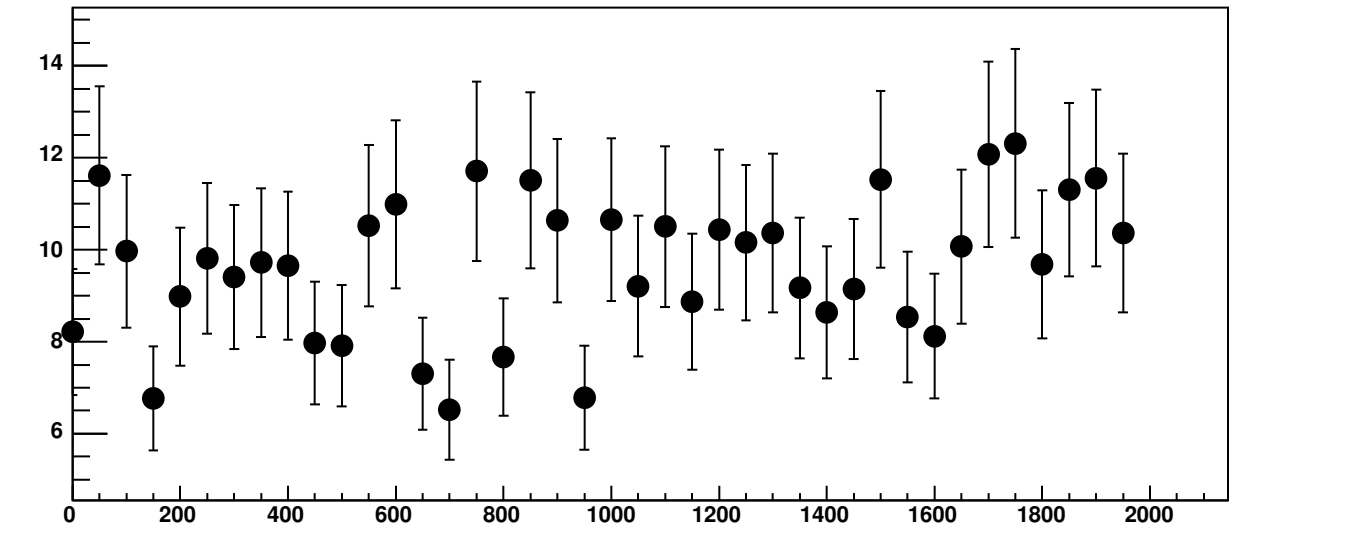
Chip 11, Channel 17, Enable 2, Hold=30, ADC Residuals vs DAC



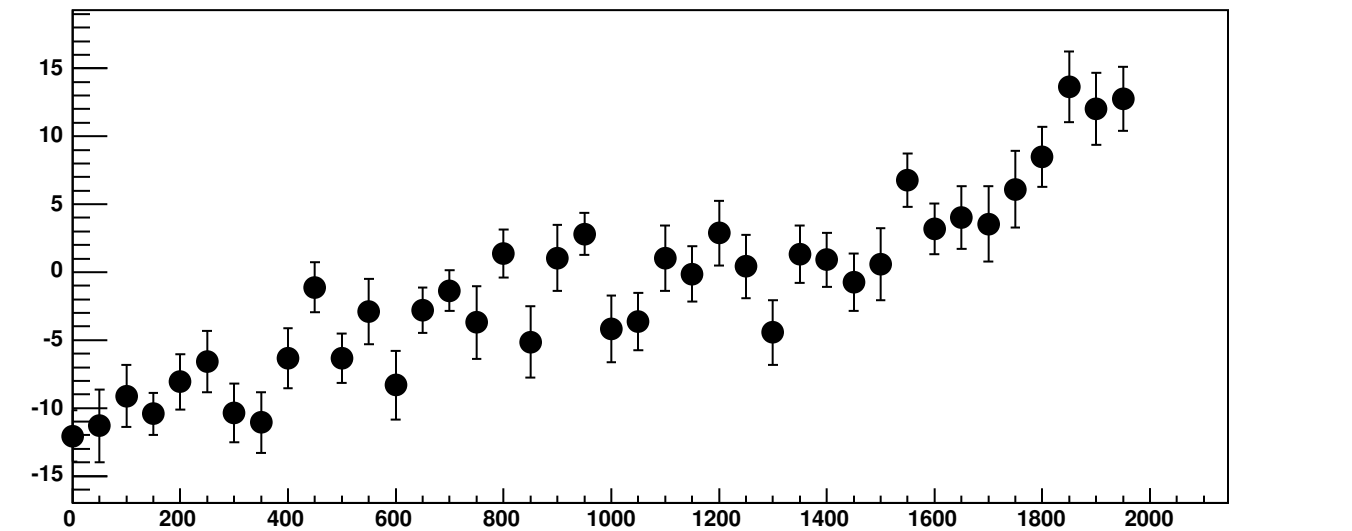
Chip 11, Channel 17, Enable 3, Hold=30, ADC Mean vs DAC

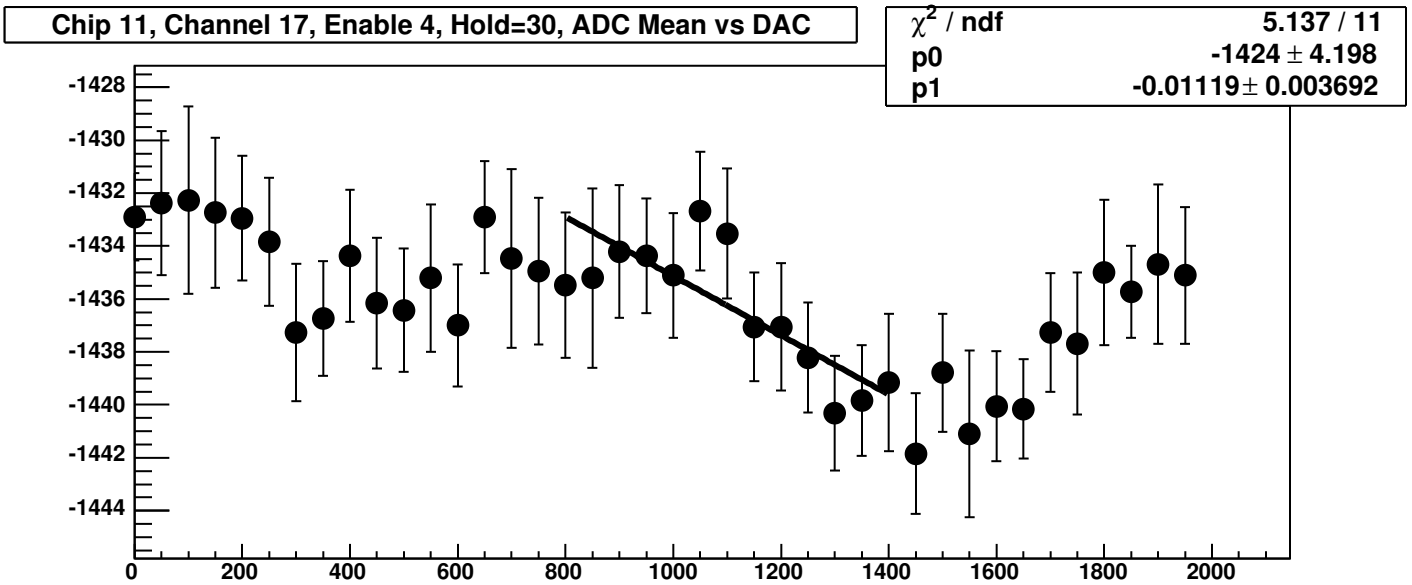


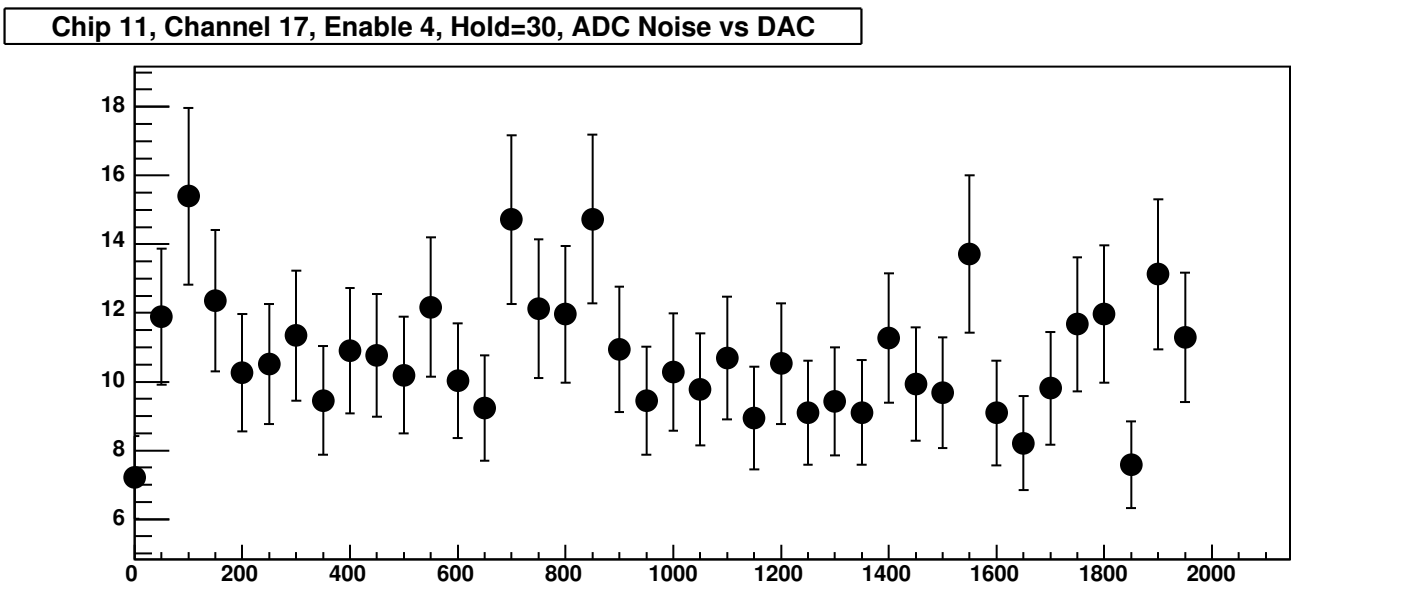
Chip 11, Channel 17, Enable 3, Hold=30, ADC Noise vs DAC

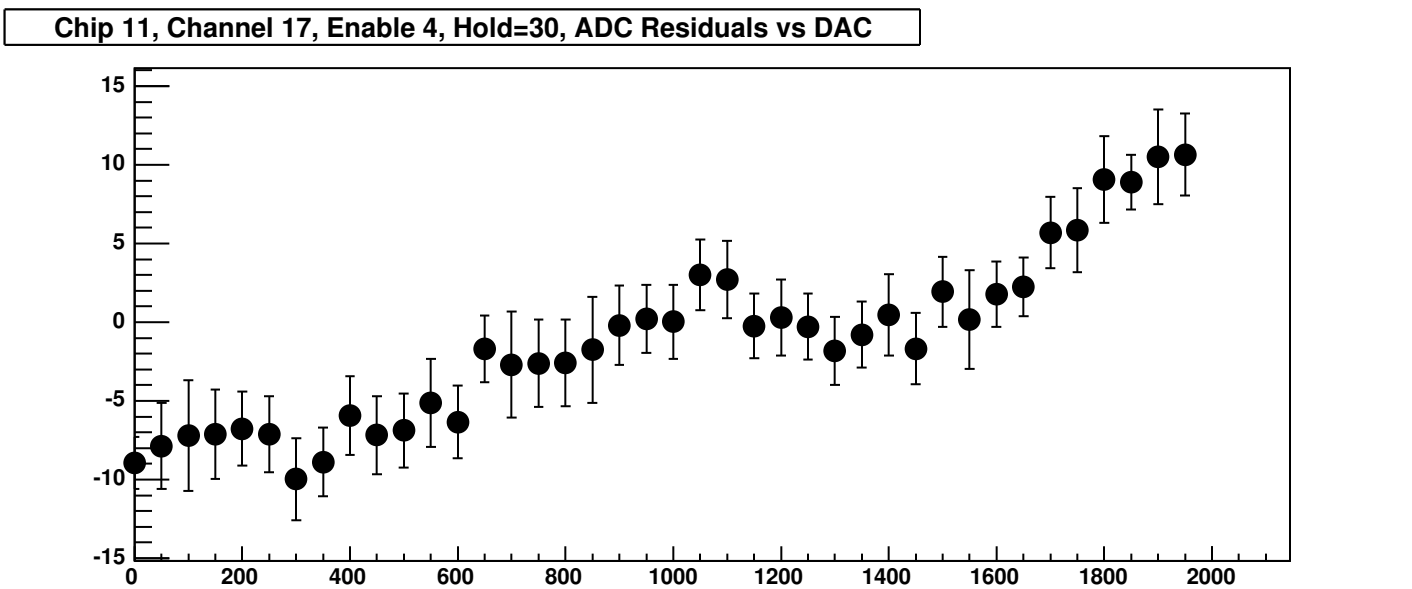


Chip 11, Channel 17, Enable 3, Hold=30, ADC Residuals vs DAC




Chip 11, Channel 17, Enable 4, Hold=30, ADC Mean vs DAC
χ² / ndf 5.137 / 11
p0 -1424 ± 4.198
p1 -0.01119 ± 0.003692
-1428
-1430
-1432
-1434
-1436
-1438
-1440
-1442
-1444
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 17, Enable 4, Hold=30, ADC Noise vs DAC
18
16
14
12
10
8
6
0
200
400
600
800
1000
1200
1400
1600
1800
2000


Chip 11, Channel 17, Enable 4, Hold=30, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
200
400
600
800
1000
1200
1400
1600
1800
2000