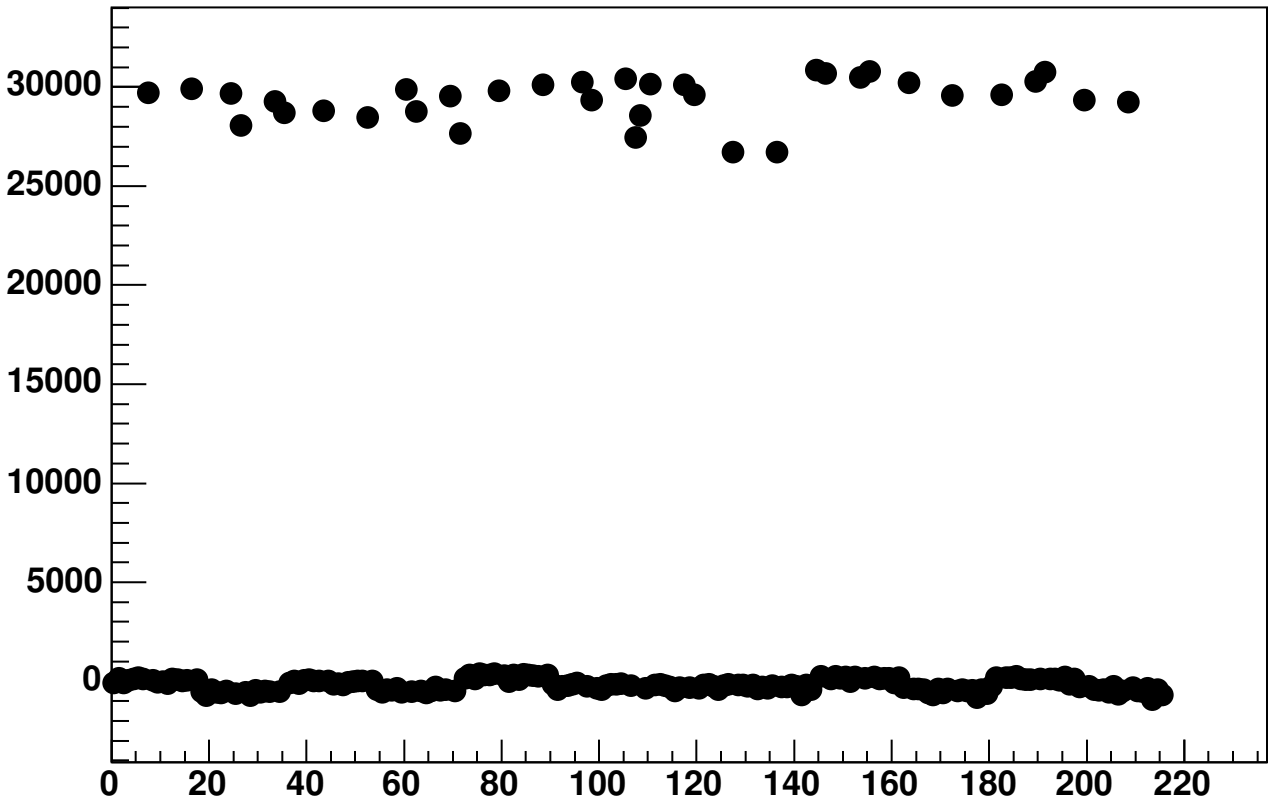
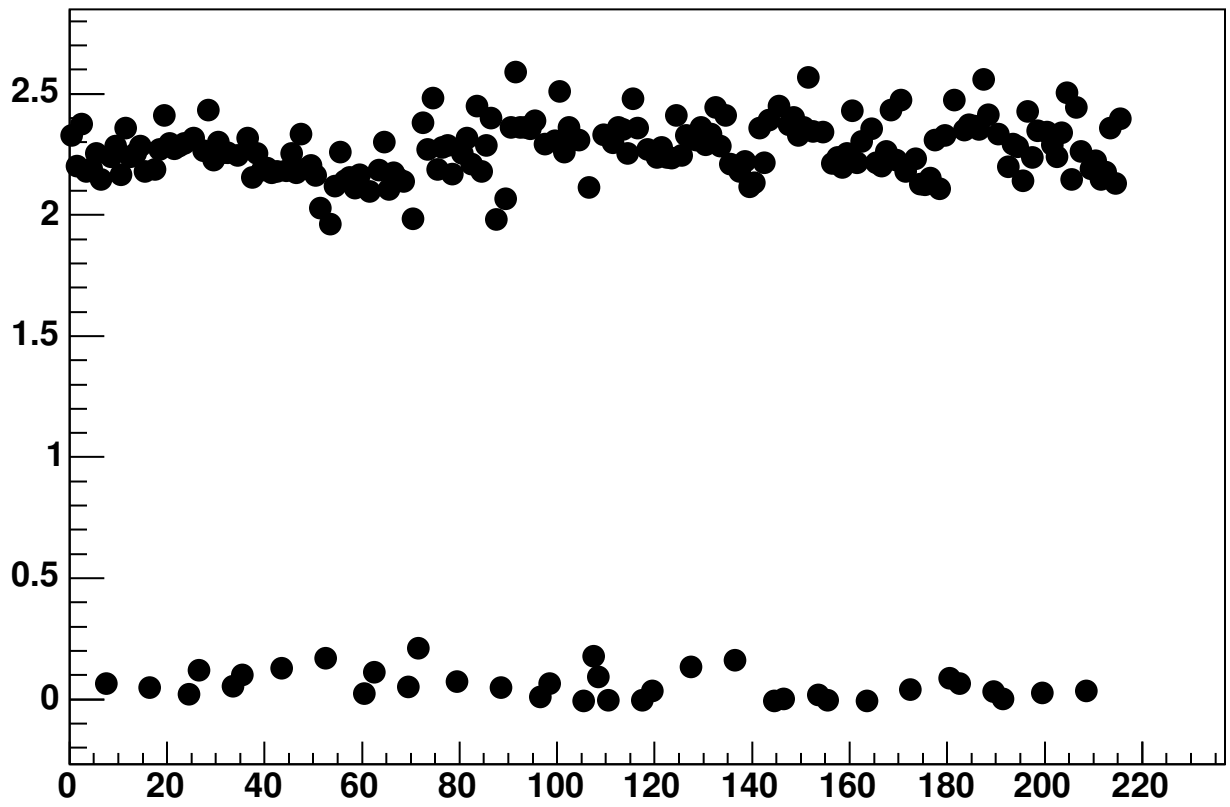
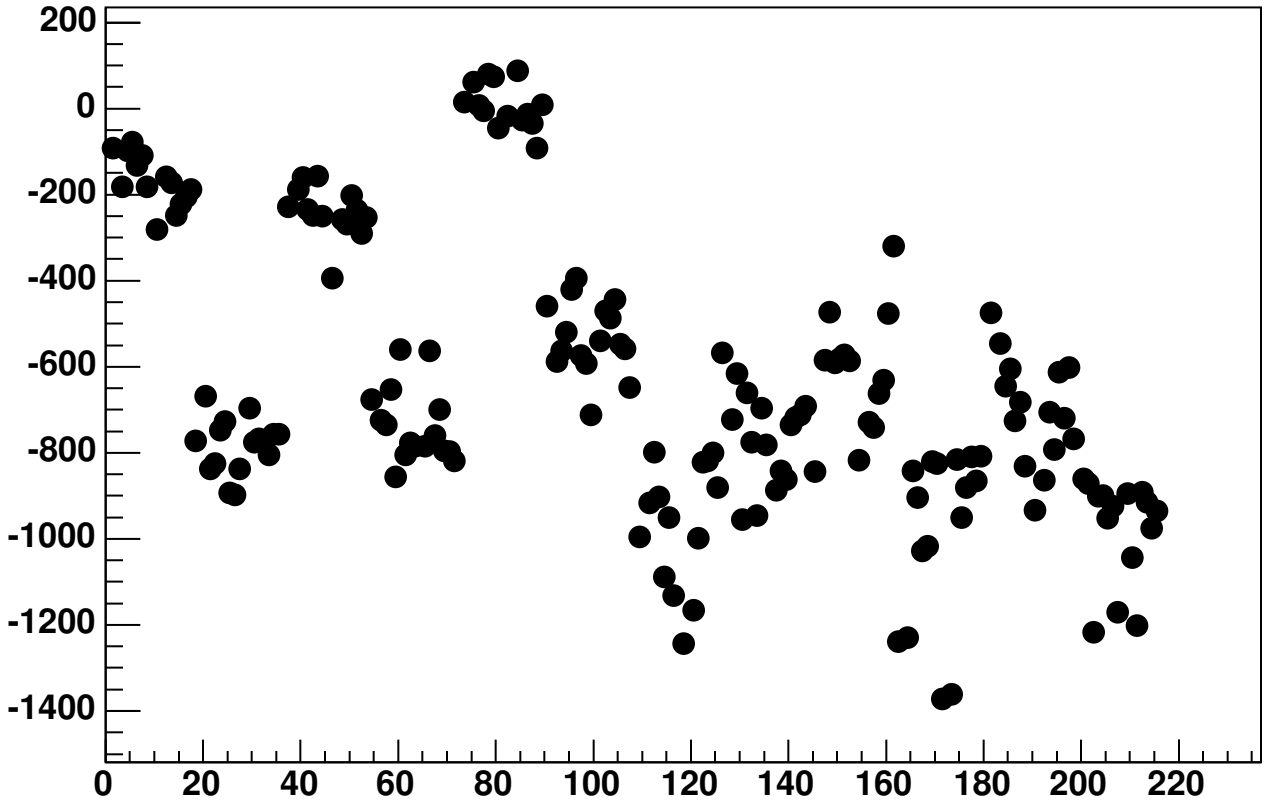
Channel Enabled, Hold=35, Fit Intercept vs 18*Chip+Chan



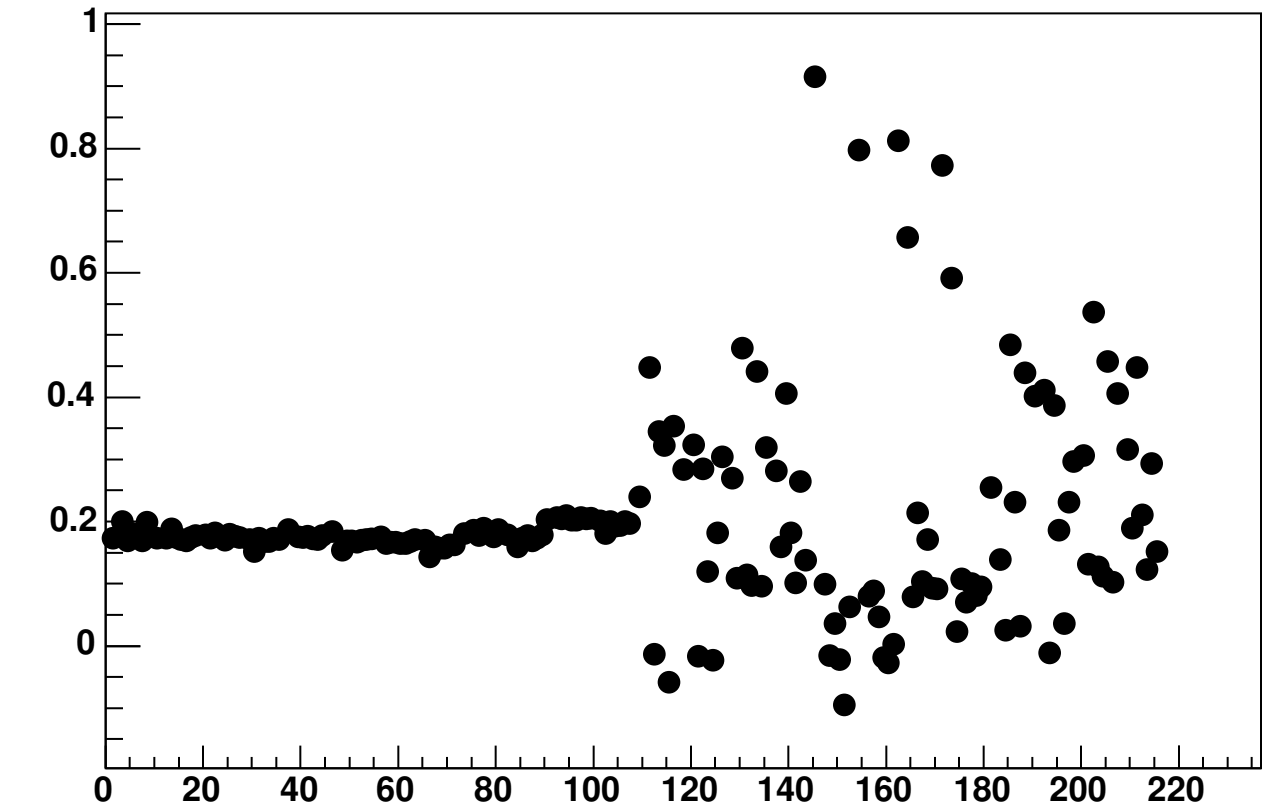
Channel Enabled, Hold=35, Fit Slope vs 18*Chip+Chan



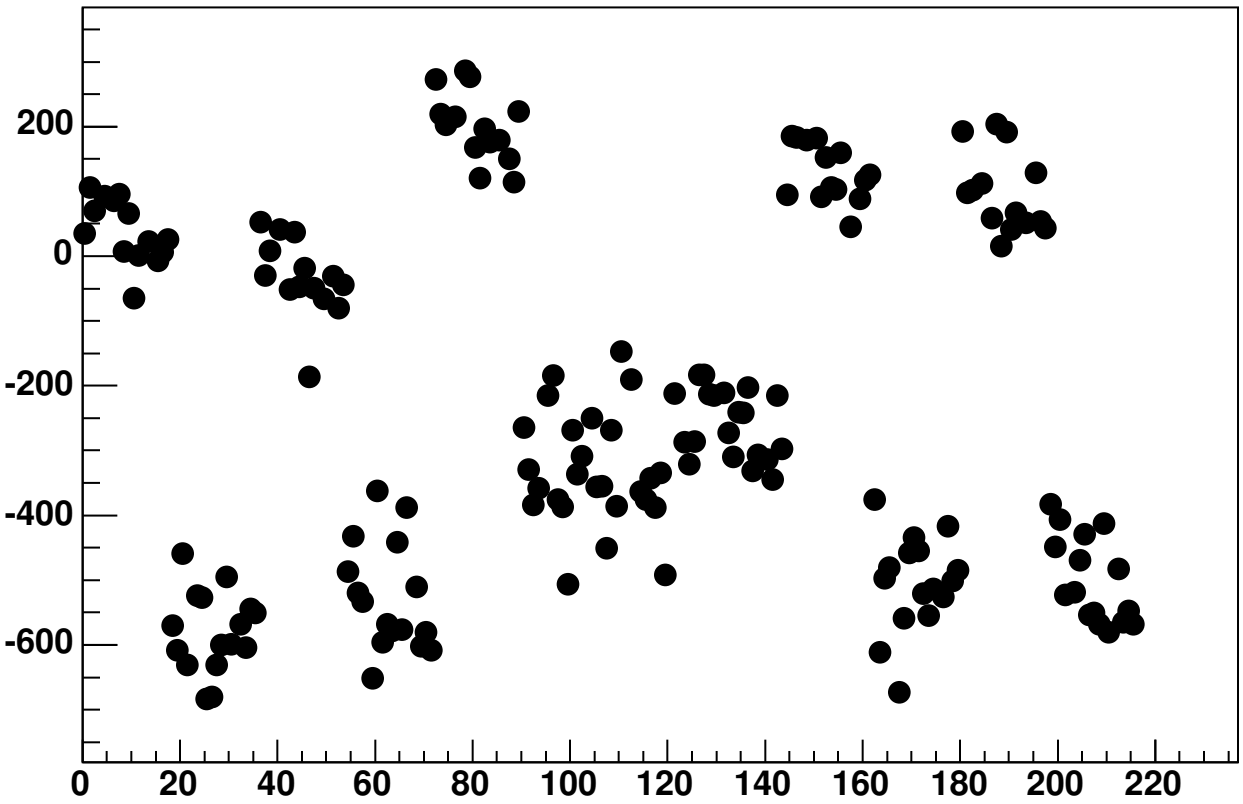
Disabled 0, Hold=35, Fit Intercept vs 18*Chip+Chan



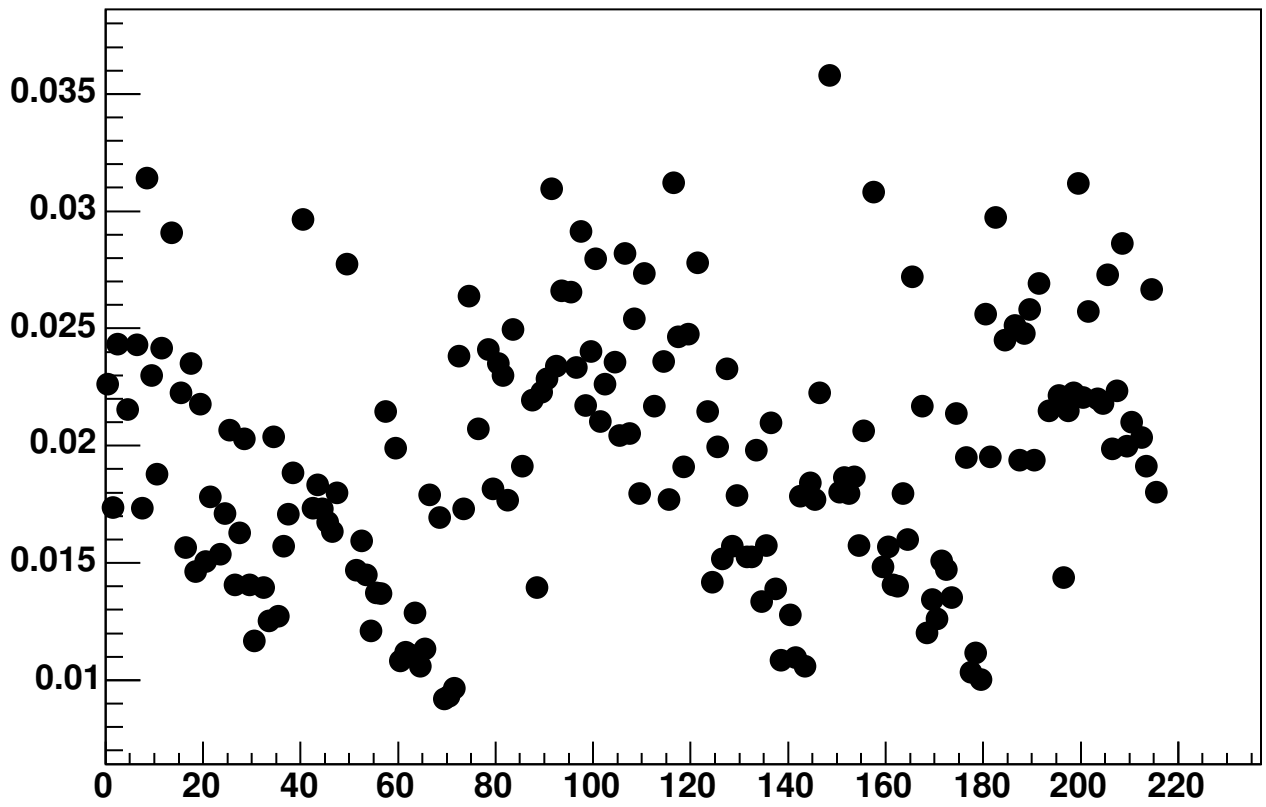
Disabled 0, Hold=35, Fit Slope vs 18*Chip+Chan



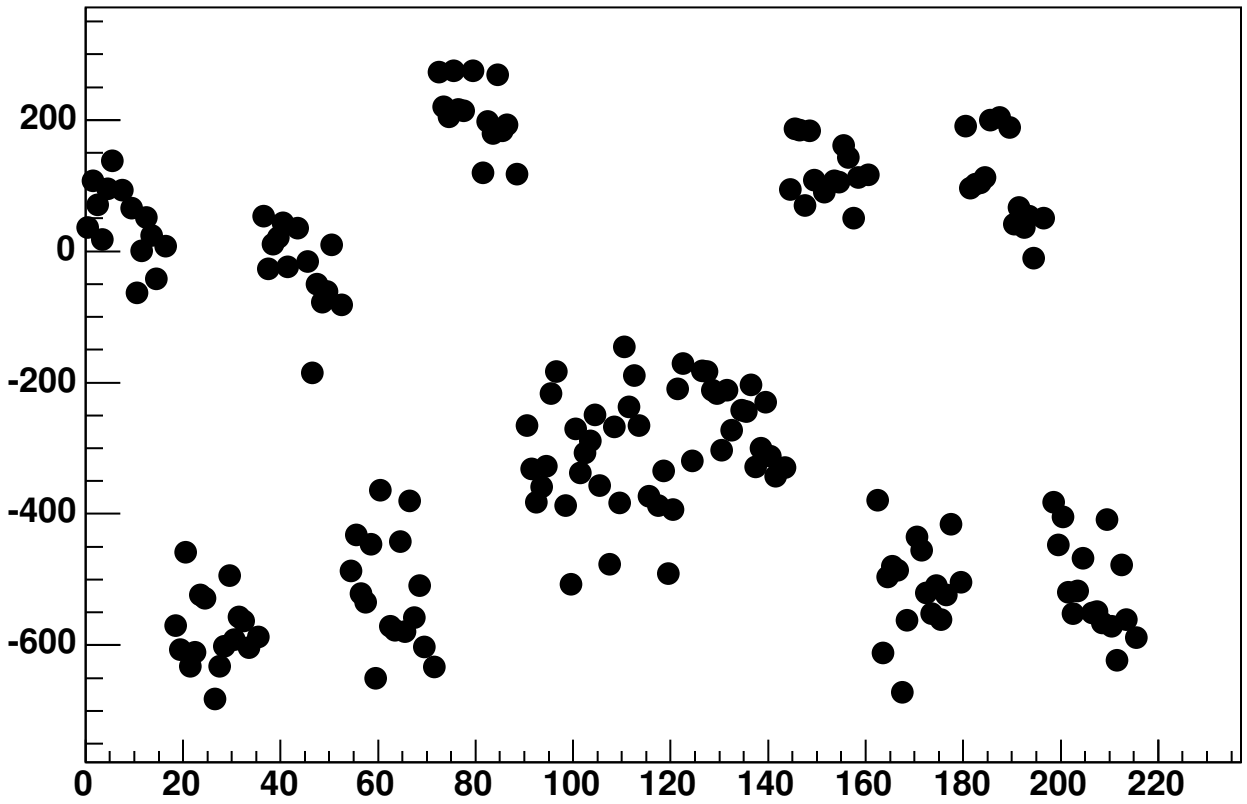
Disabled 1, Hold=35, Fit Intercept vs 18*Chip+Chan



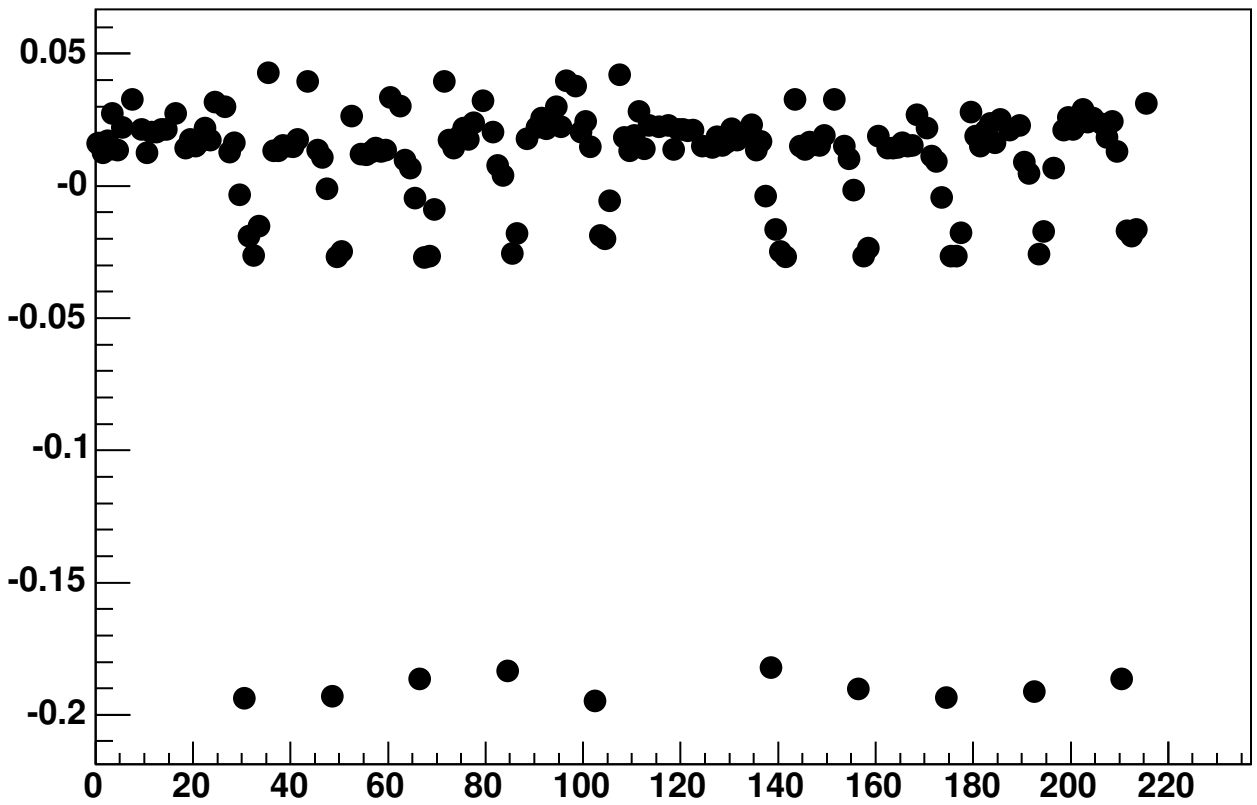
Disabled 1, Hold=35, Fit Slope vs 18*Chip+Chan



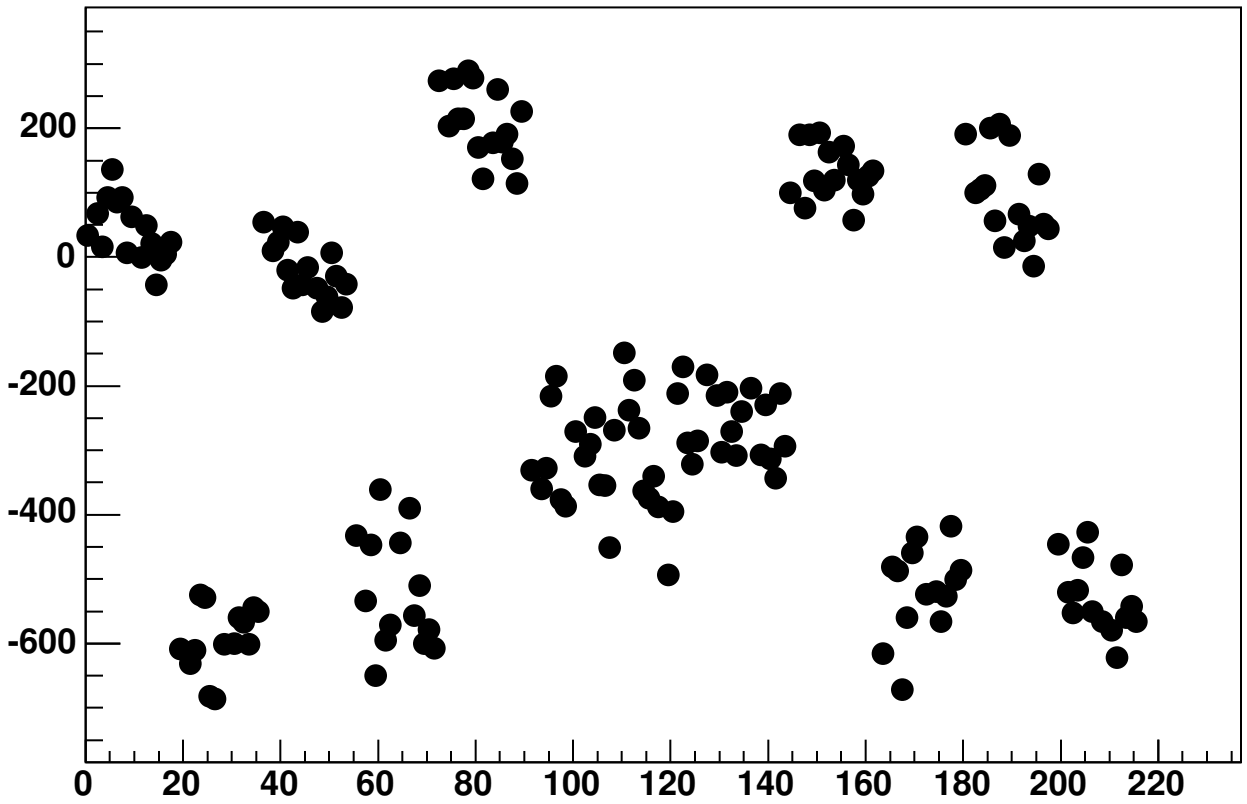
Disabled 2, Hold=35, Fit Intercept vs 18*Chip+Chan



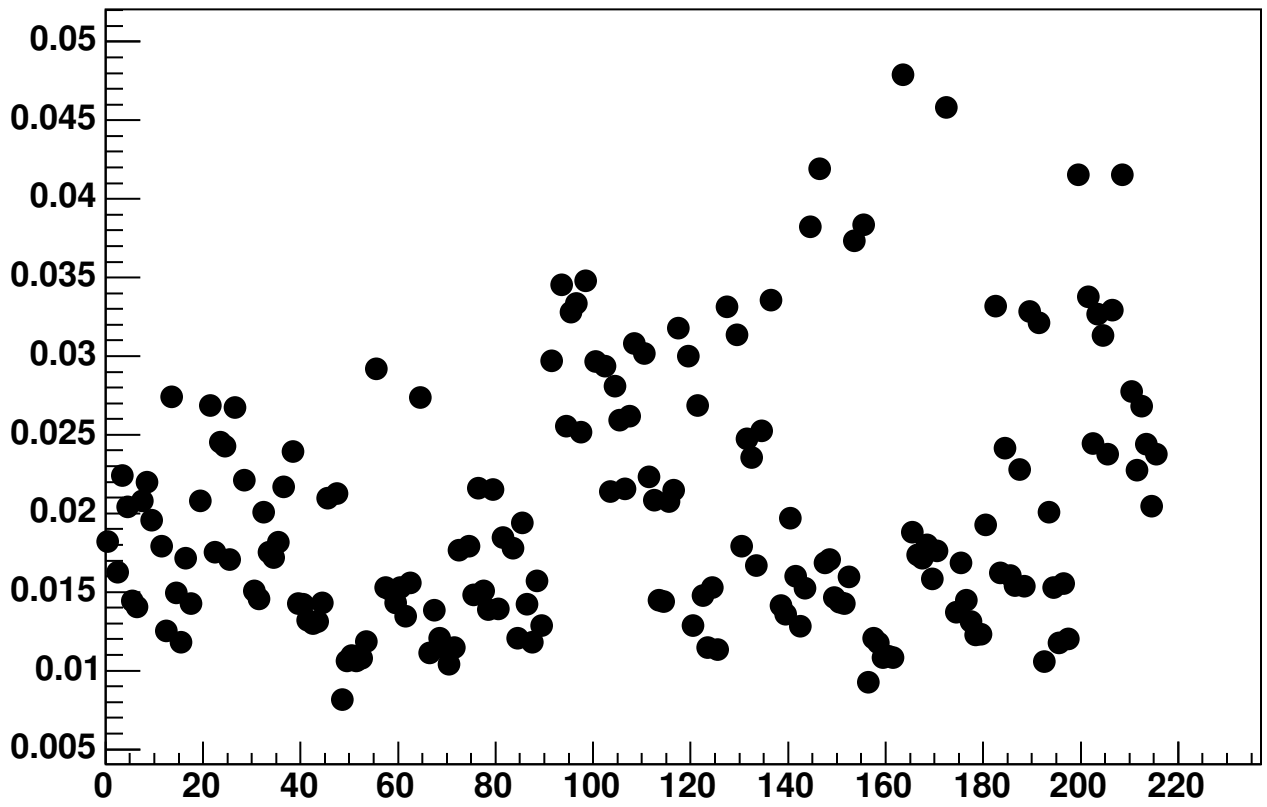
Disabled 2, Hold=35, Fit Slope vs 18*Chip+Chan



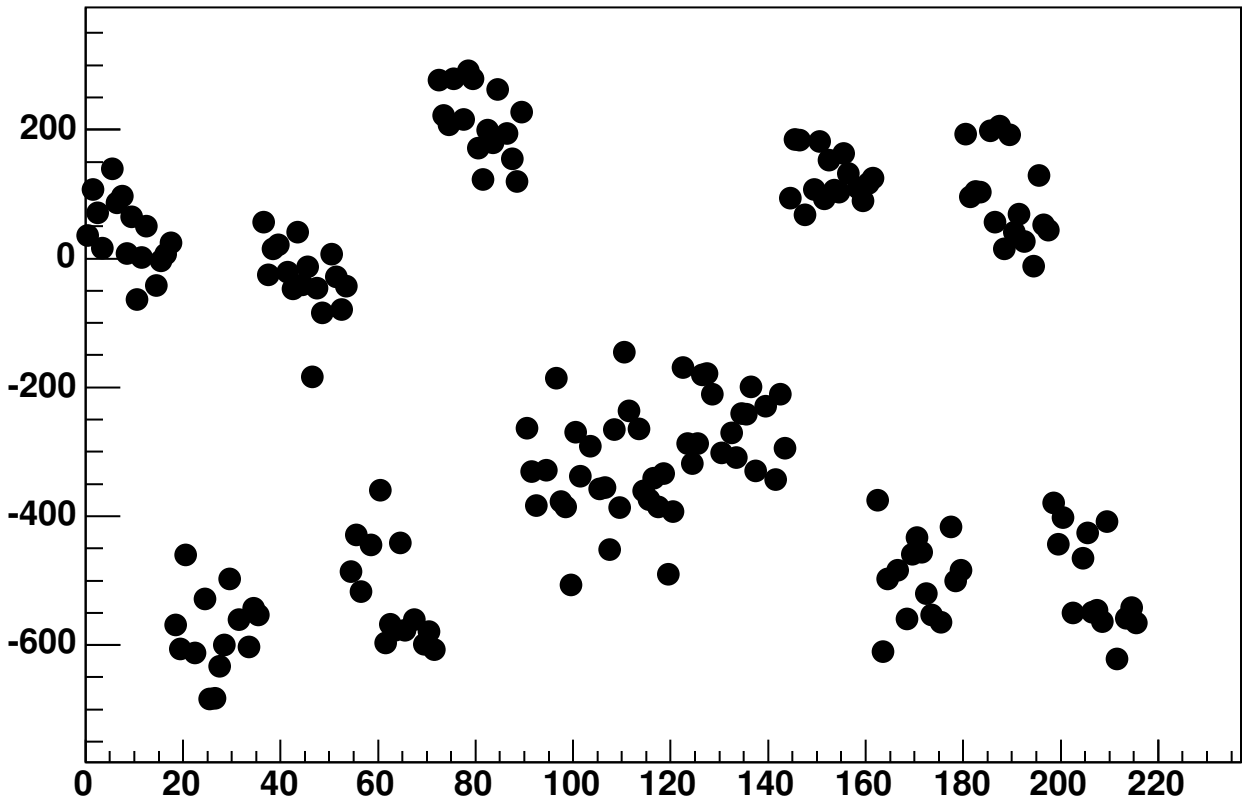
Disabled 3, Hold=35, Fit Intercept vs 18*Chip+Chan



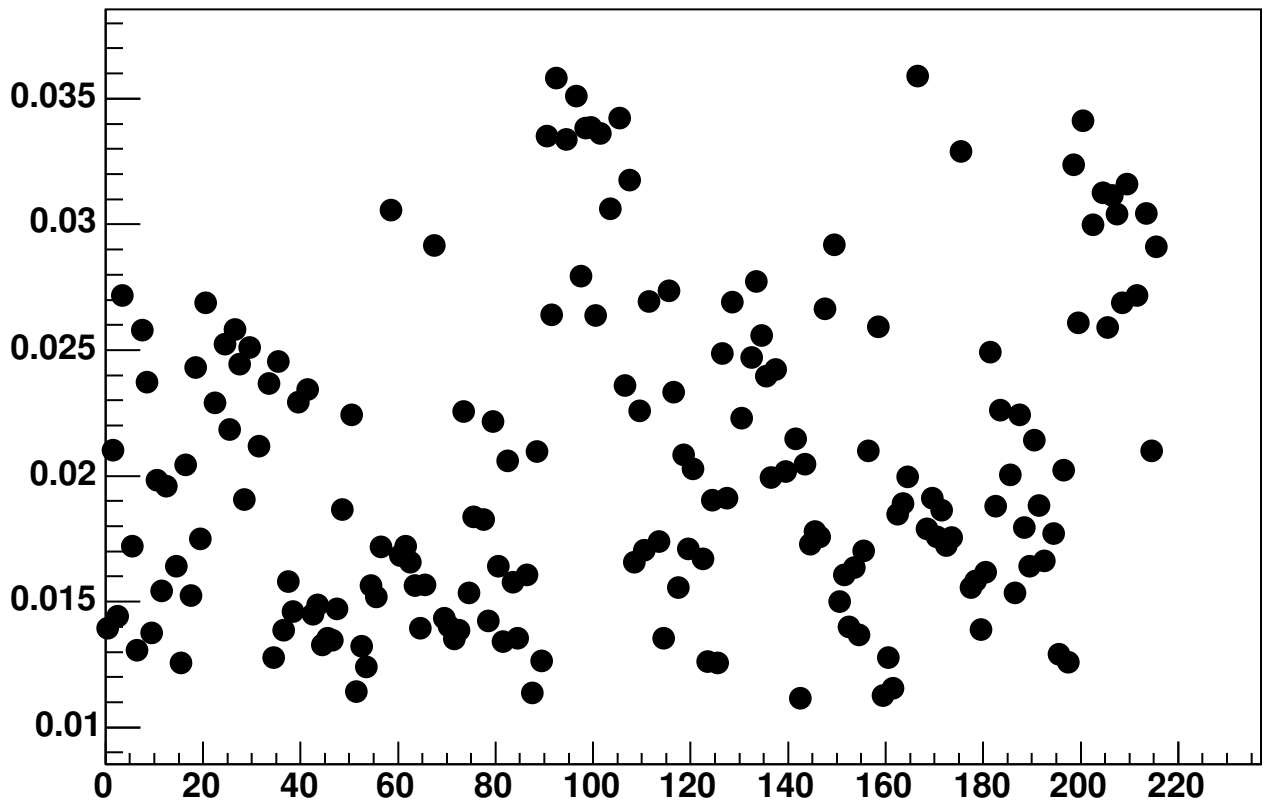
Disabled 3, Hold=35, Fit Slope vs 18*Chip+Chan



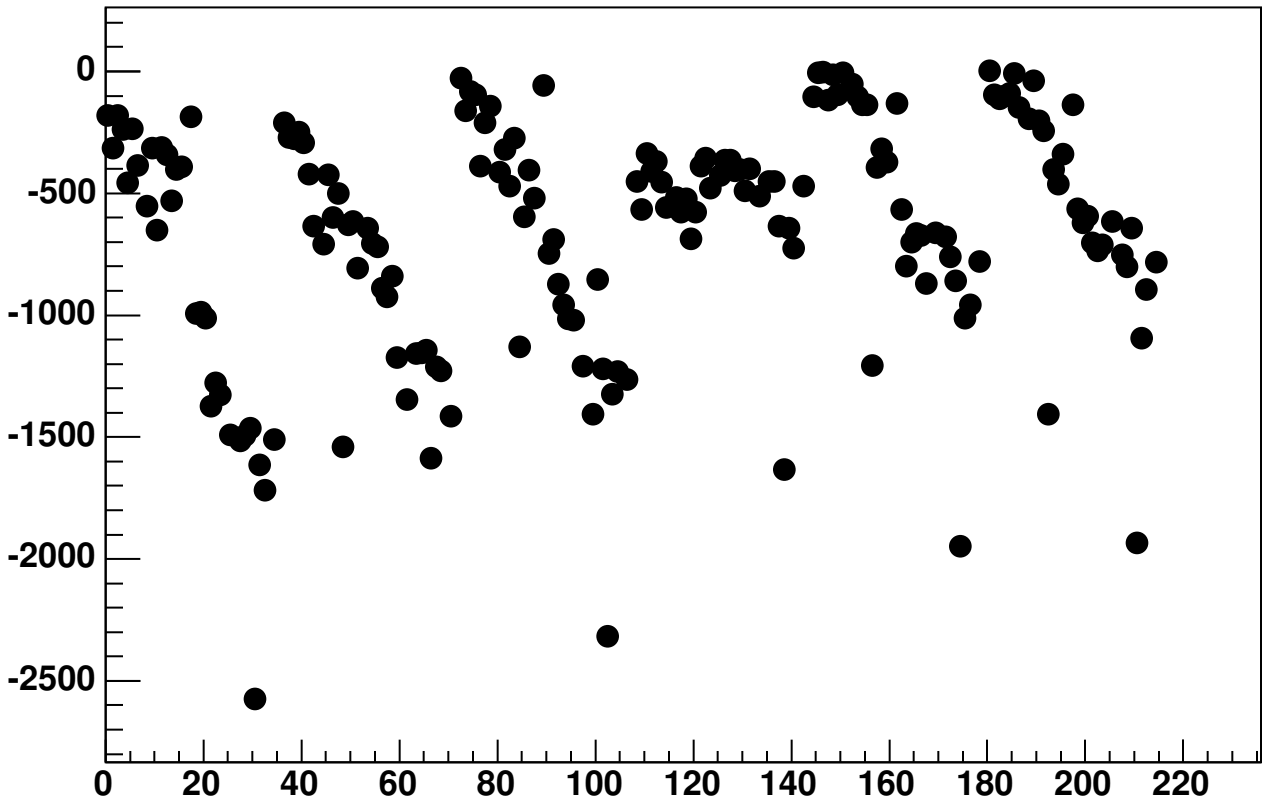
Disabled 4, Hold=35, Fit Intercept vs 18*Chip+Chan



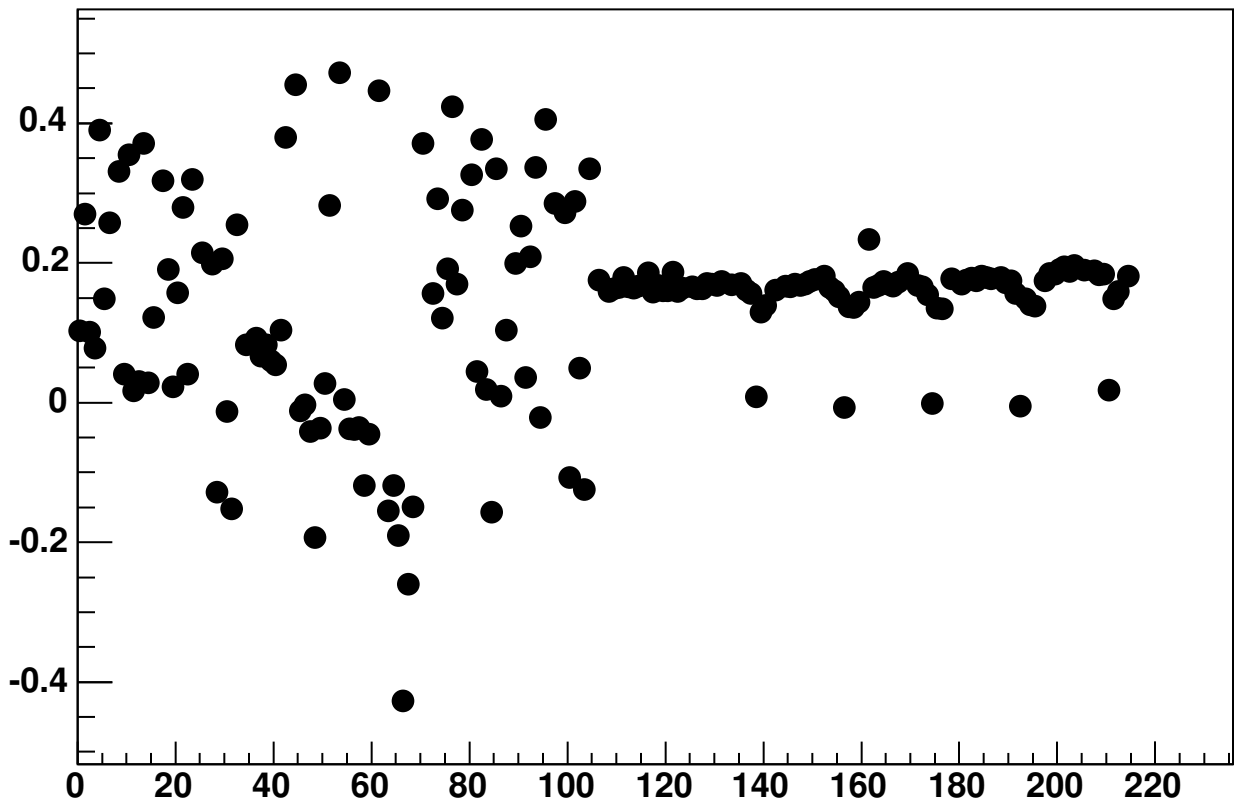
Disabled 4, Hold=35, Fit Slope vs 18*Chip+Chan



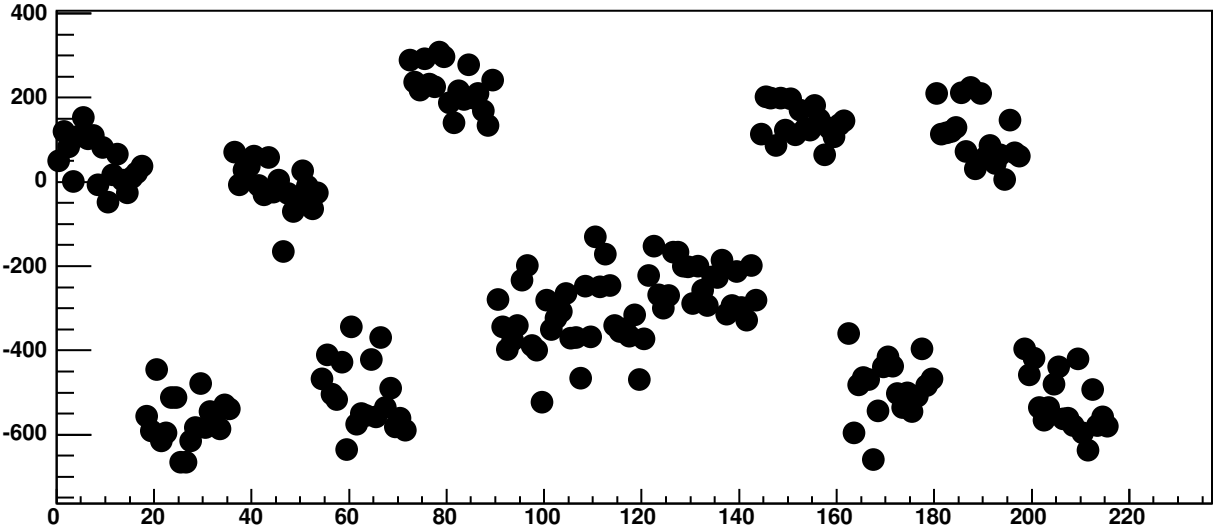
Disabled 5, Hold=35, Fit Intercept vs 18*Chip+Chan



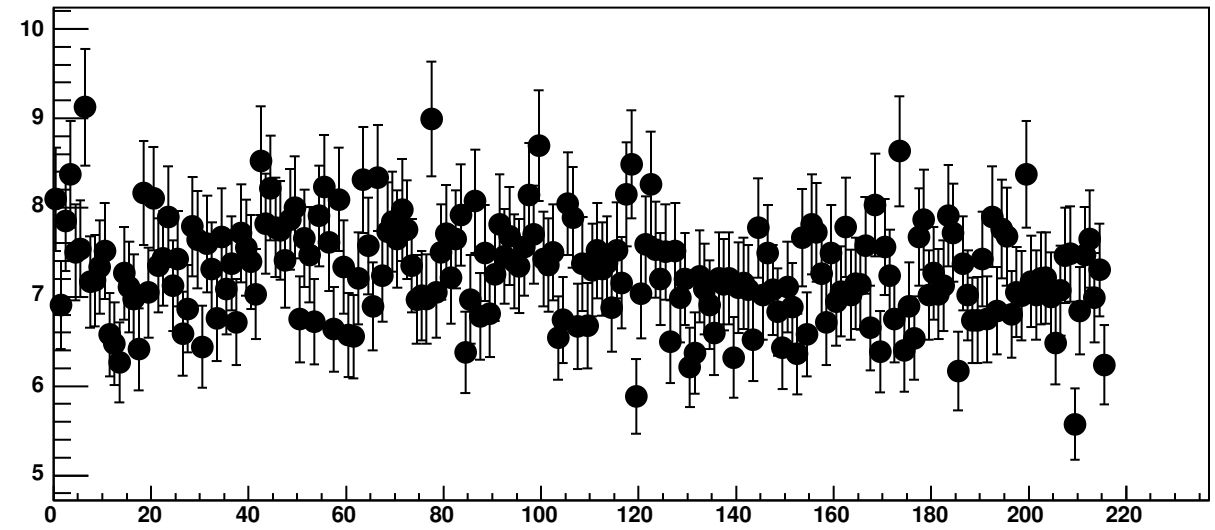
Disabled 5, Hold=35, Fit Slope vs 18*Chip+Chan



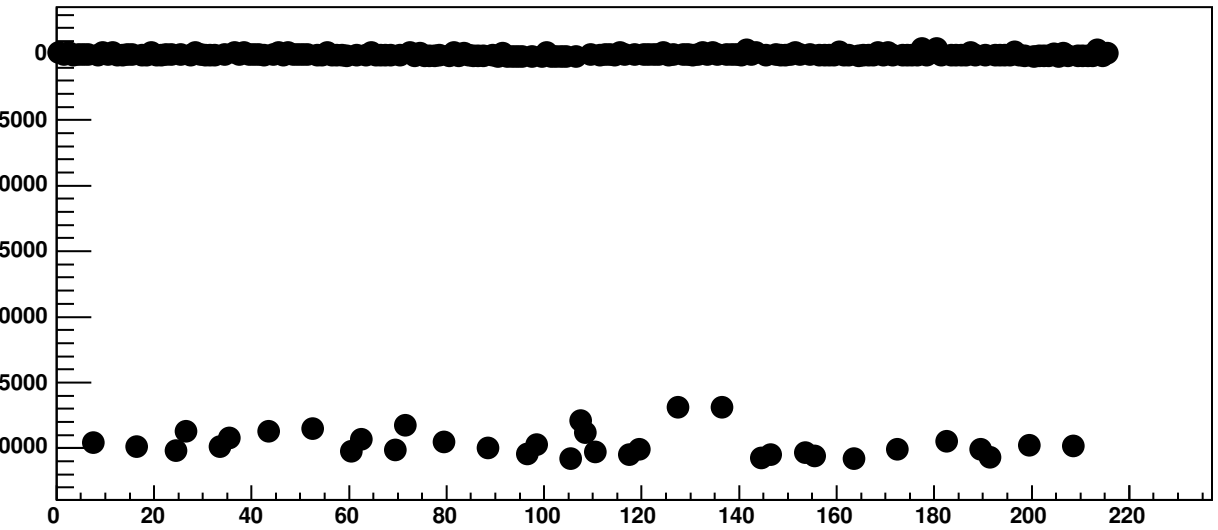
Enables off, ADC Mean vs 18*Chip+Chan



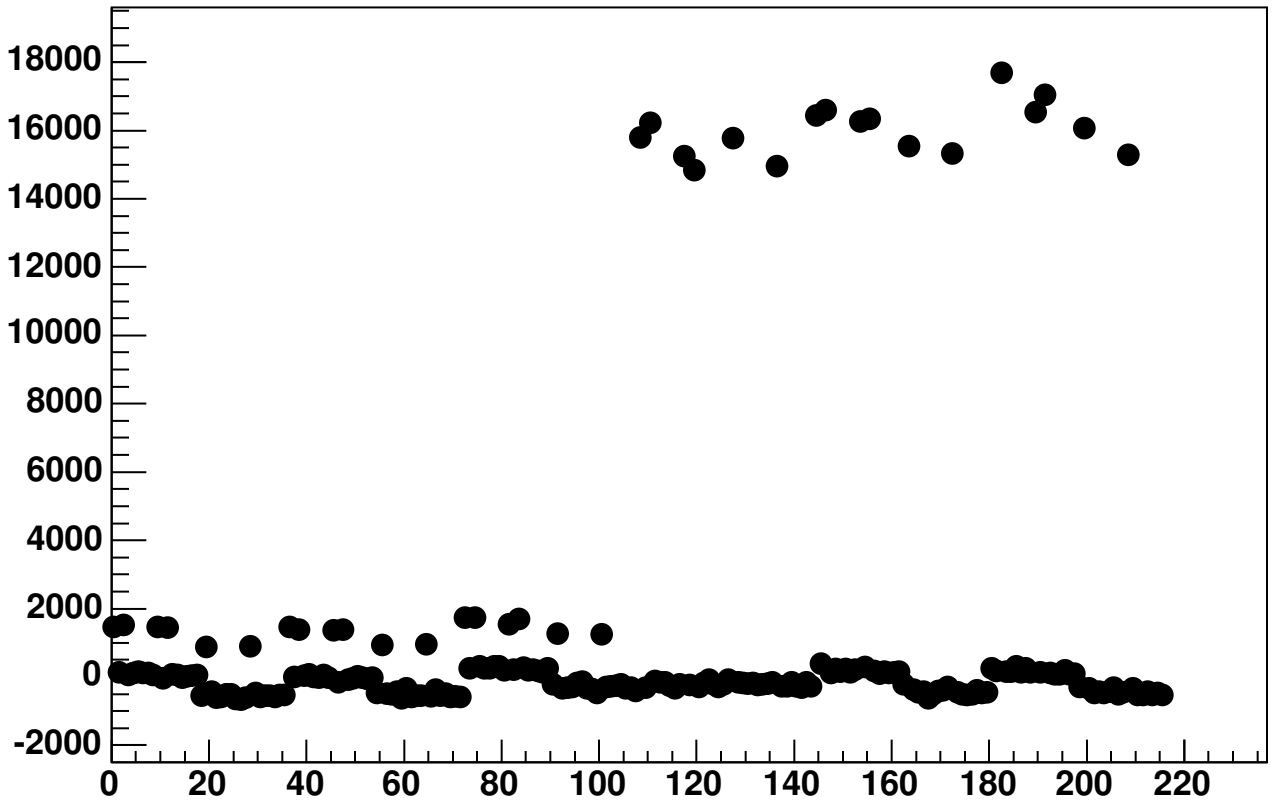
Enables off, ADC Noise vs 18*Chip+Chan



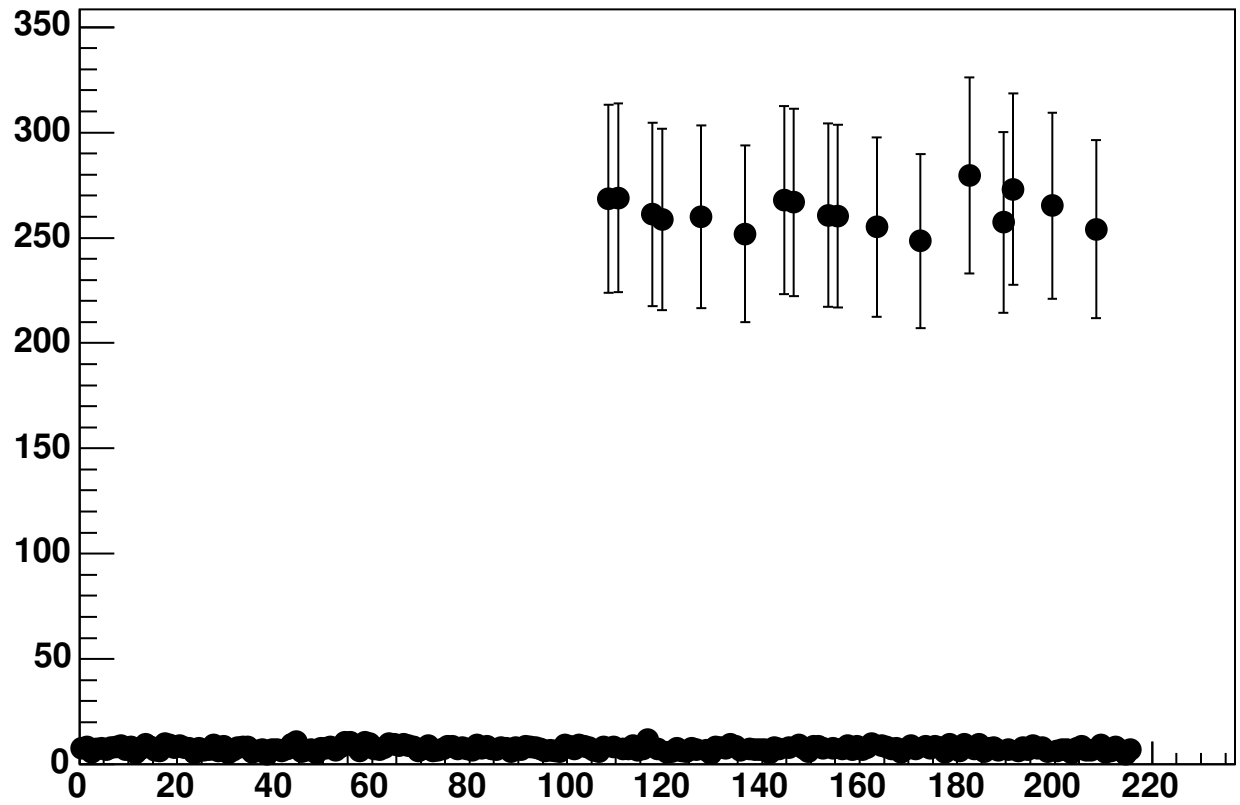
Enables off, ADC Residuals vs 18*Chip+Chan



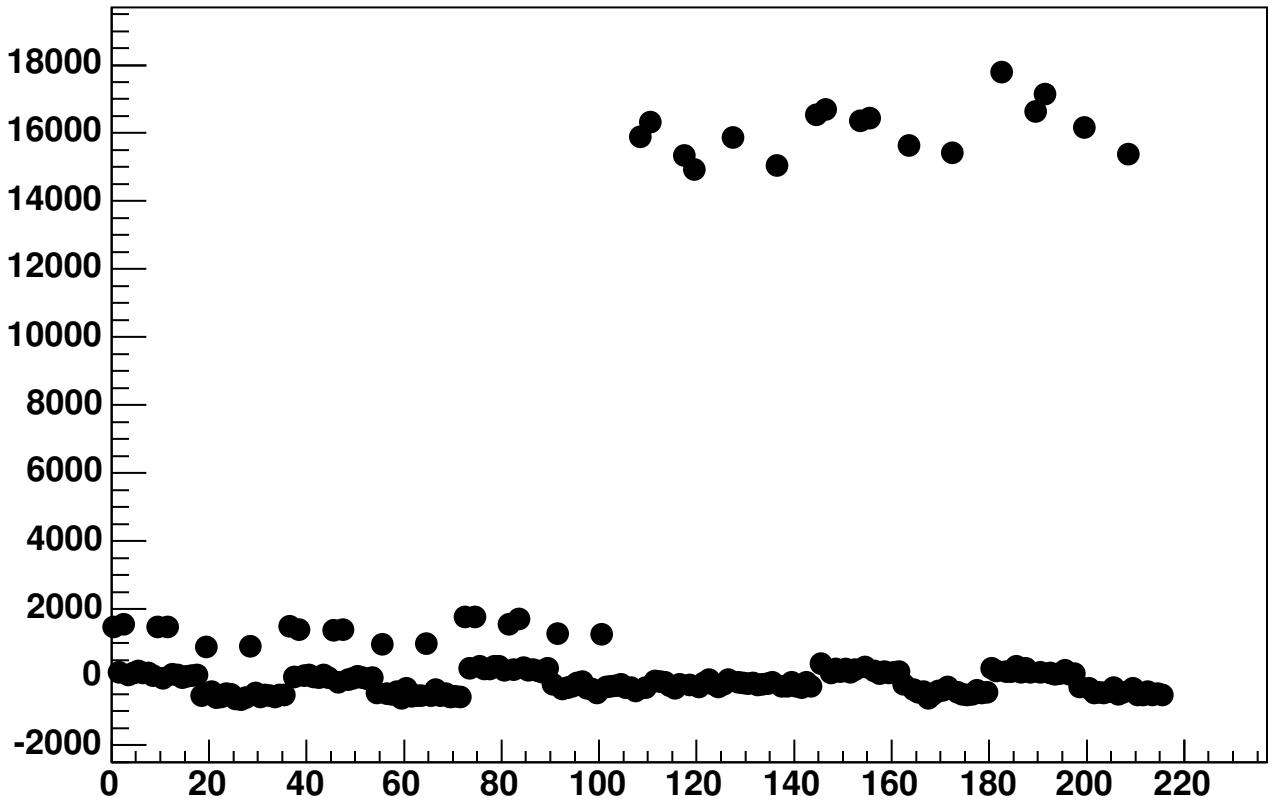
Enable 0, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



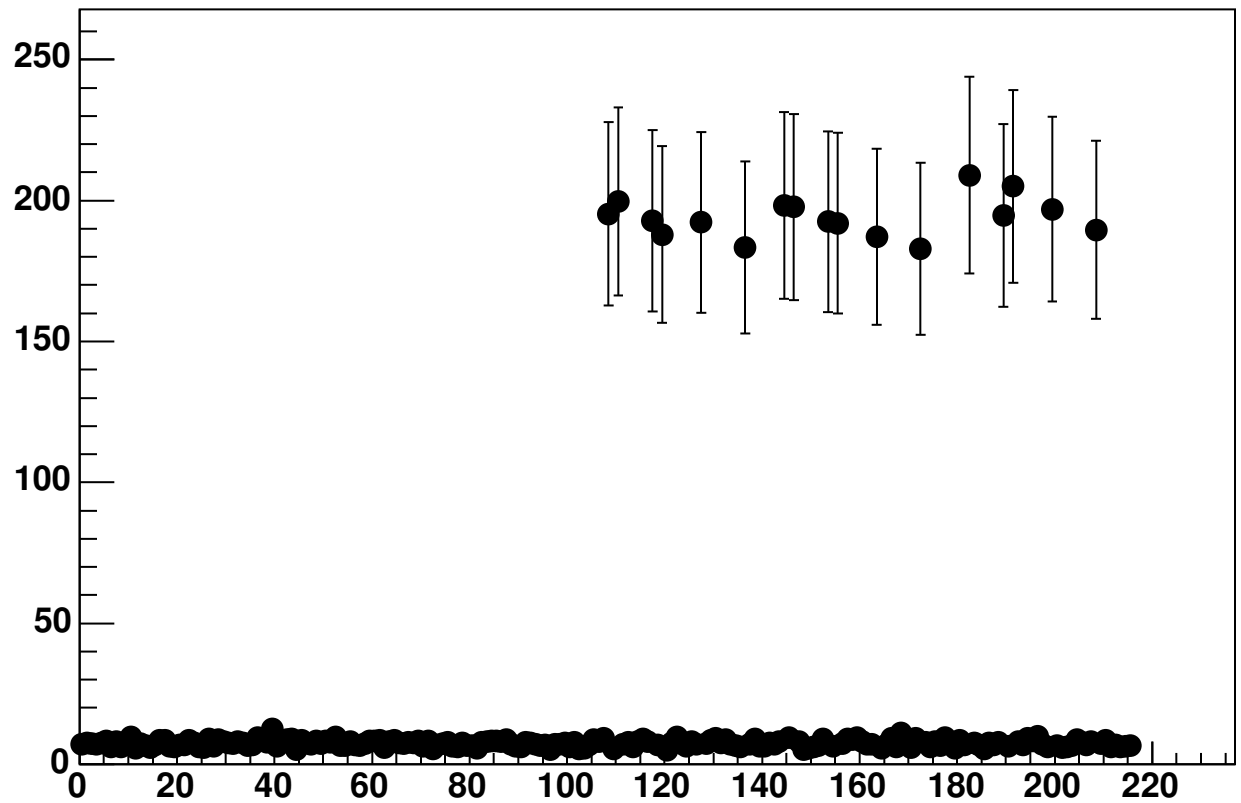
Enable 0, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



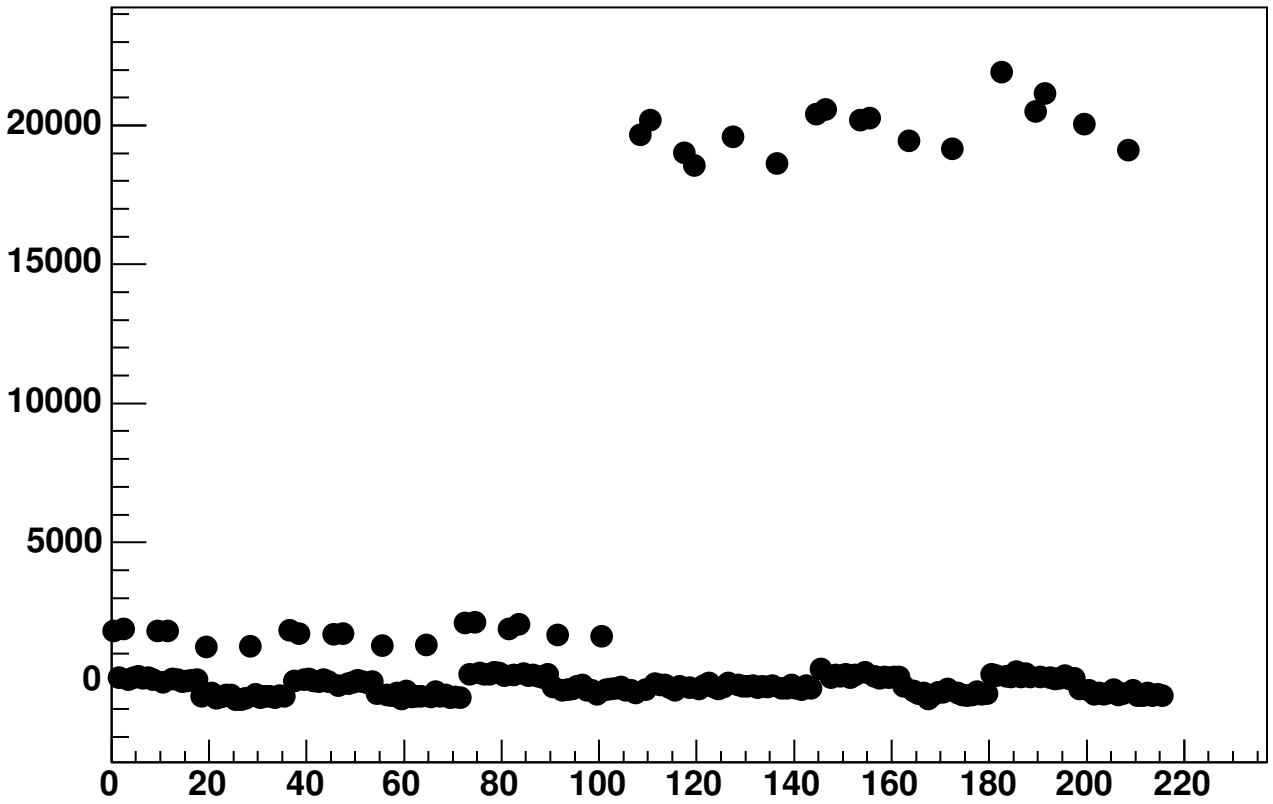
Enable 0, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



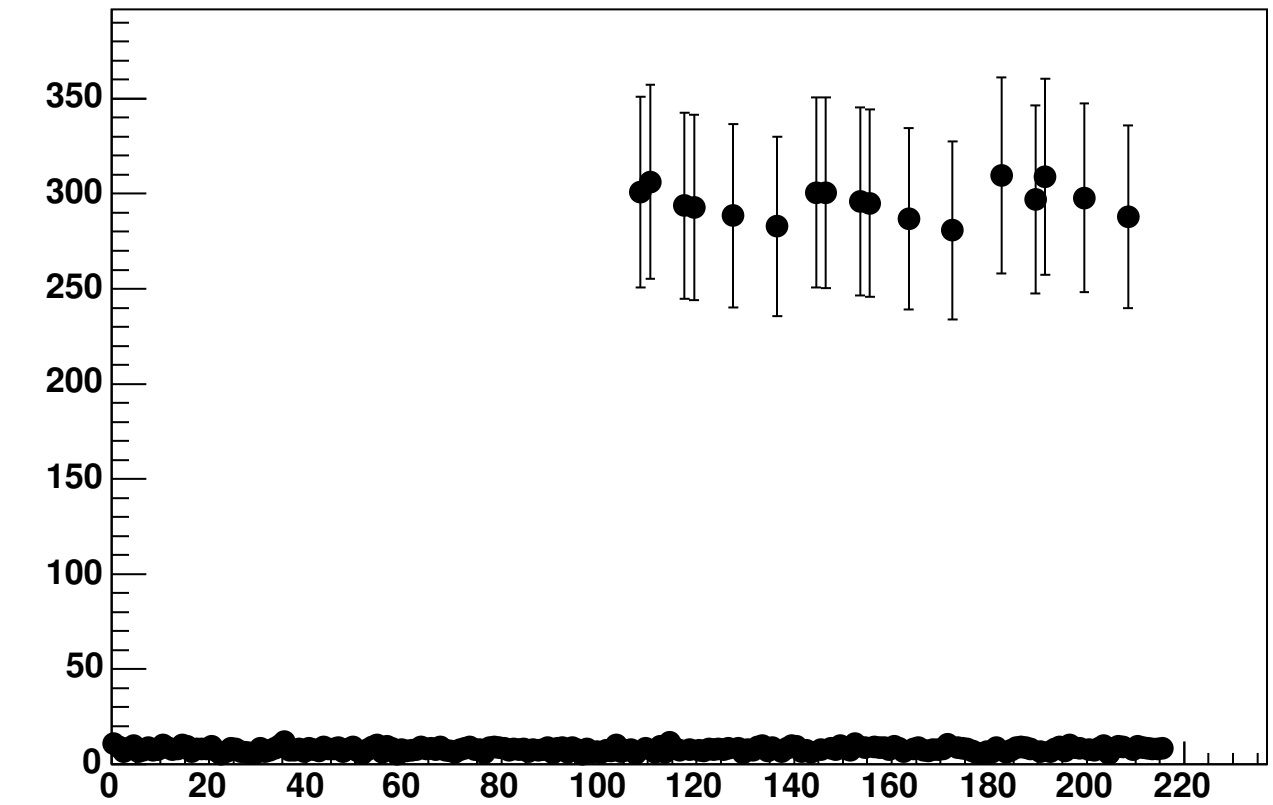
Enable 0, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



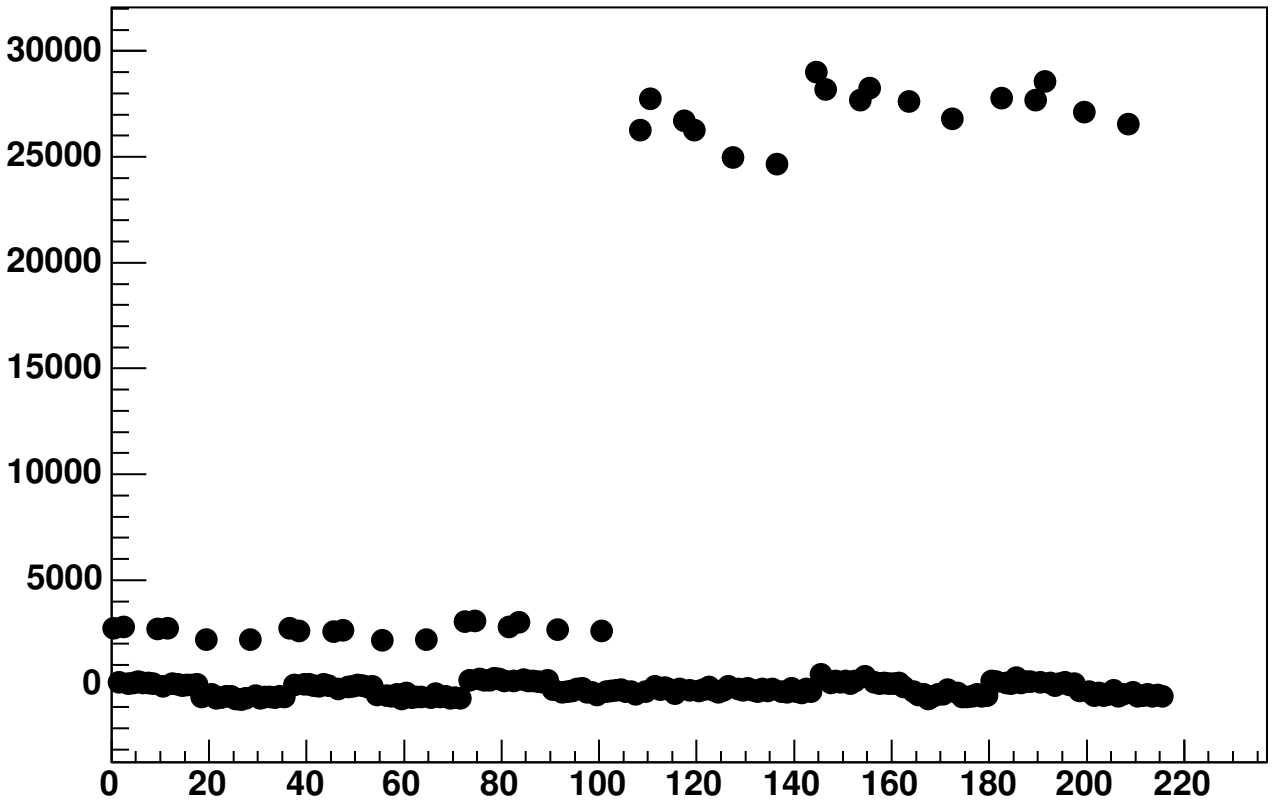
Enable 0, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



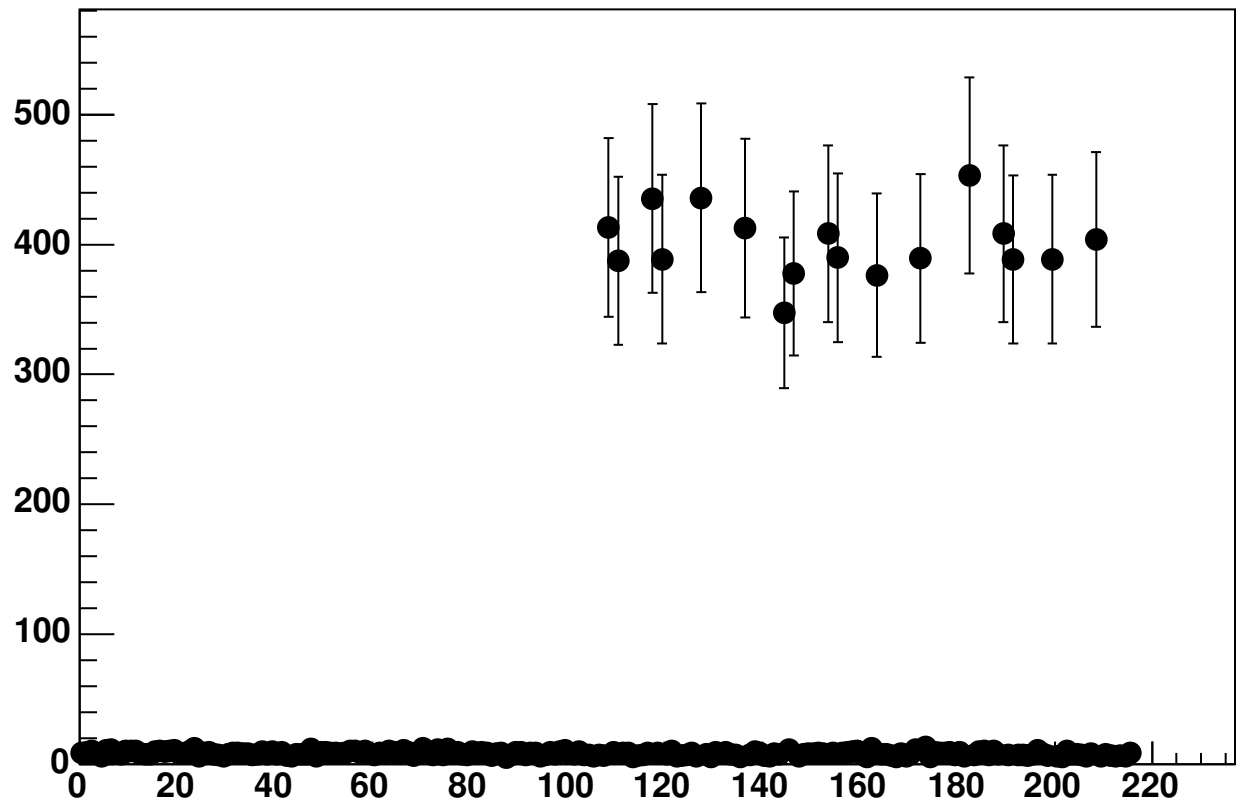
Enable 0, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



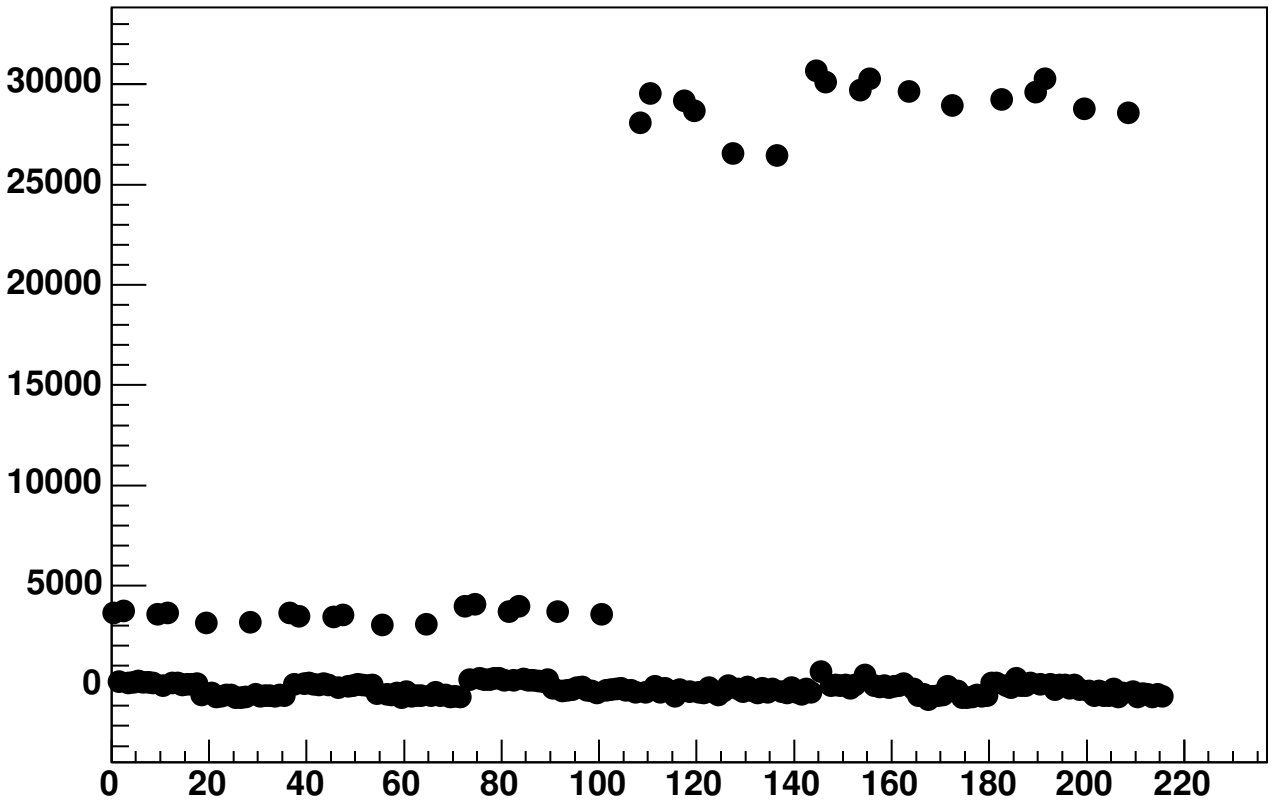
Enable 0, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



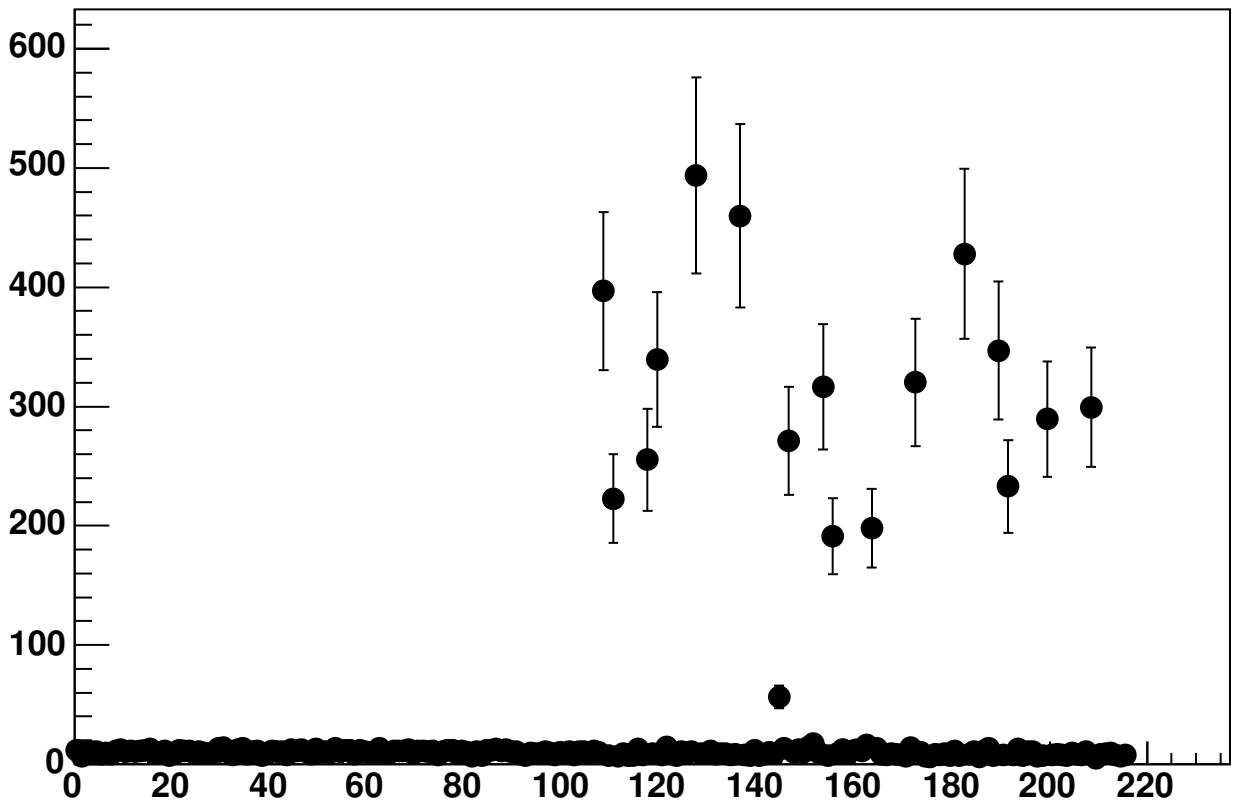
Enable 0, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



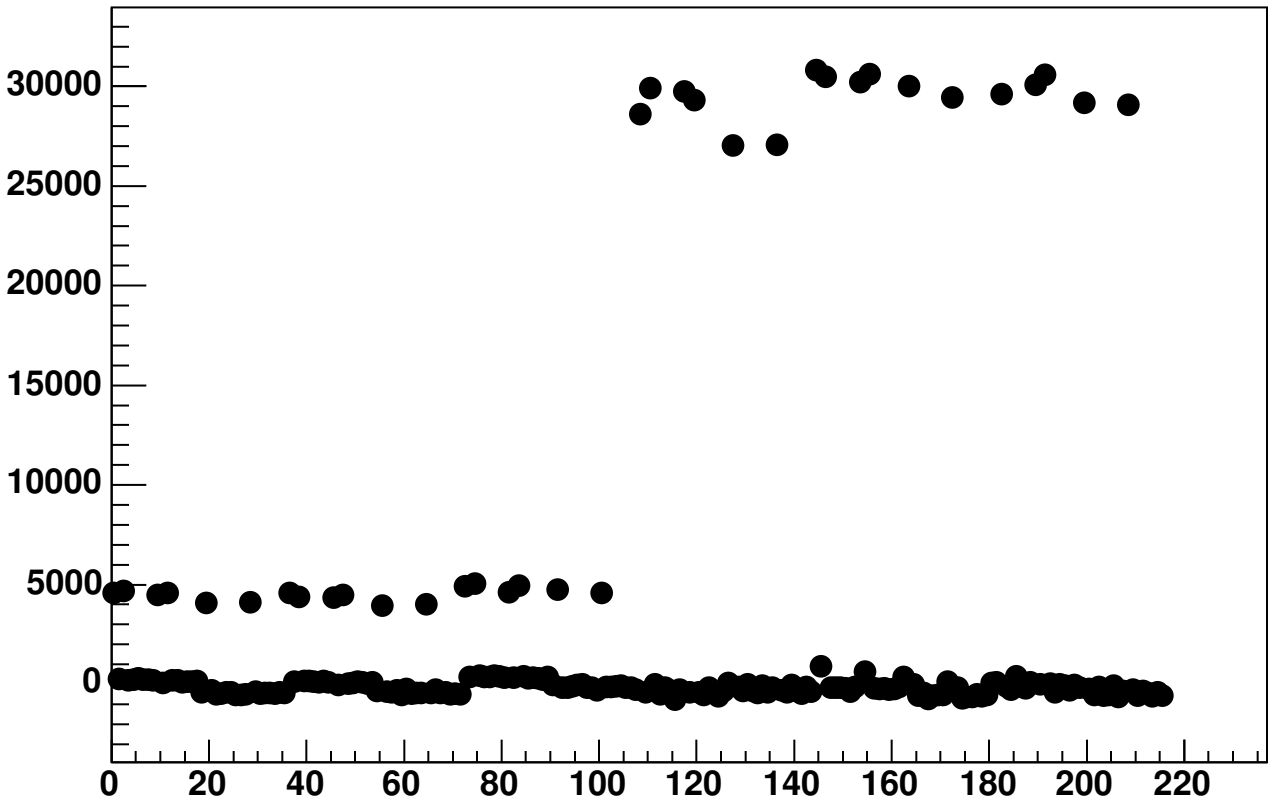
Enable 0, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



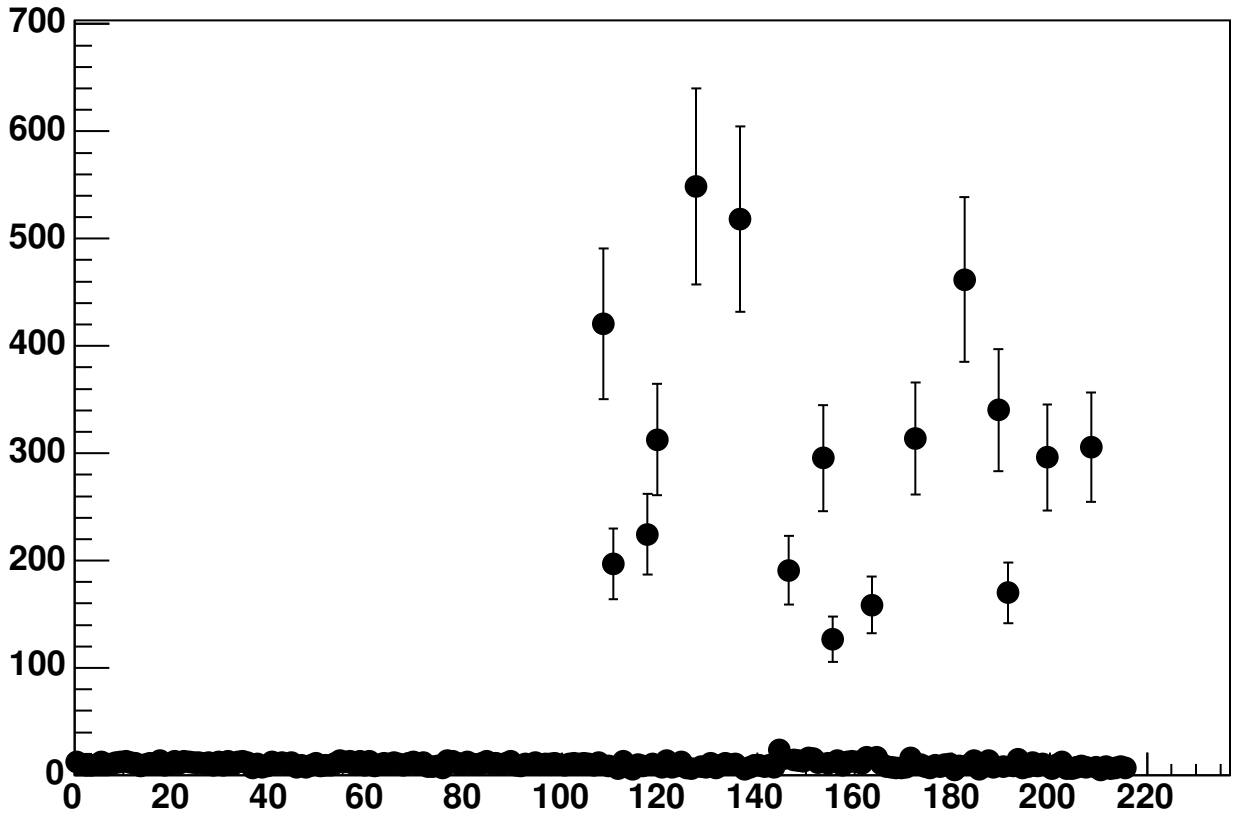
Enable 0, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



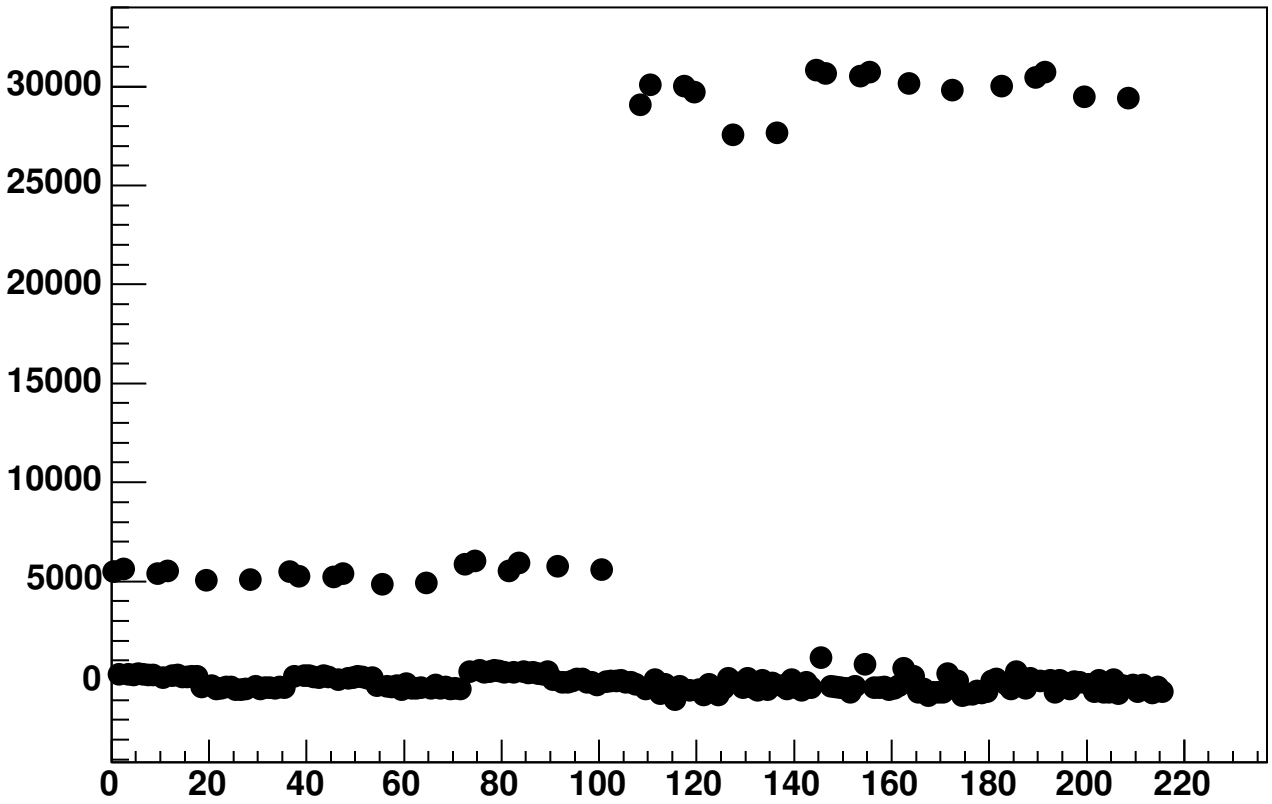
Enable 0, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



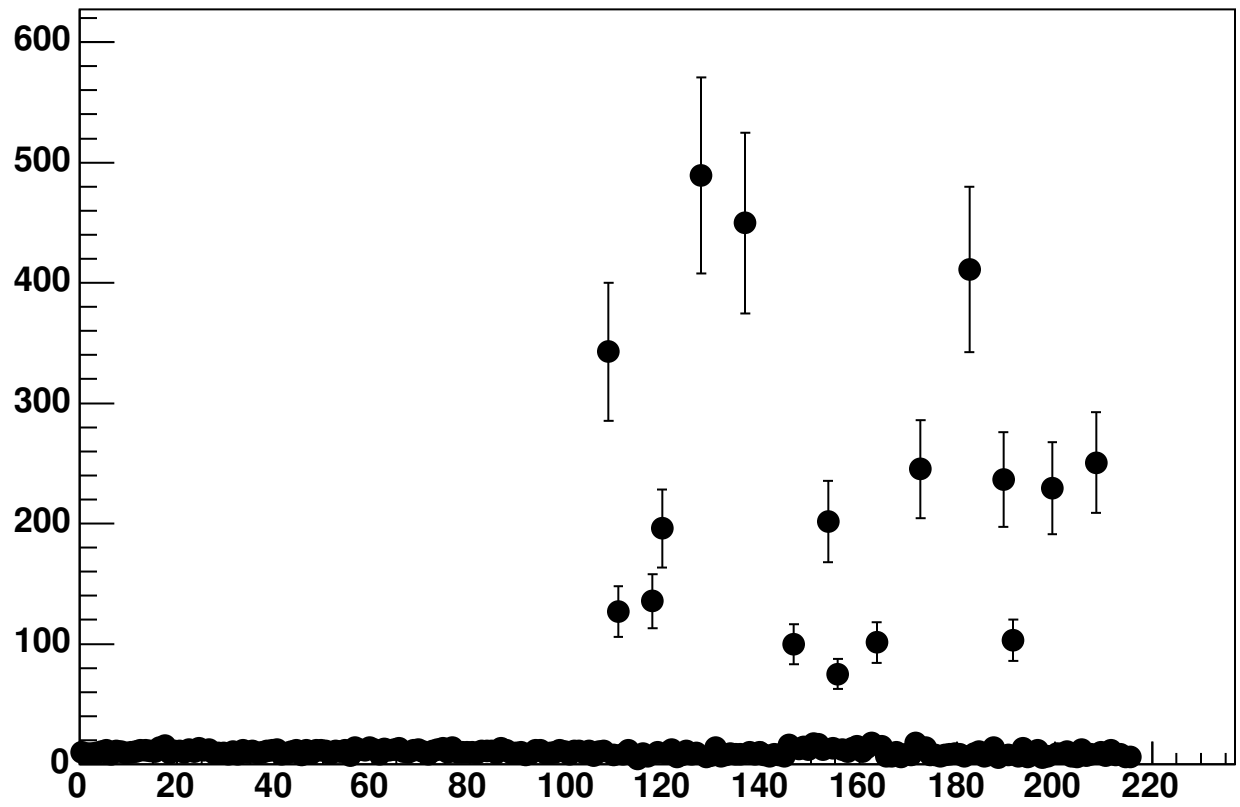
Enable 0, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



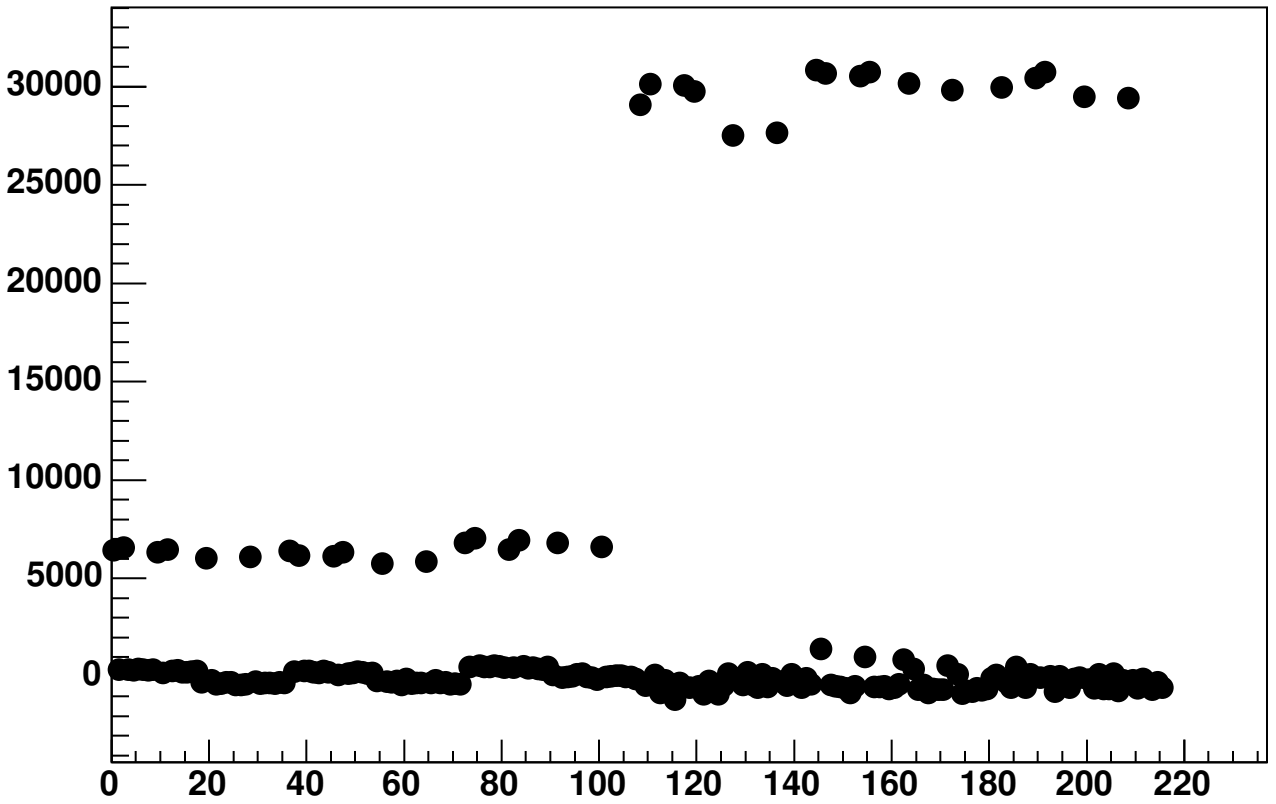
Enable 0, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



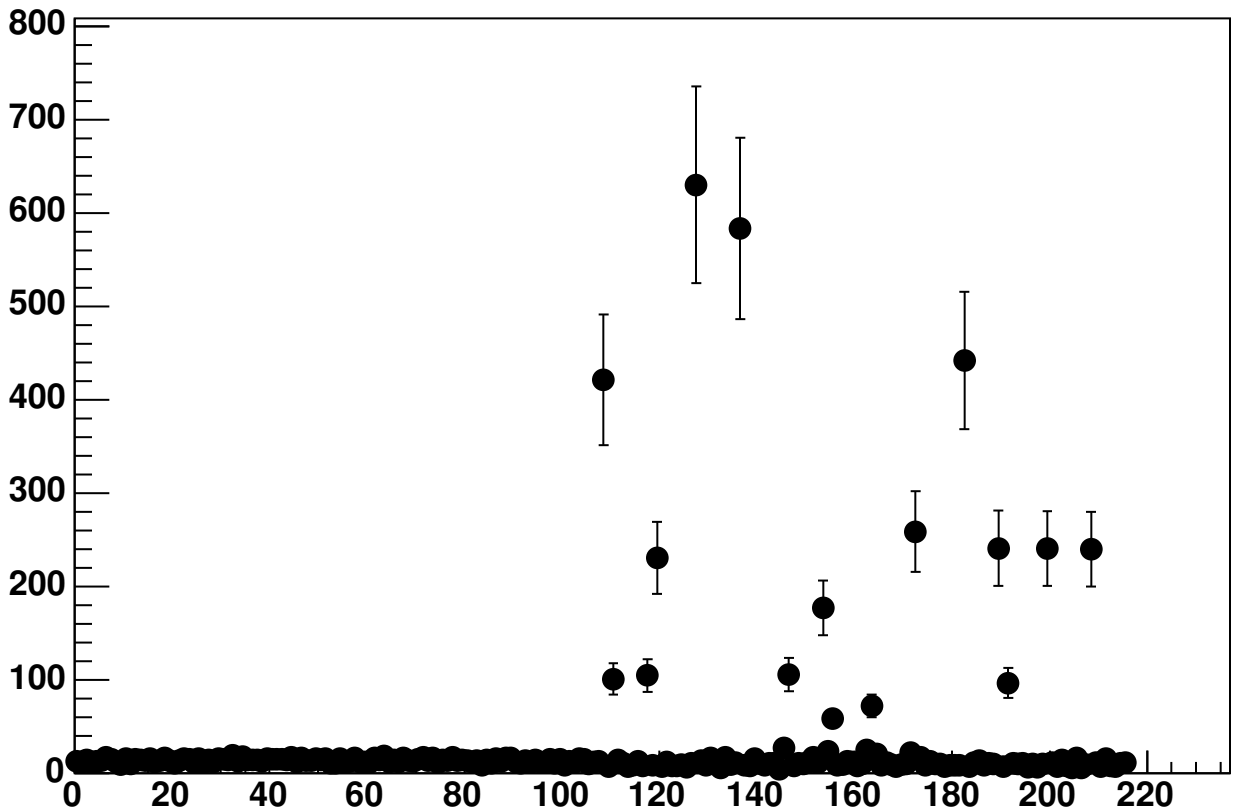
Enable 0, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



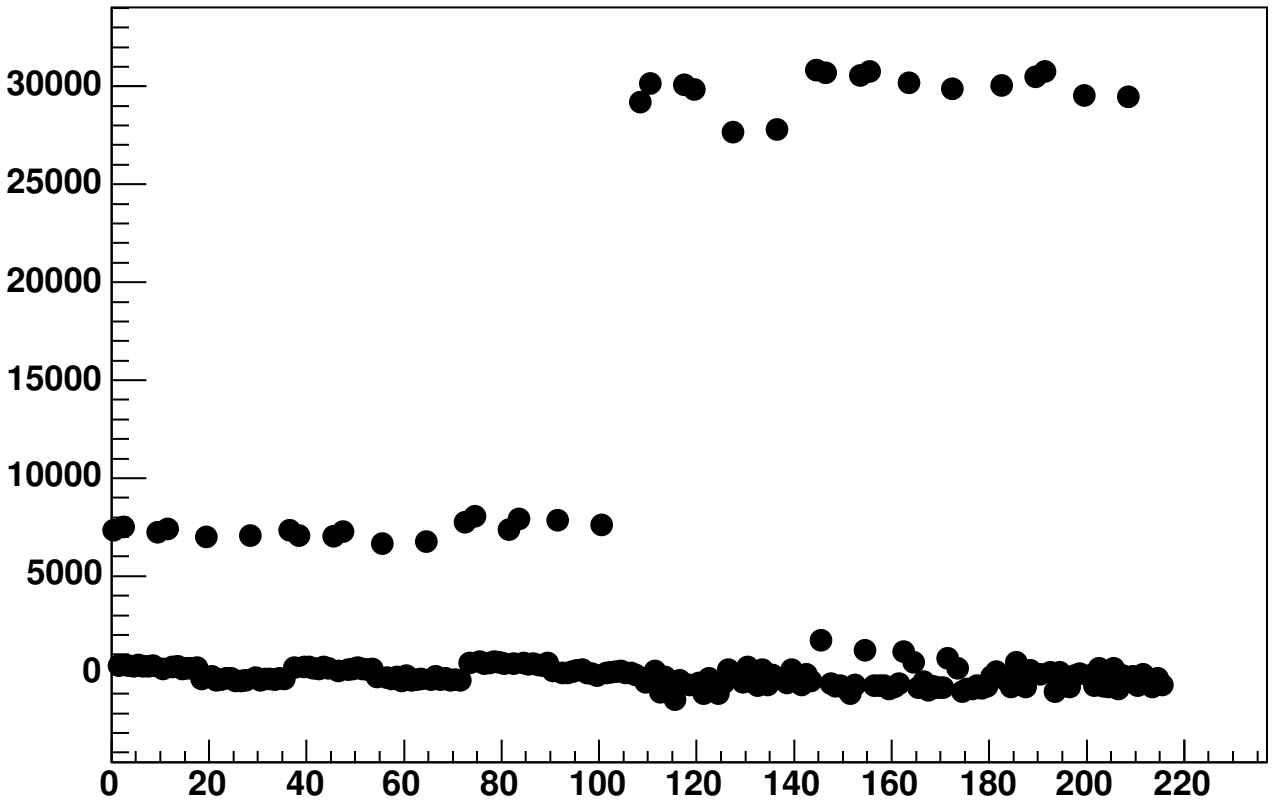
Enable 0, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



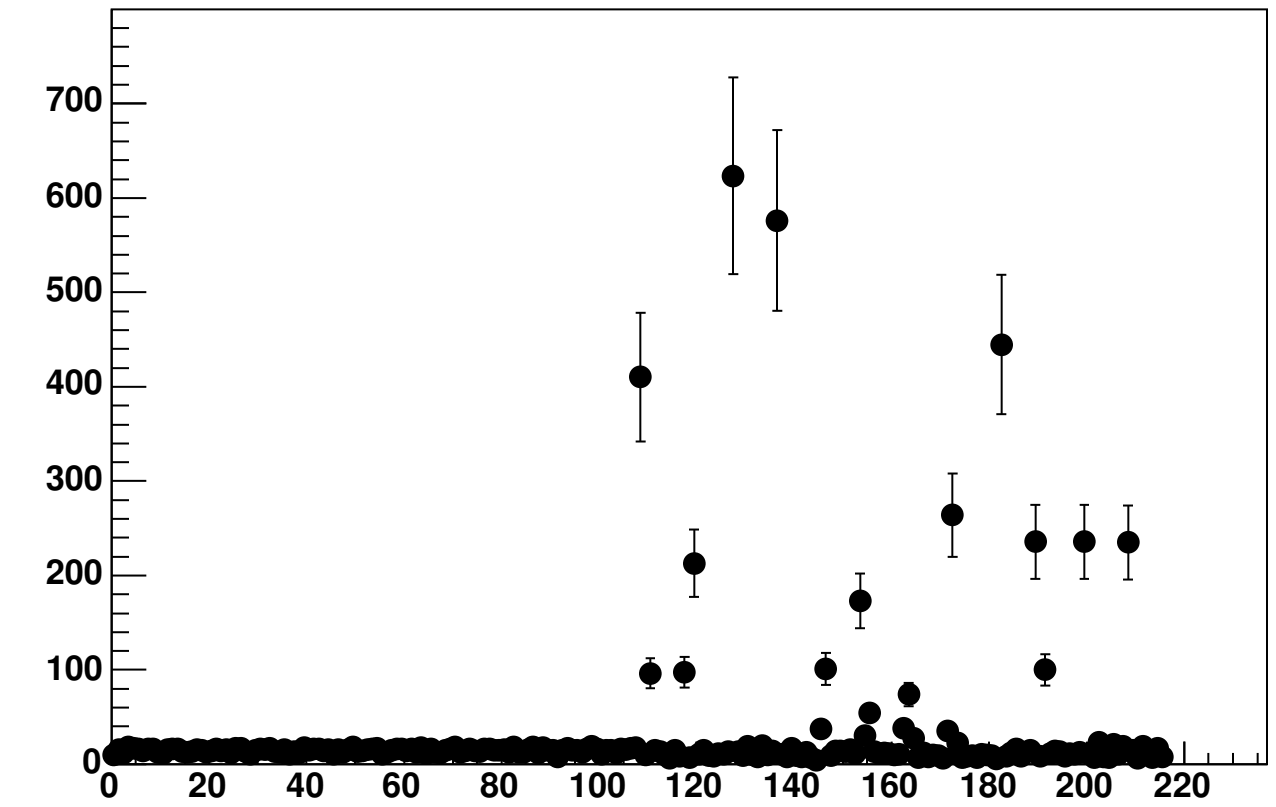
Enable 0, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



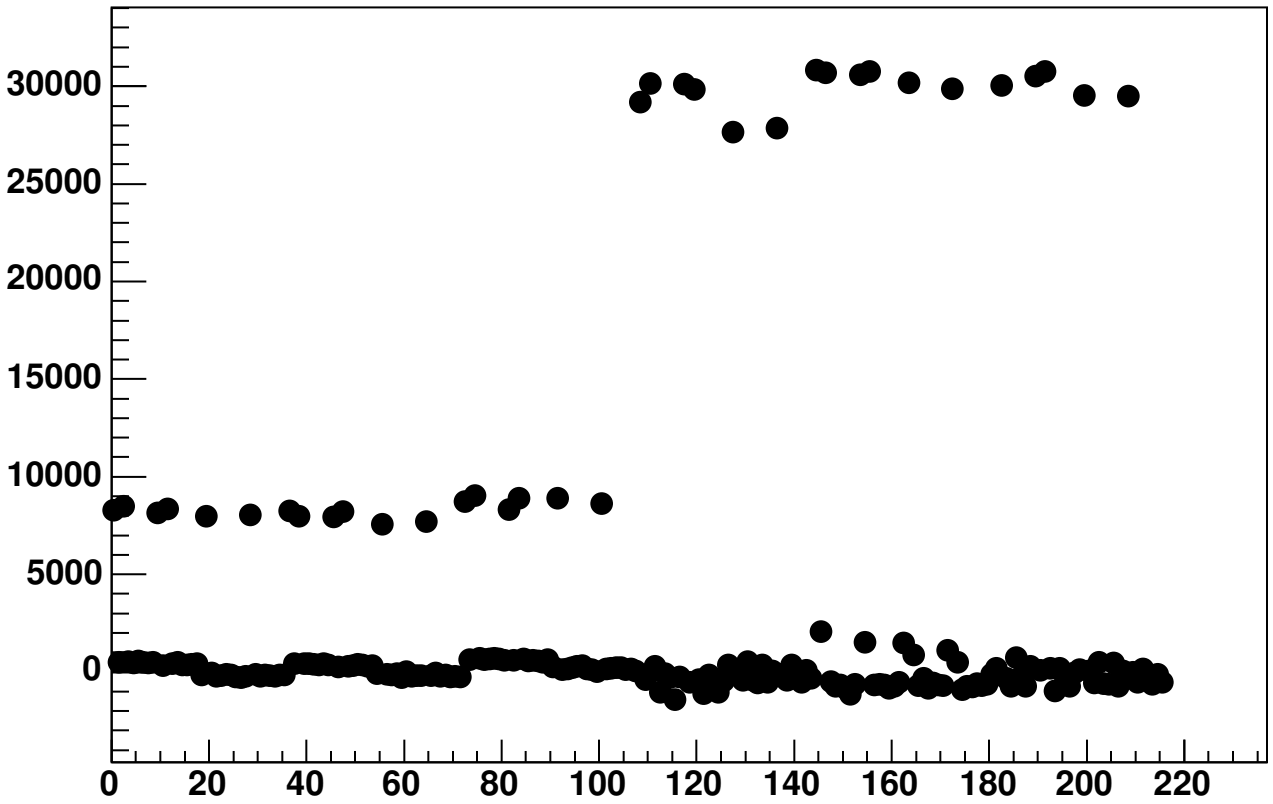
Enable 0, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



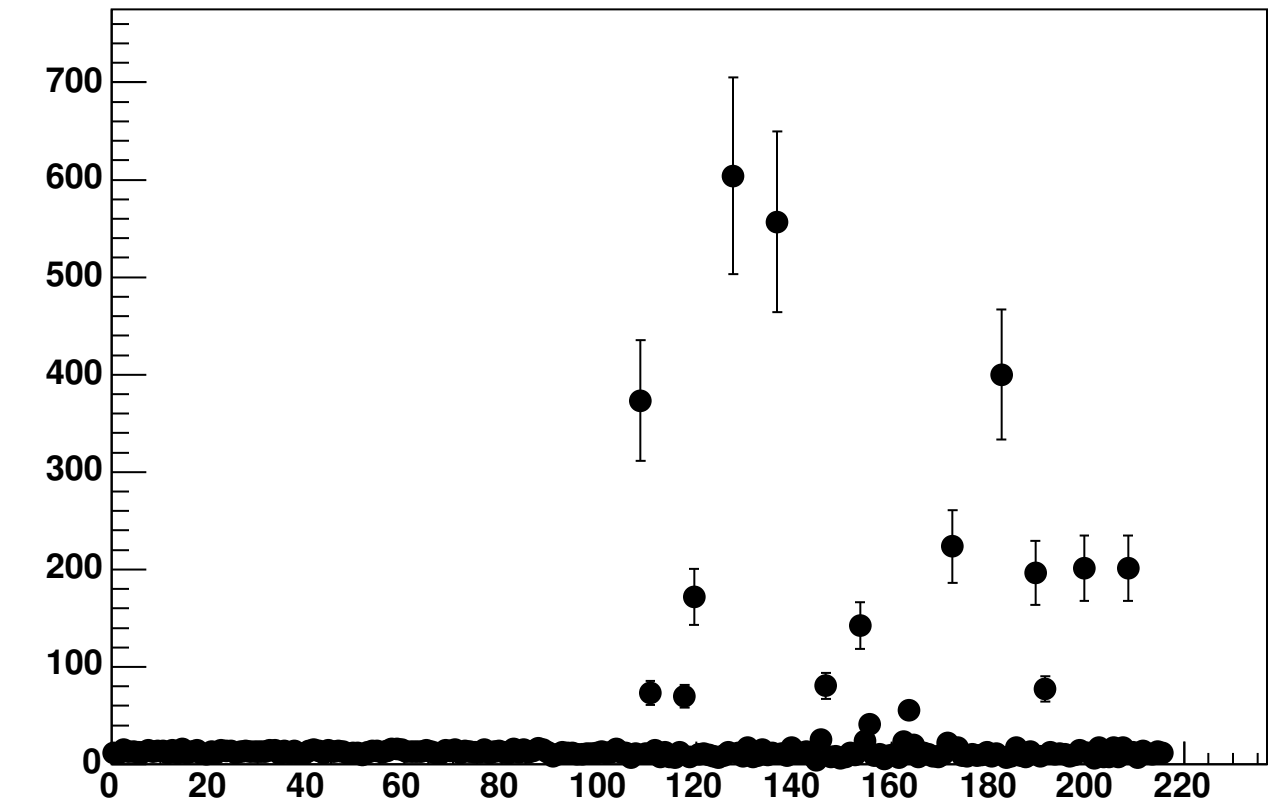
Enable 0, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



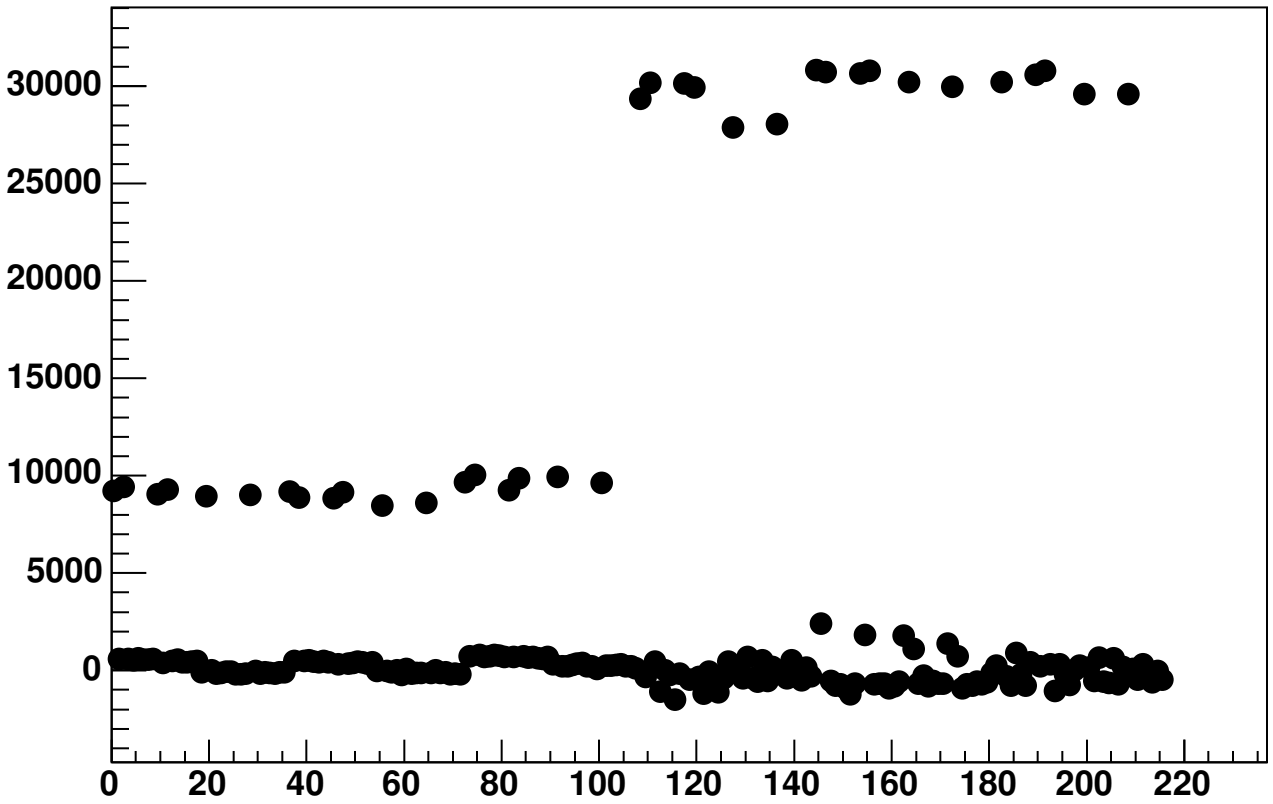
Enable 0, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



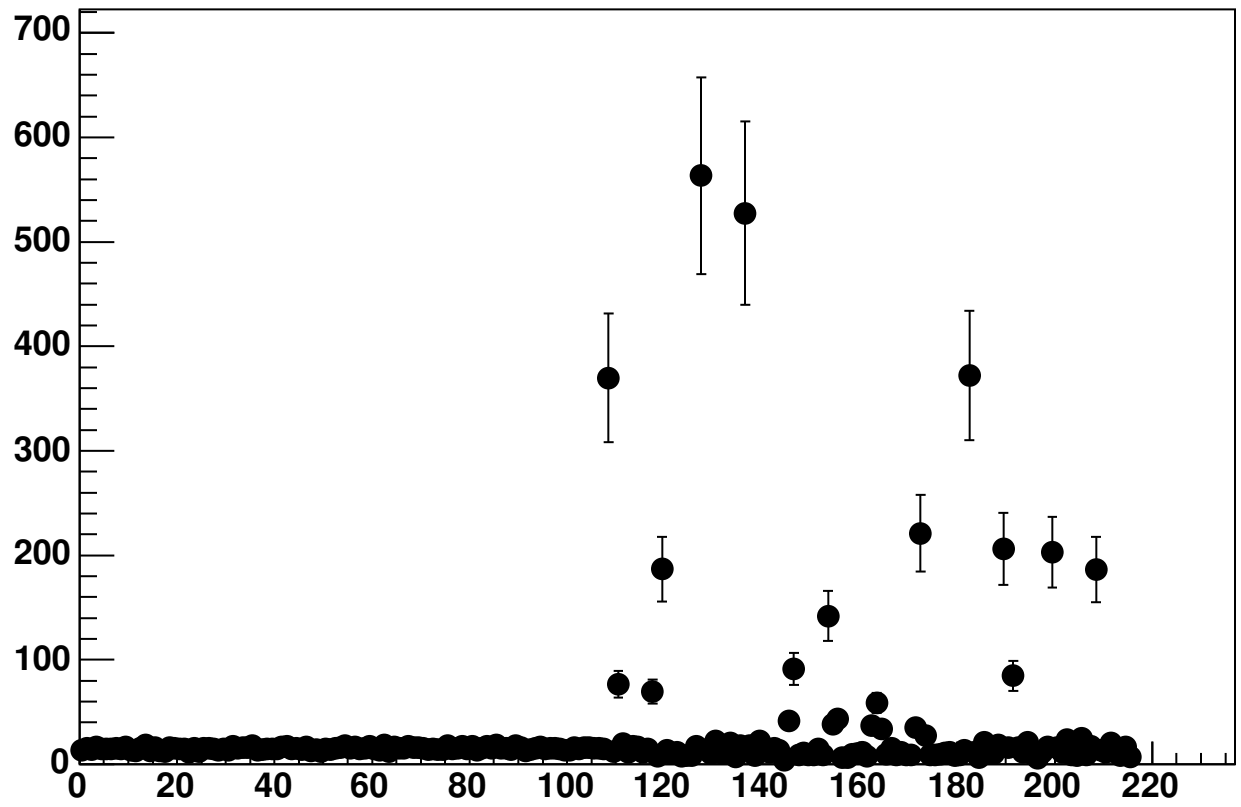
Enable 0, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



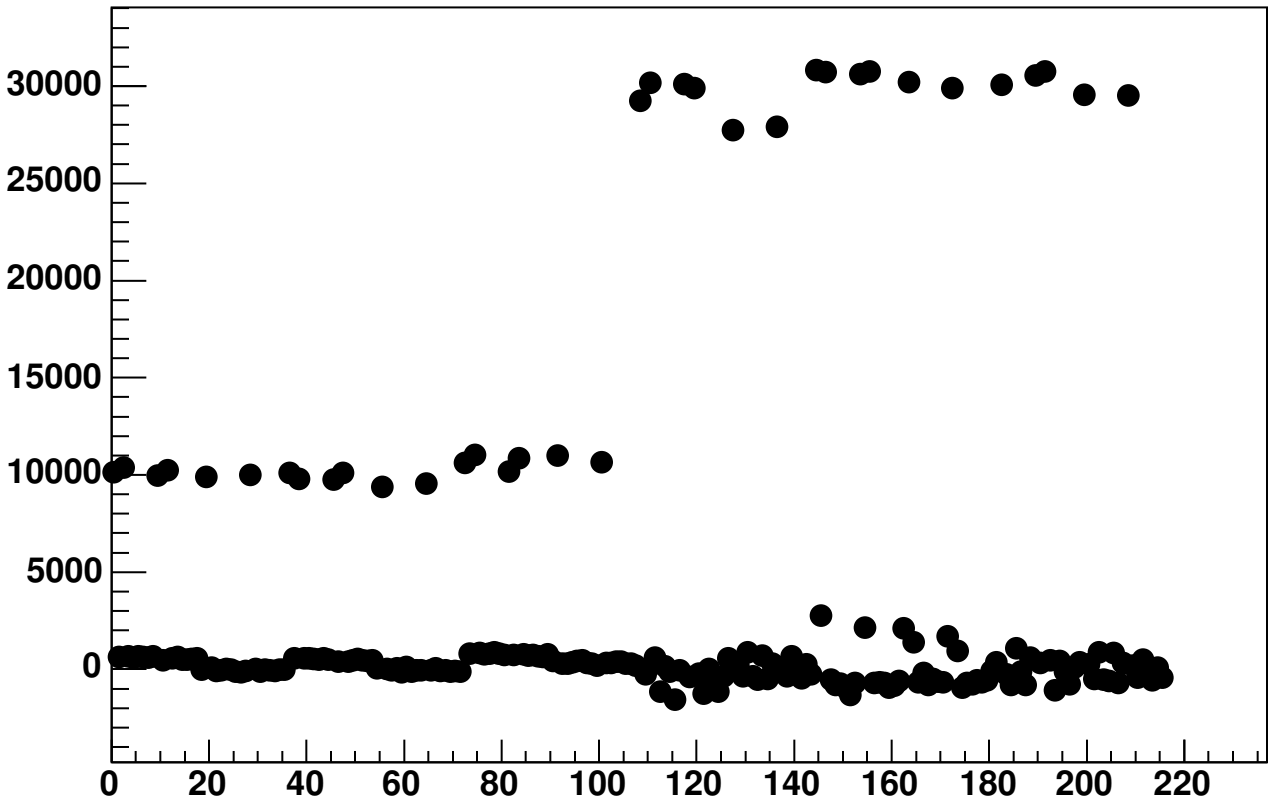
Enable 0, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



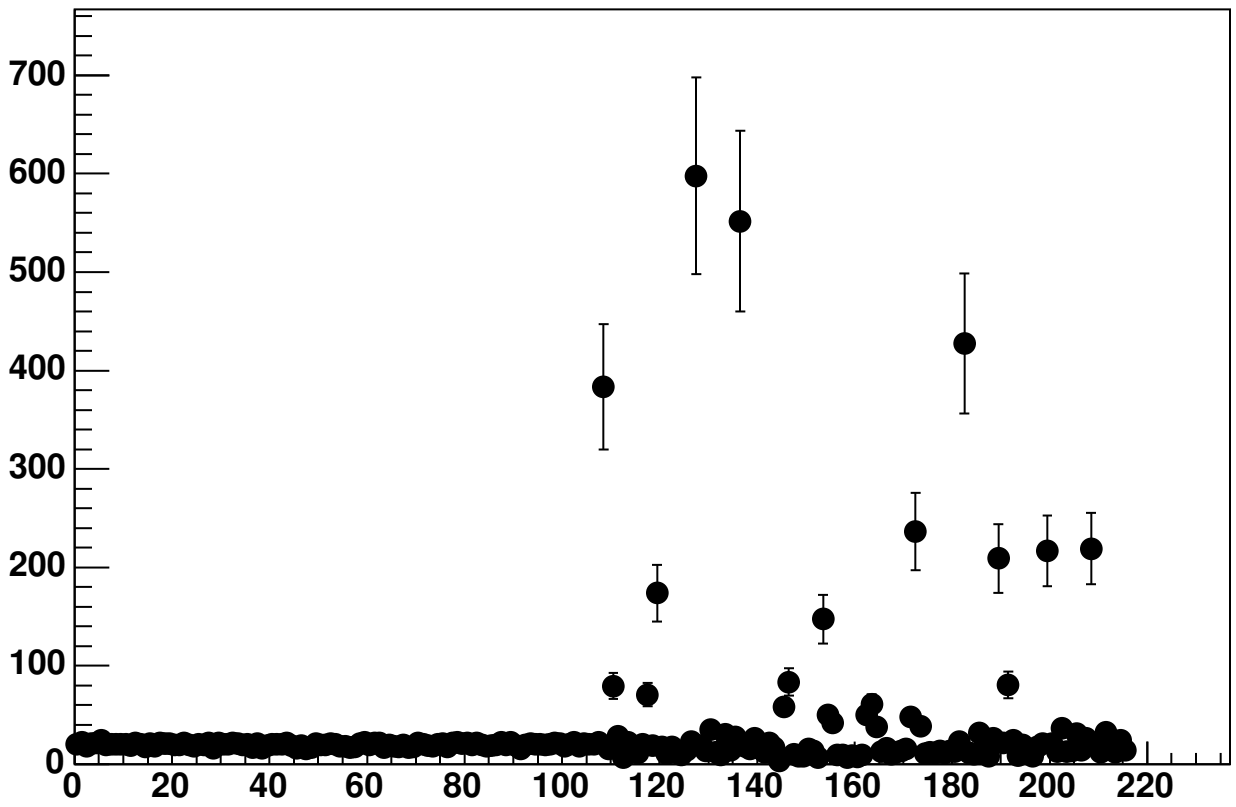
Enable 0, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



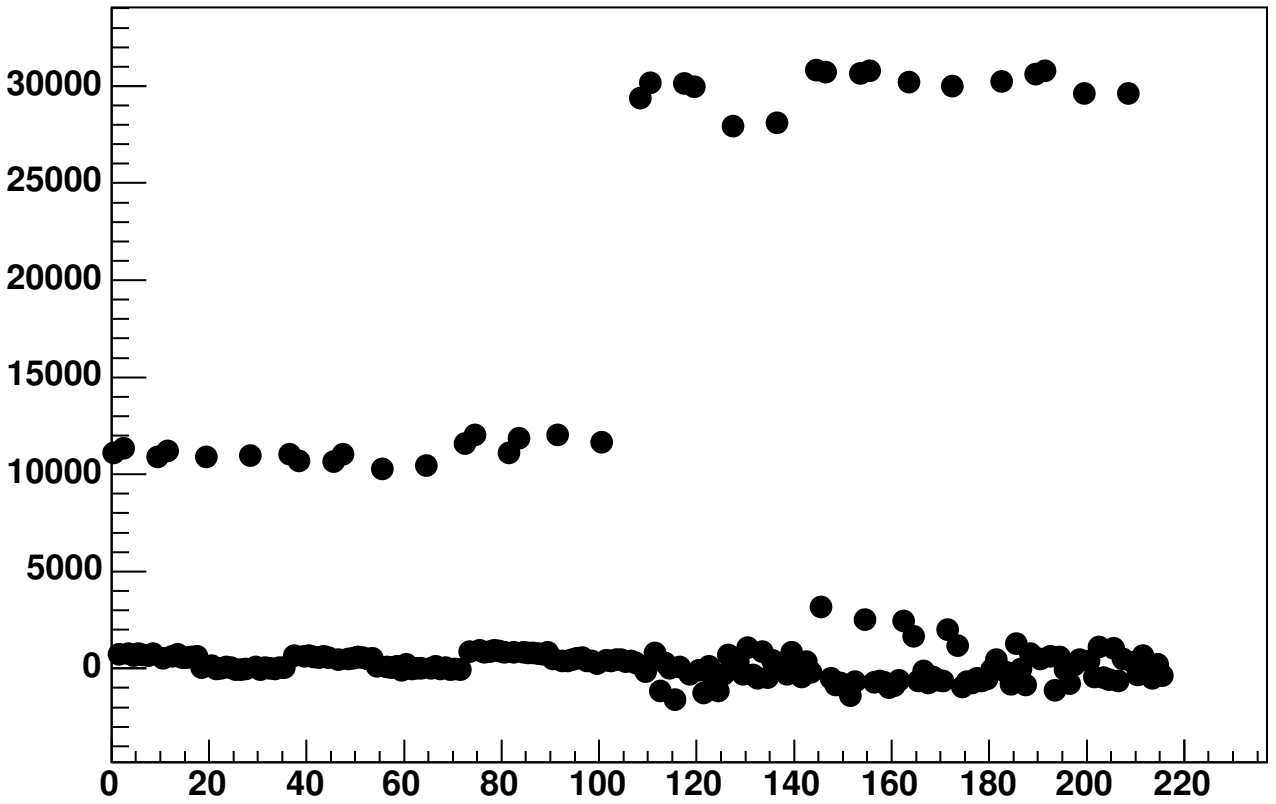
Enable 0, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



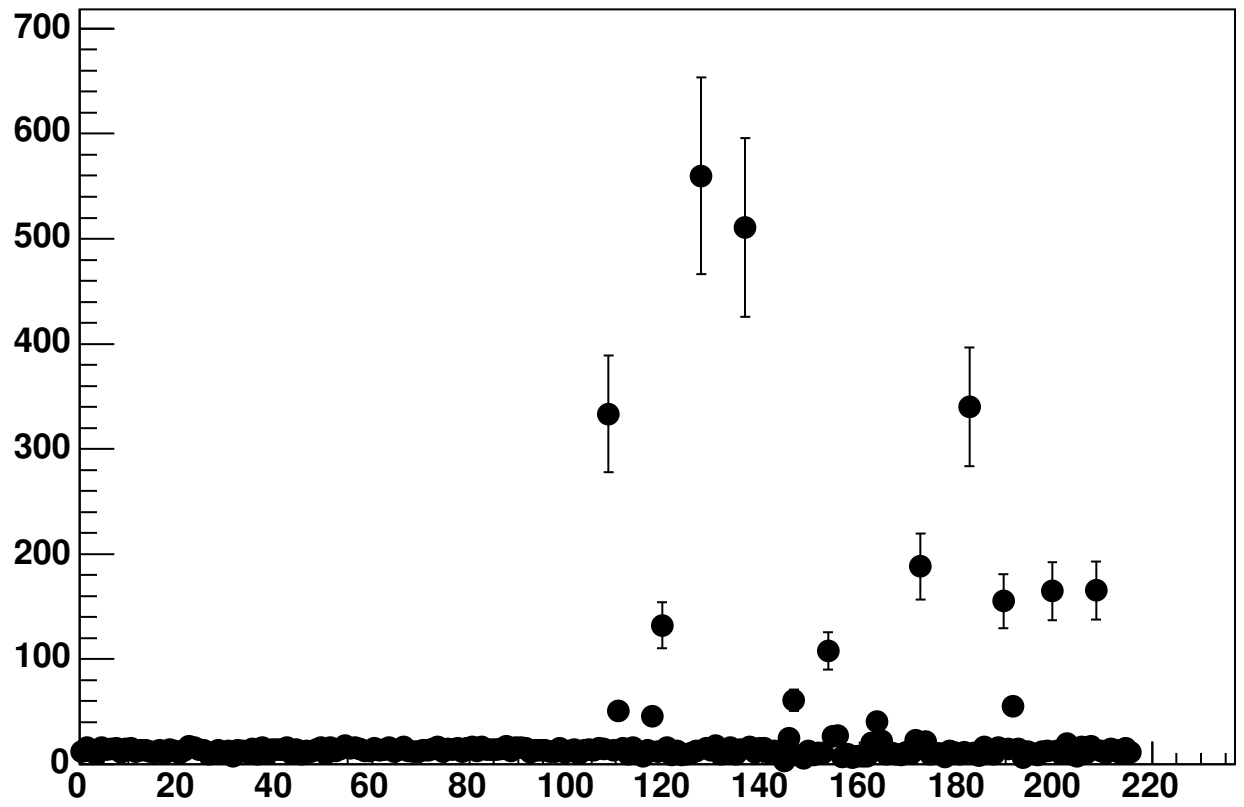
Enable 0, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



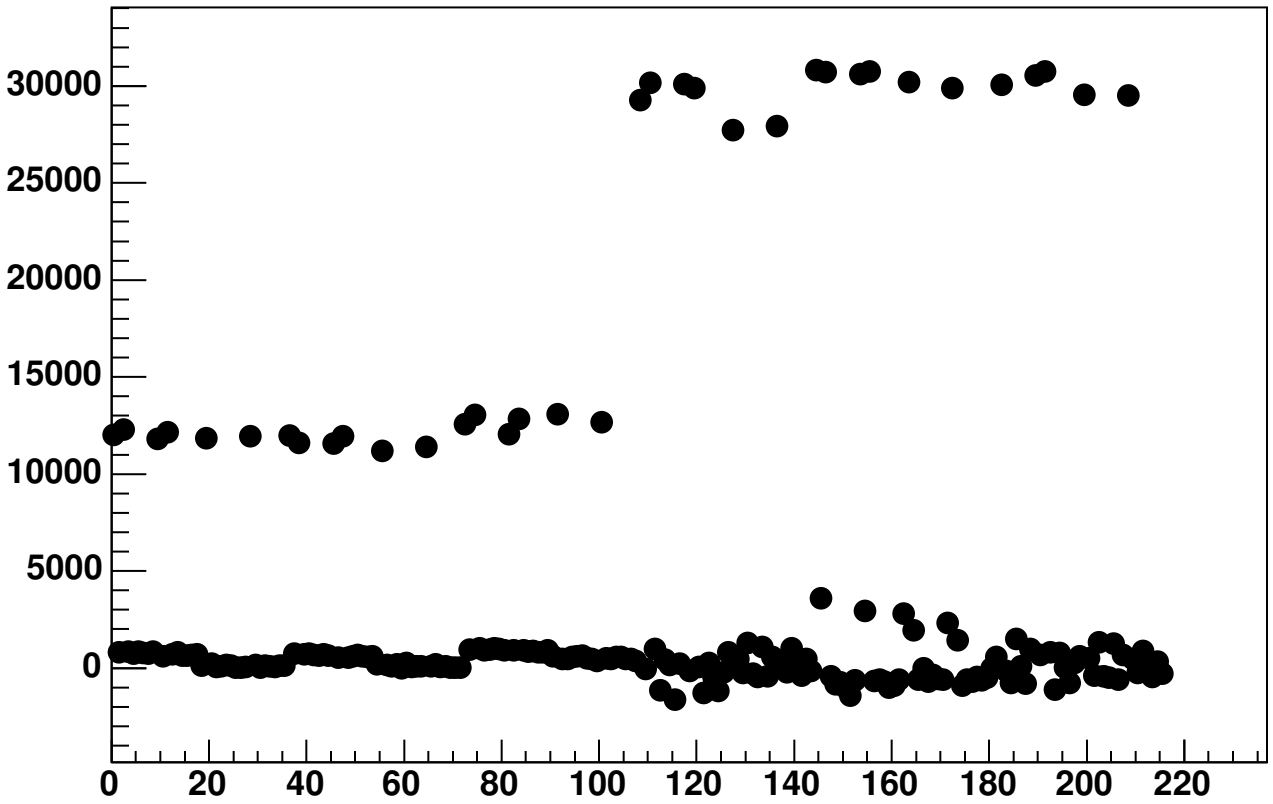
Enable 0, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



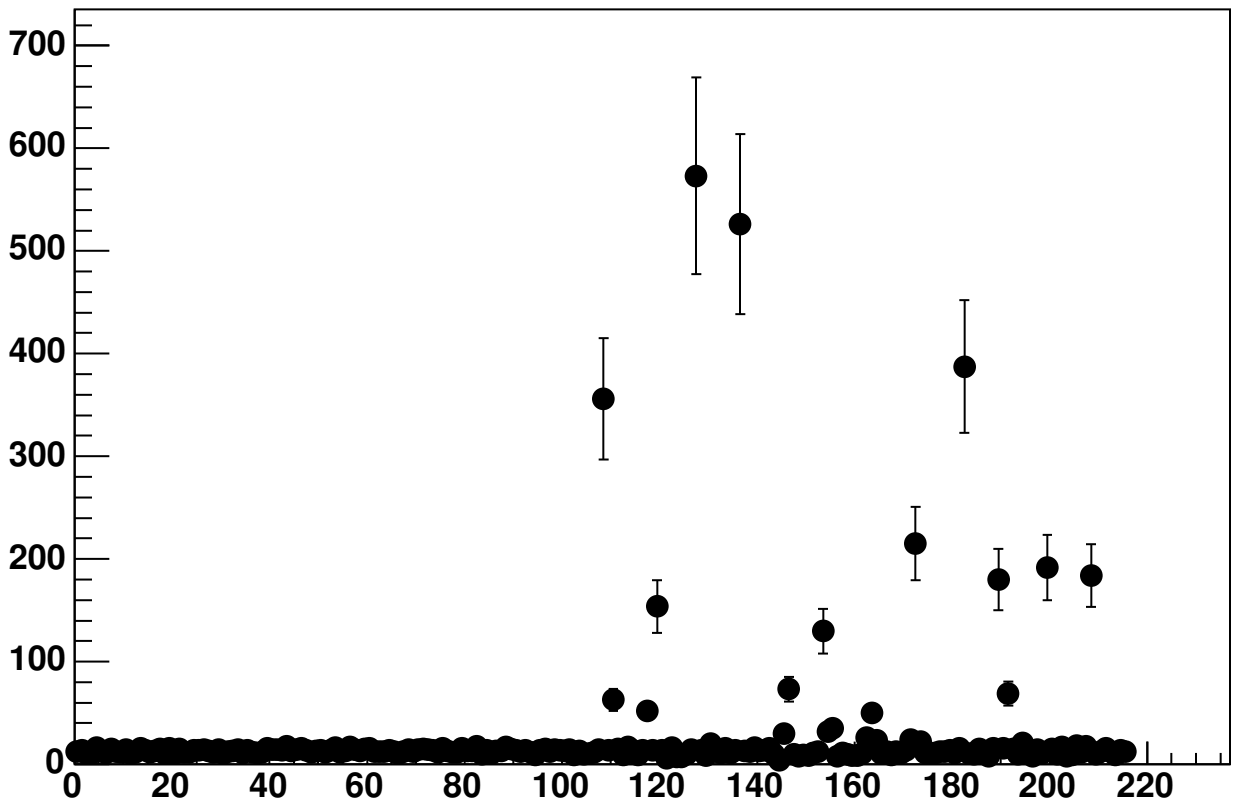
Enable 0, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



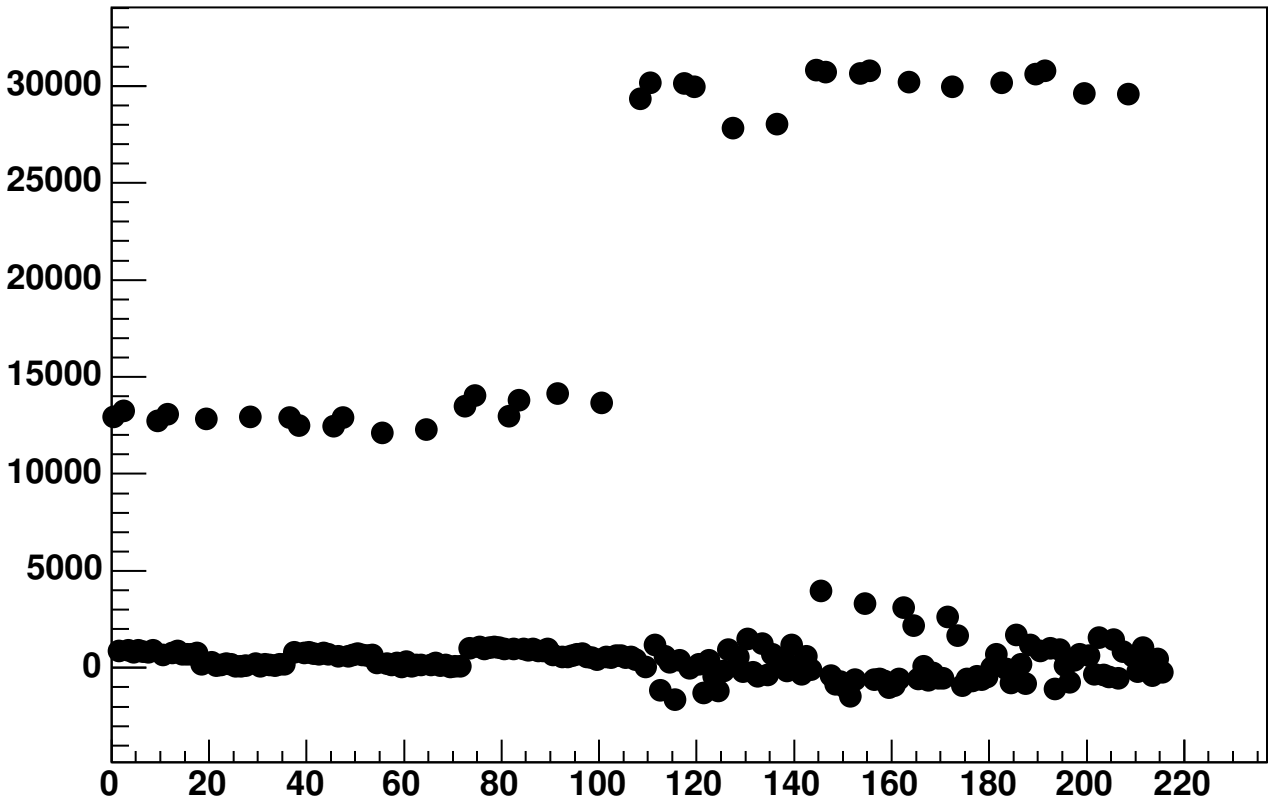
Enable 0, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



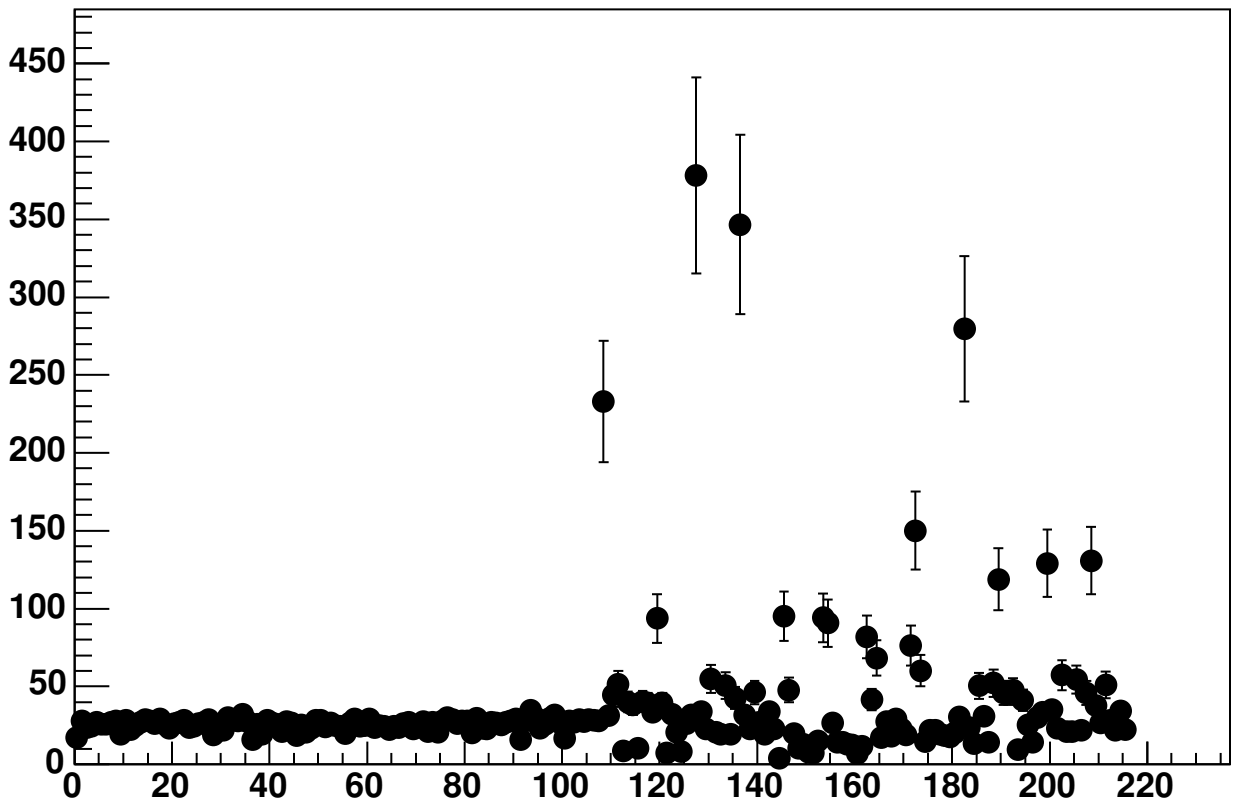
Enable 0, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



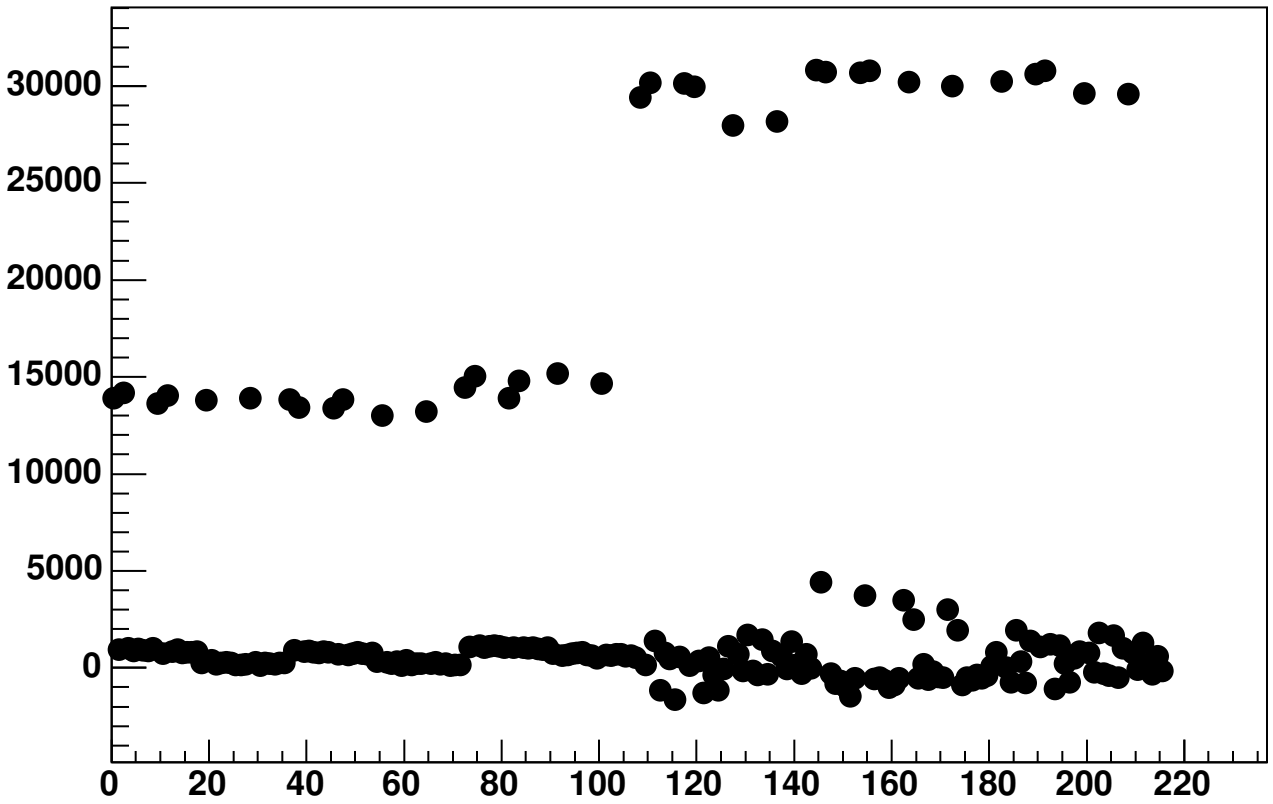
Enable 0, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



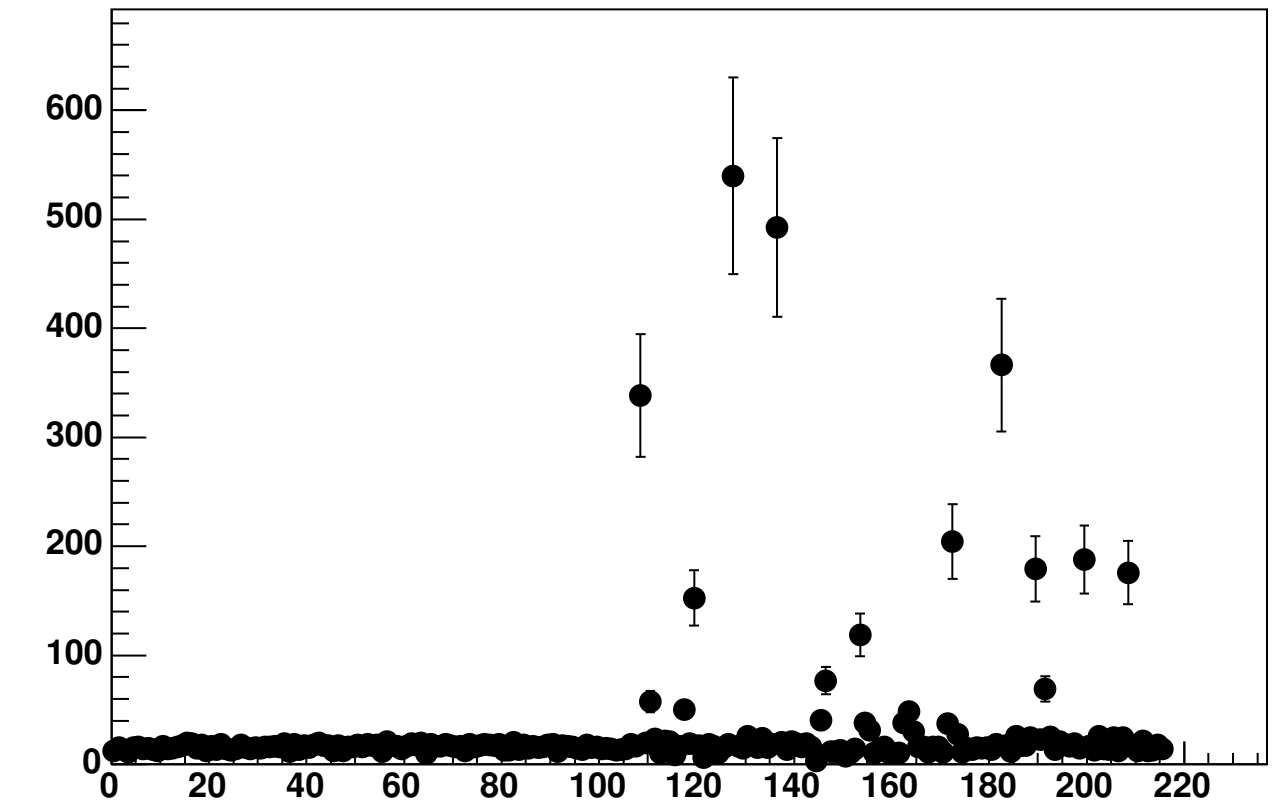
Enable 0, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



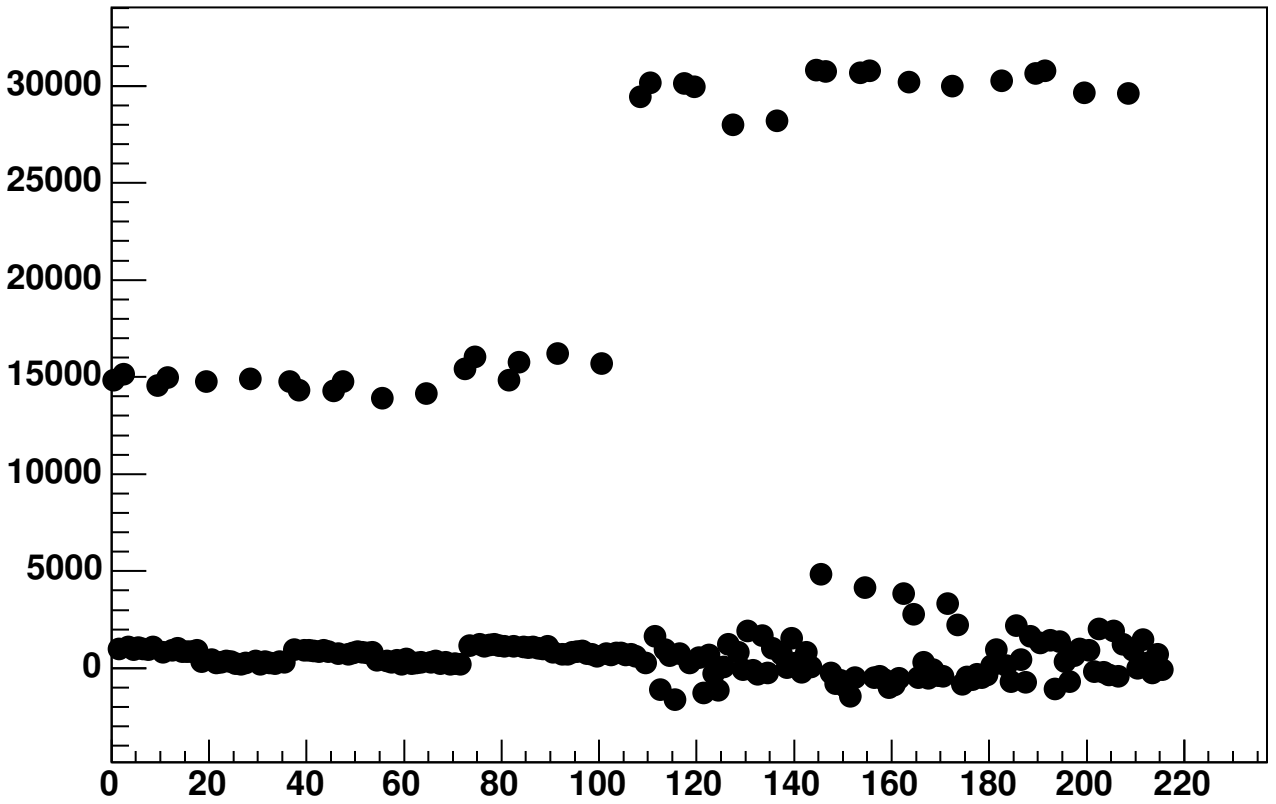
Enable 0, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



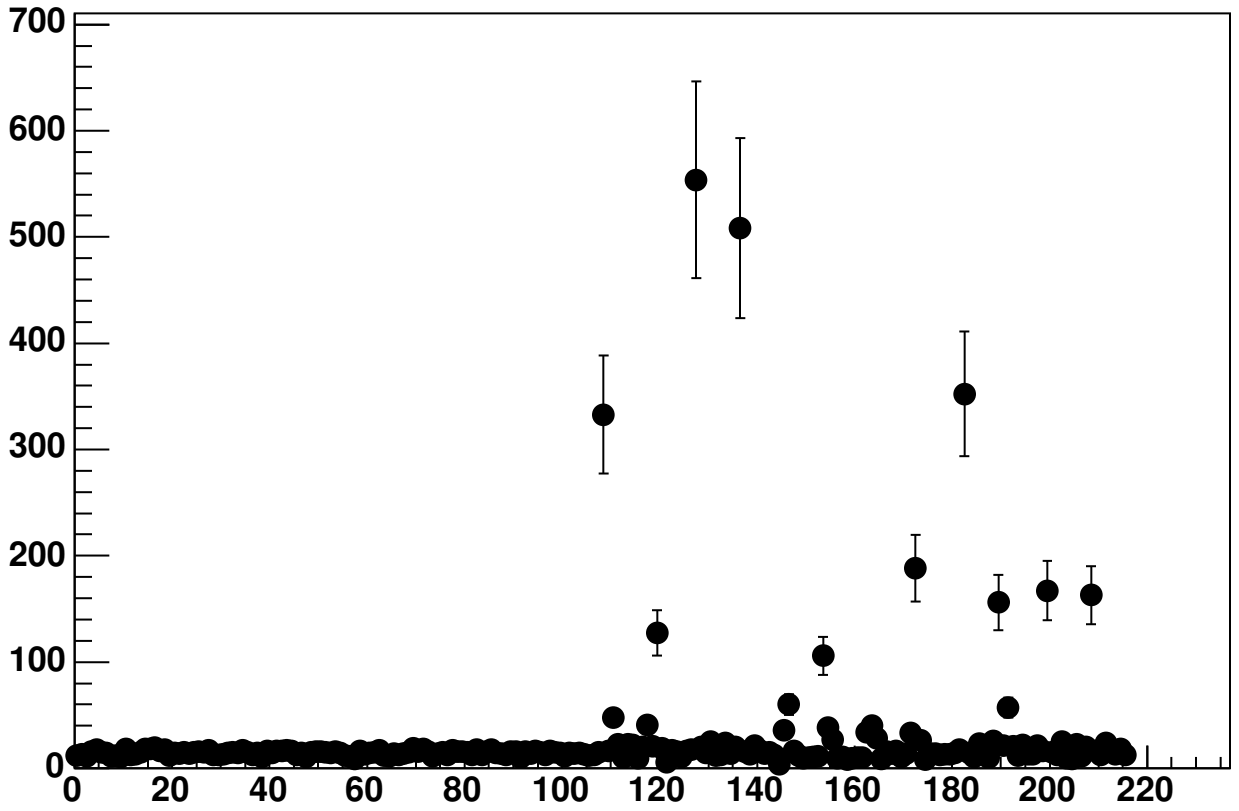
Enable 0, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



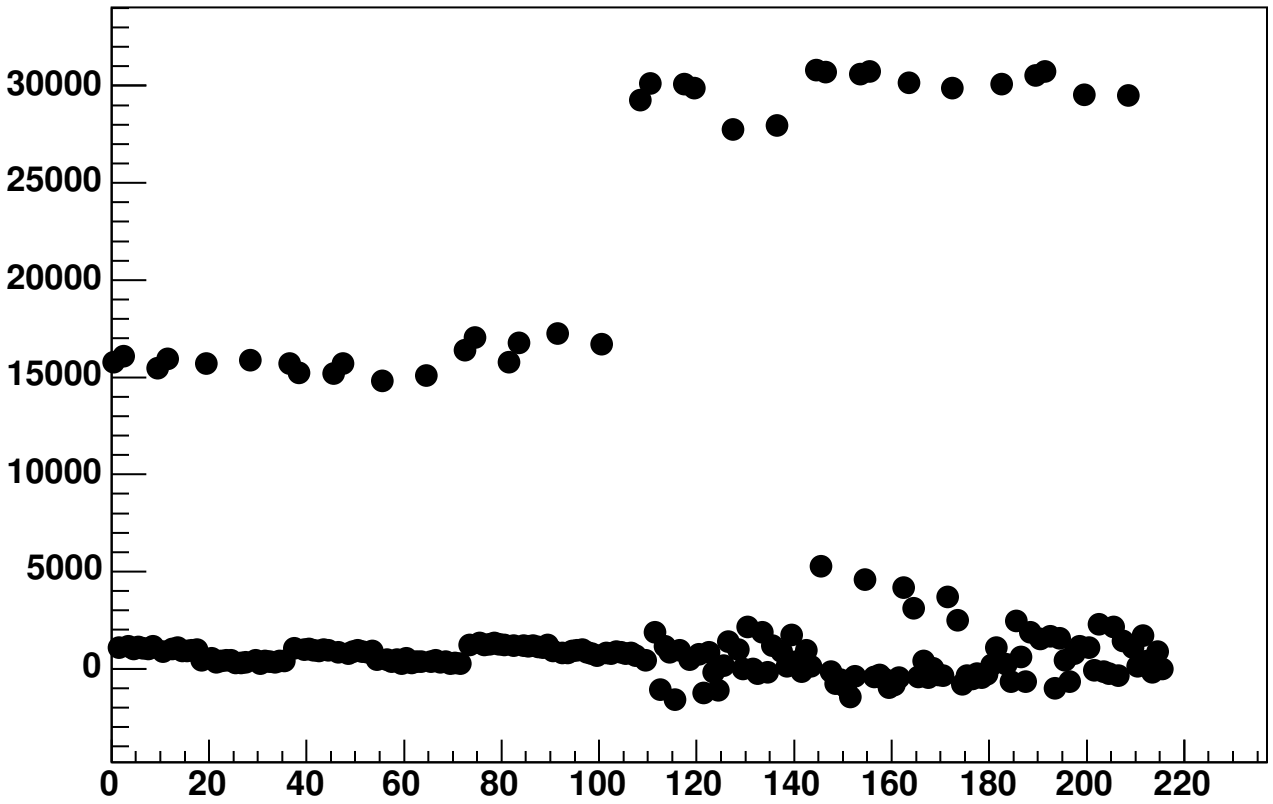
Enable 0, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



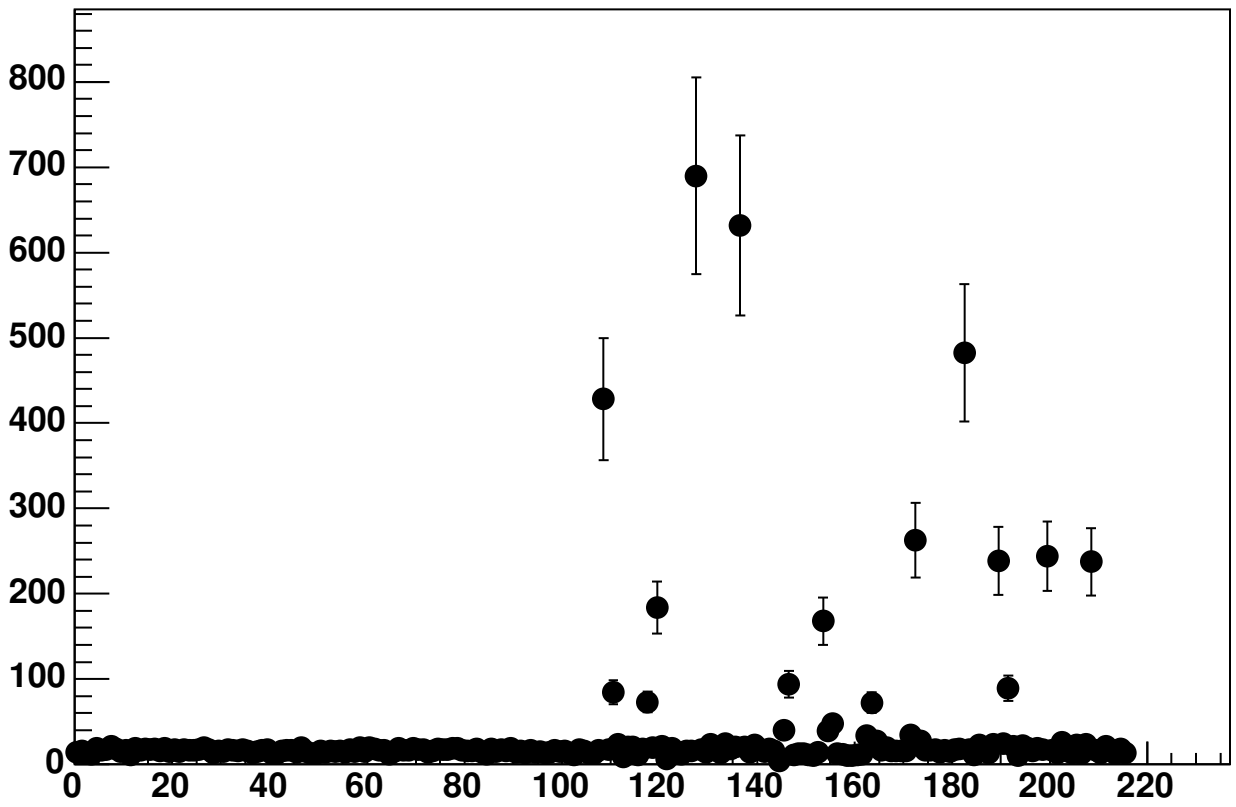
Enable 0, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



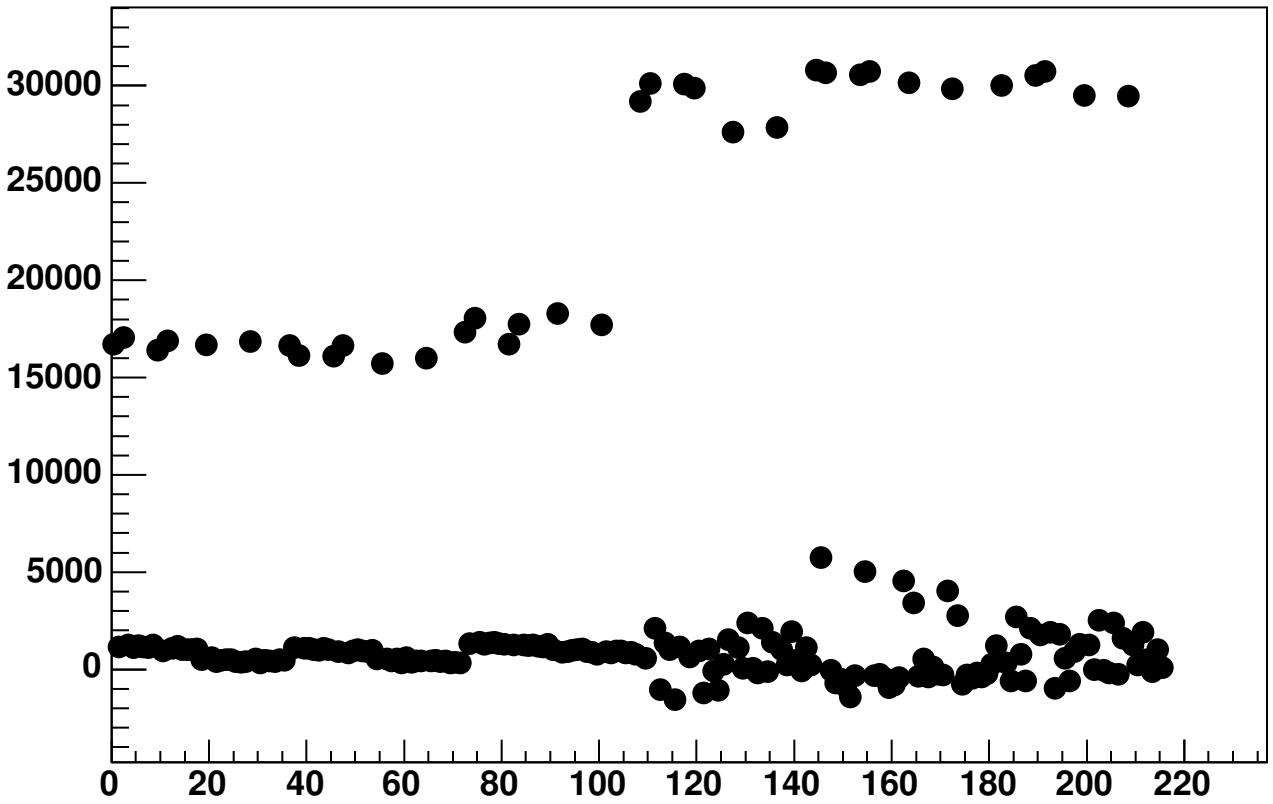
Enable 0, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



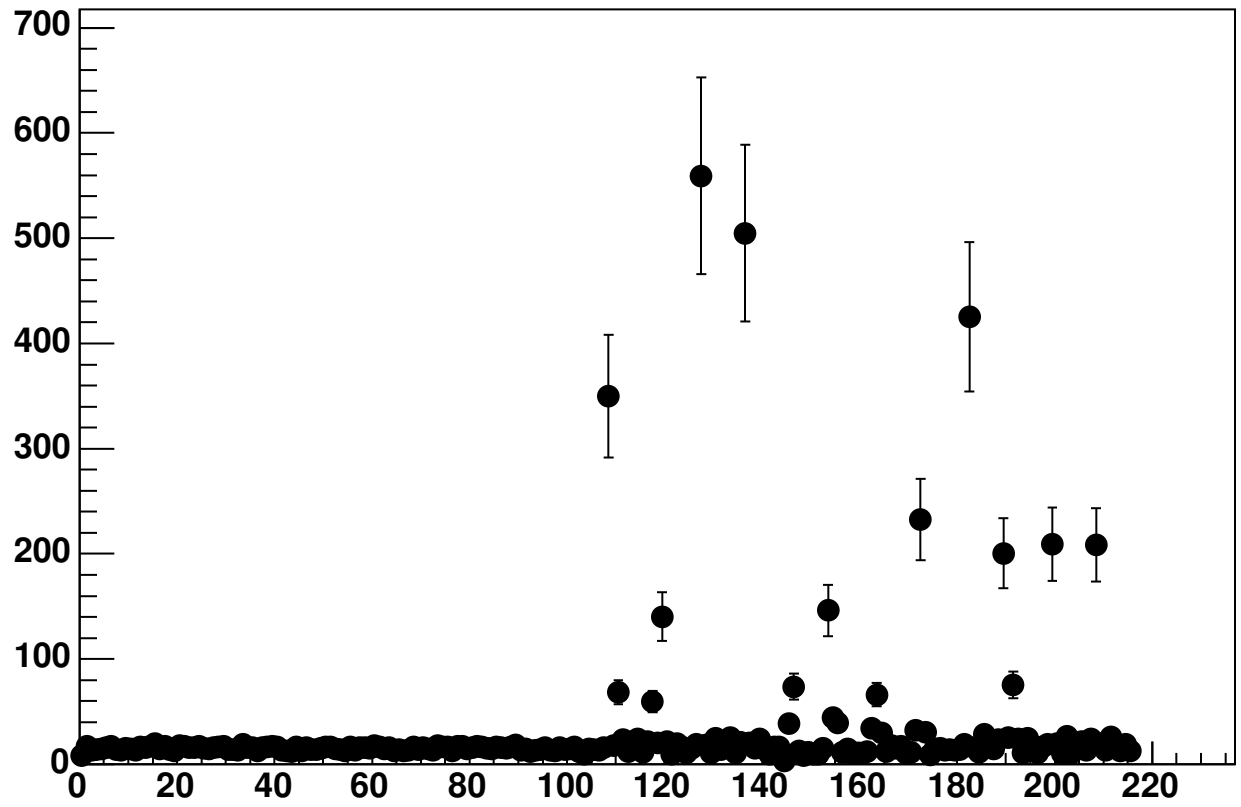
Enable 0, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



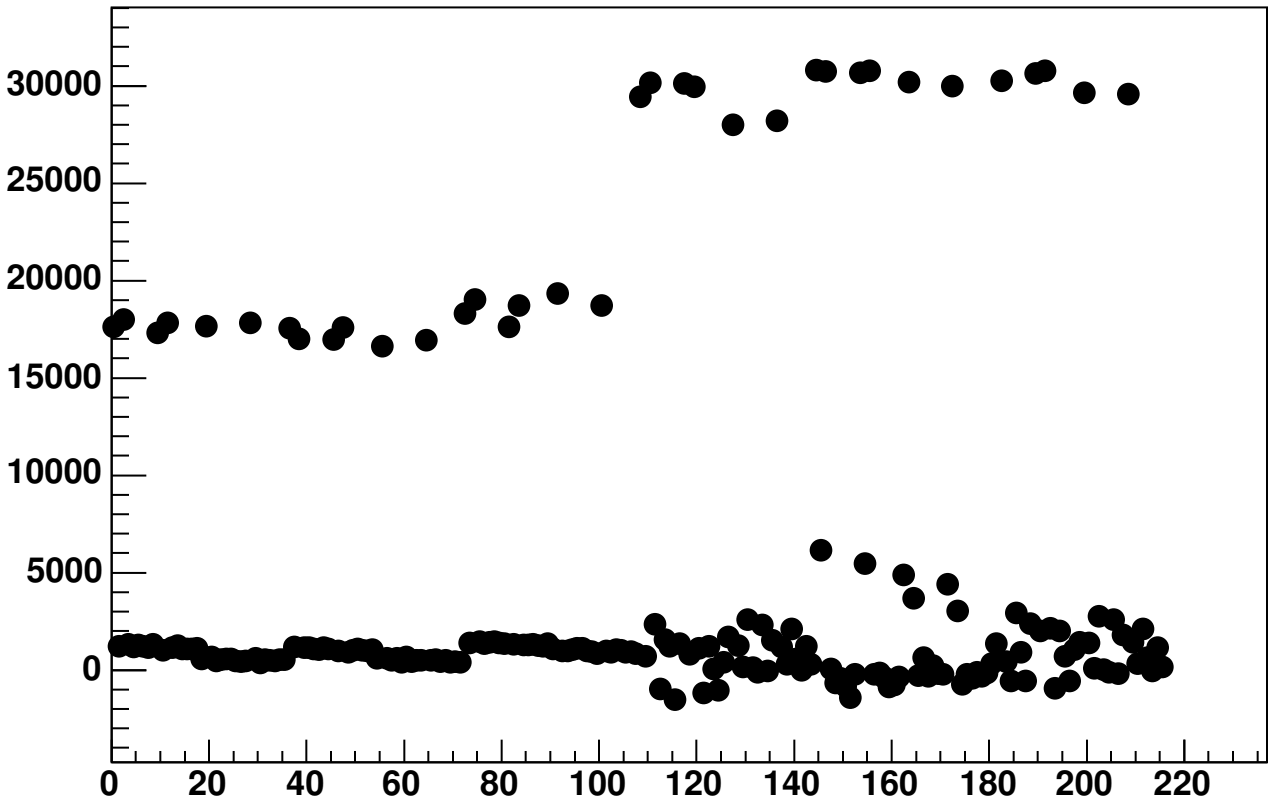
Enable 0, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



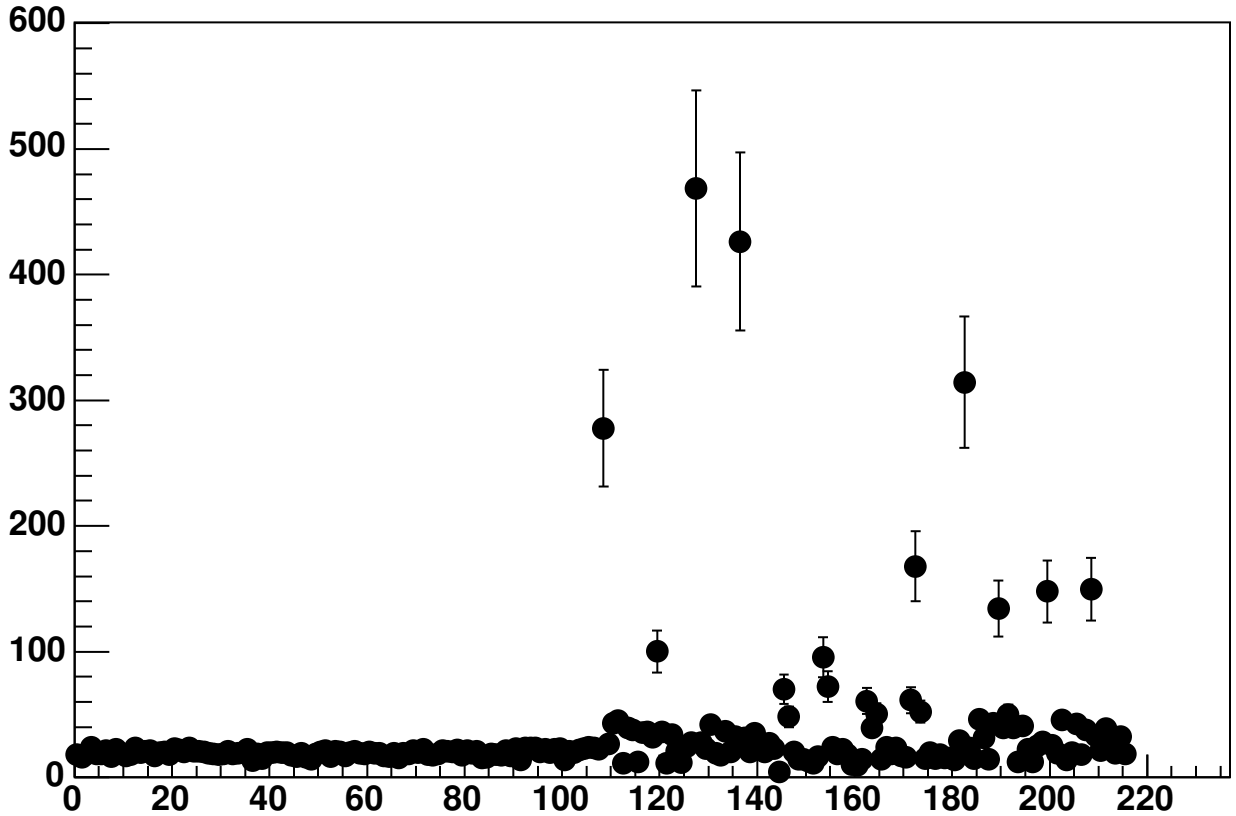
Enable 0, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



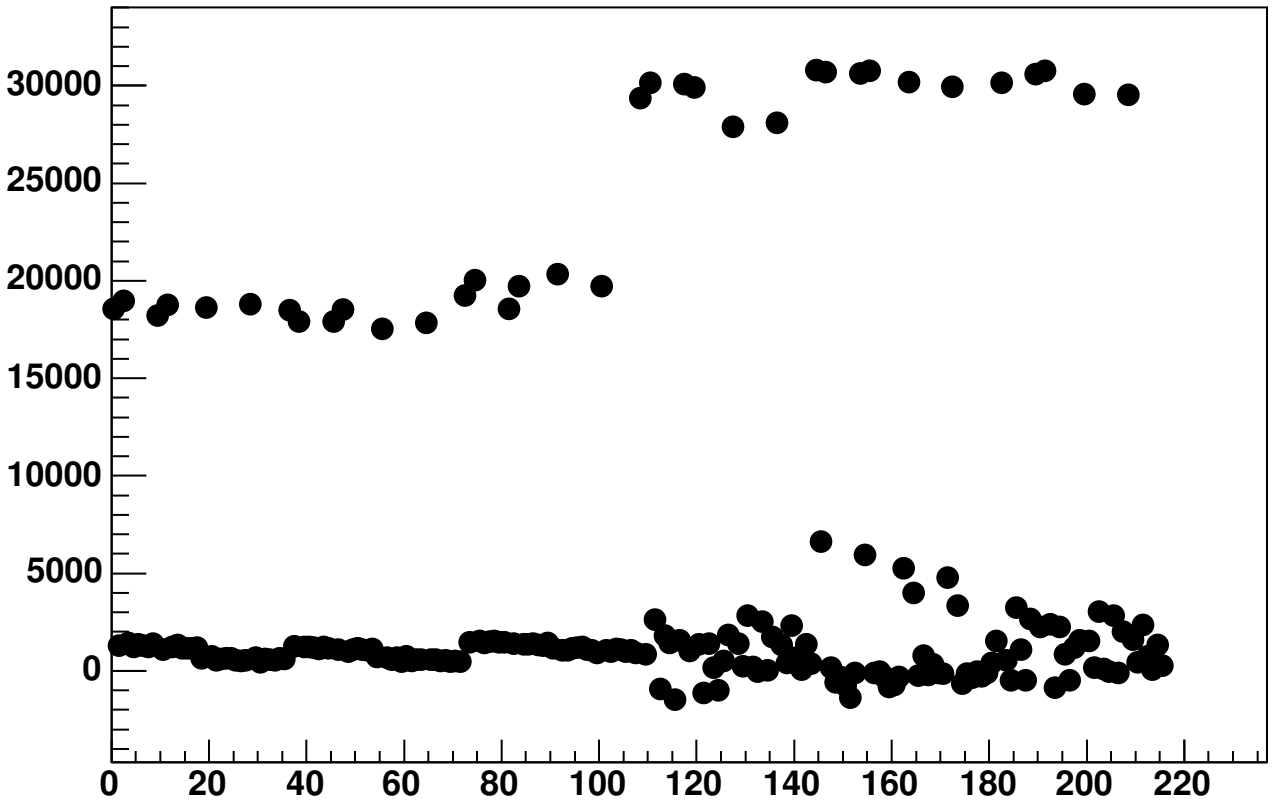
Enable 0, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



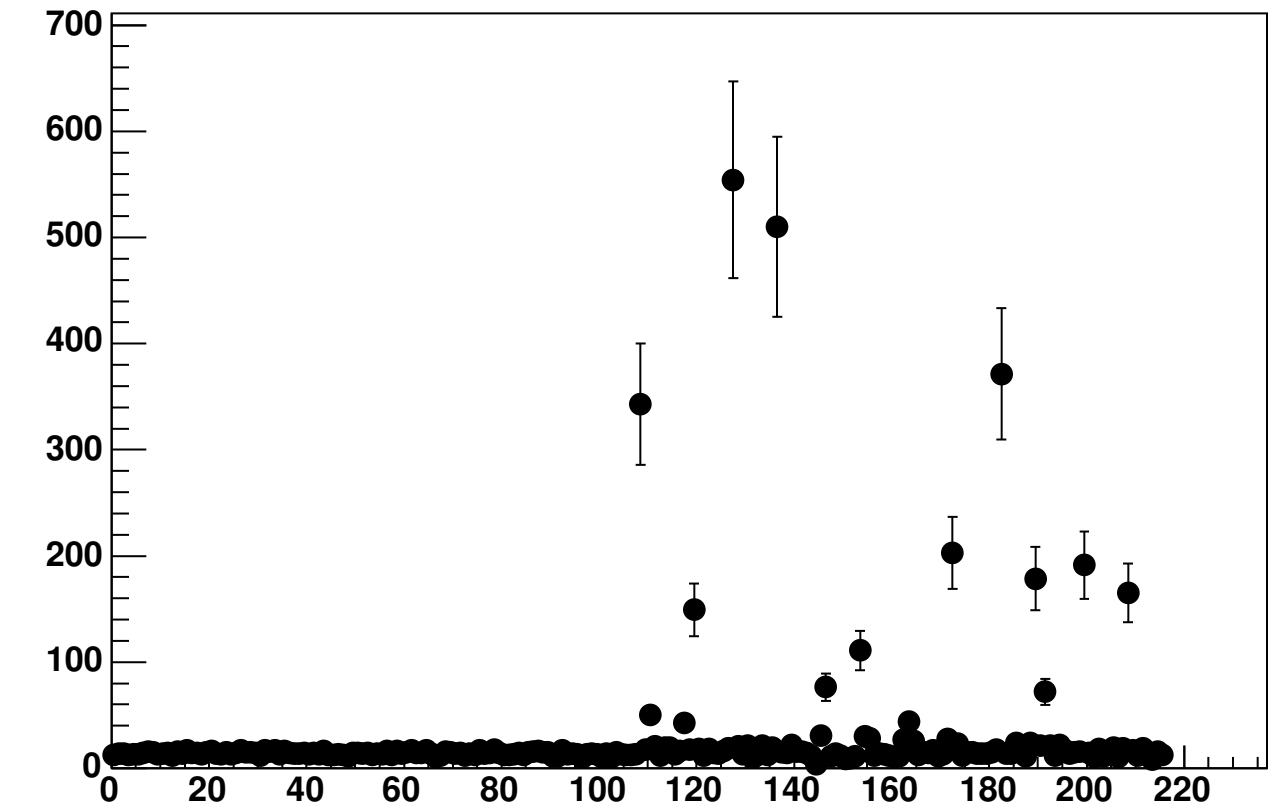
Enable 0, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



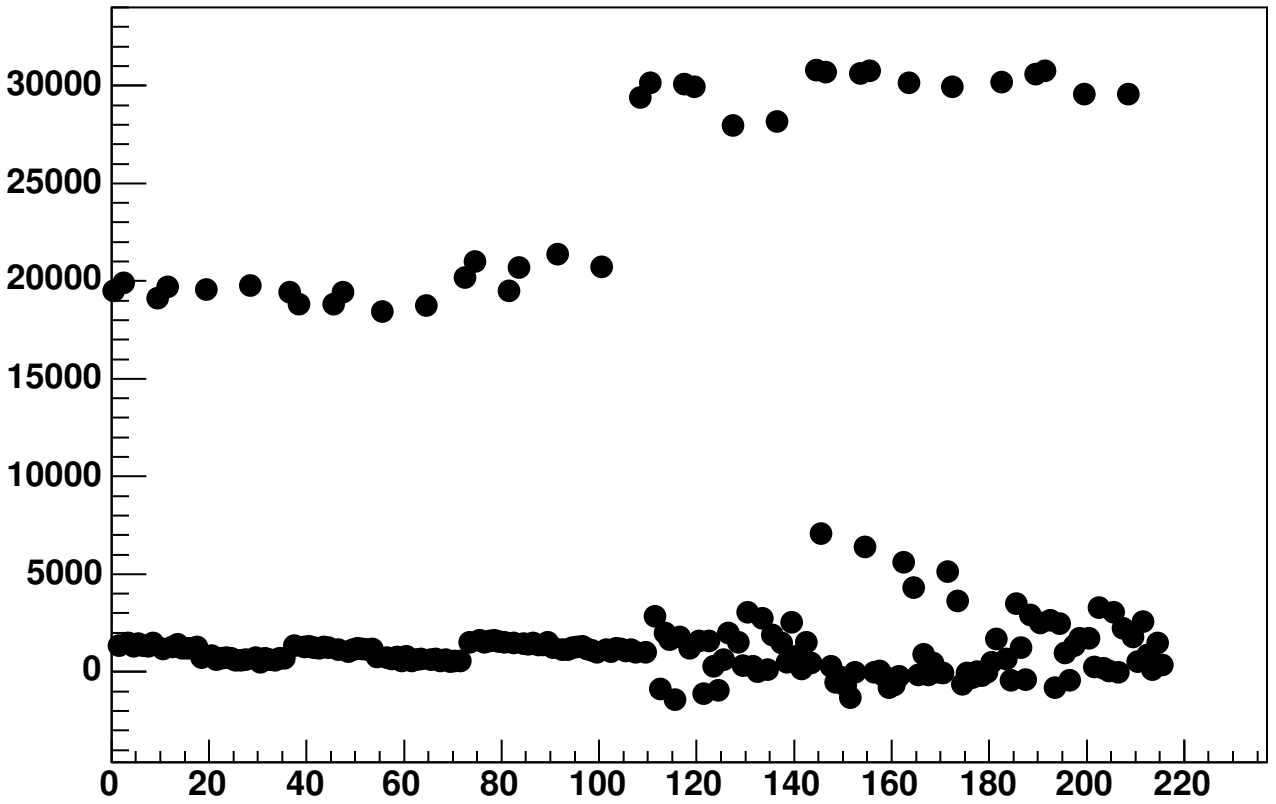
Enable 0, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



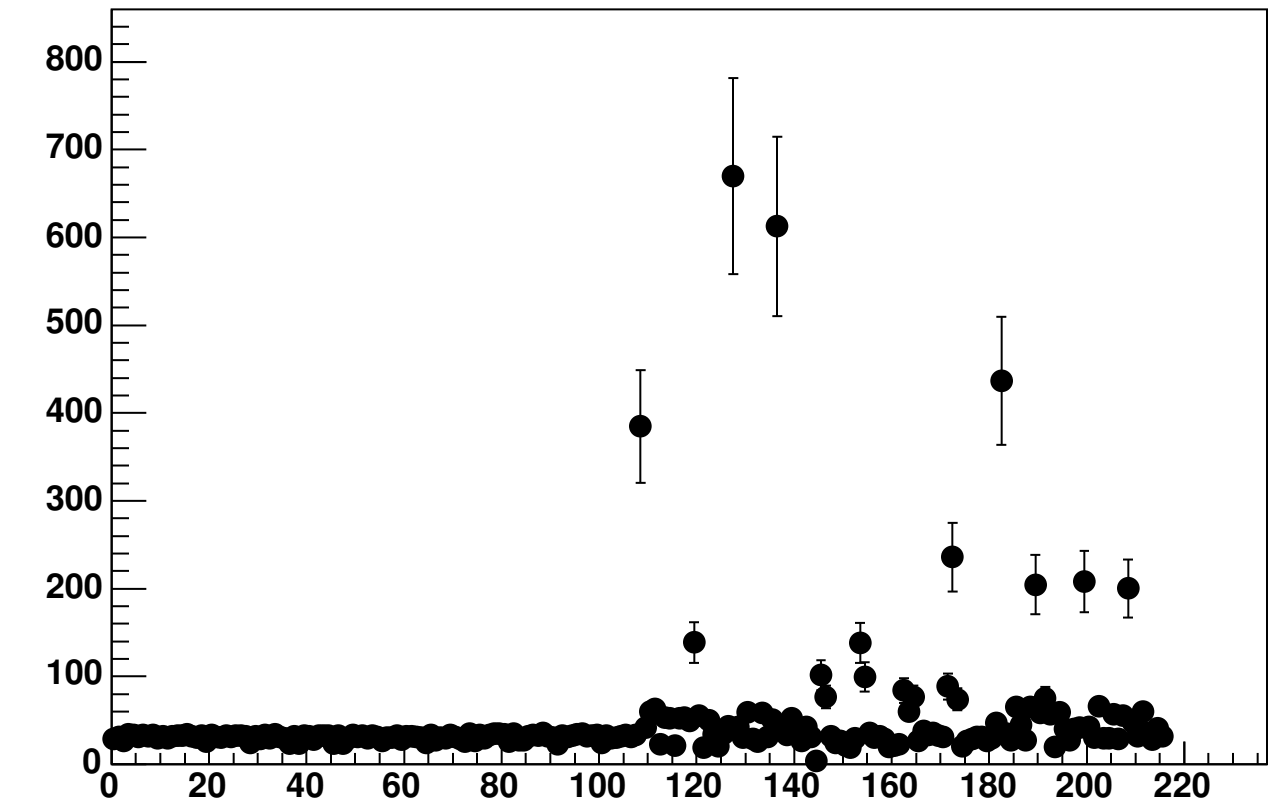
Enable 0, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



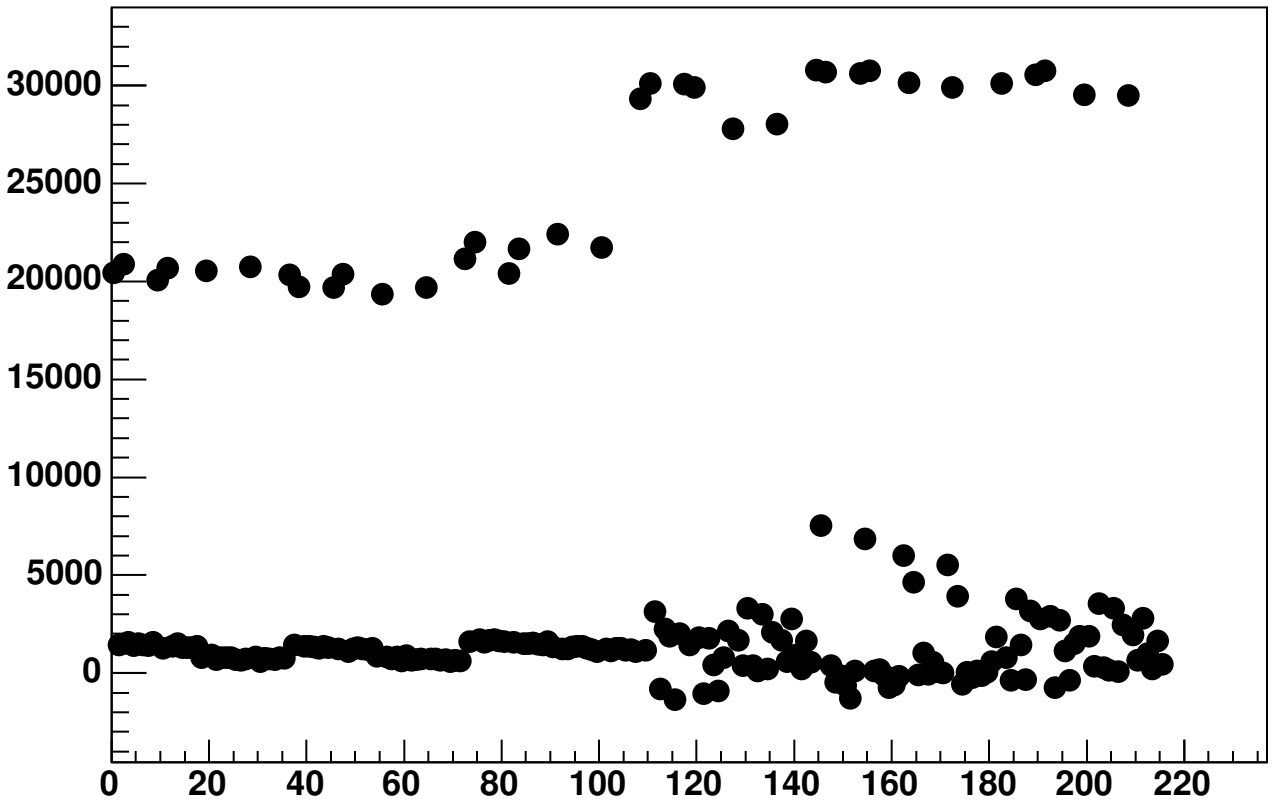
Enable 0, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



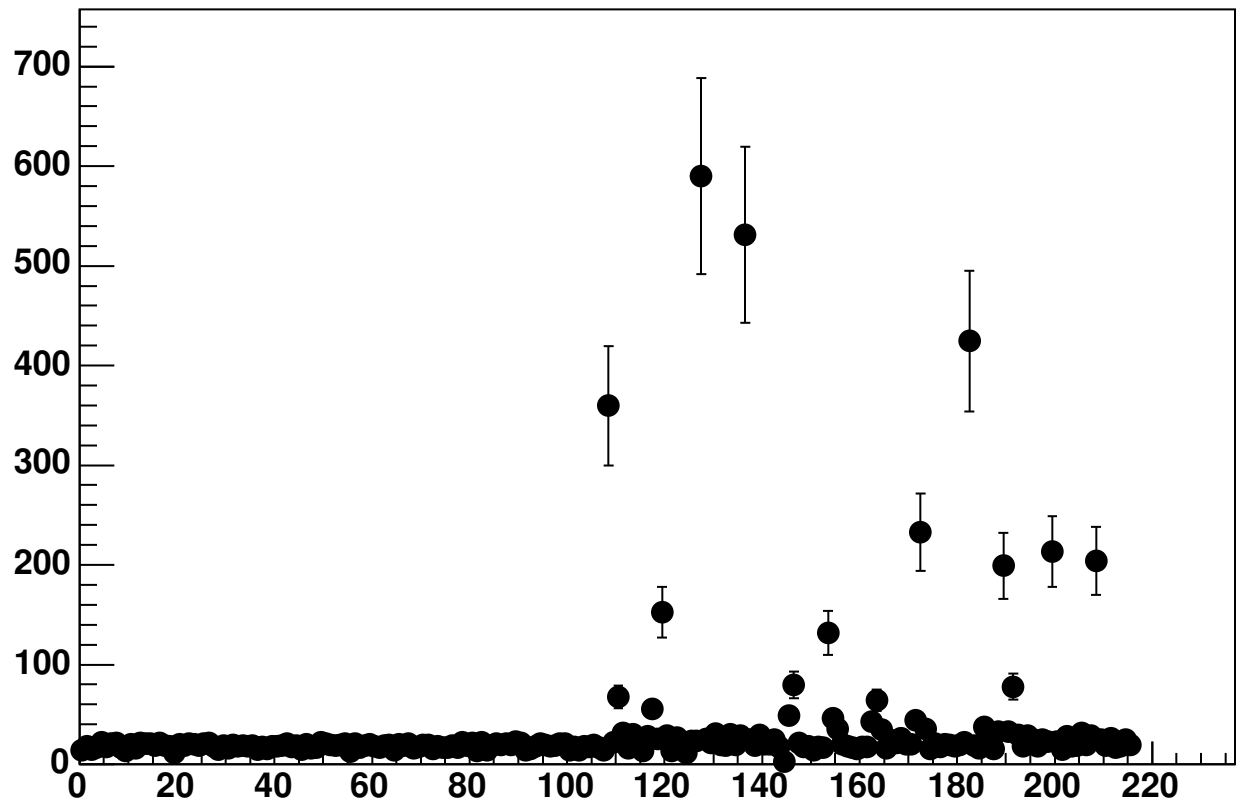
Enable 0, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



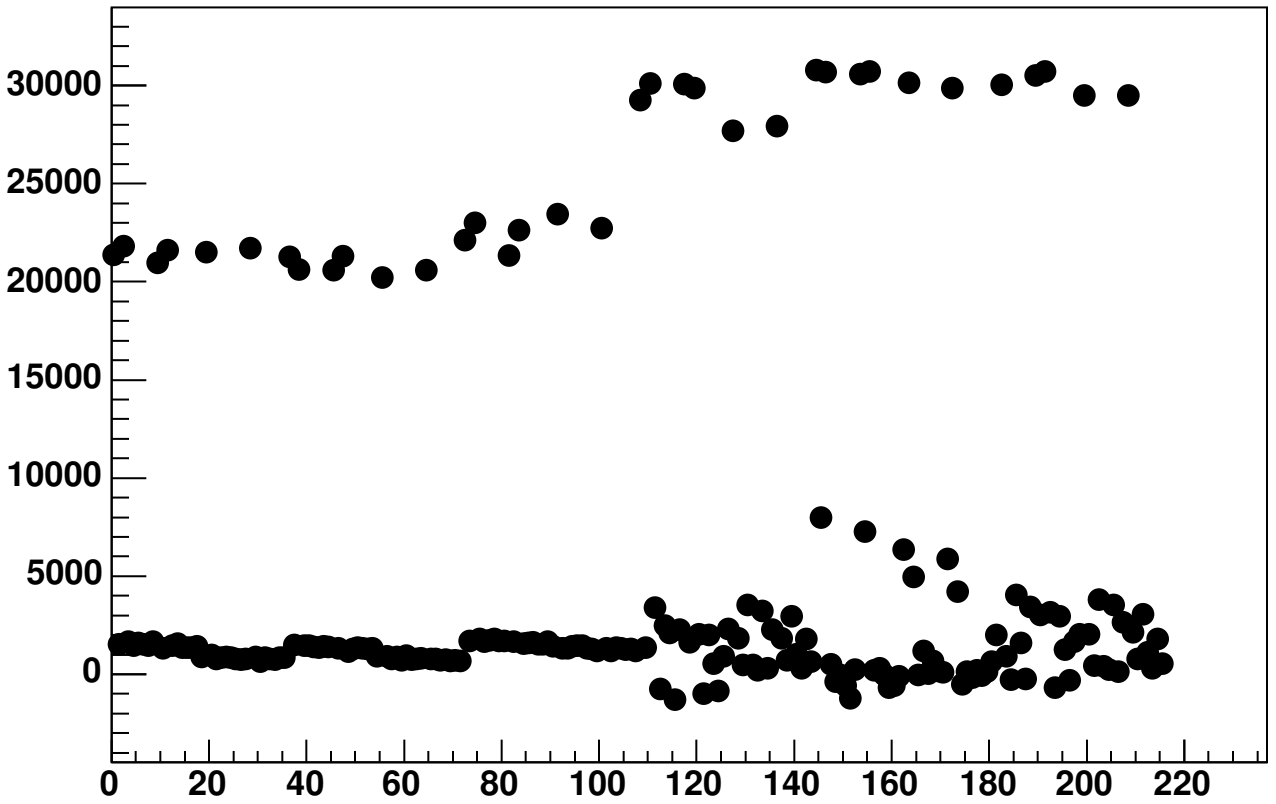
Enable 0, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



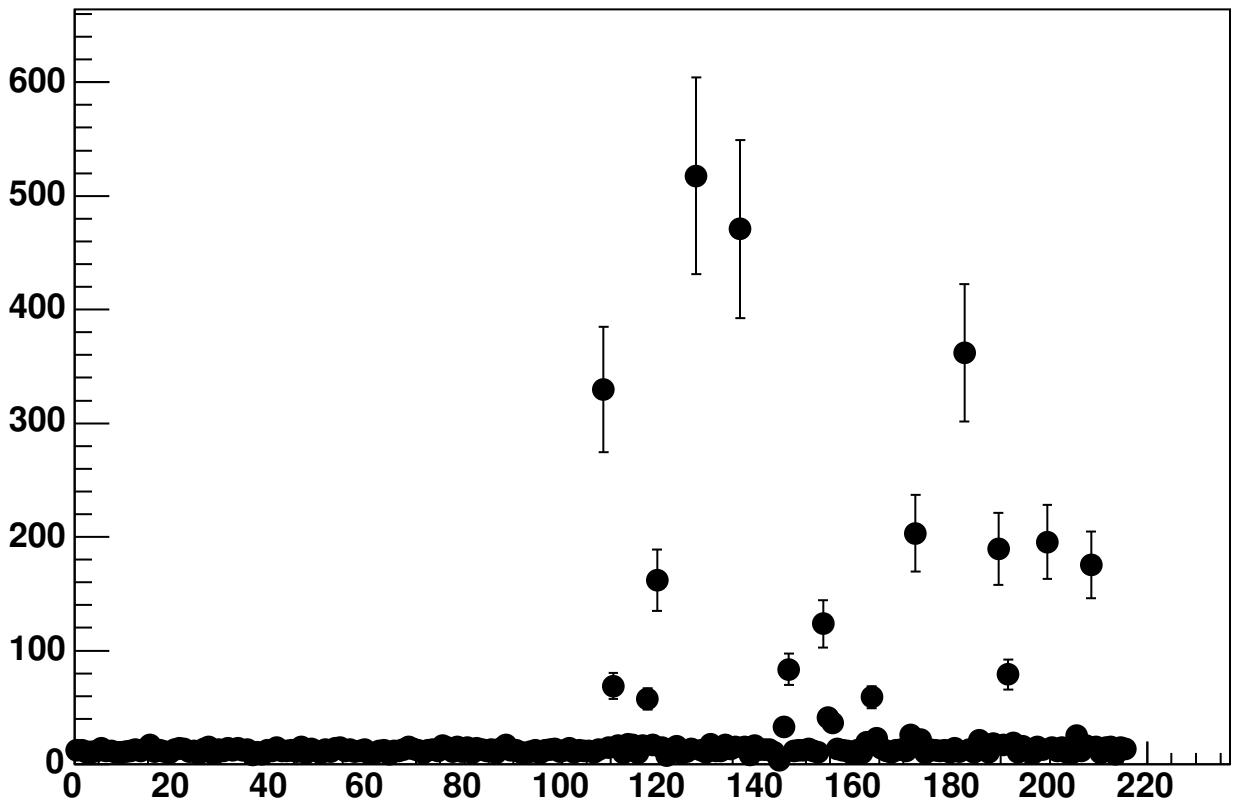
Enable 0, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



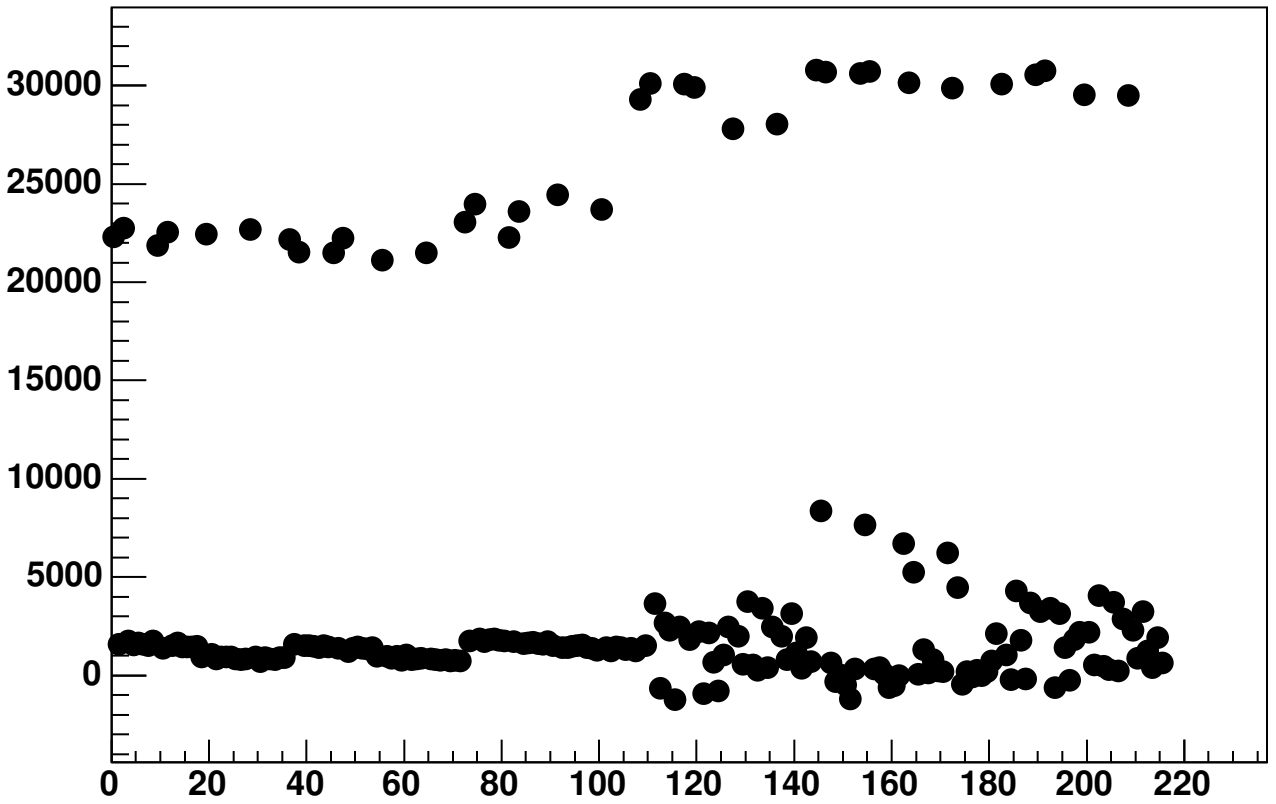
Enable 0, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



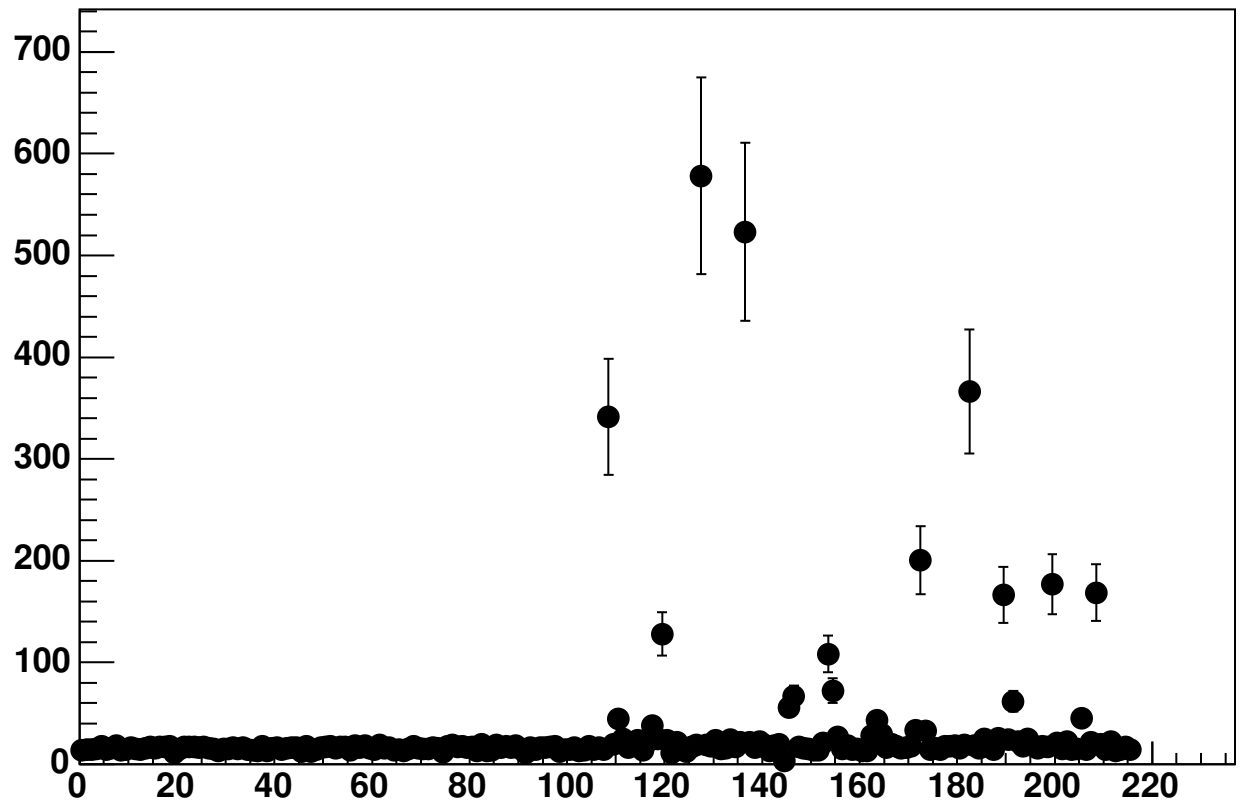
Enable 0, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



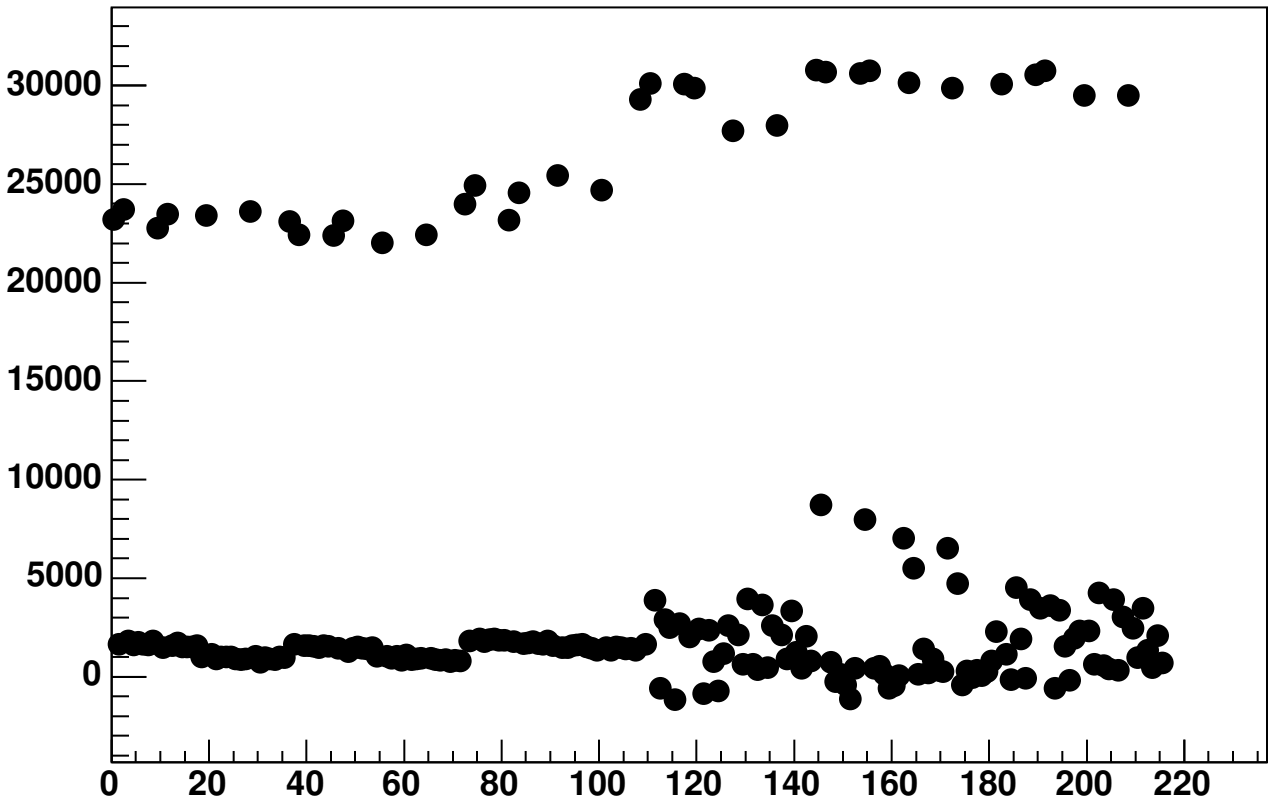
Enable 0, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



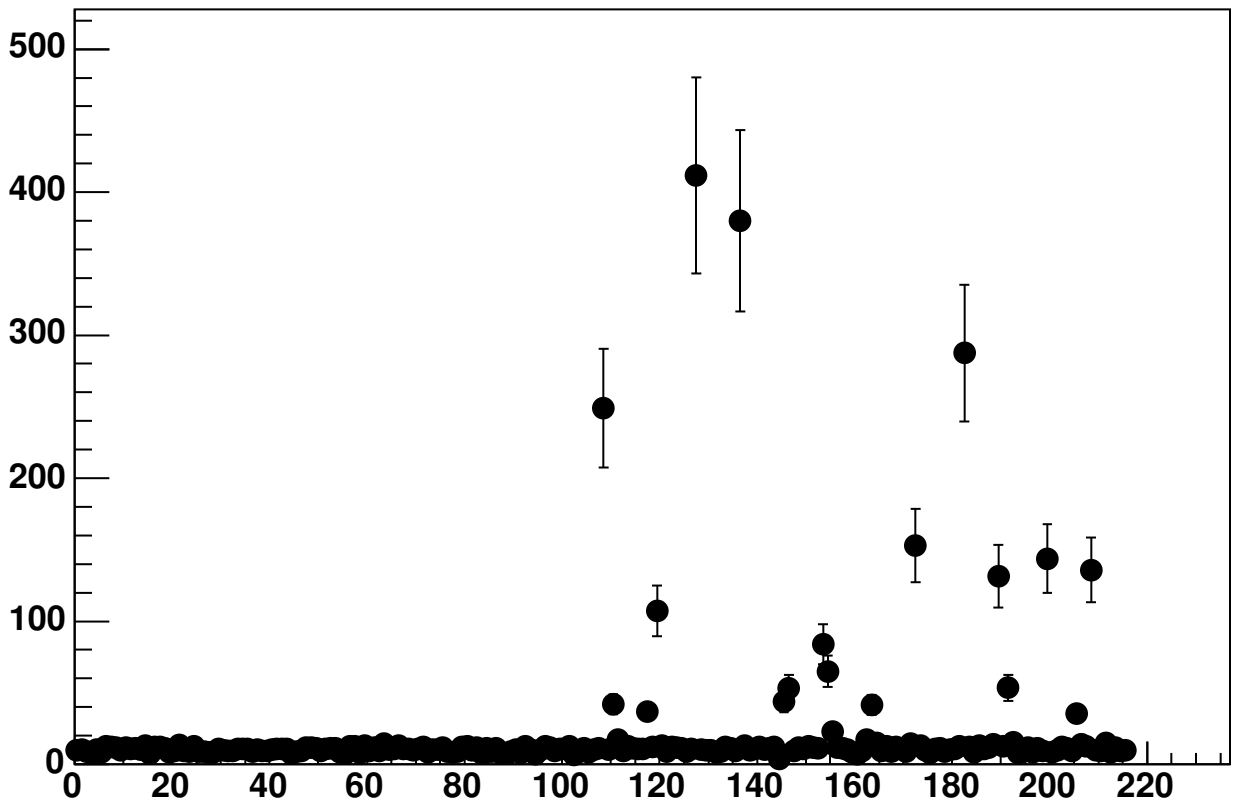
Enable 0, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



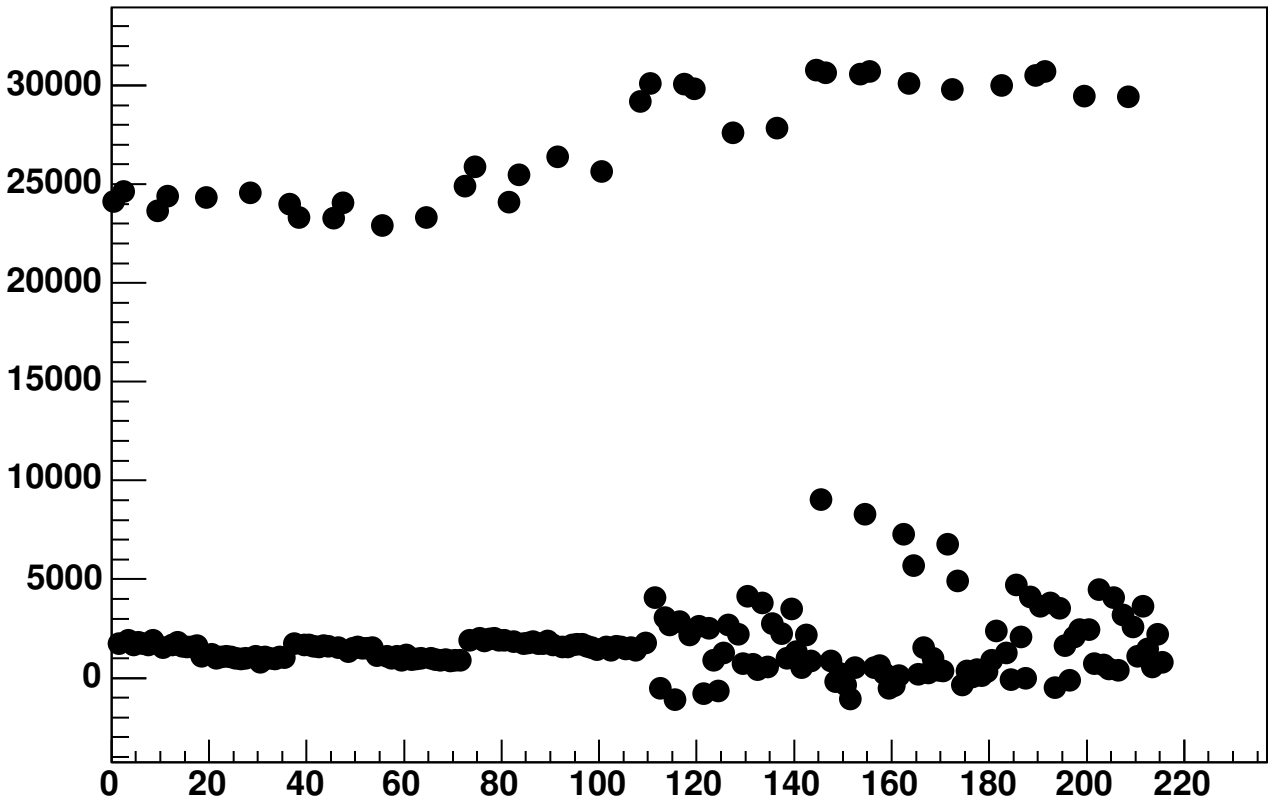
Enable 0, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



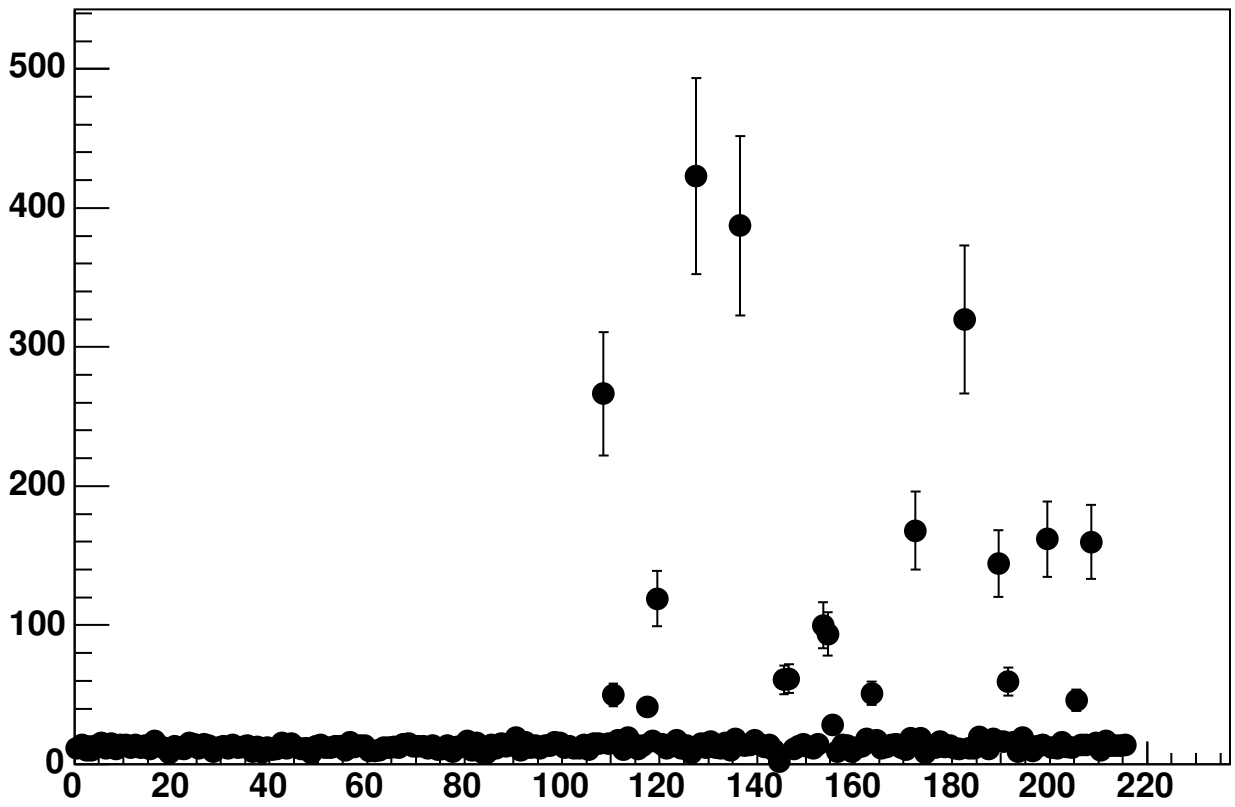
Enable 0, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



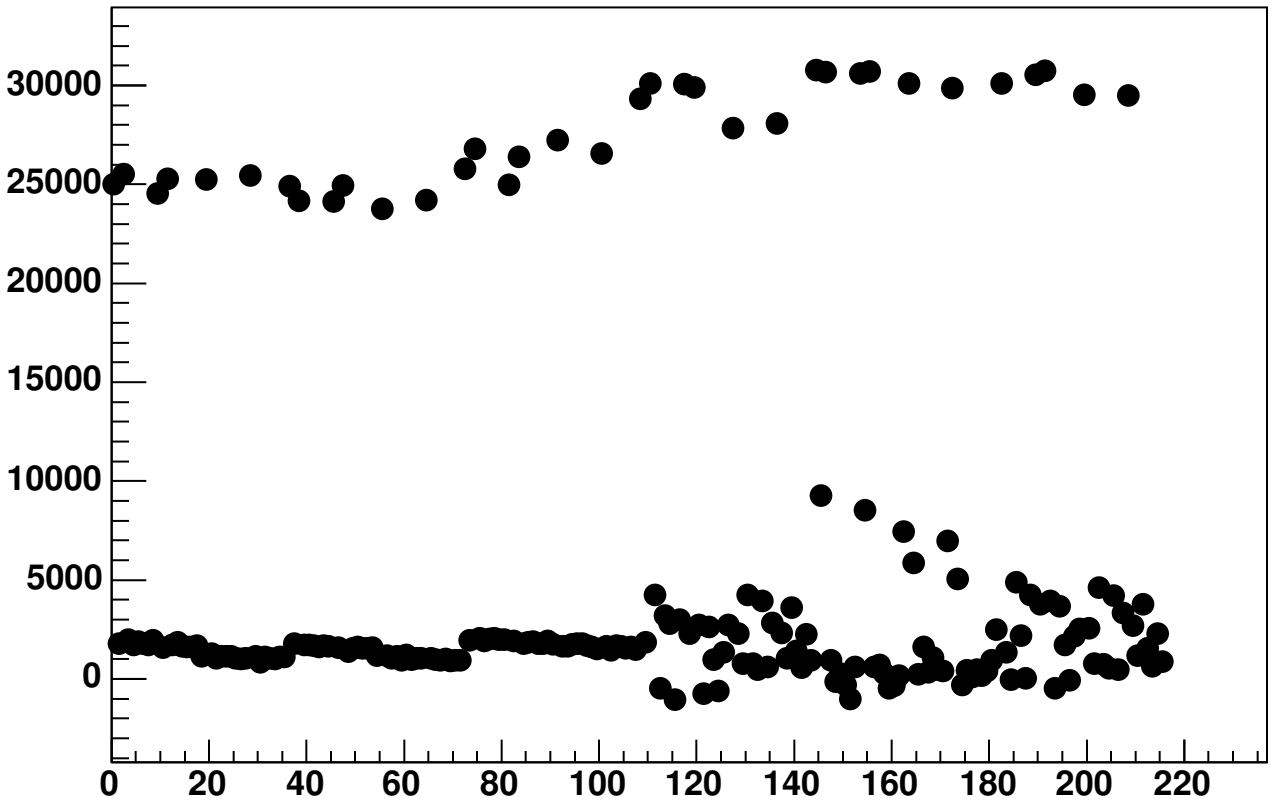
Enable 0, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



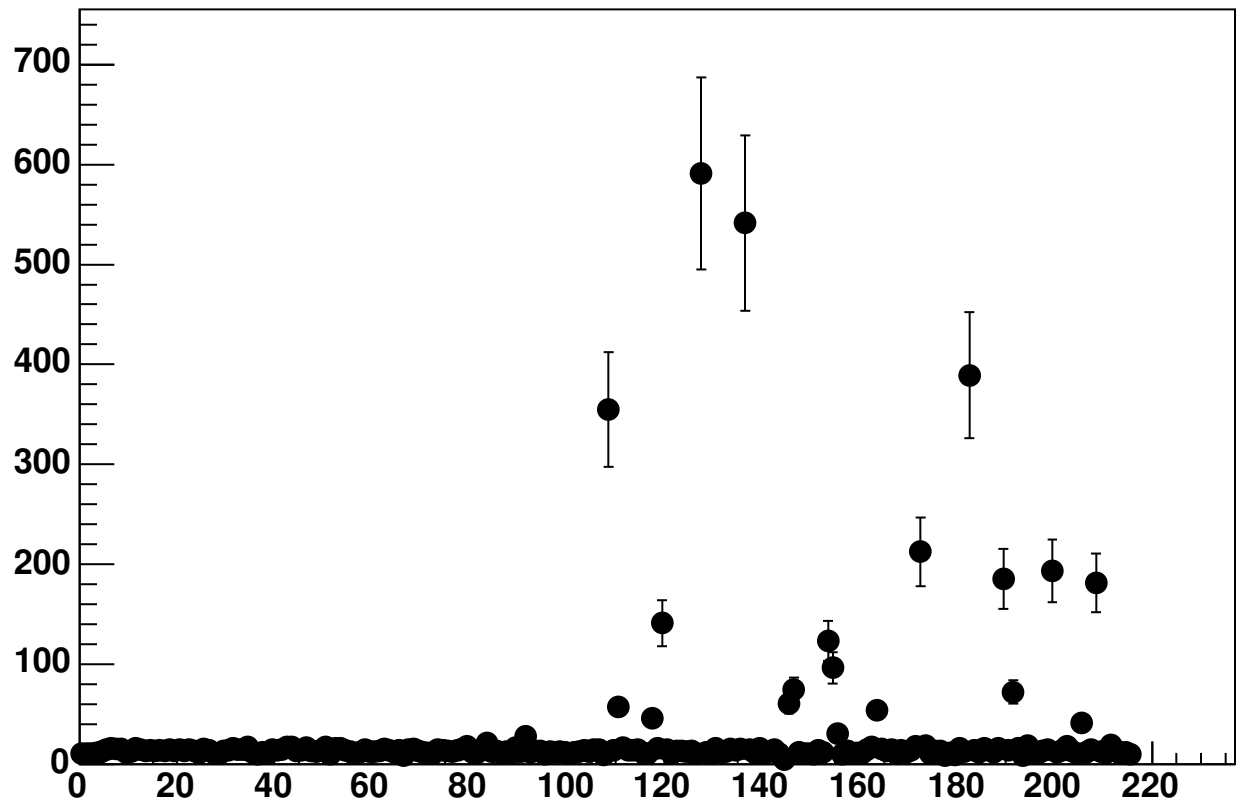
Enable 0, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



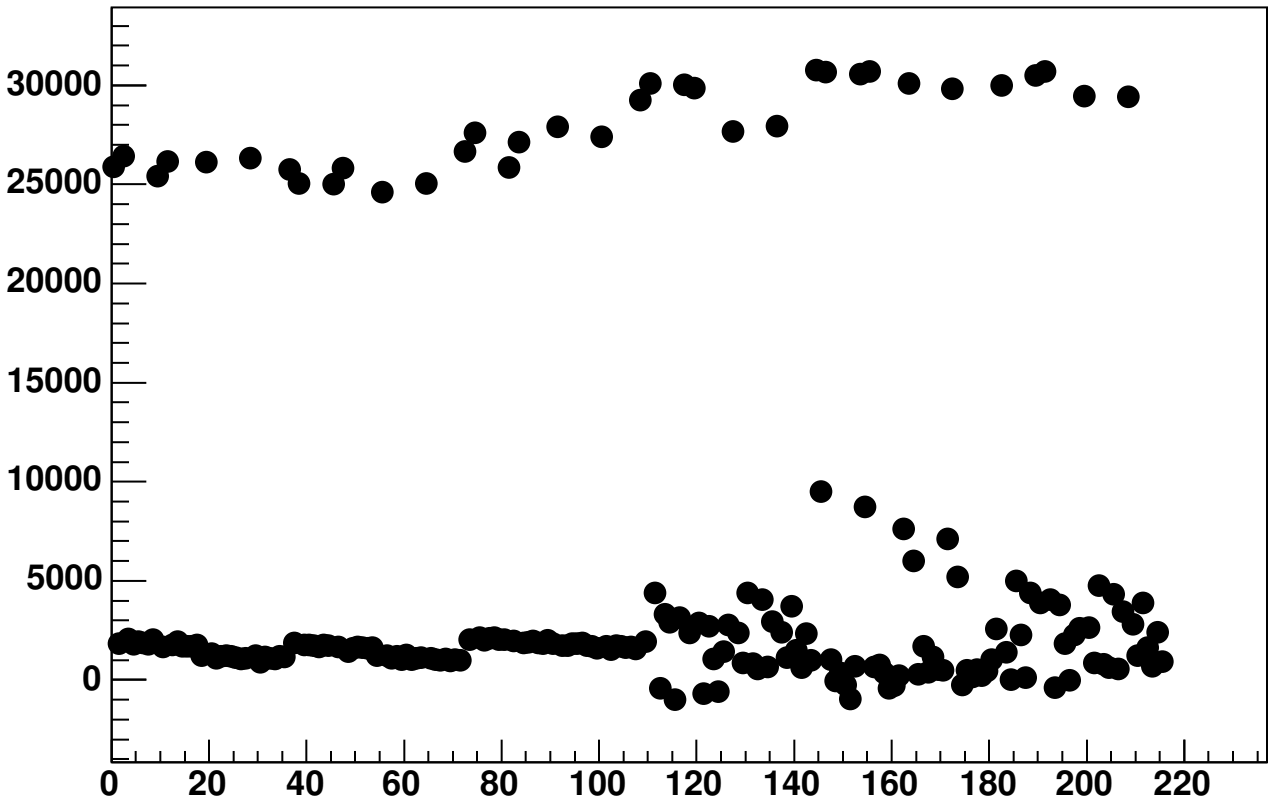
Enable 0, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



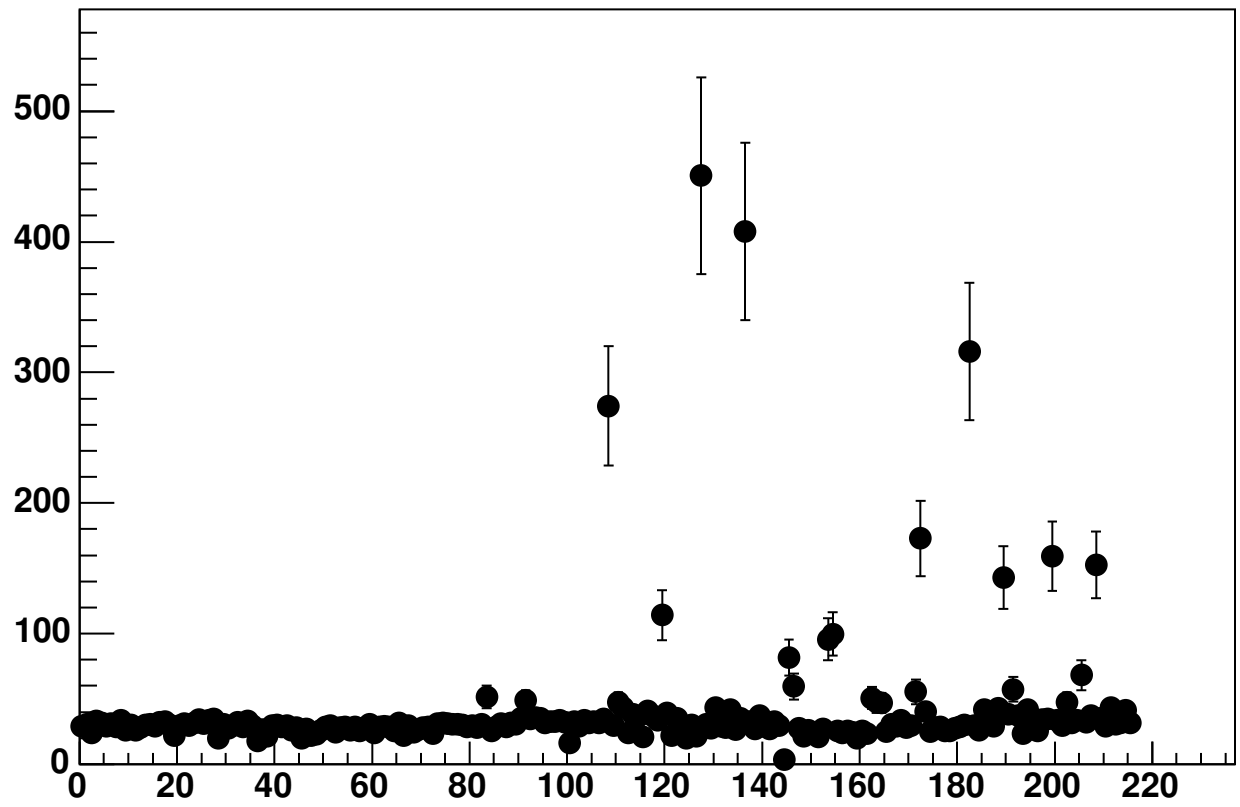
Enable 0, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



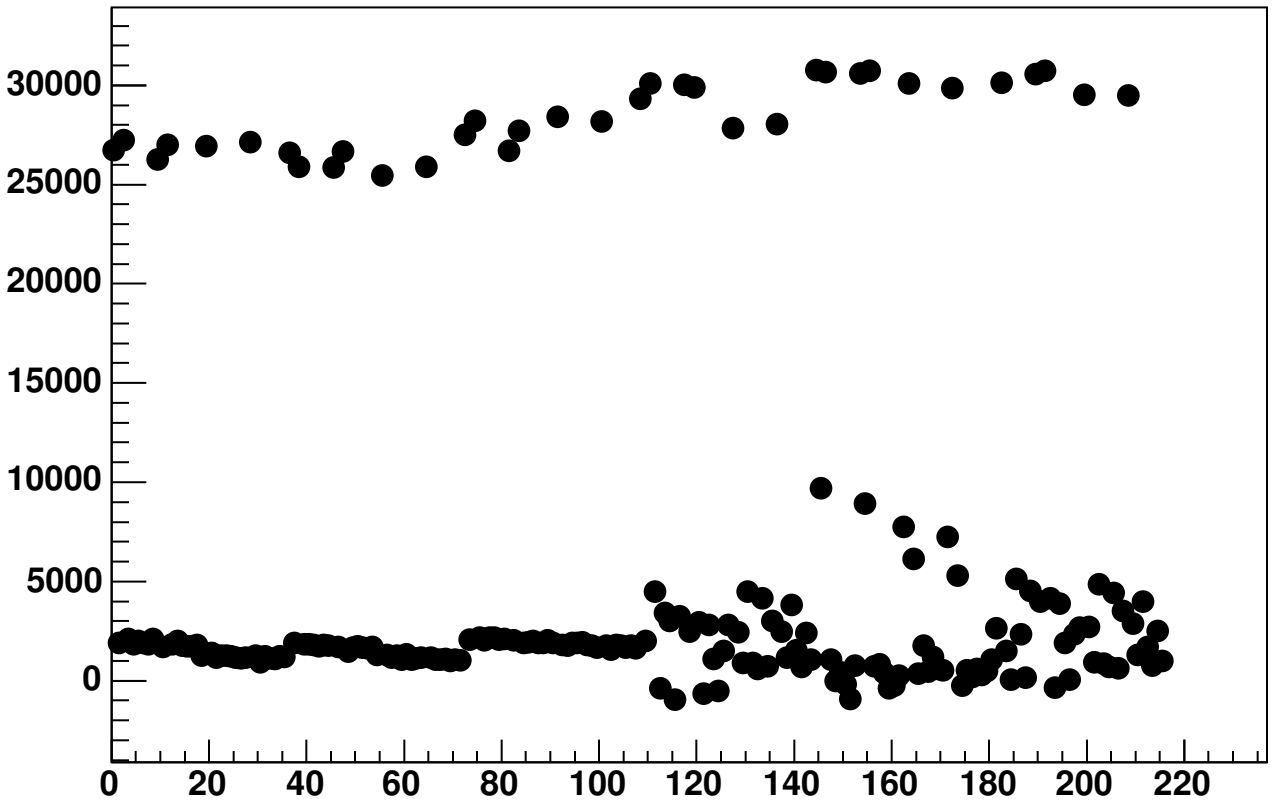
Enable 0, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



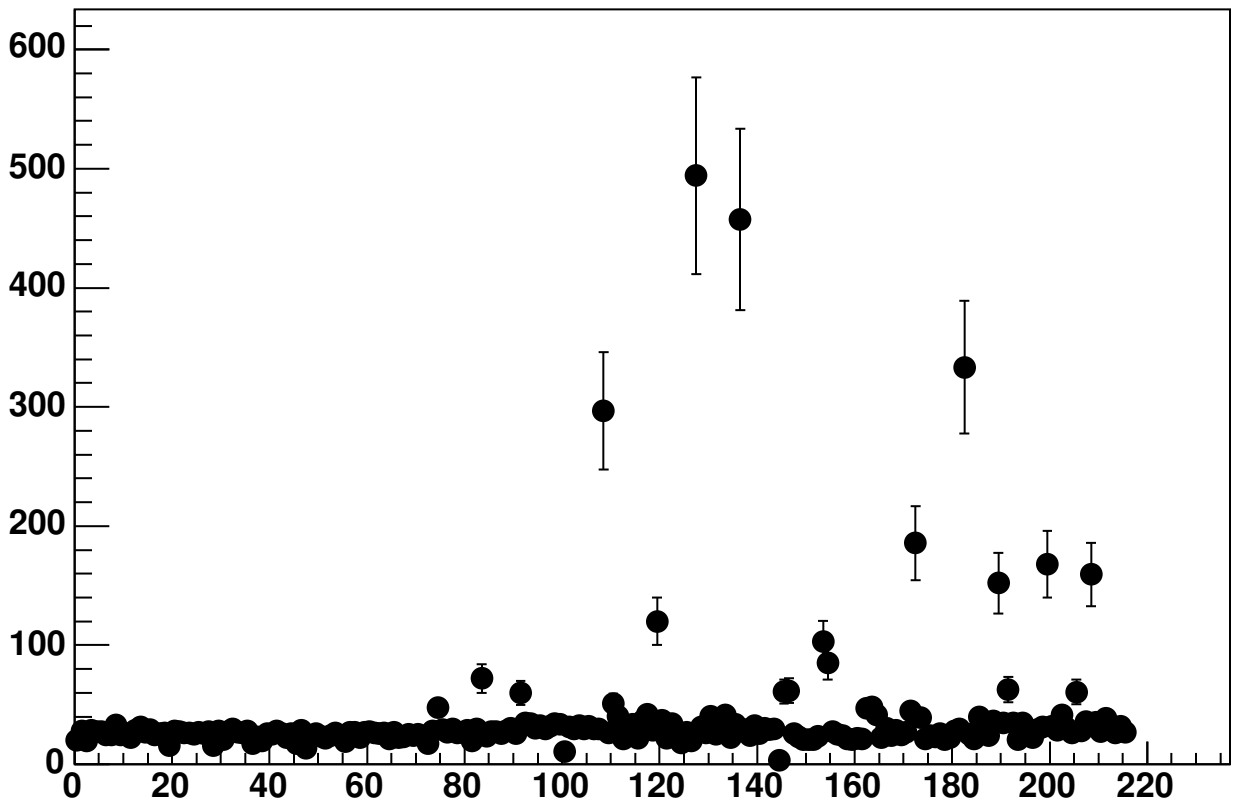
Enable 0, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



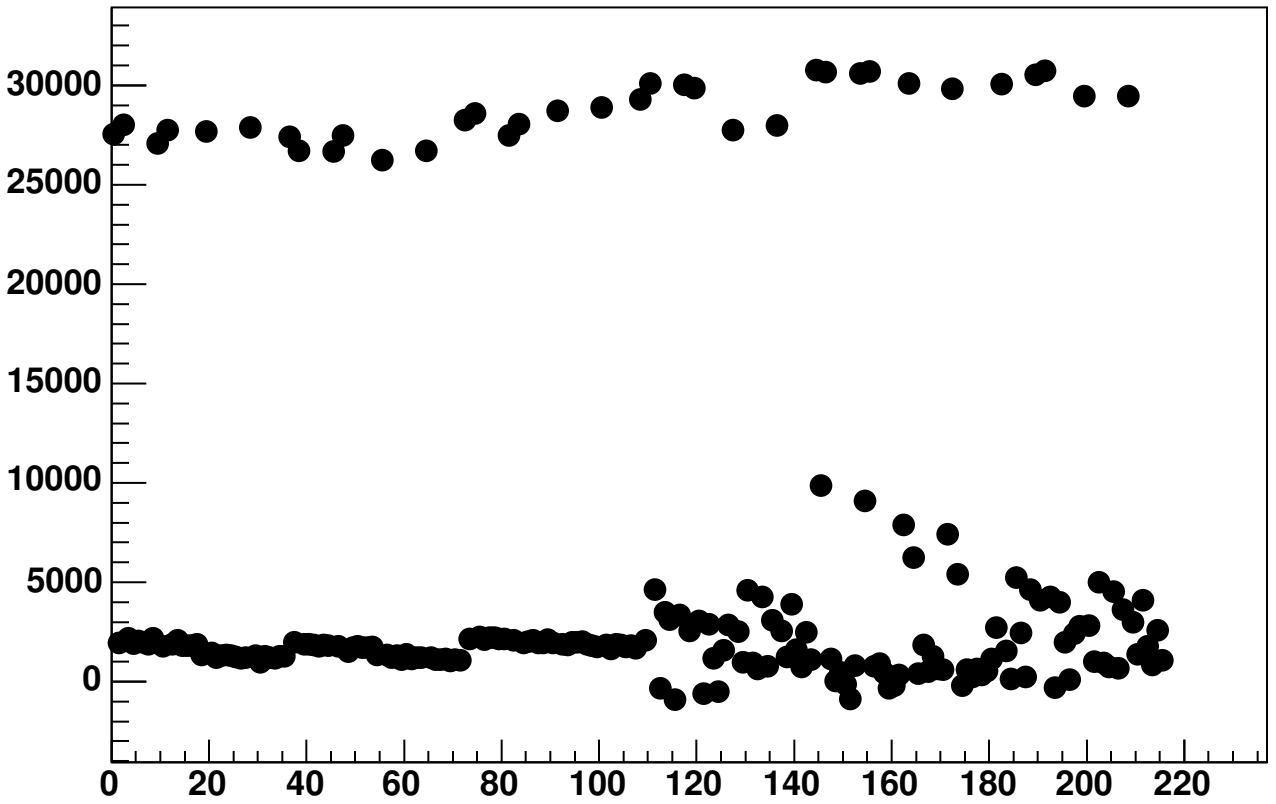
Enable 0, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



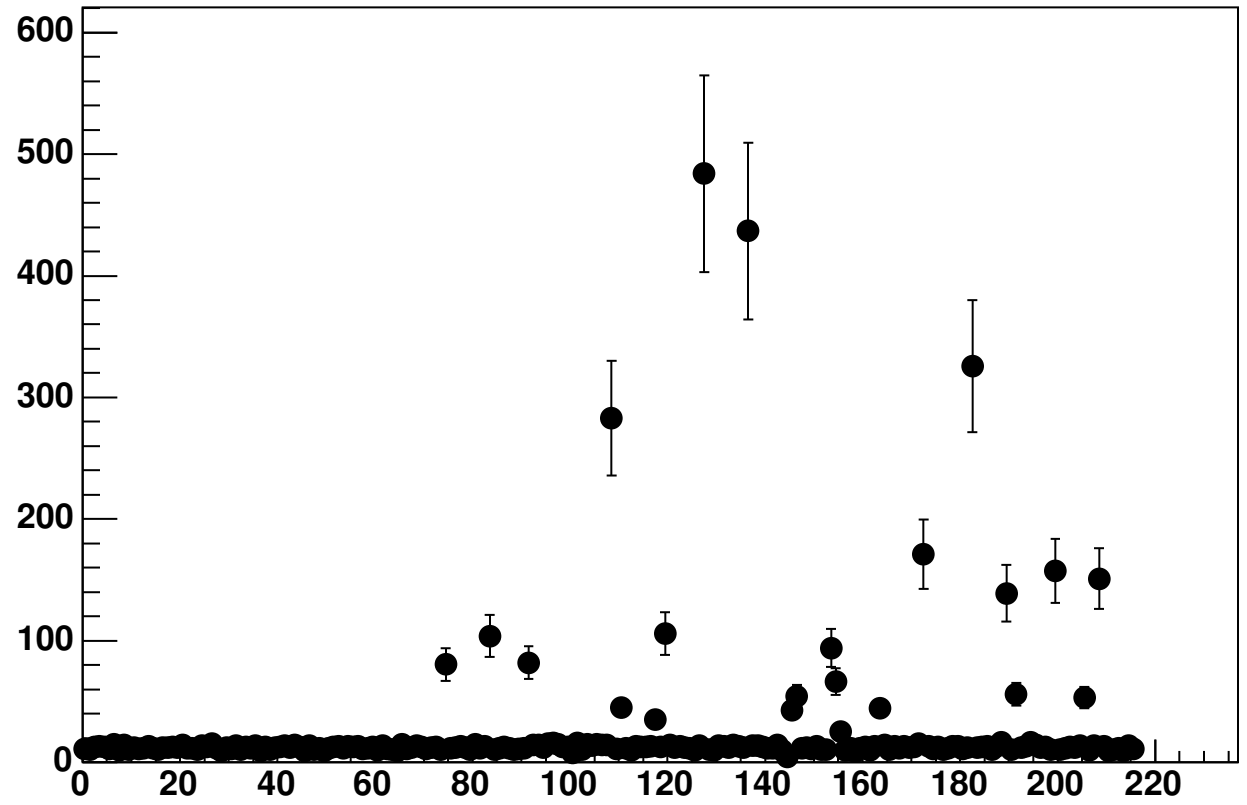
Enable 0, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



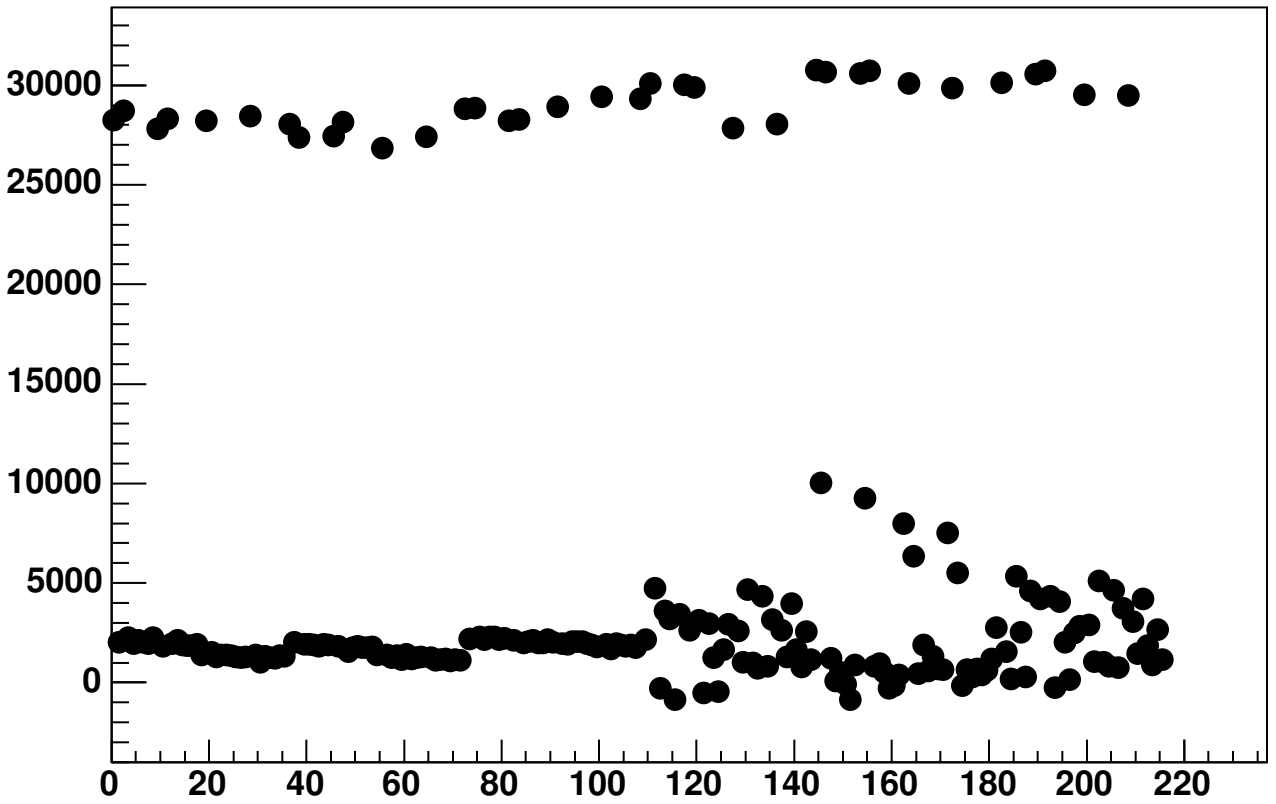
Enable 0, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



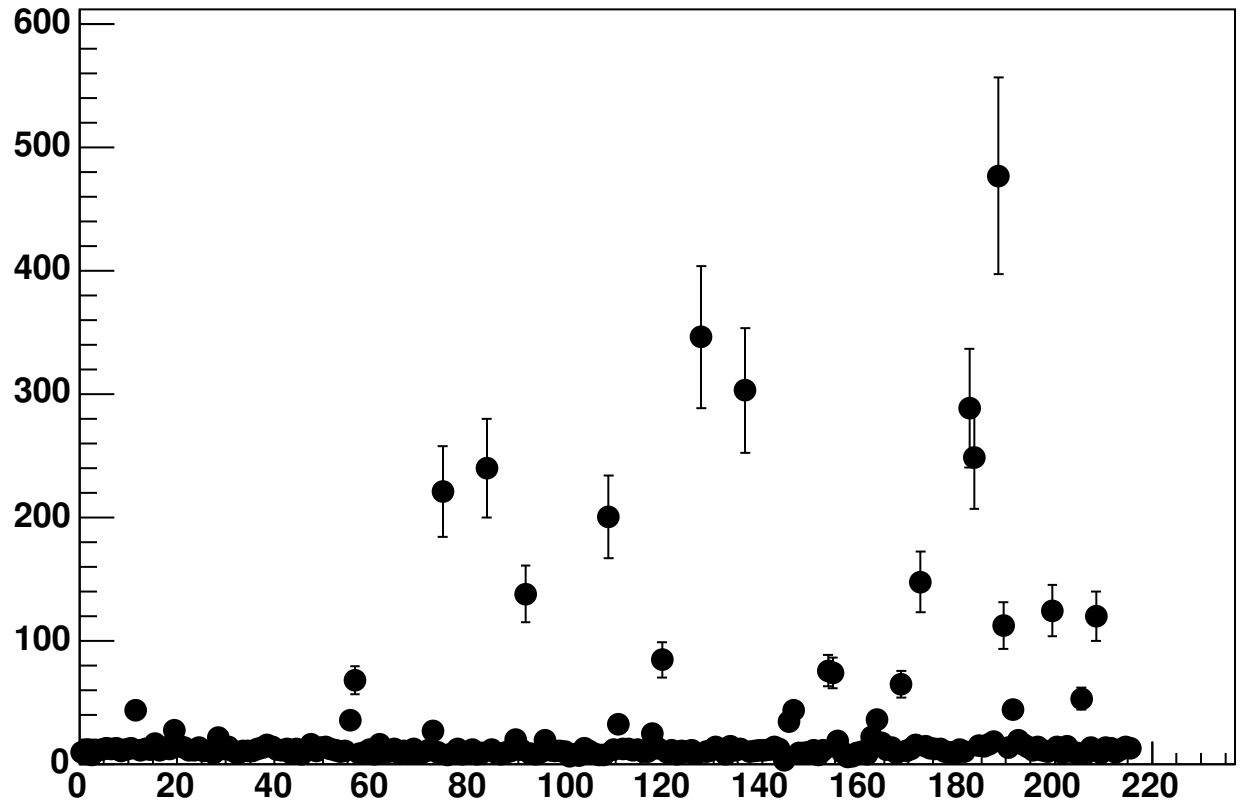
Enable 0, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



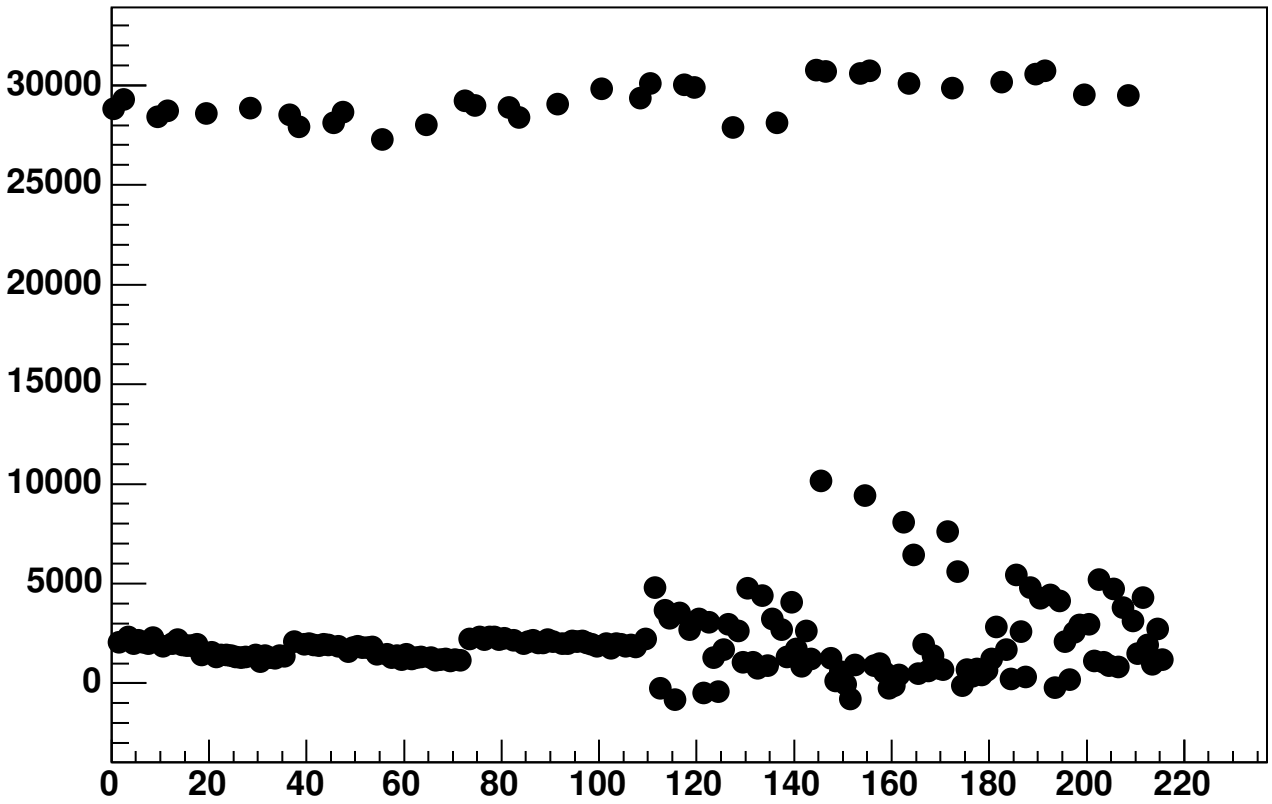
Enable 0, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



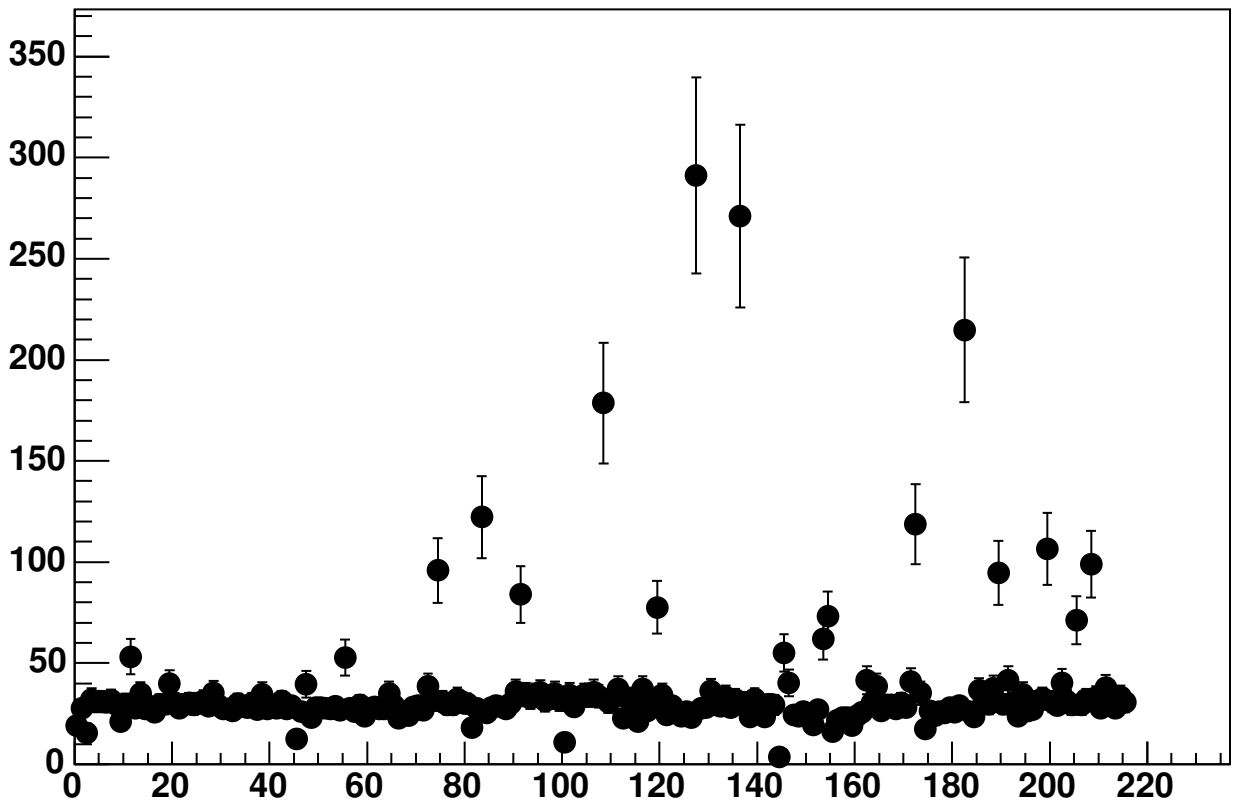
Enable 0, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



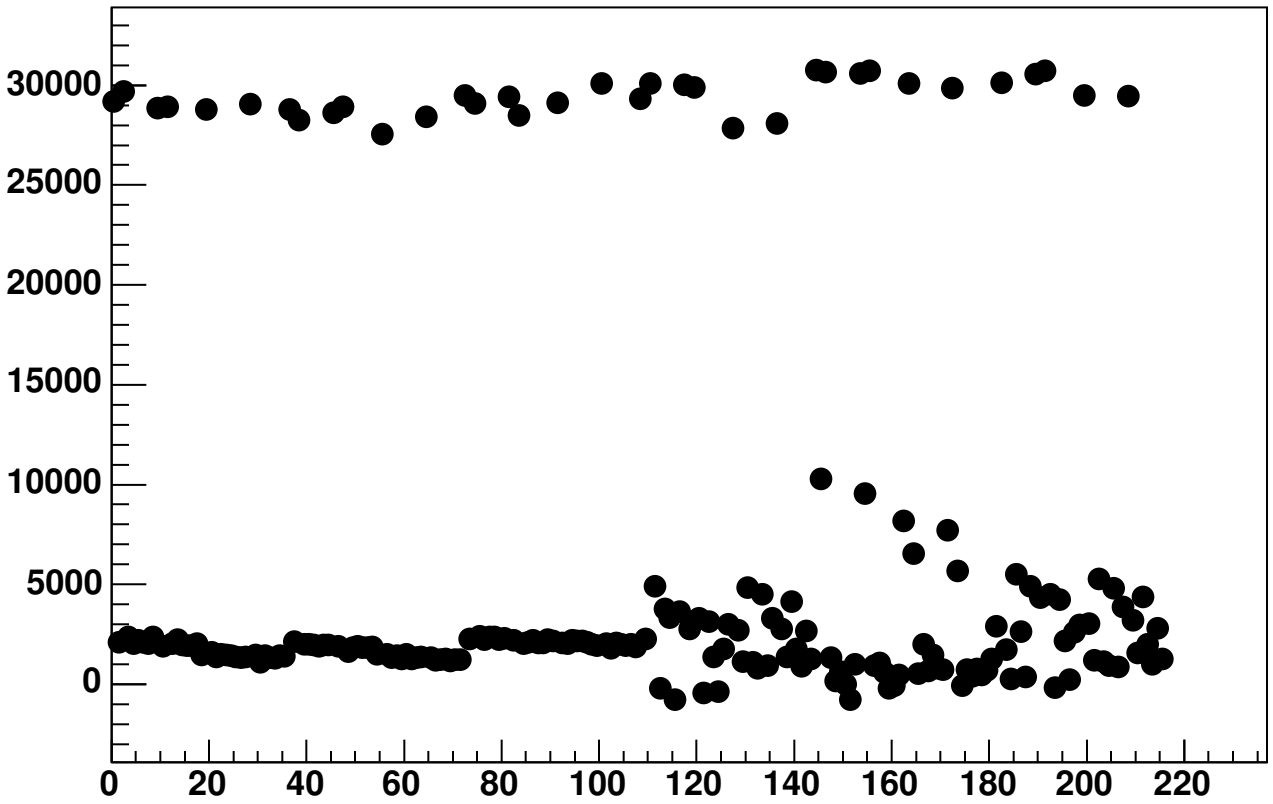
Enable 0, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



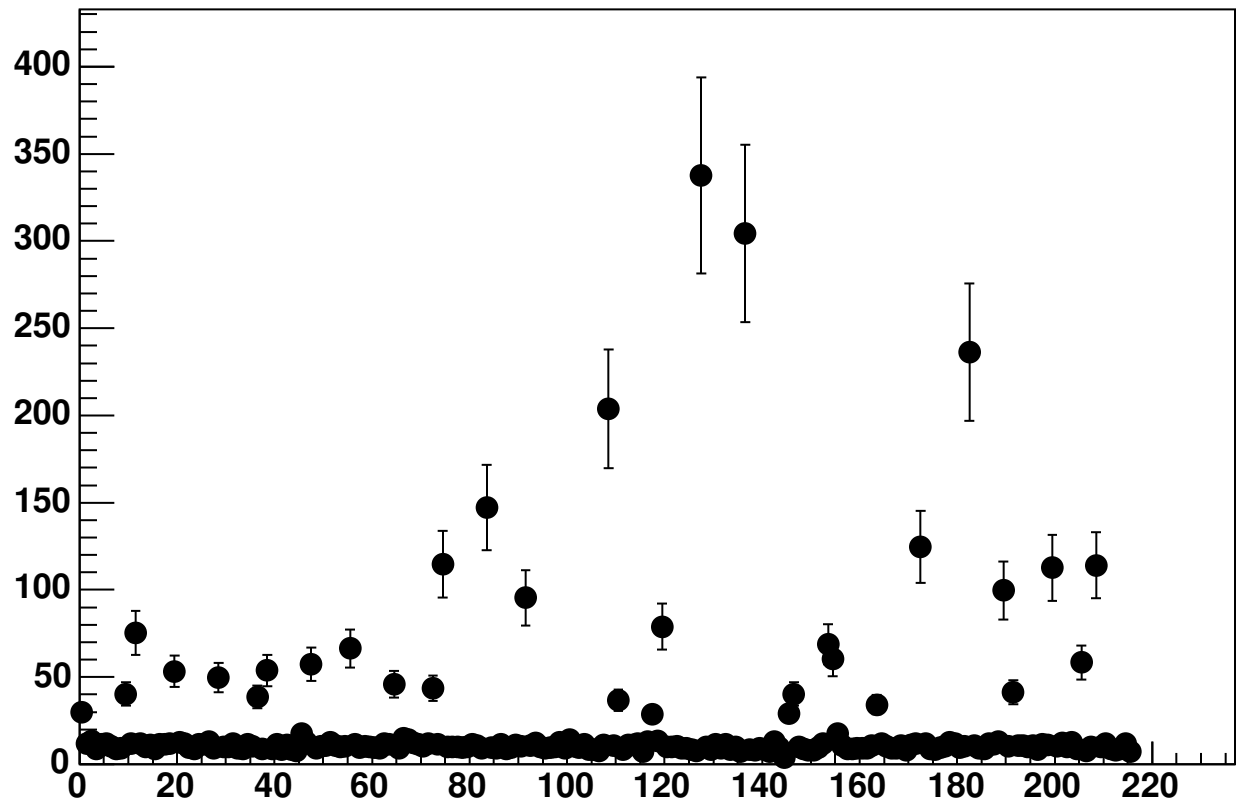
Enable 0, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



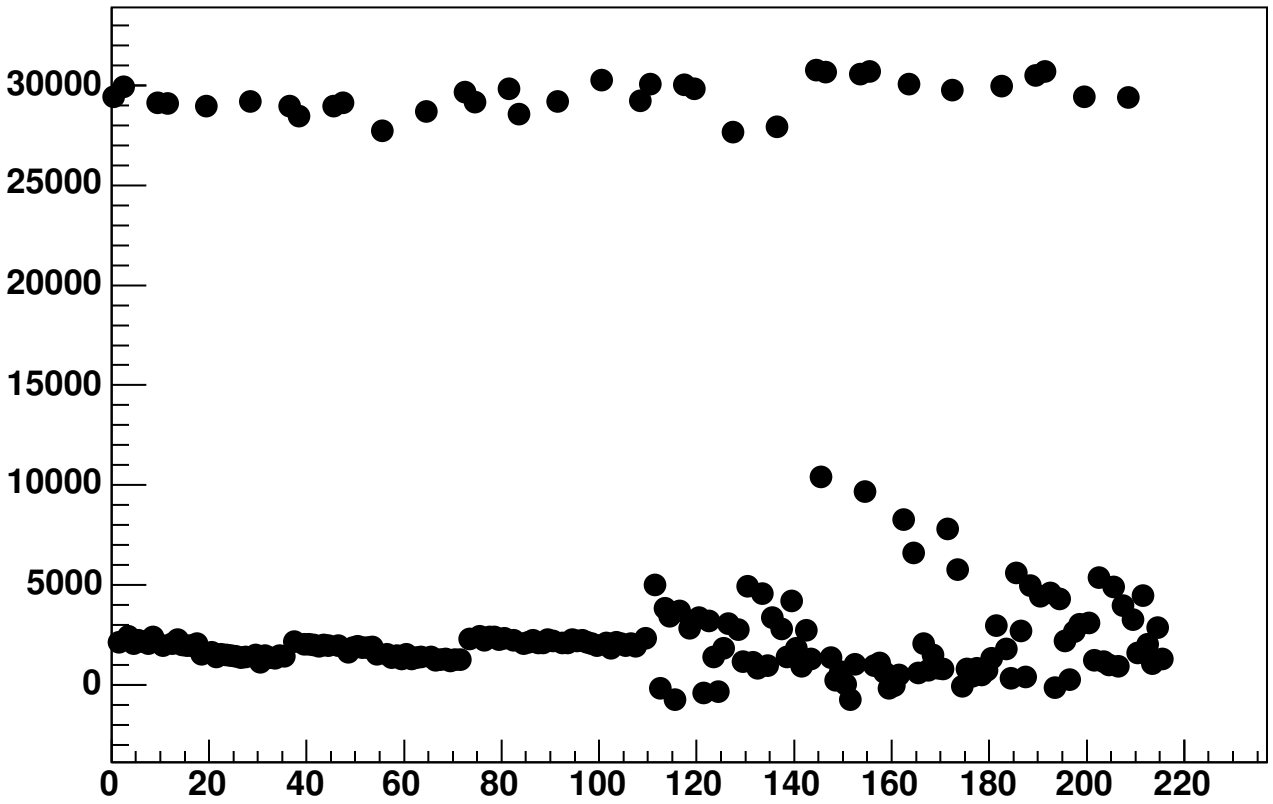
Enable 0, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



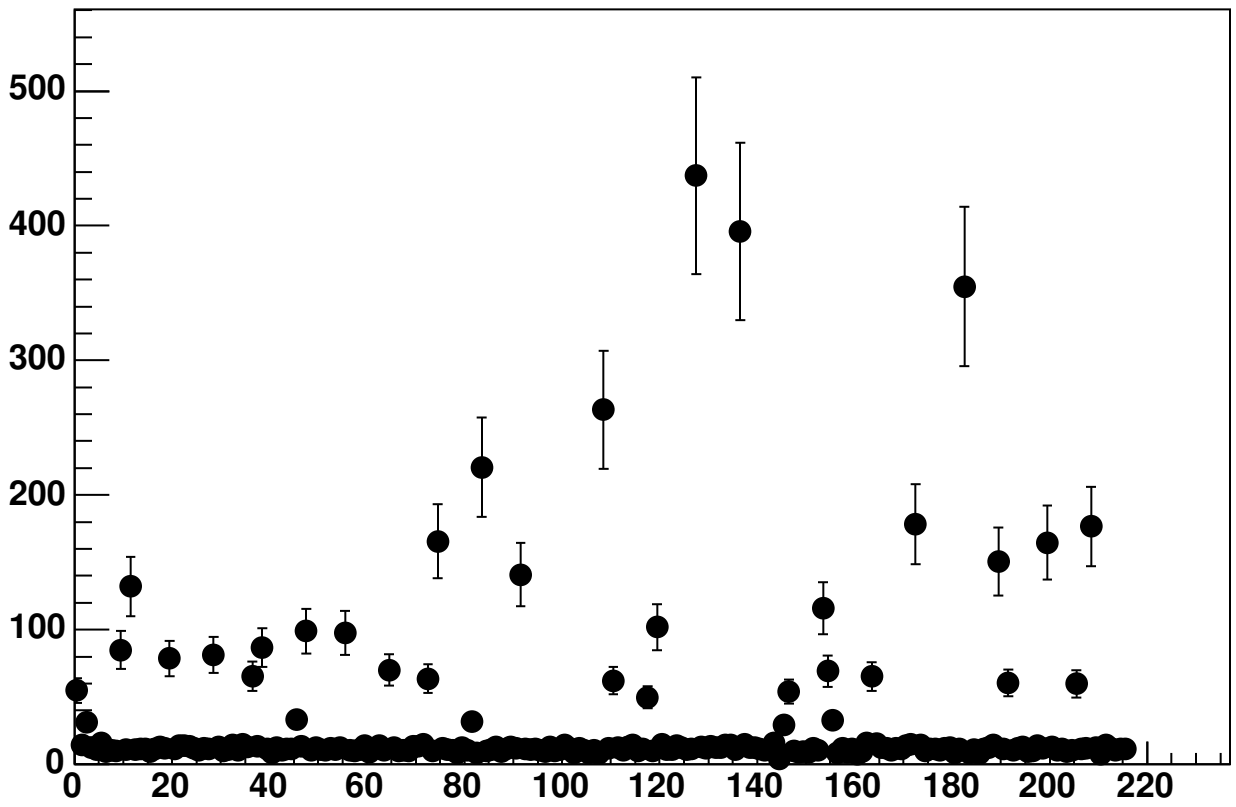
Enable 0, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



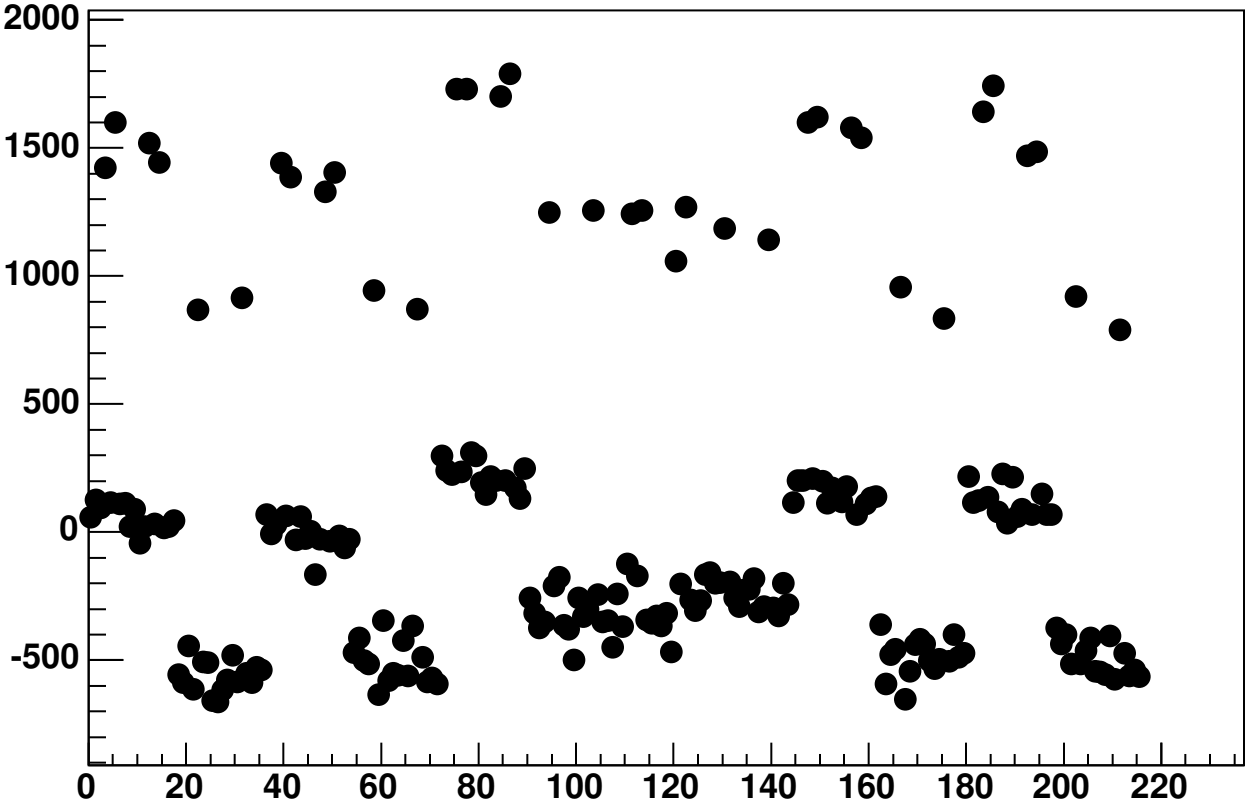
Enable 0, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



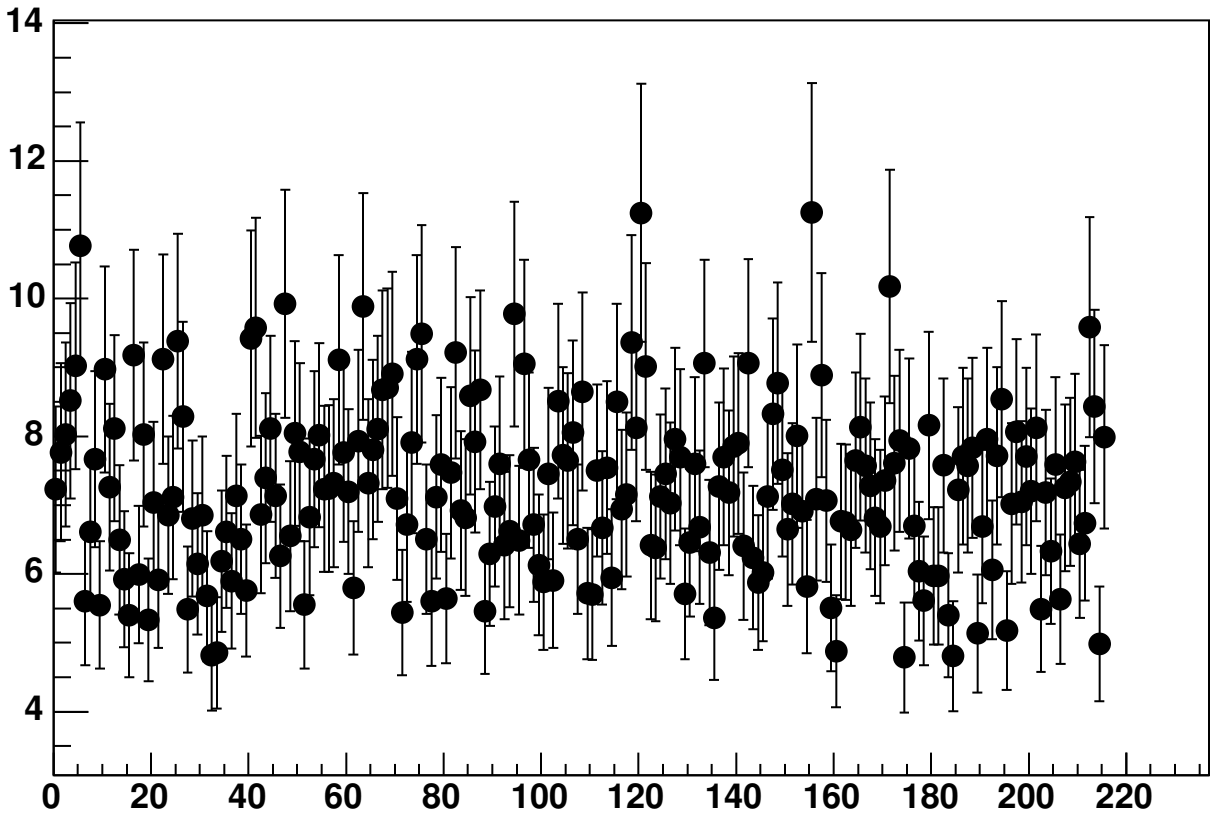
Enable 0, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



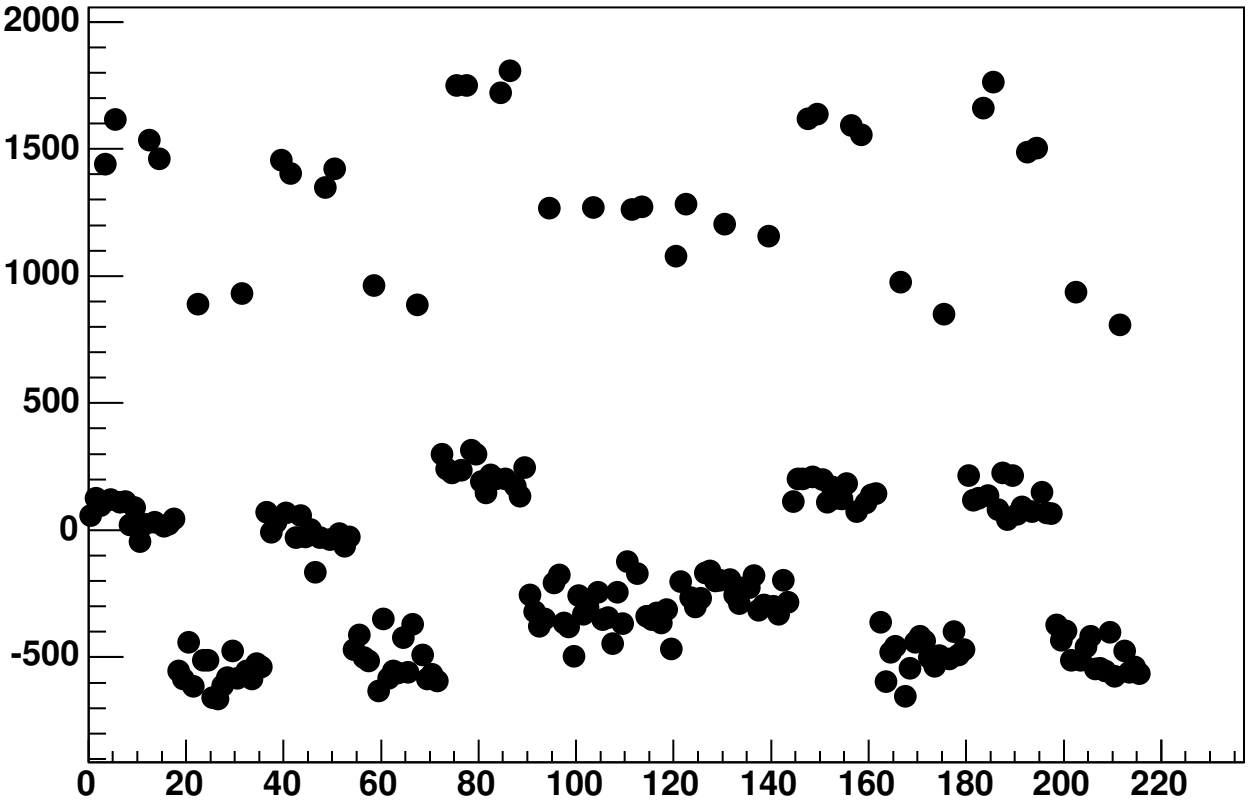
Enable 1, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



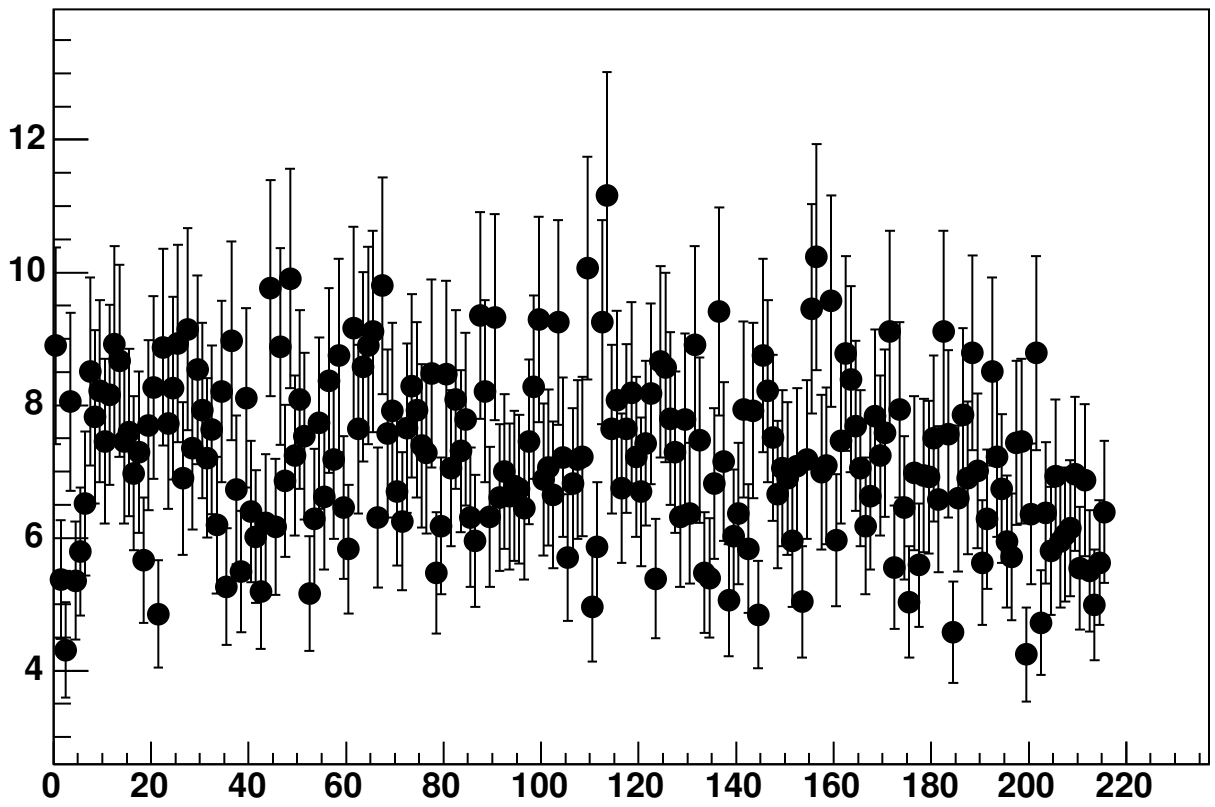
Enable 1, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



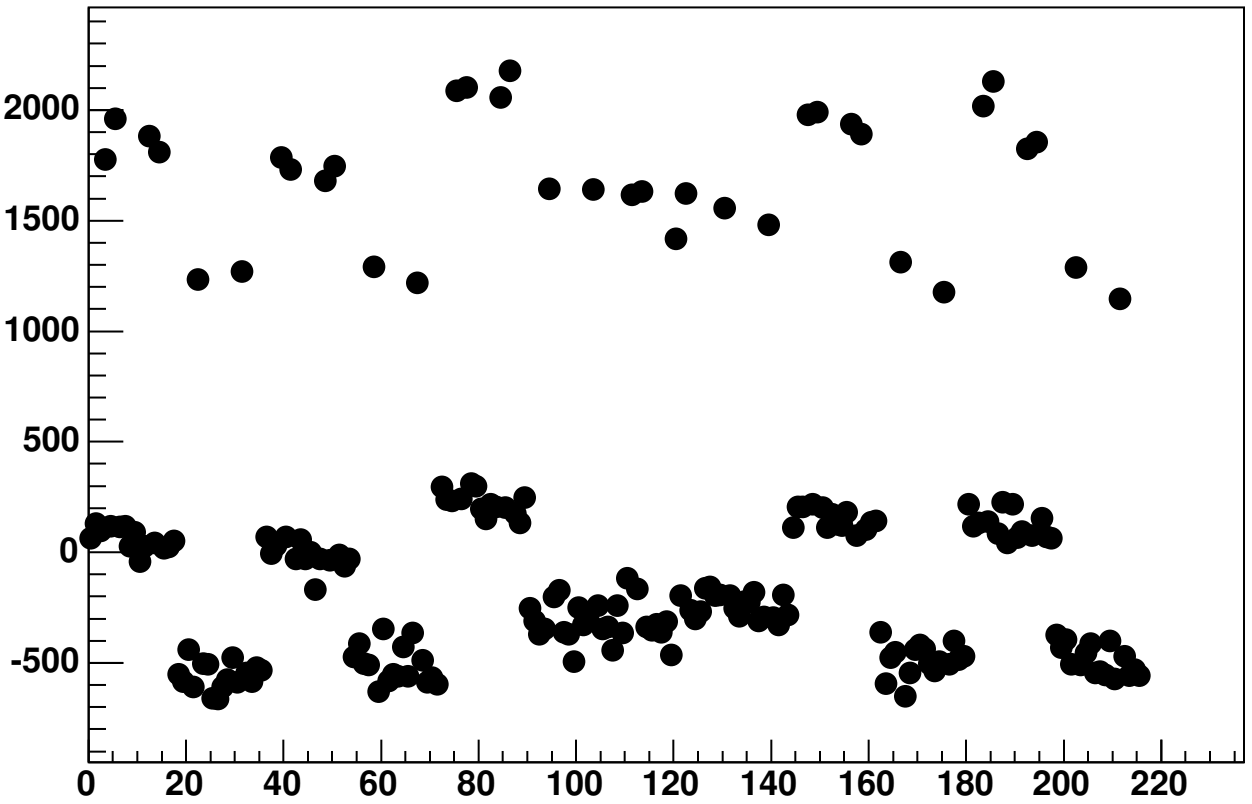
Enable 1, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



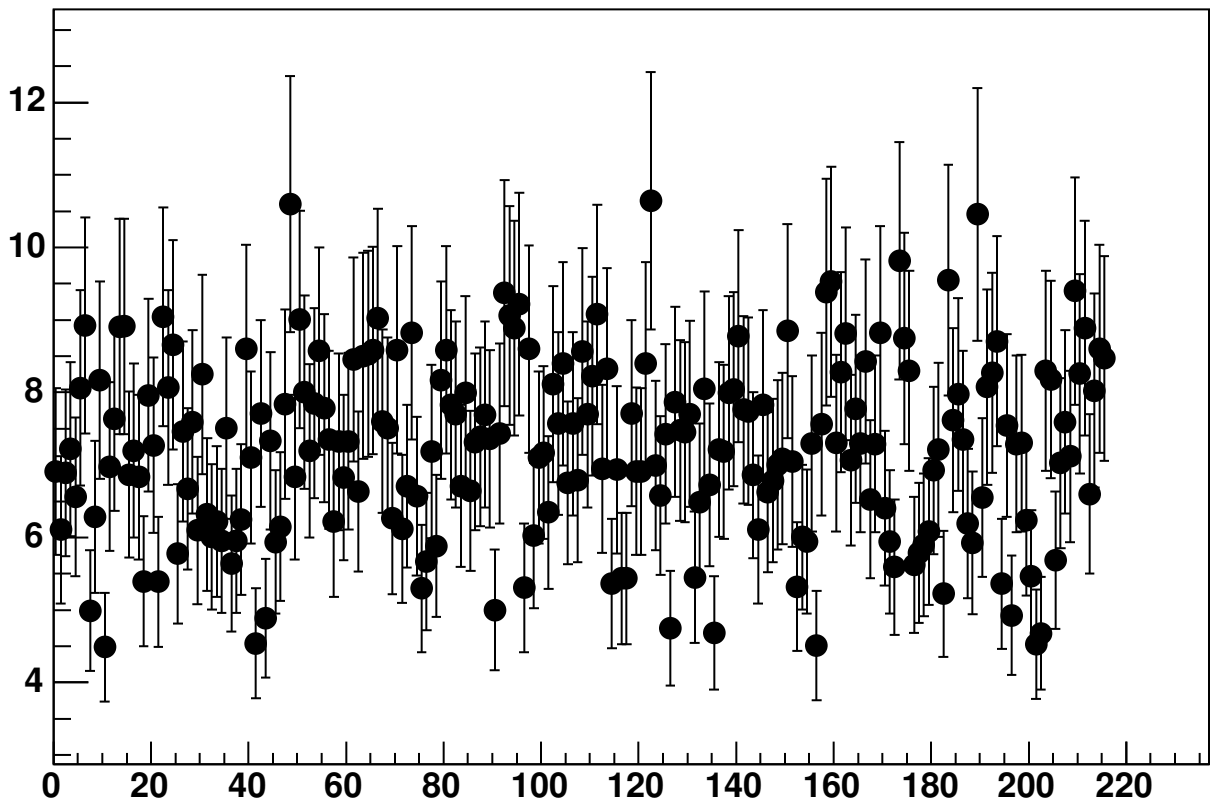
Enable 1, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



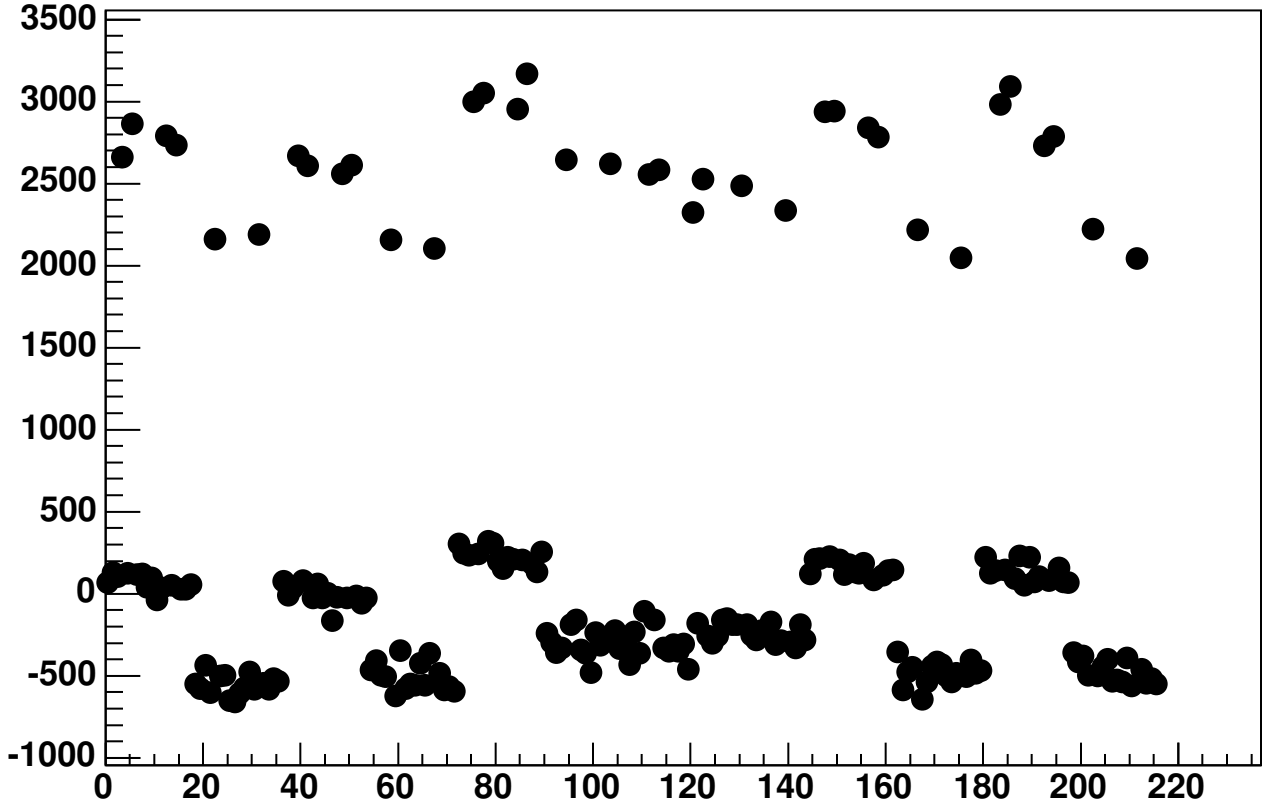
Enable 1, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



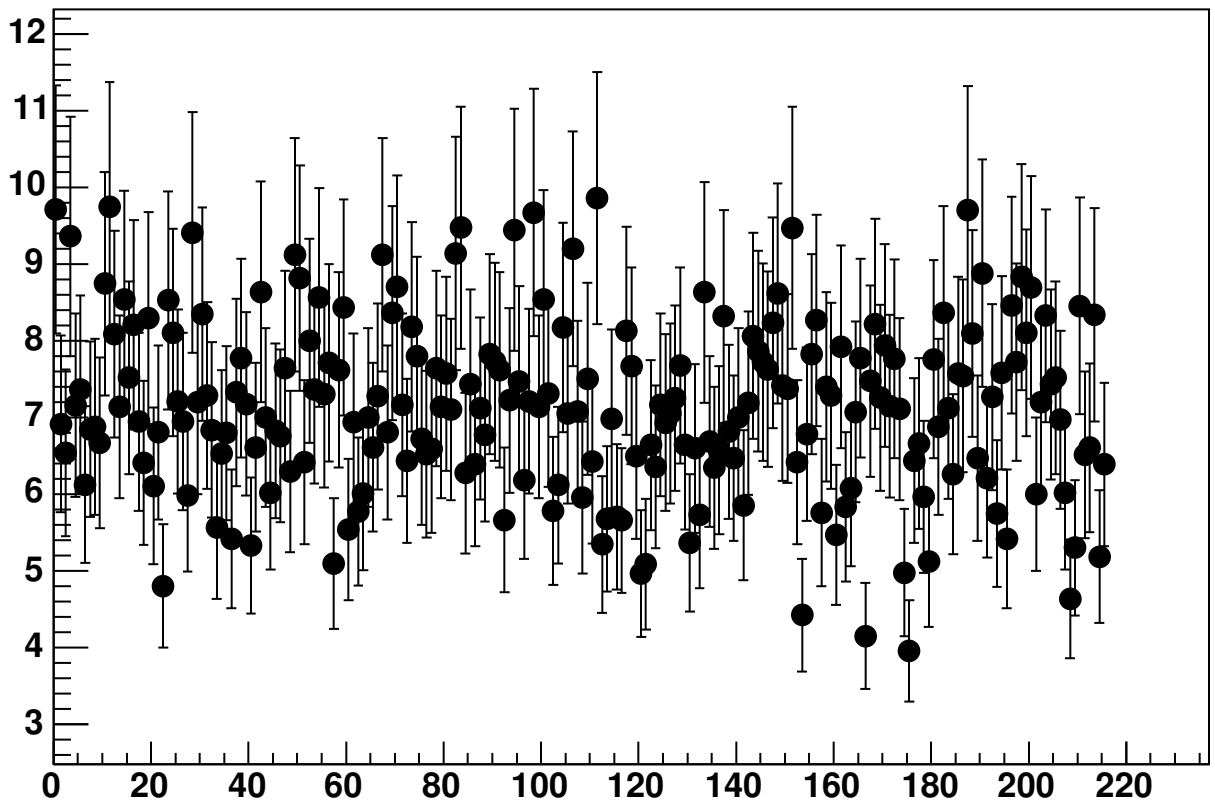
Enable 1, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



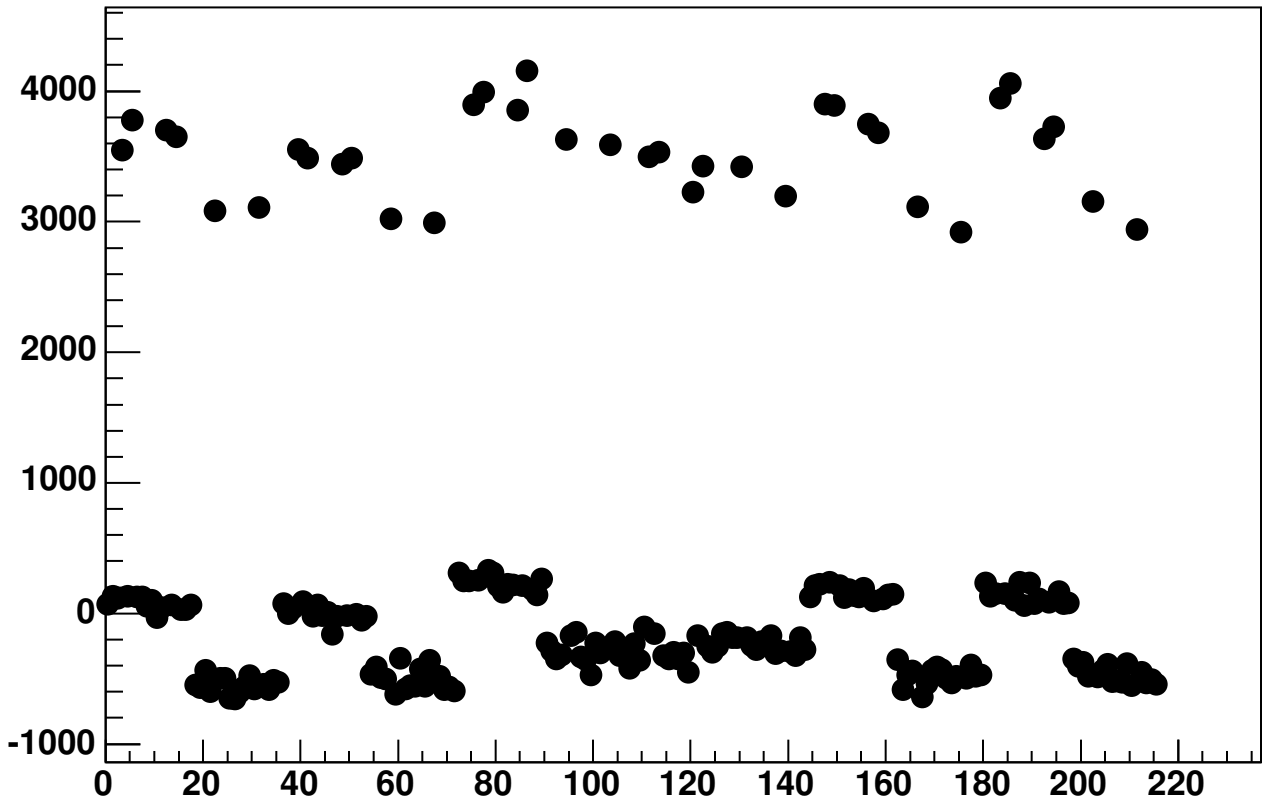
Enable 1, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



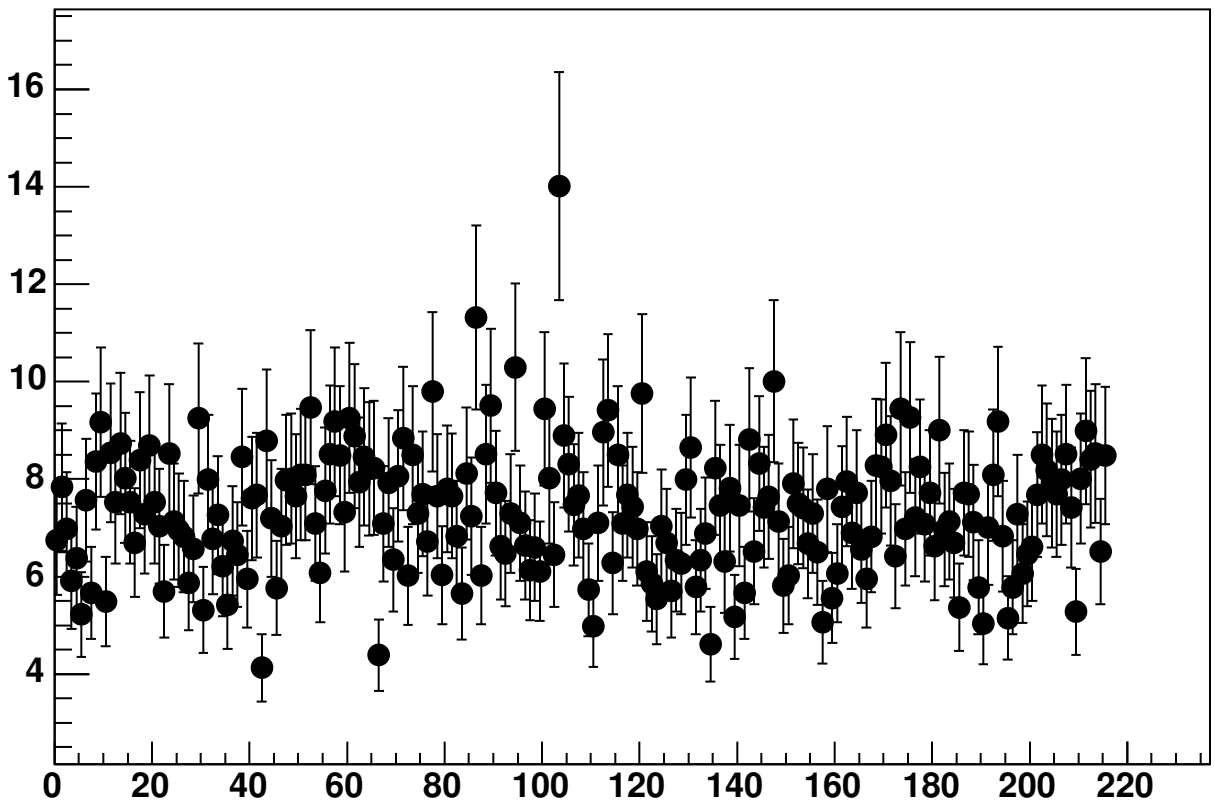
Enable 1, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



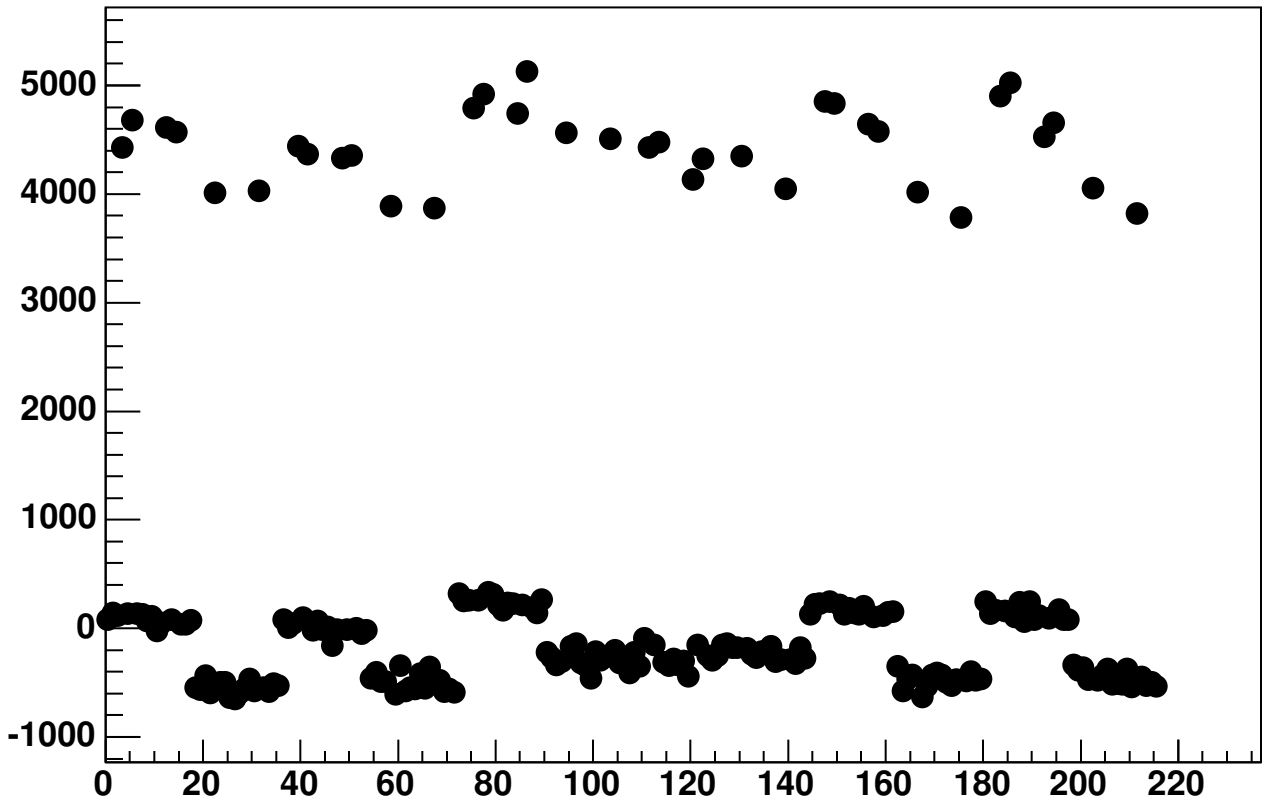
Enable 1, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



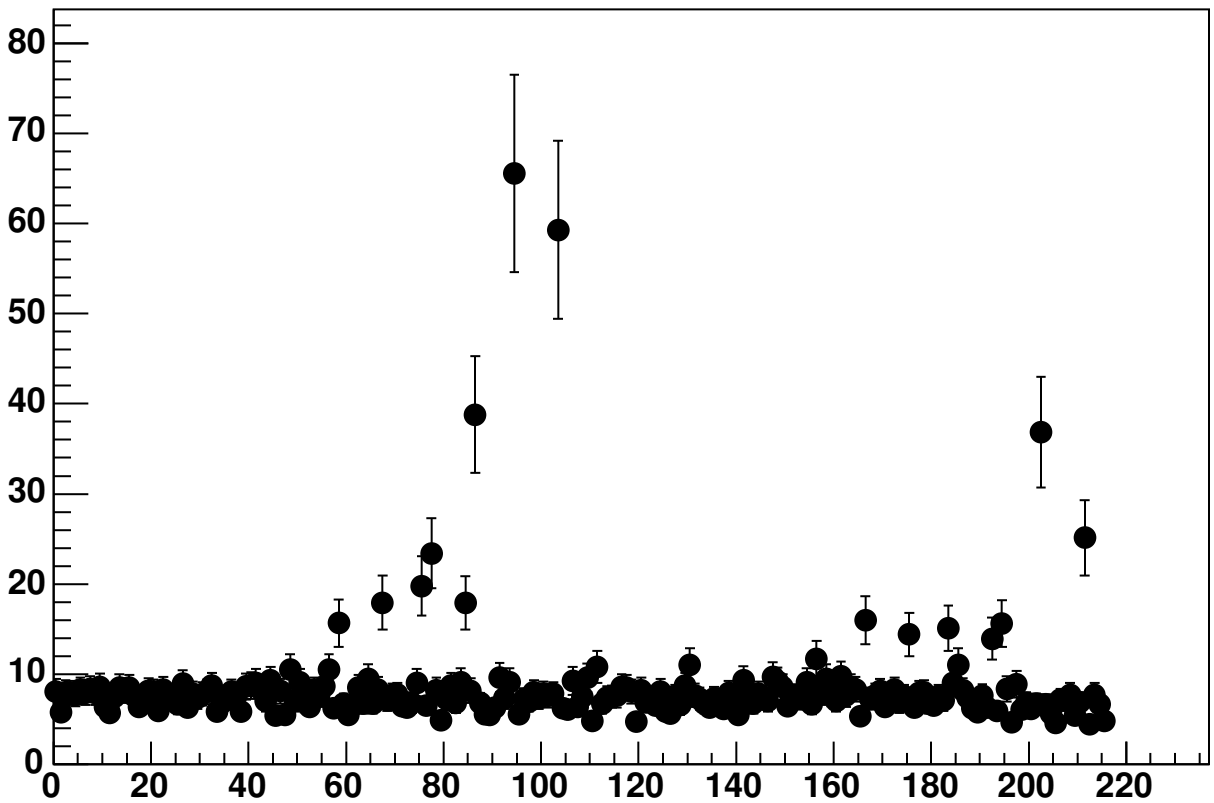
Enable 1, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



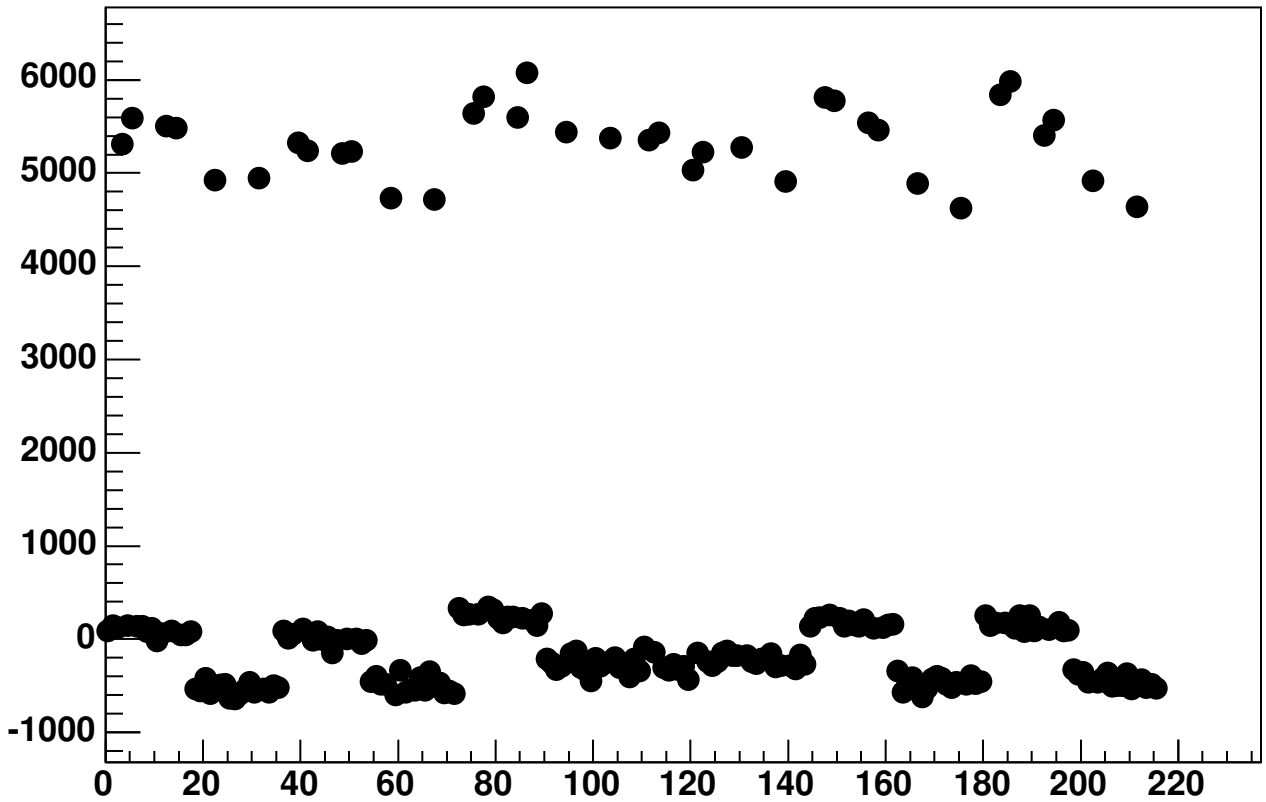
Enable 1, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



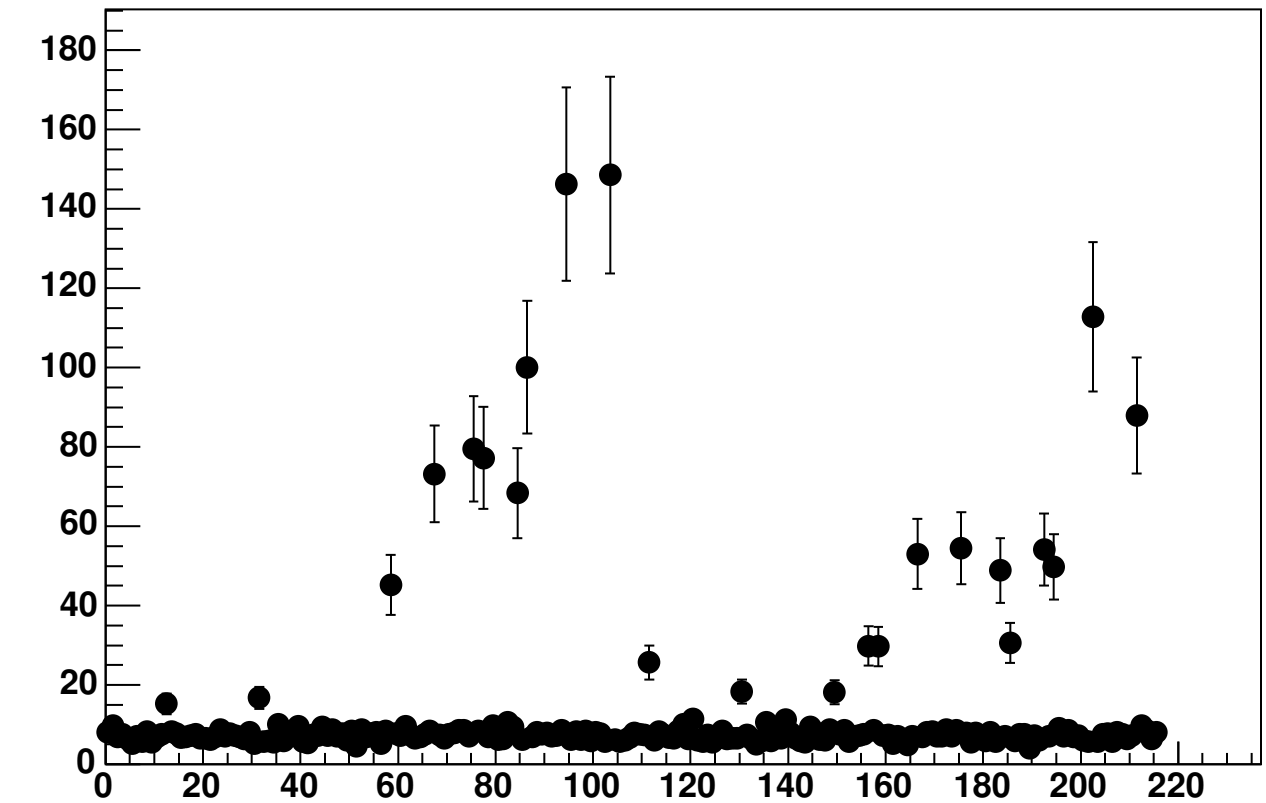
Enable 1, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



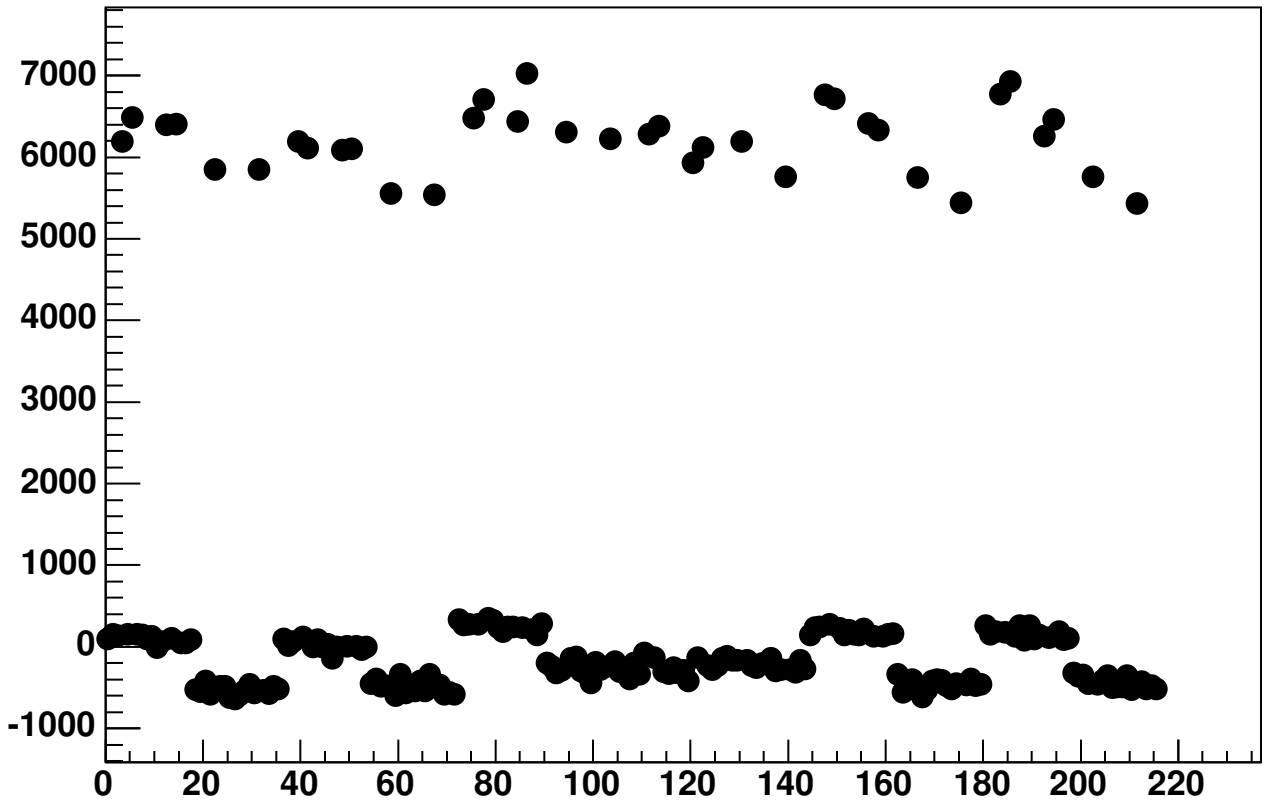
Enable 1, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



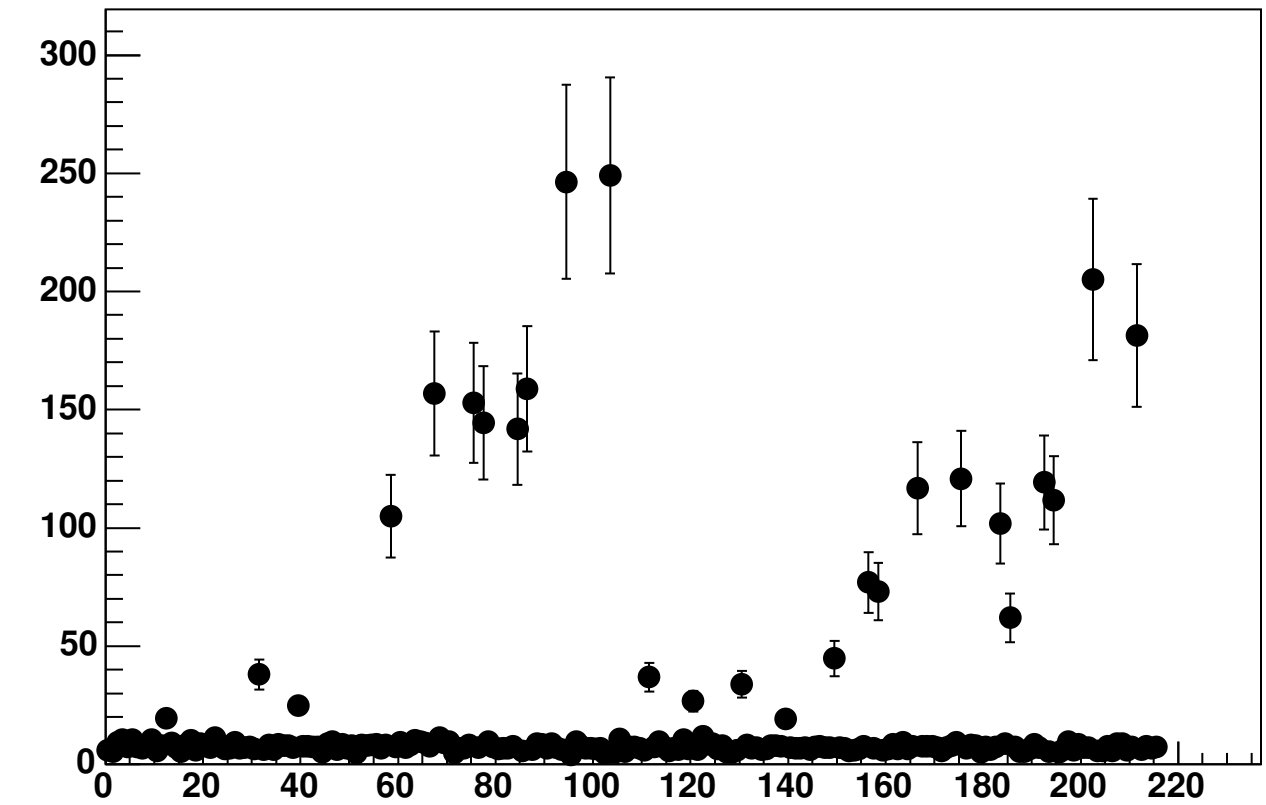
Enable 1, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



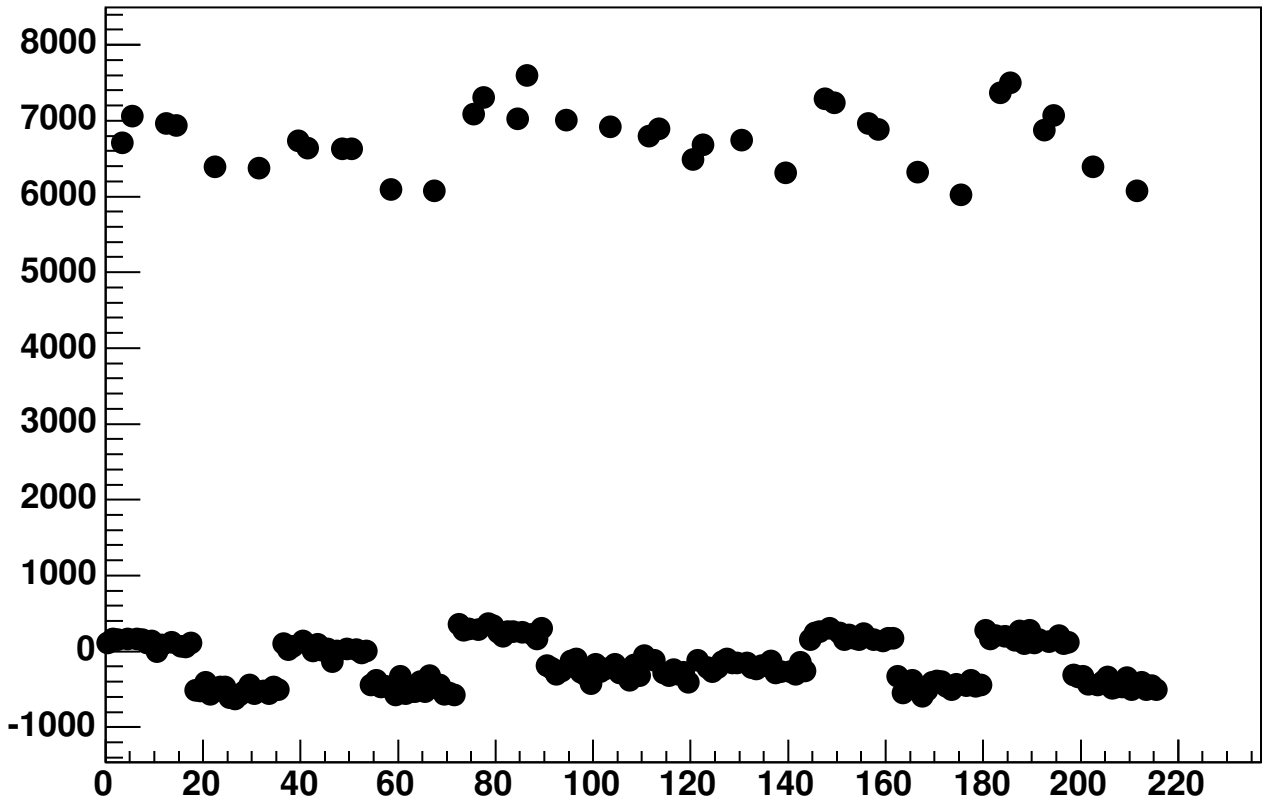
Enable 1, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



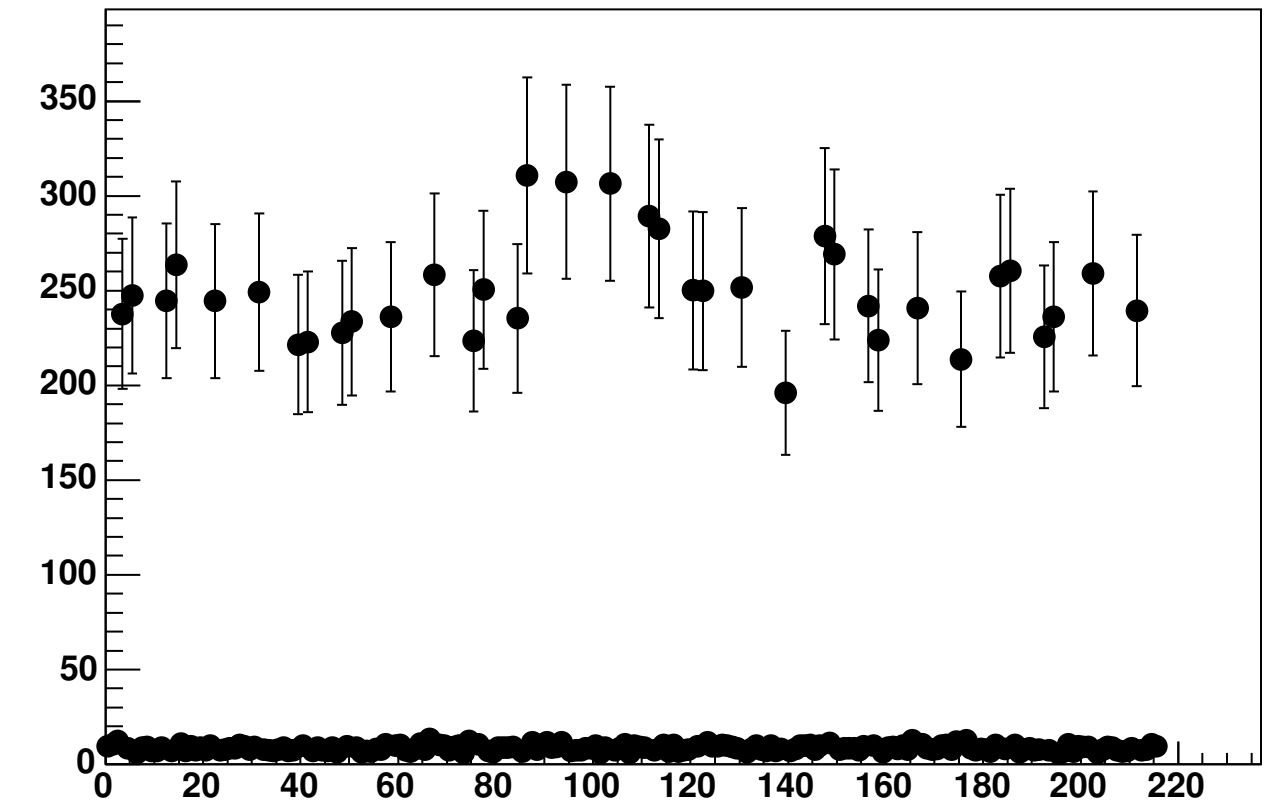
Enable 1, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



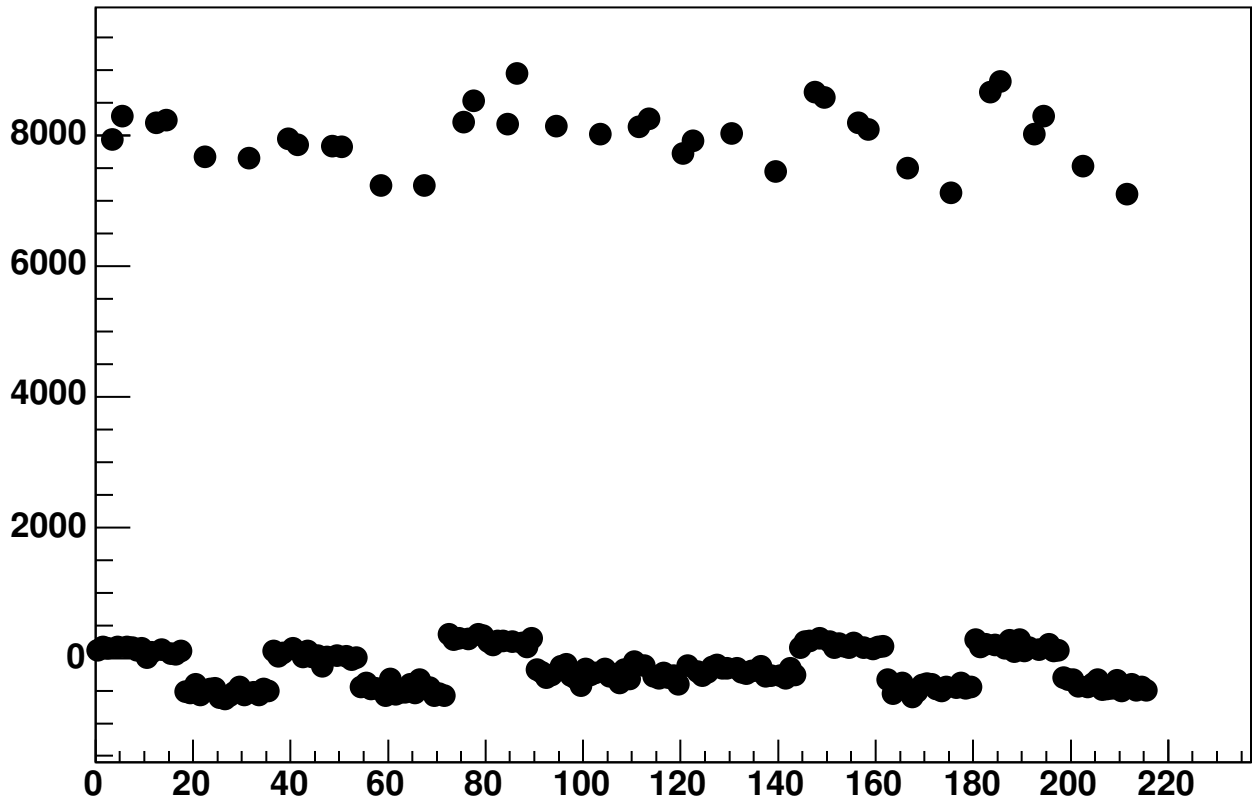
Enable 1, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



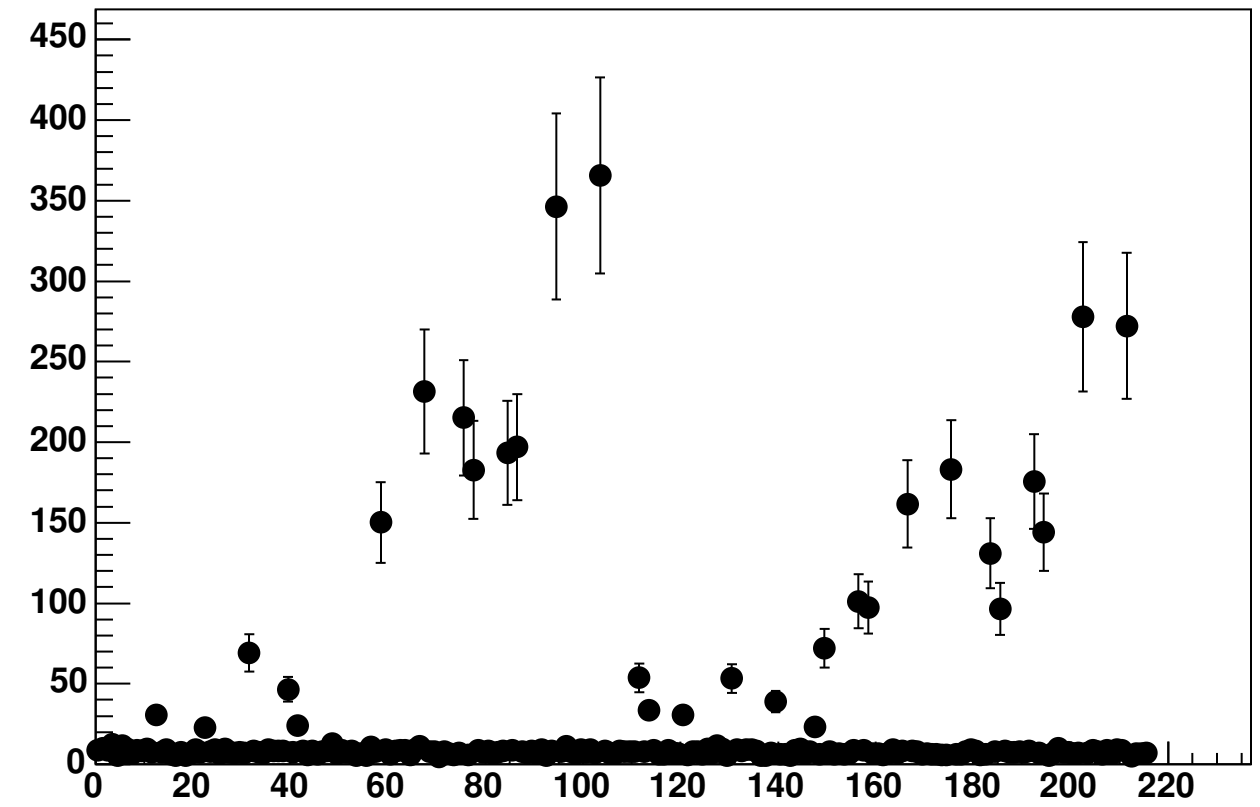
Enable 1, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



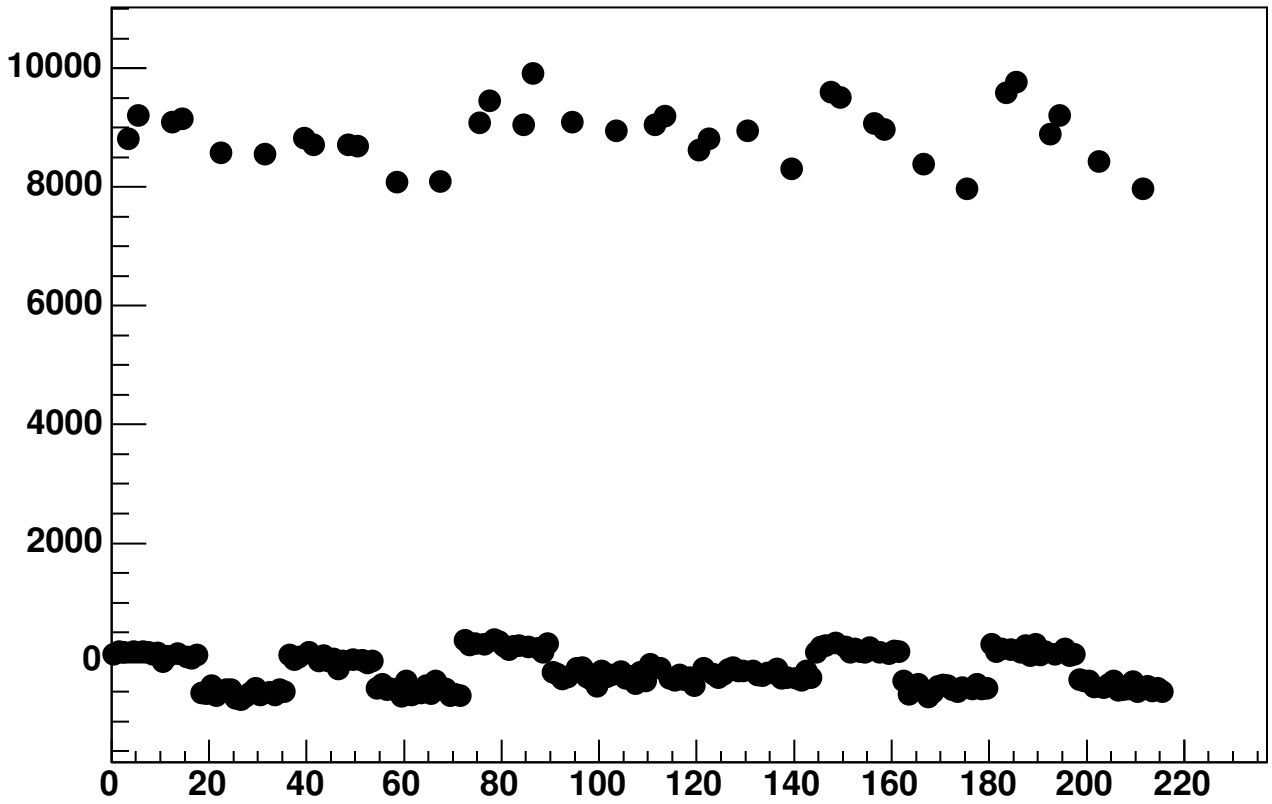
Enable 1, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



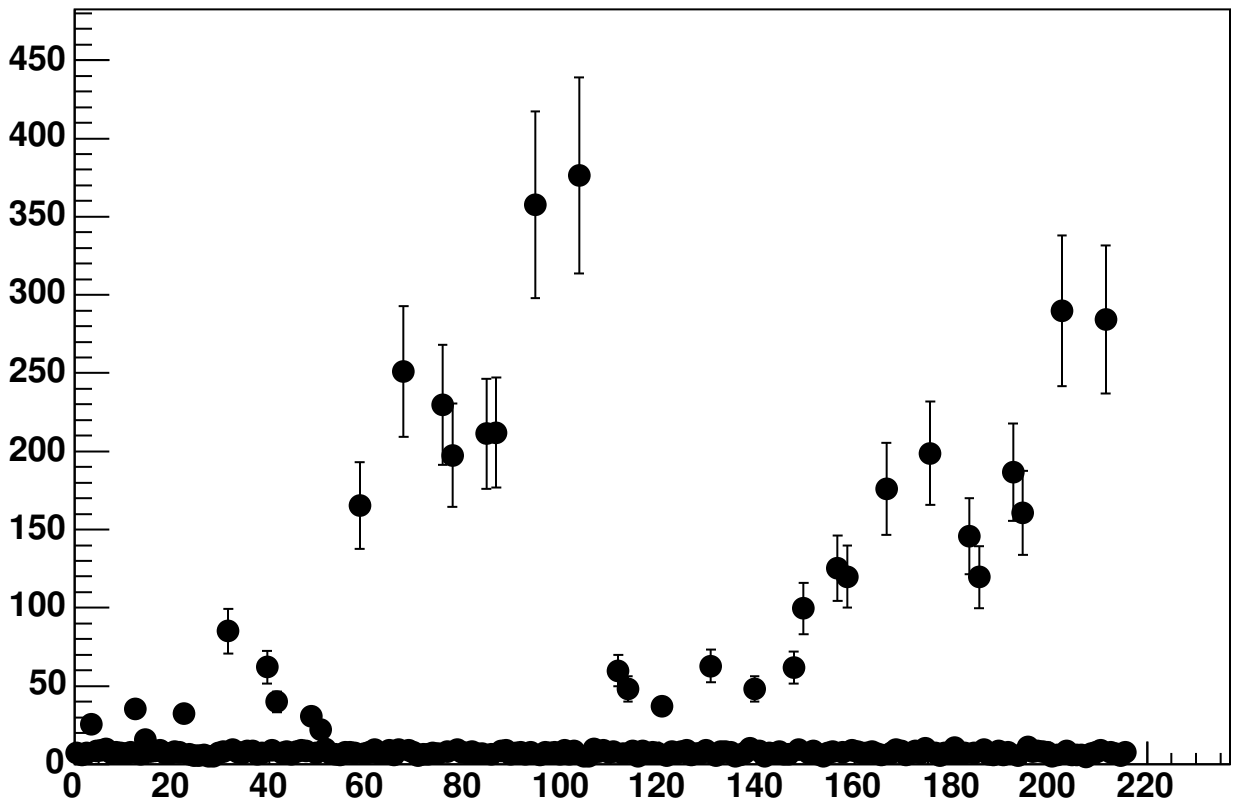
Enable 1, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



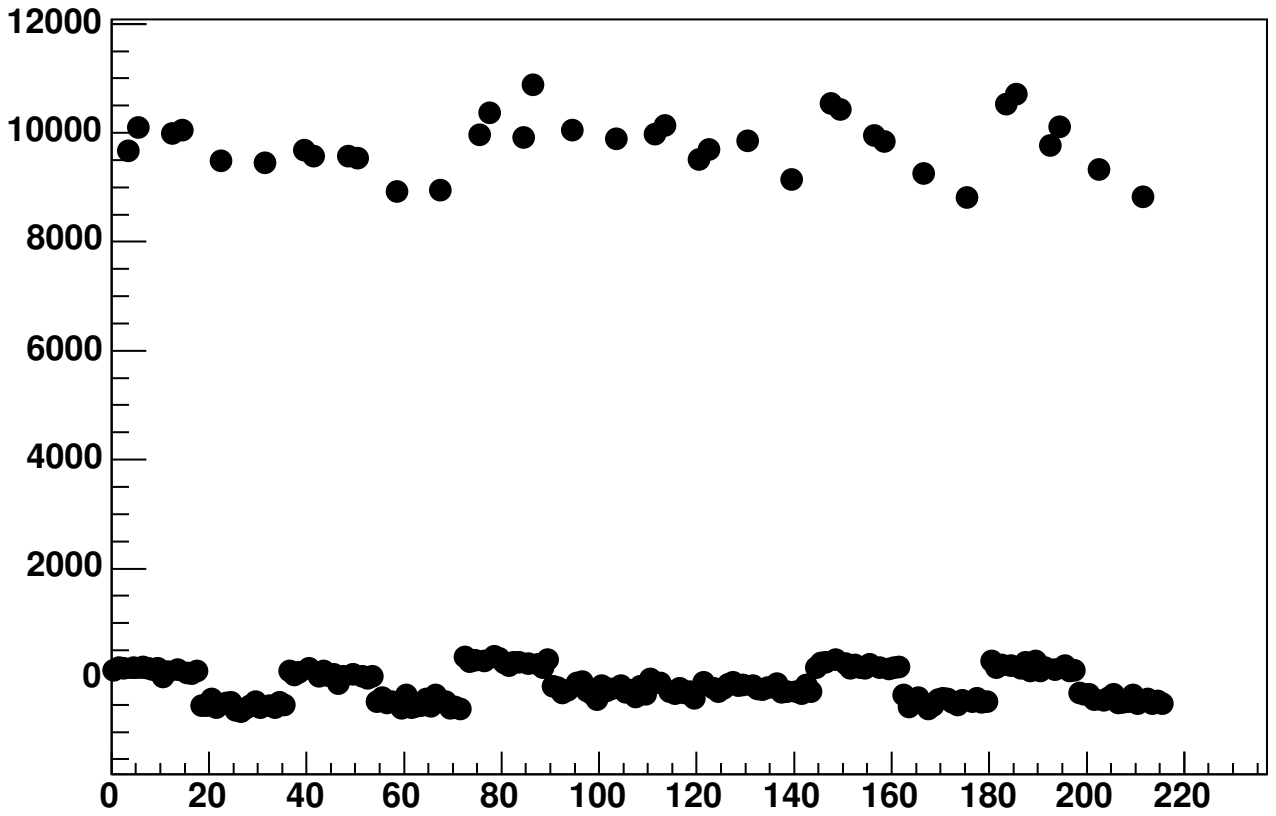
Enable 1, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



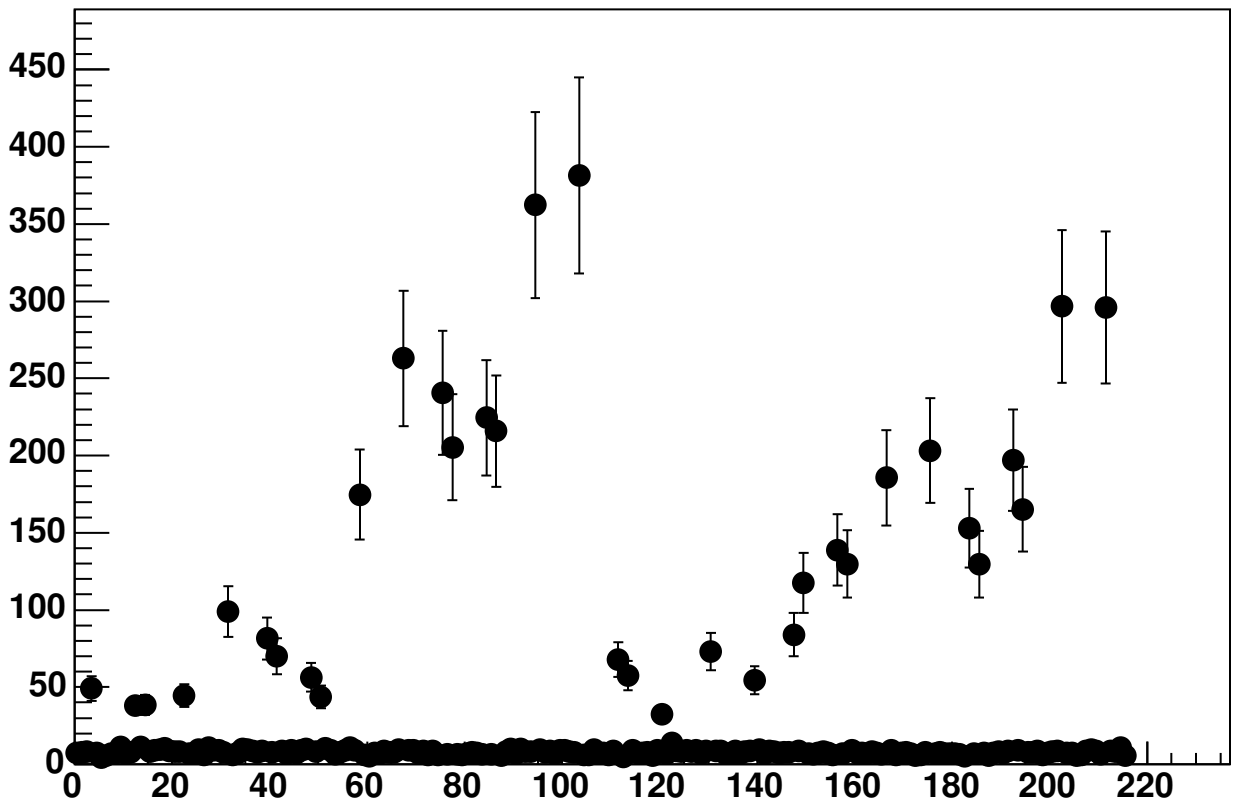
Enable 1, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



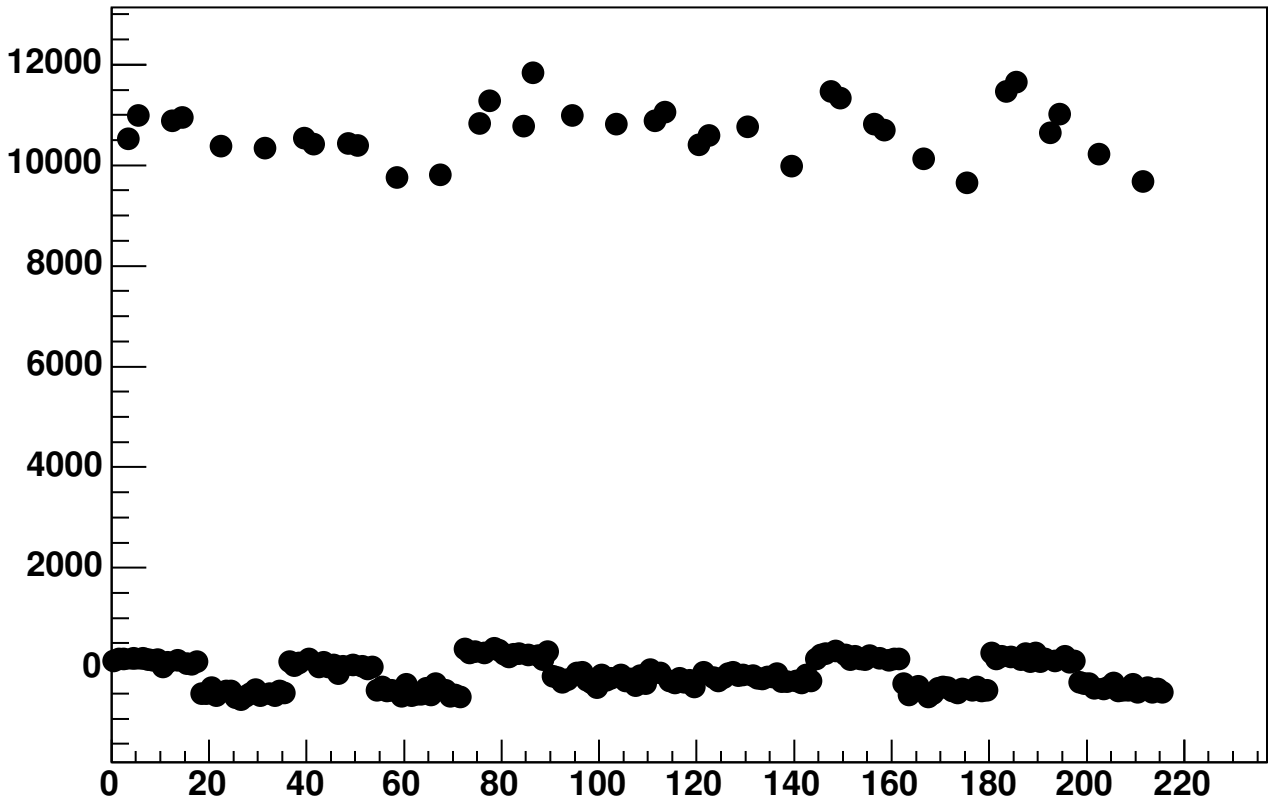
Enable 1, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



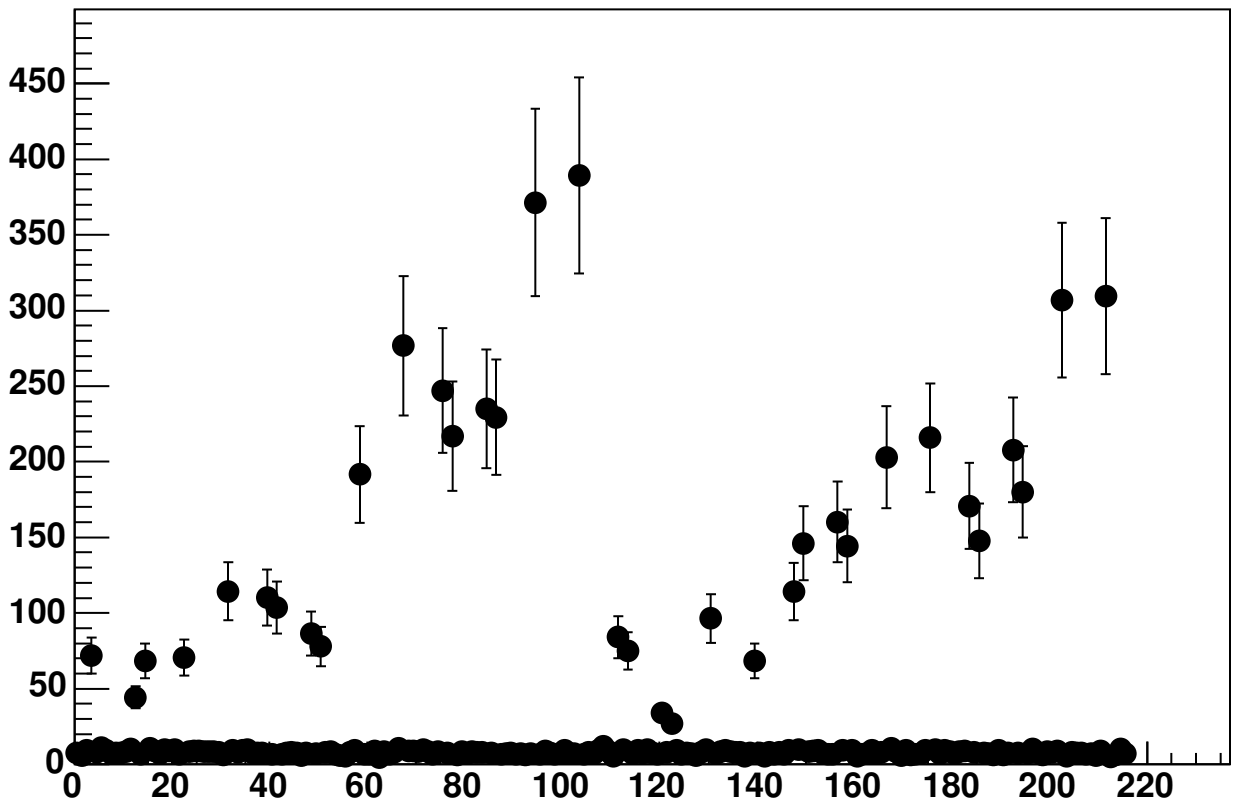
Enable 1, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



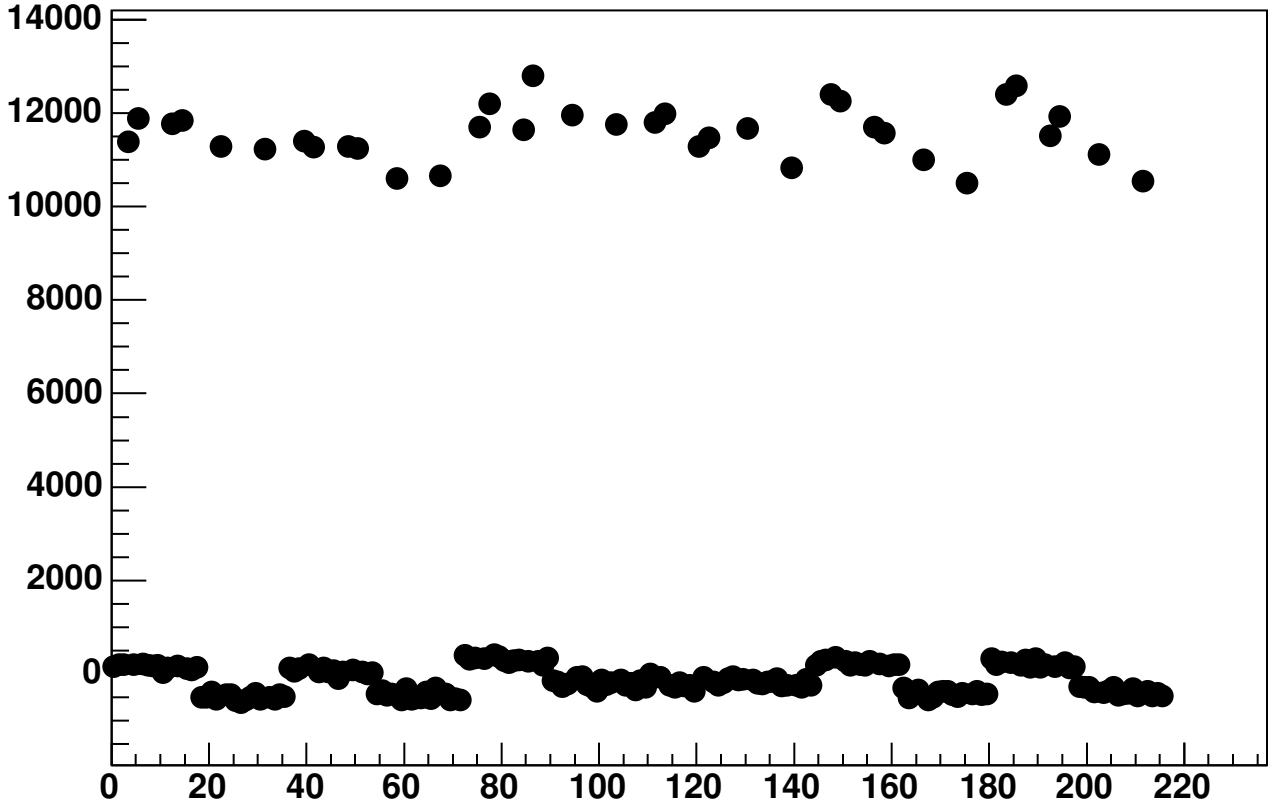
Enable 1, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



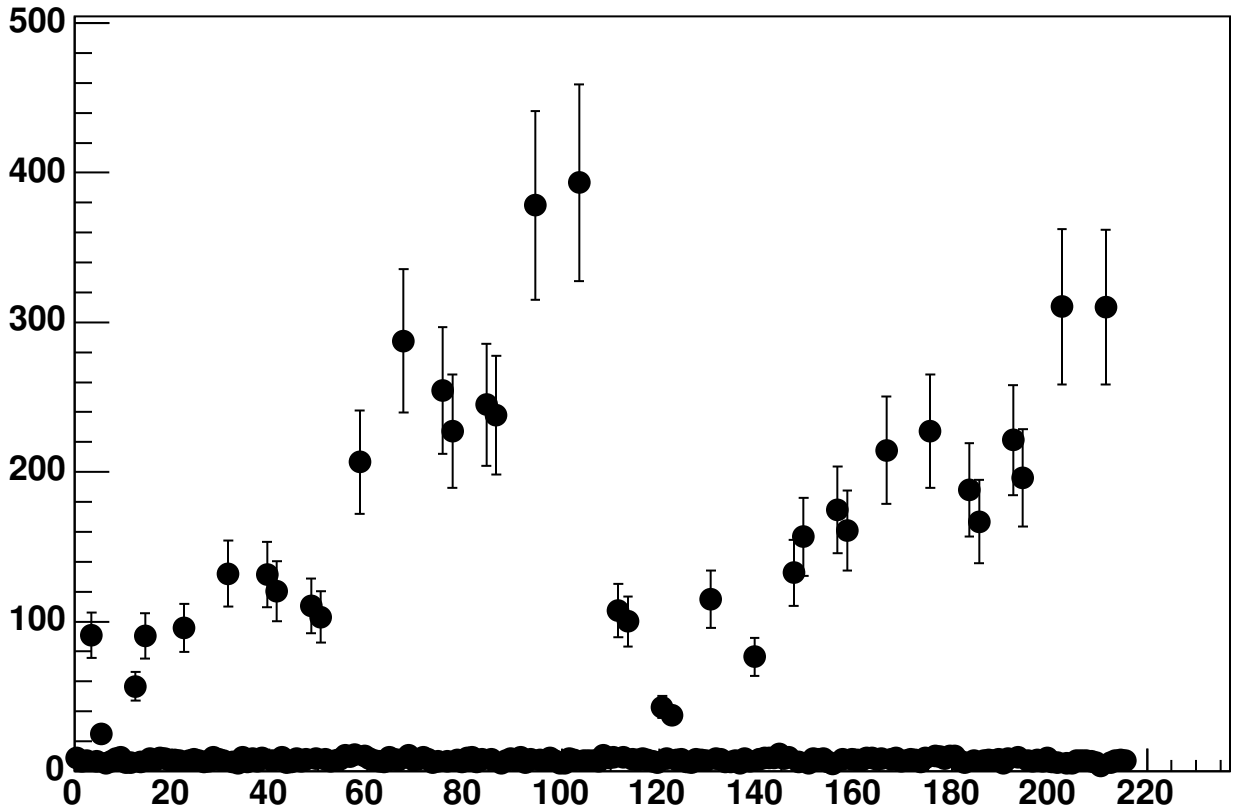
Enable 1, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



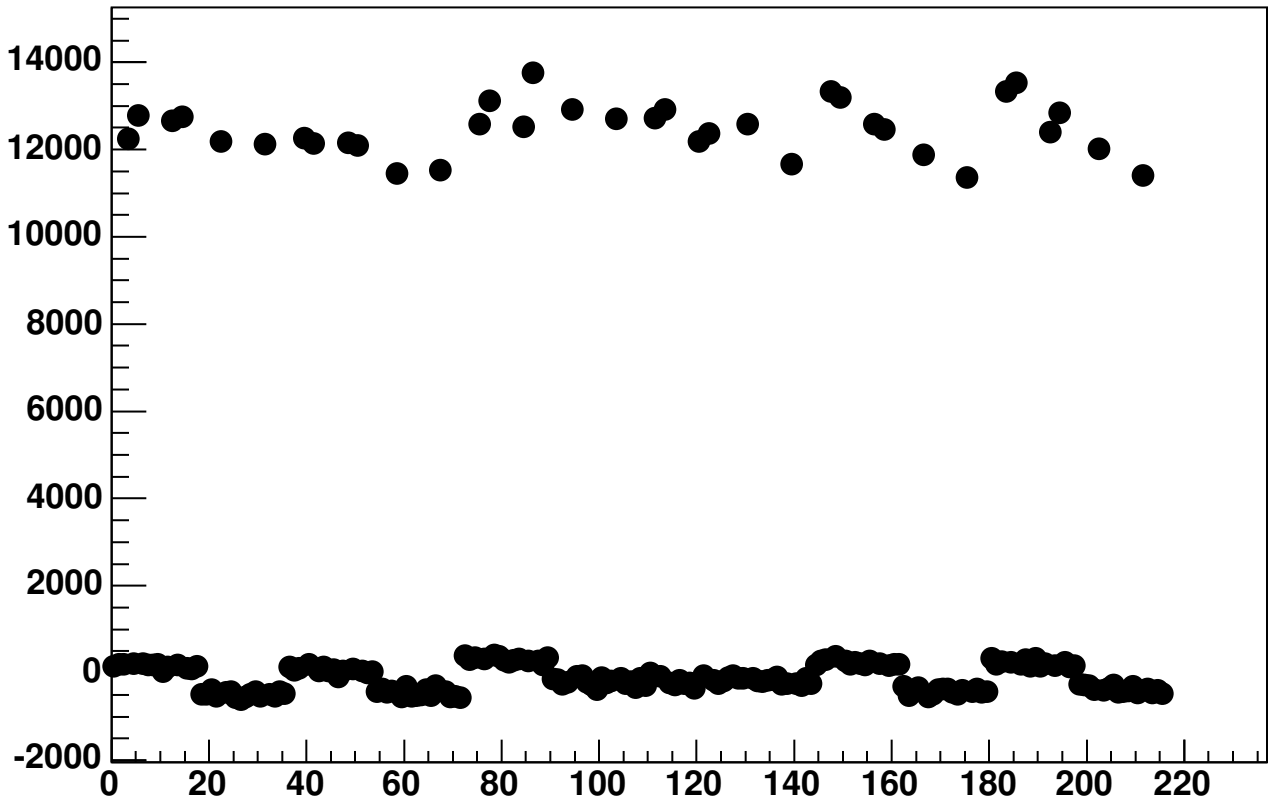
Enable 1, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



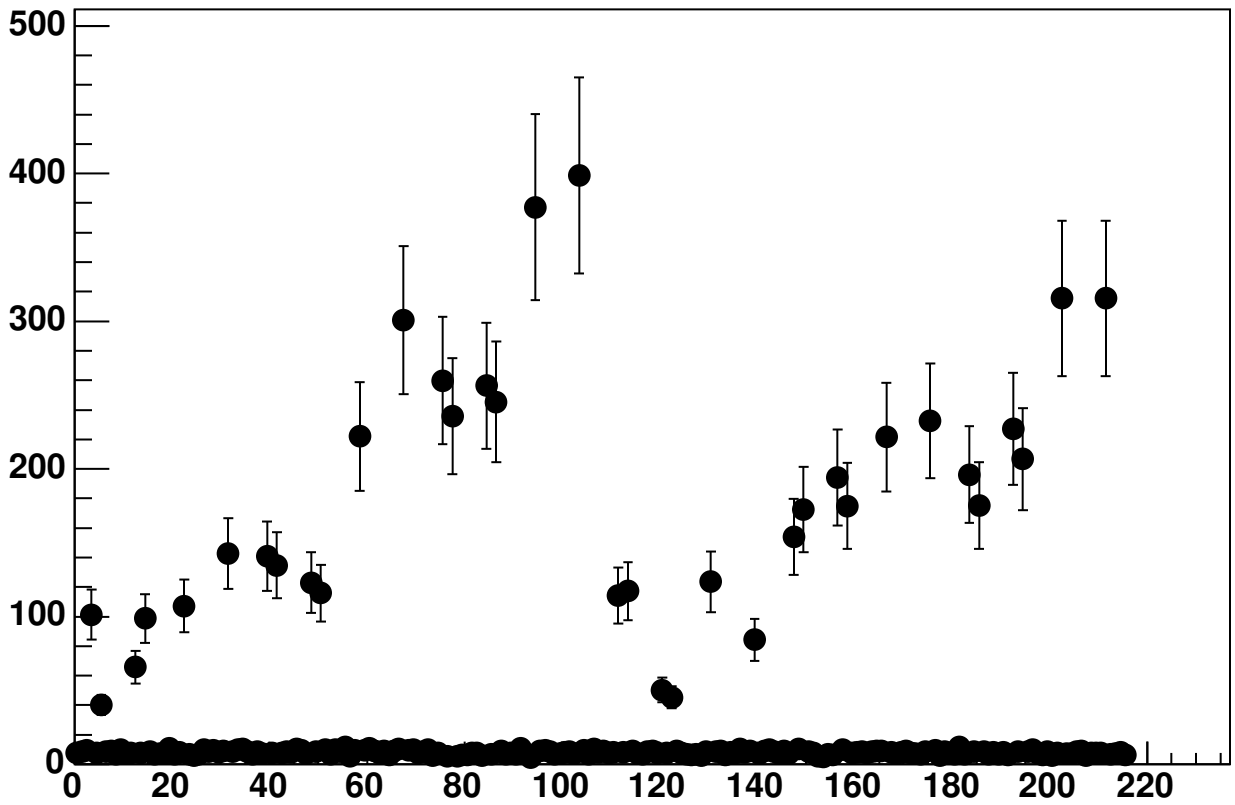
Enable 1, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



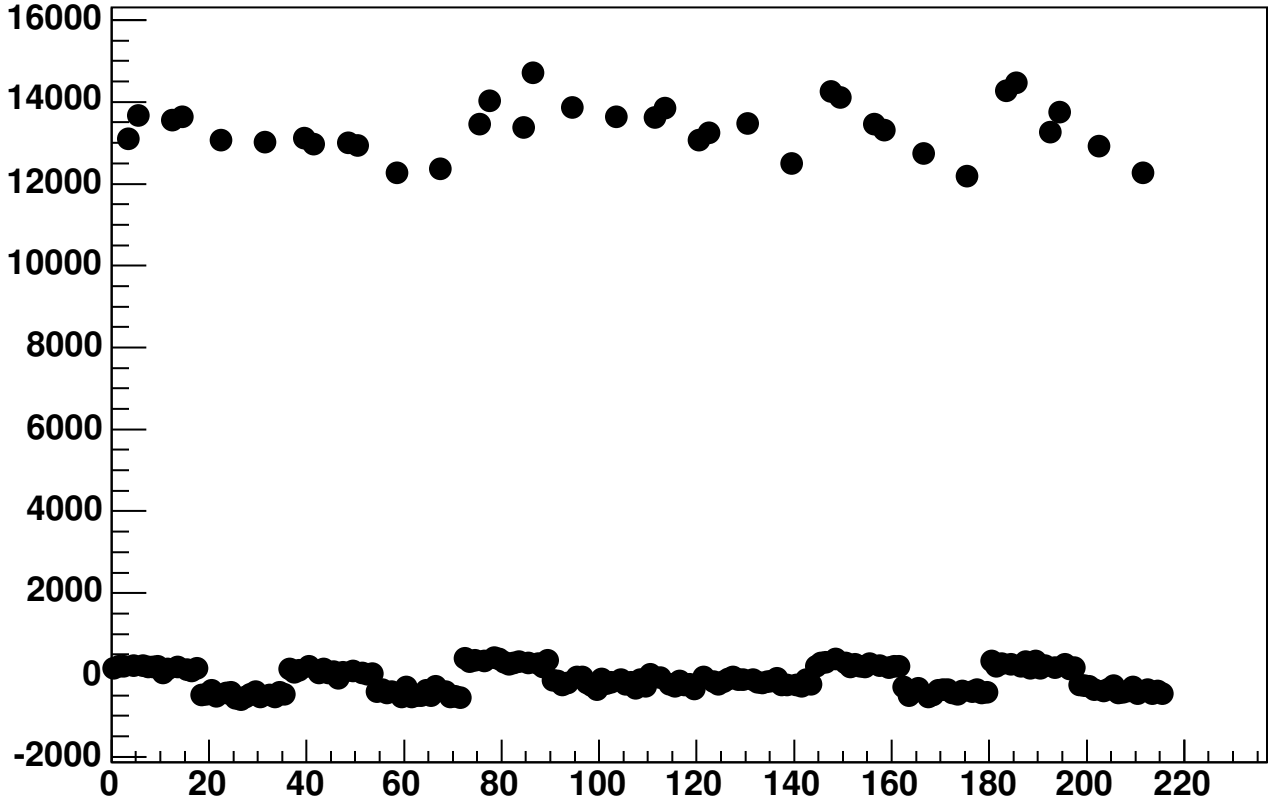
Enable 1, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



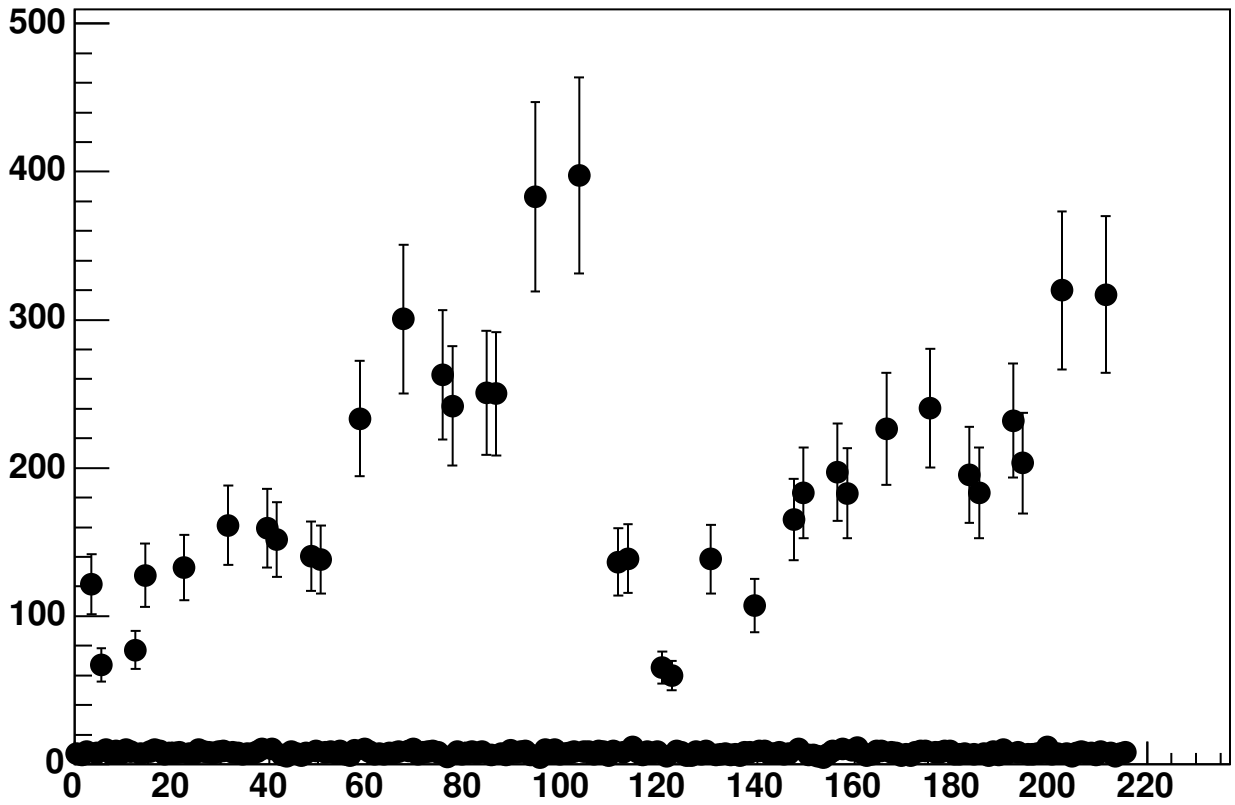
Enable 1, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



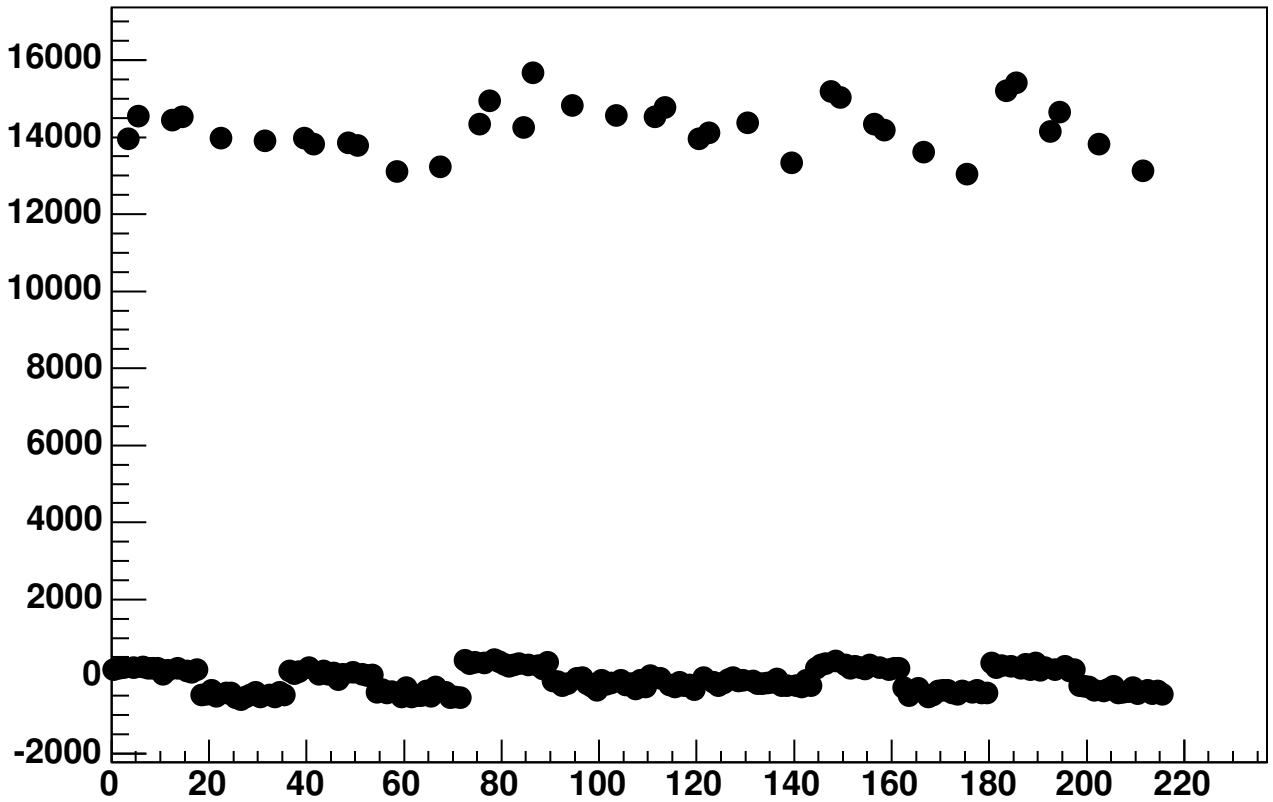
Enable 1, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



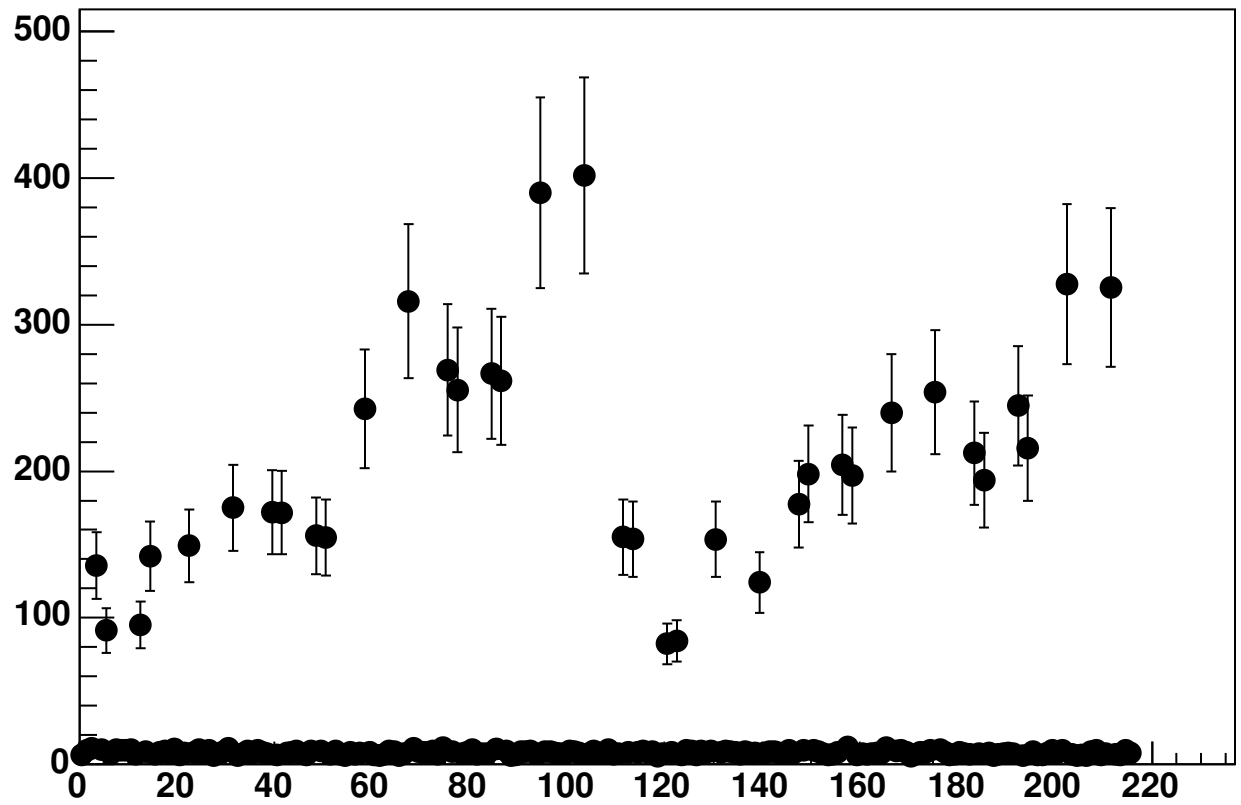
Enable 1, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



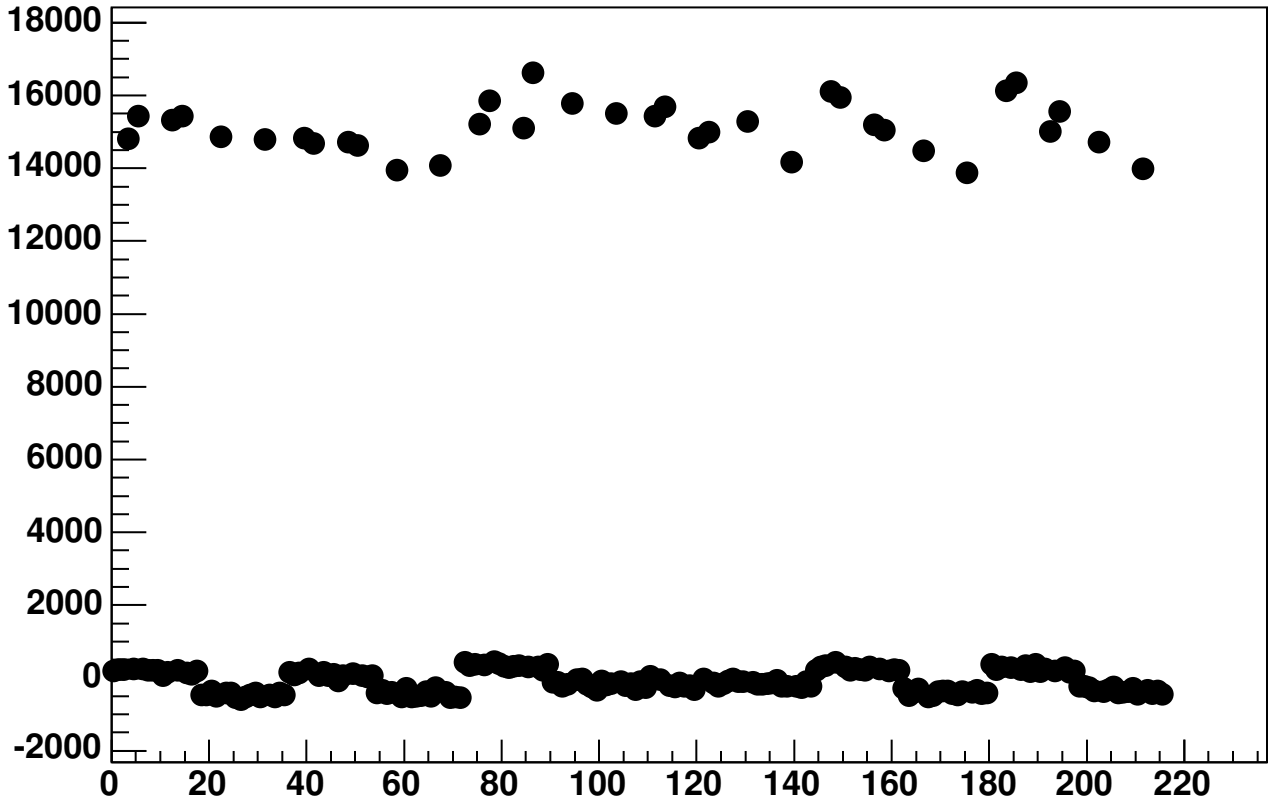
Enable 1, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



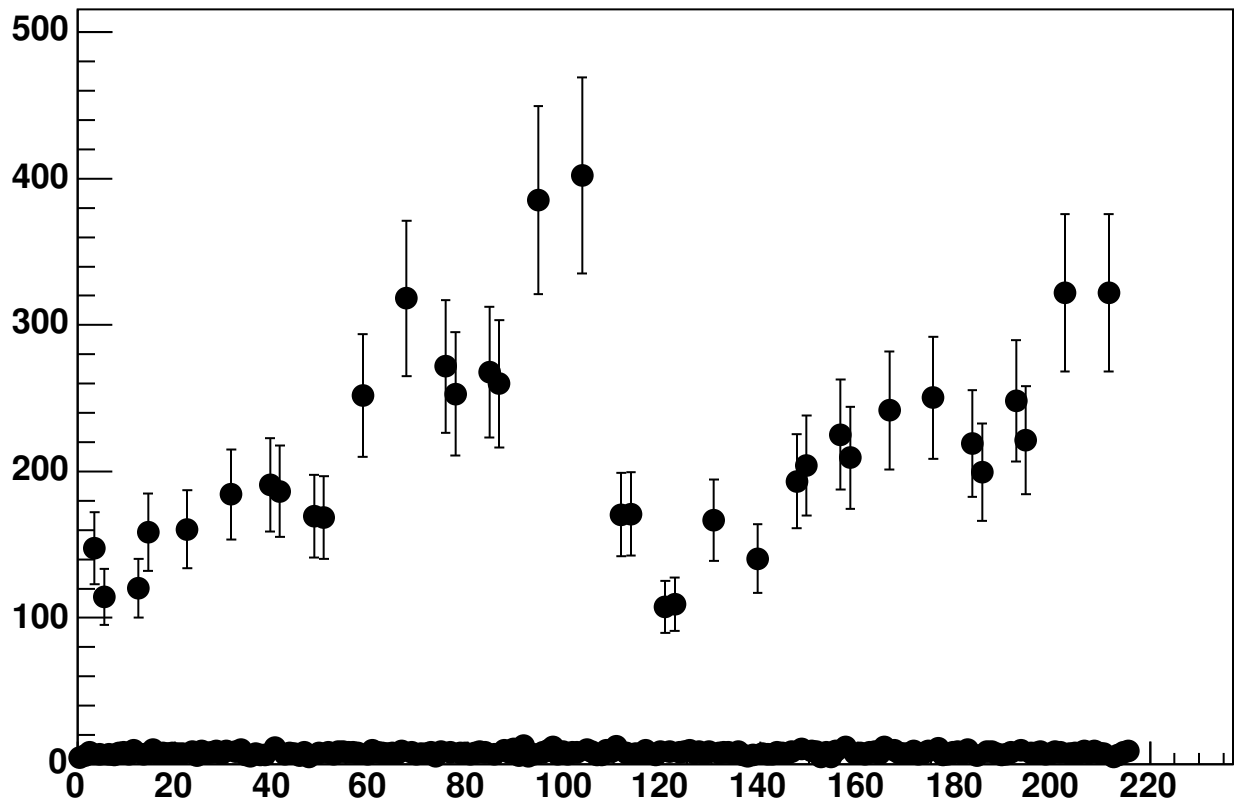
Enable 1, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



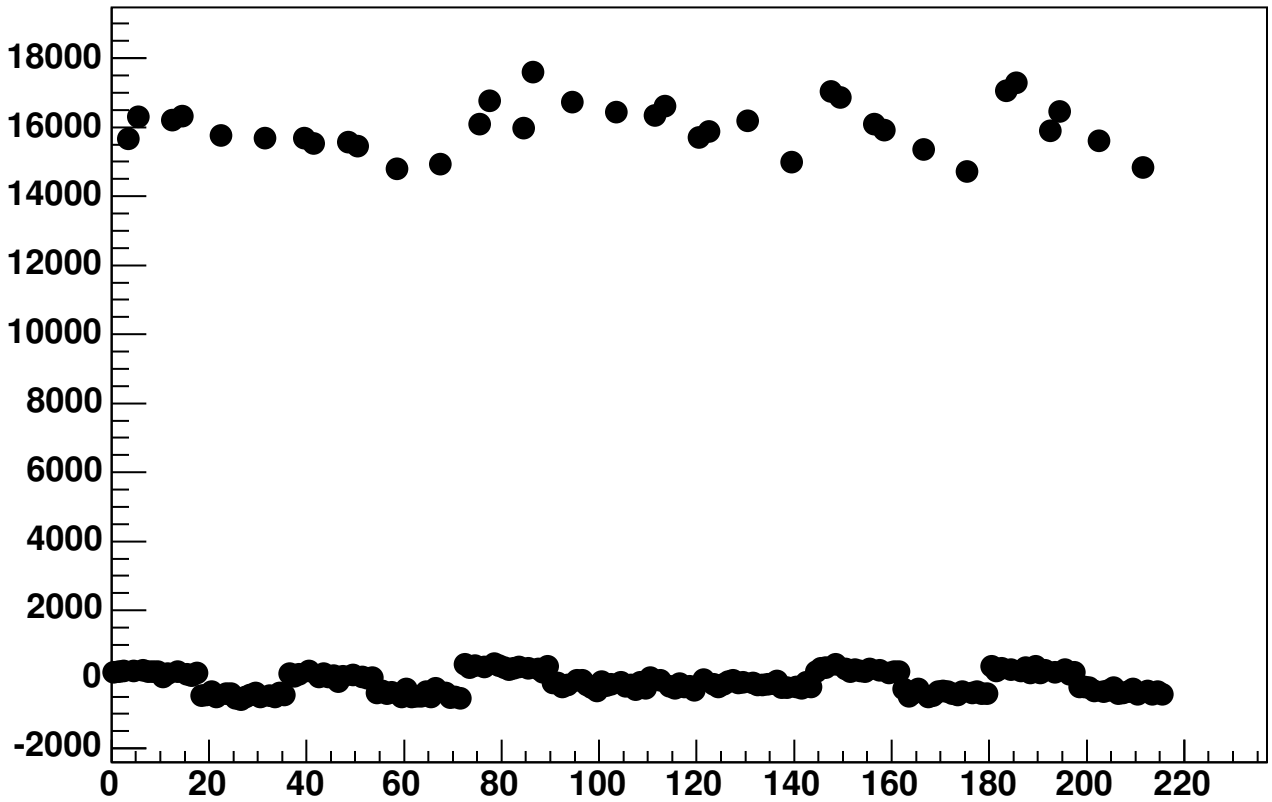
Enable 1, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



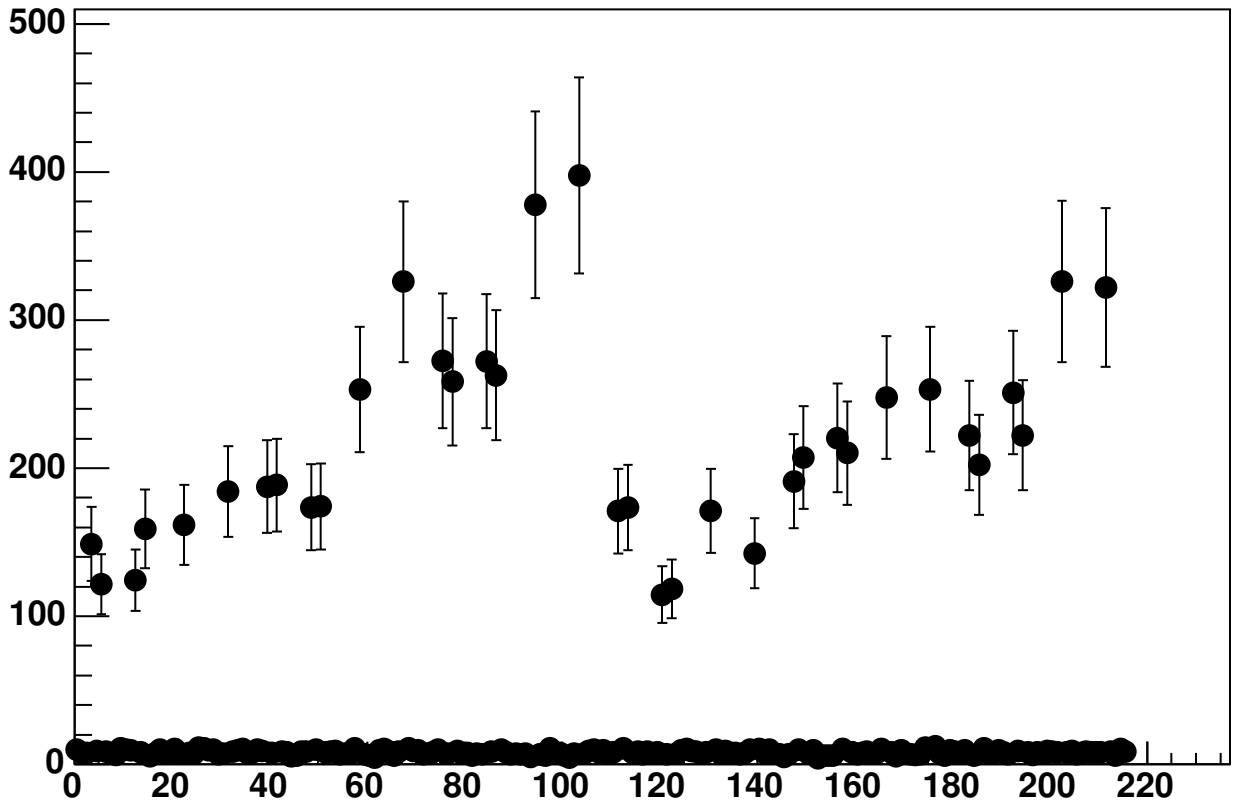
Enable 1, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



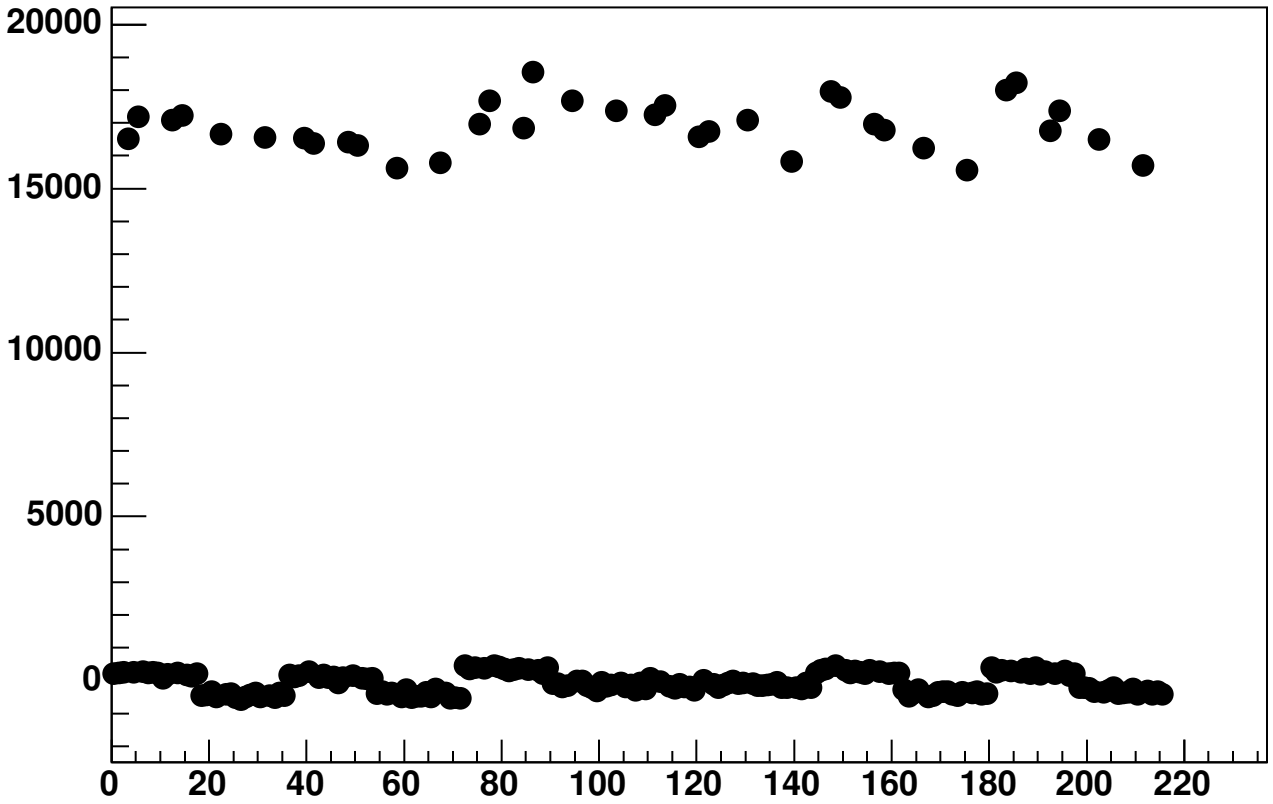
Enable 1, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



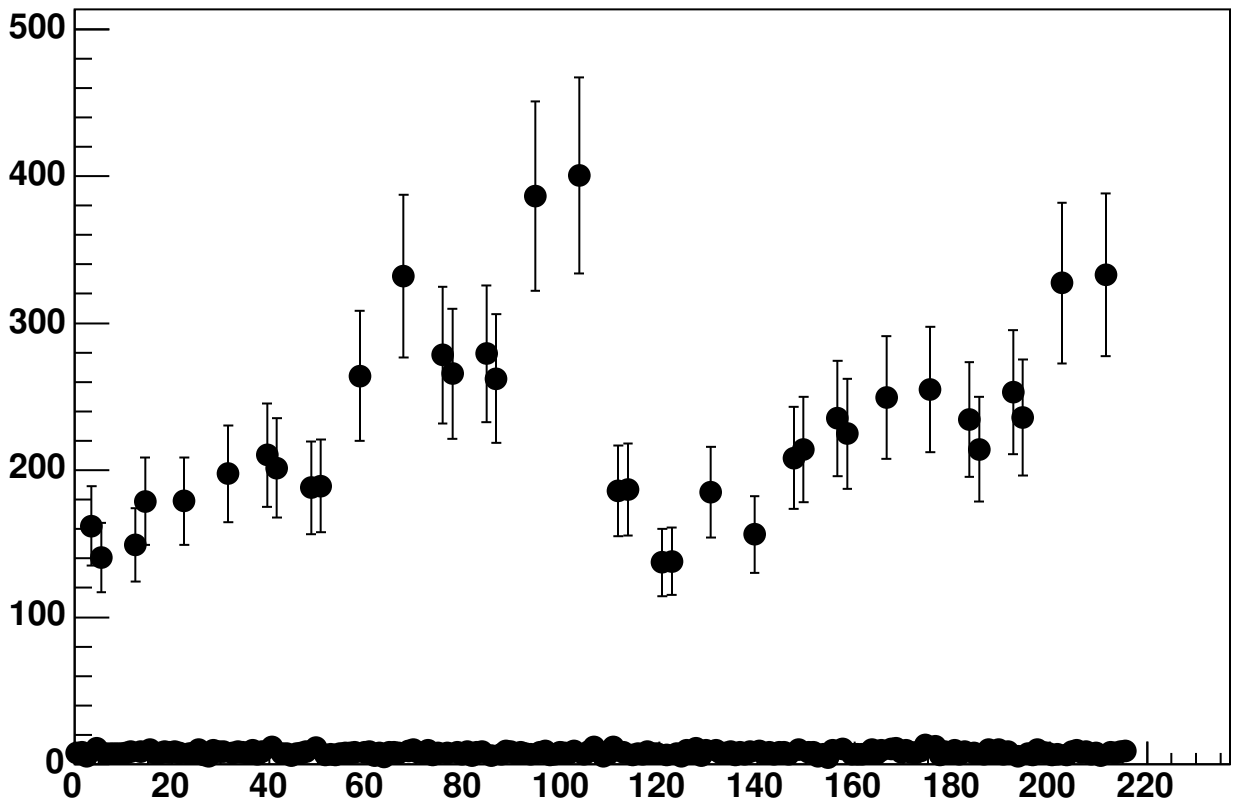
Enable 1, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



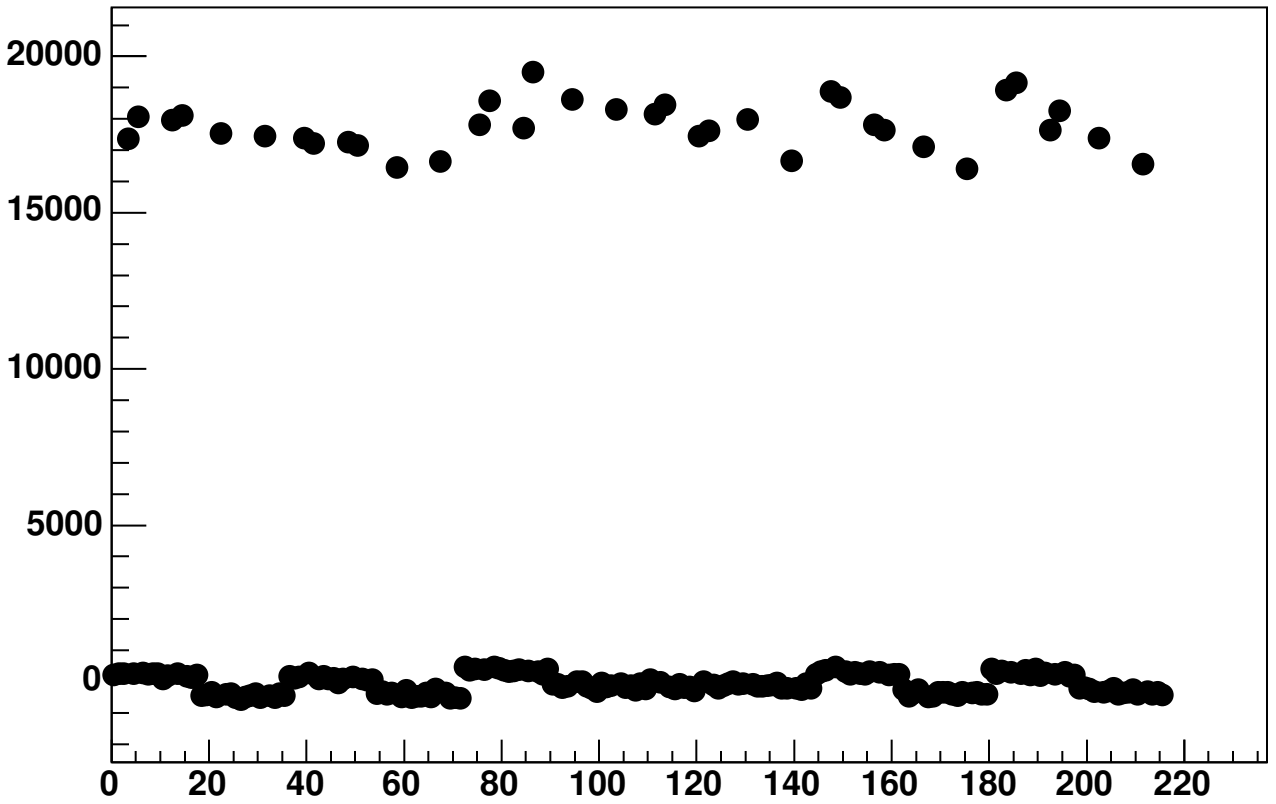
Enable 1, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



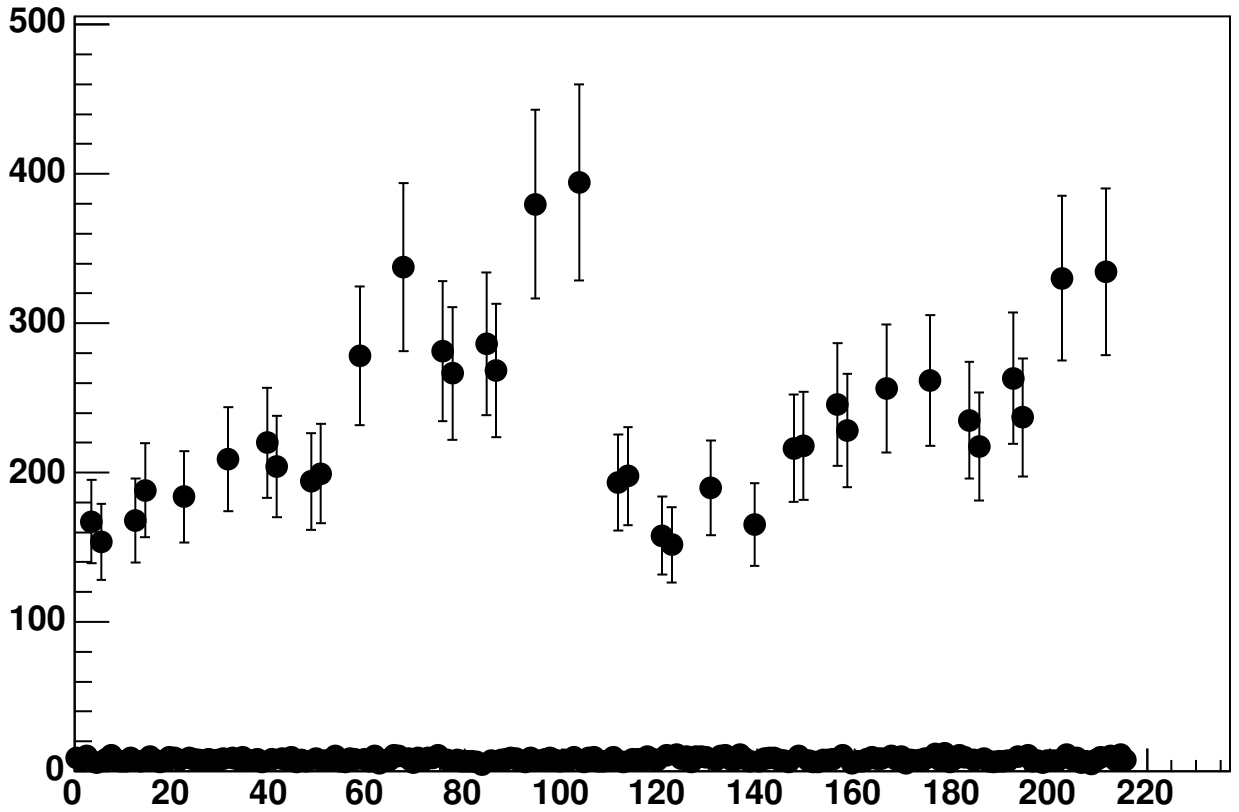
Enable 1, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



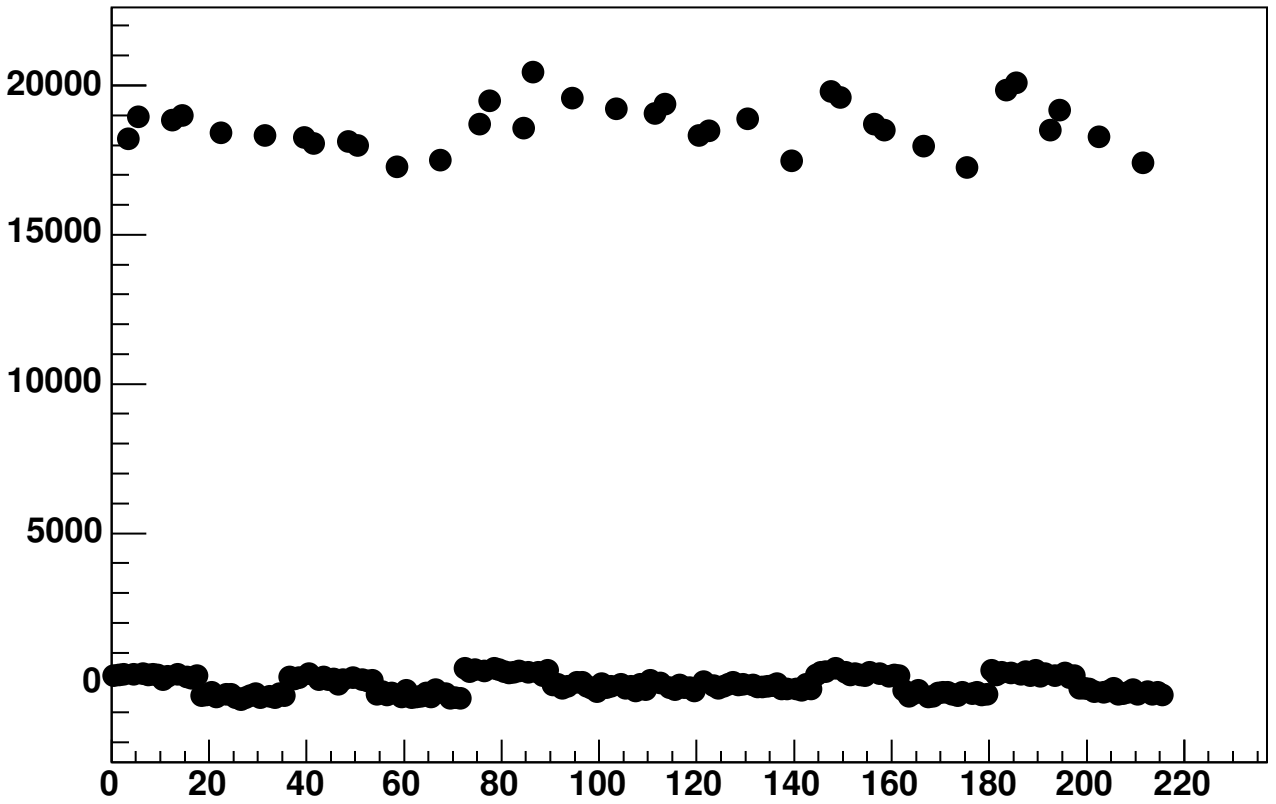
Enable 1, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



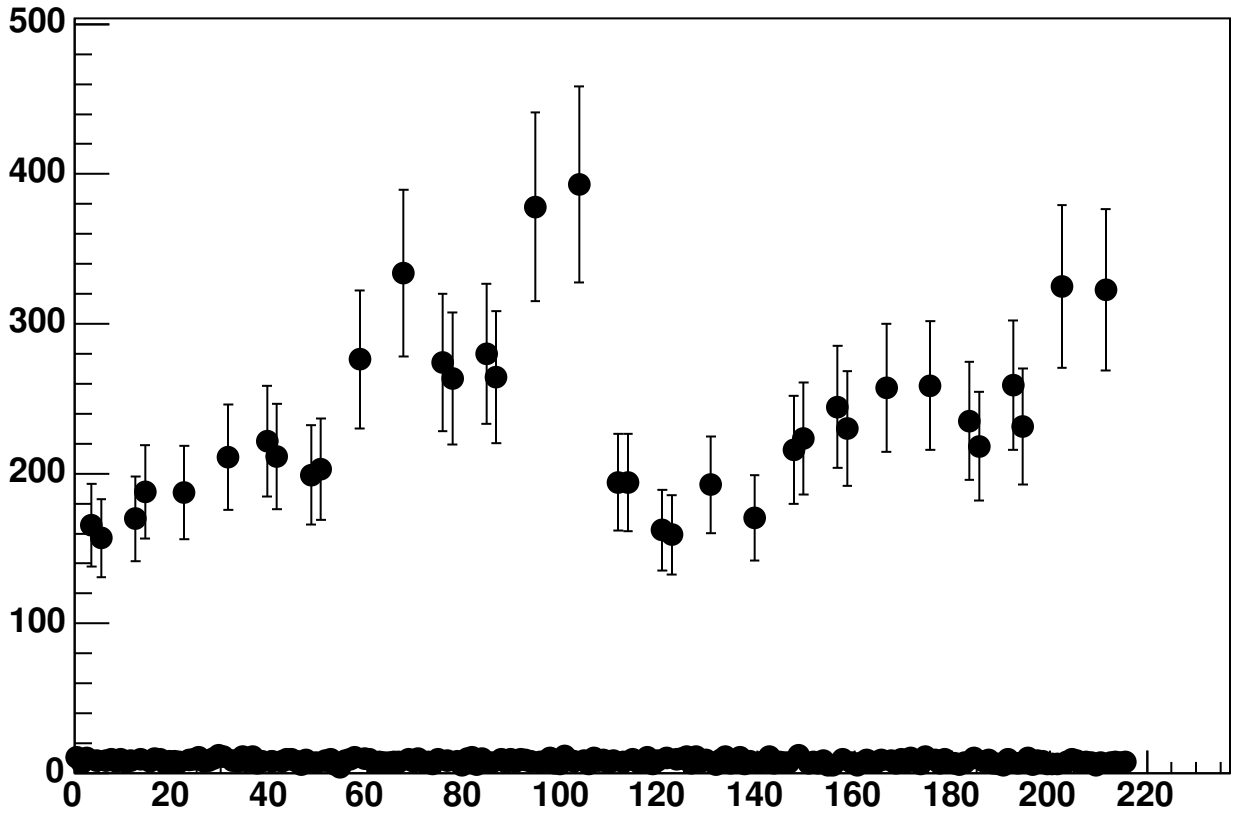
Enable 1, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



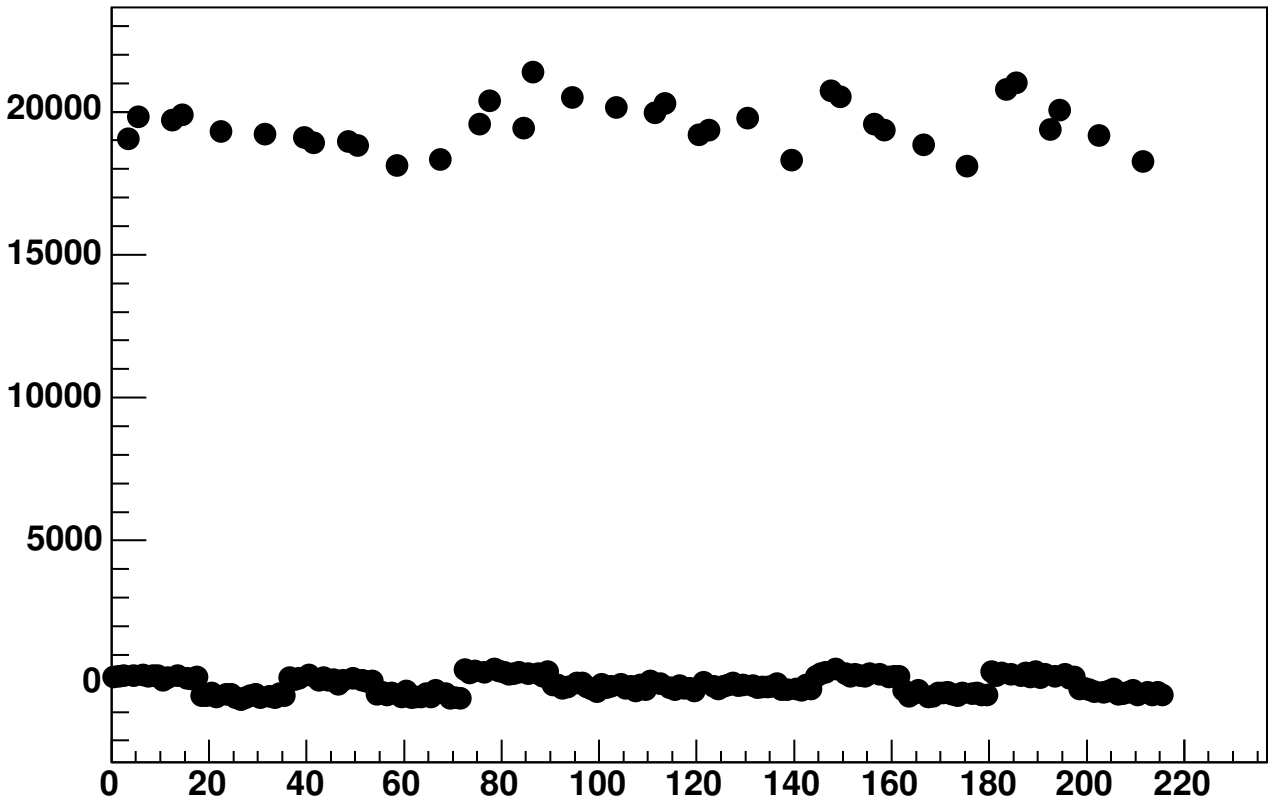
Enable 1, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



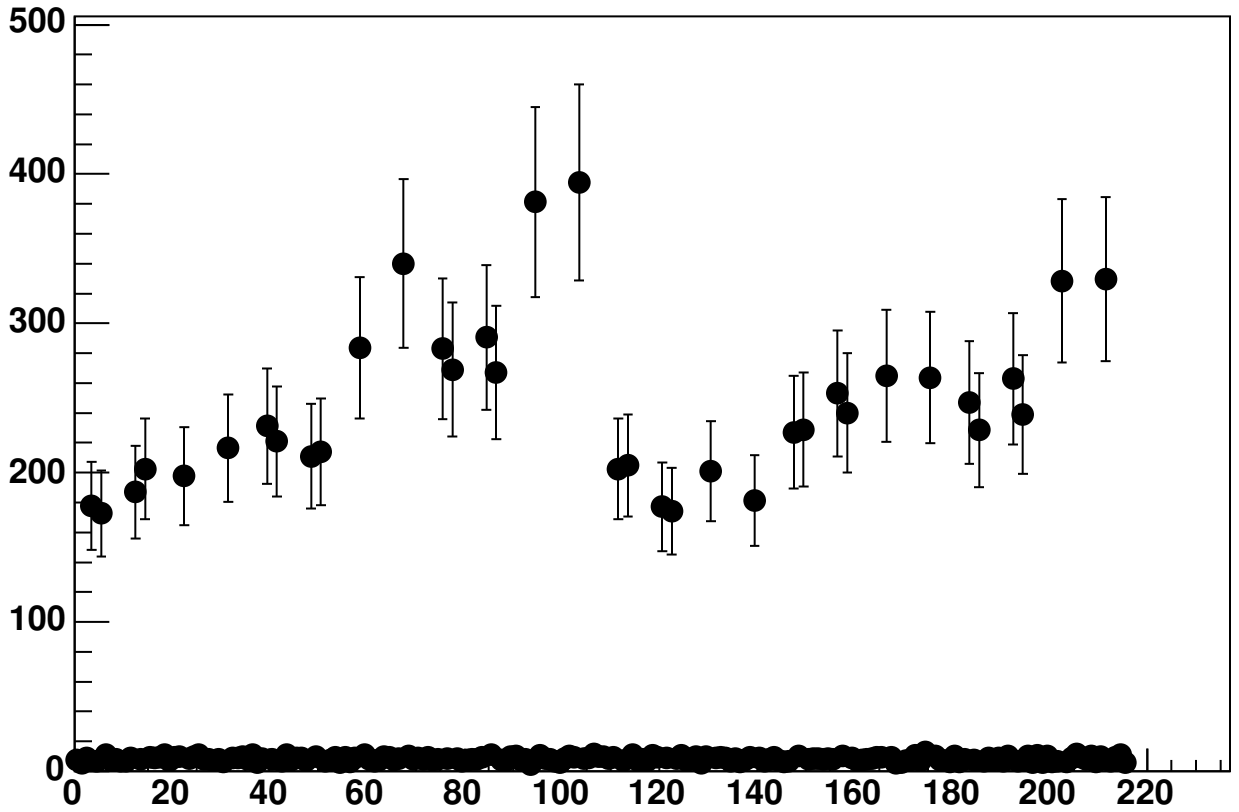
Enable 1, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



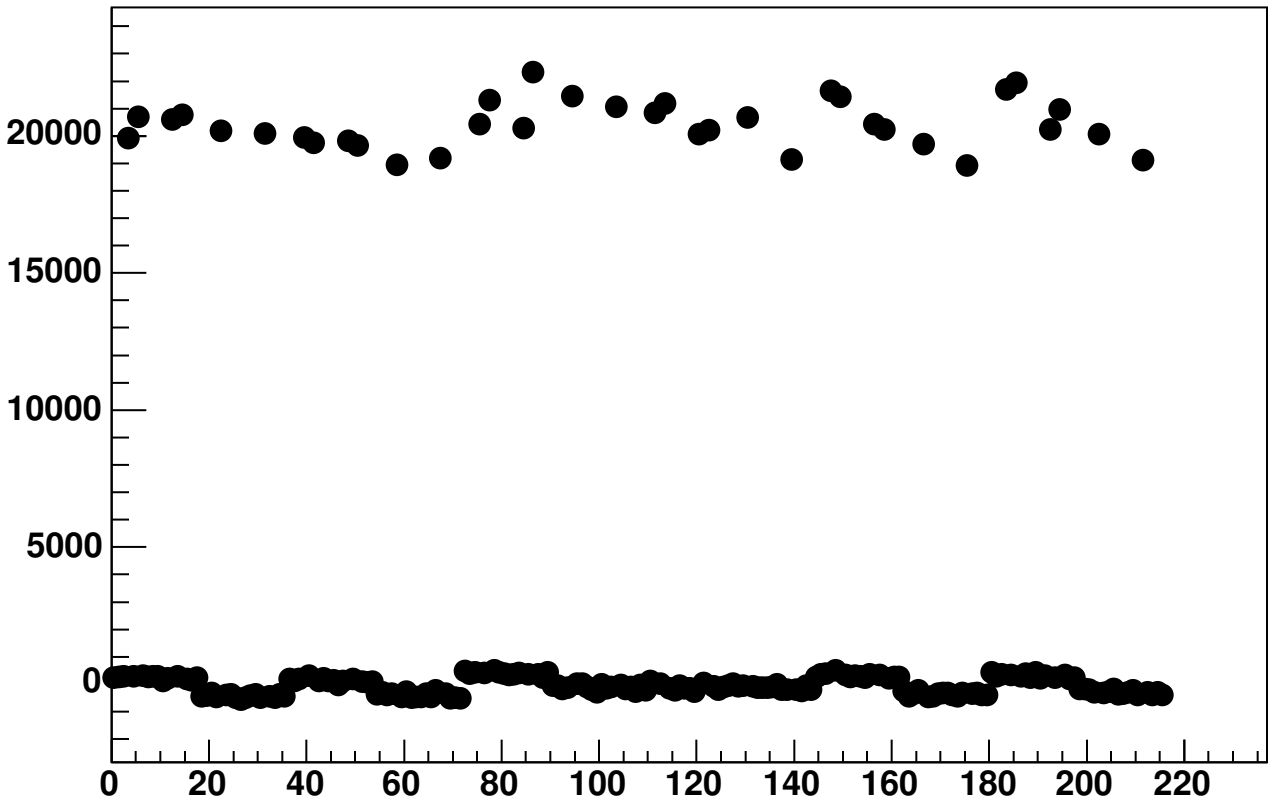
Enable 1, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



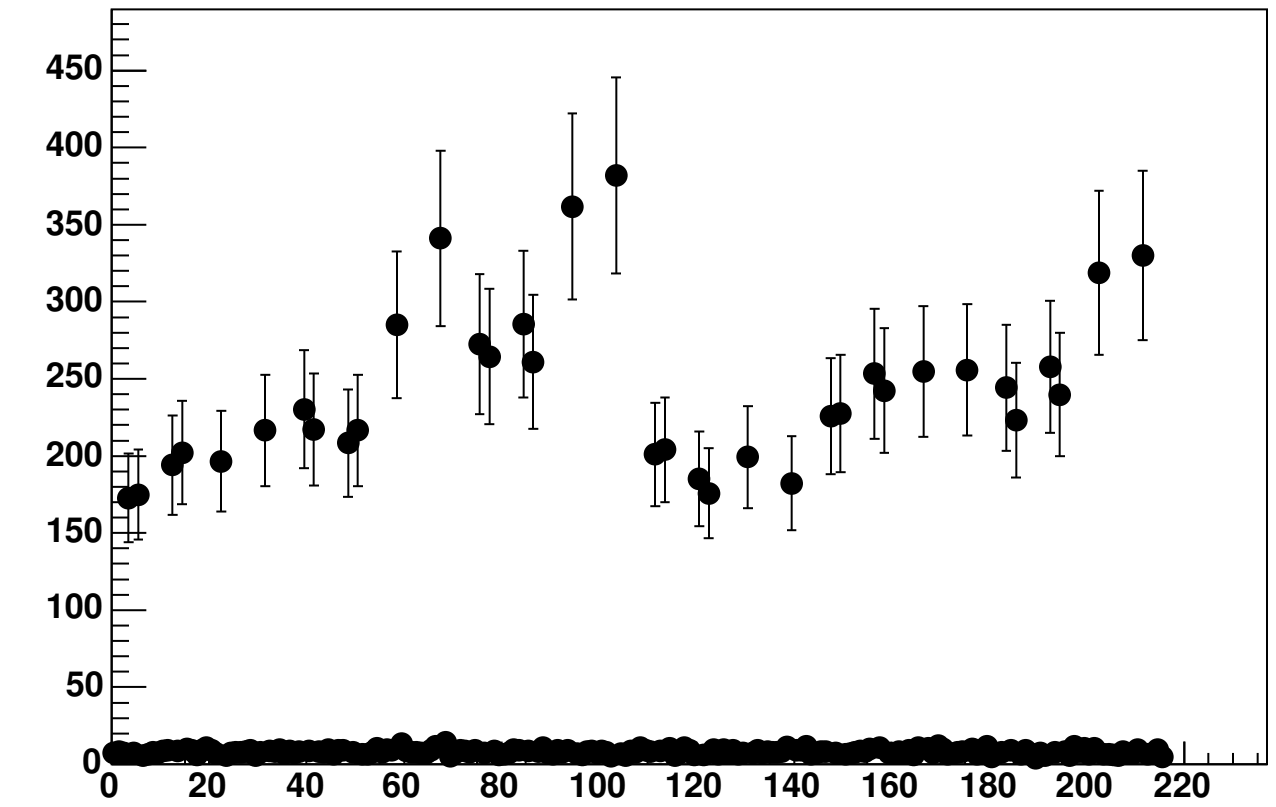
Enable 1, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



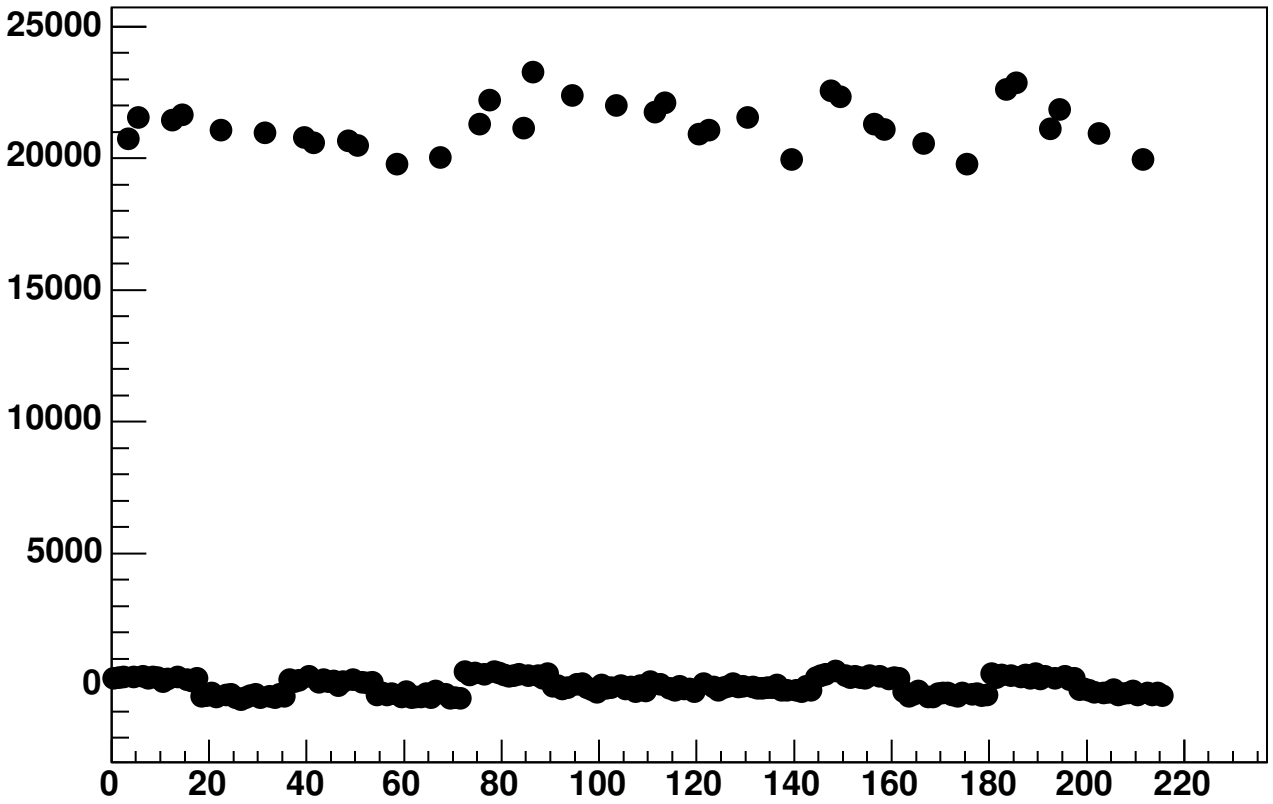
Enable 1, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



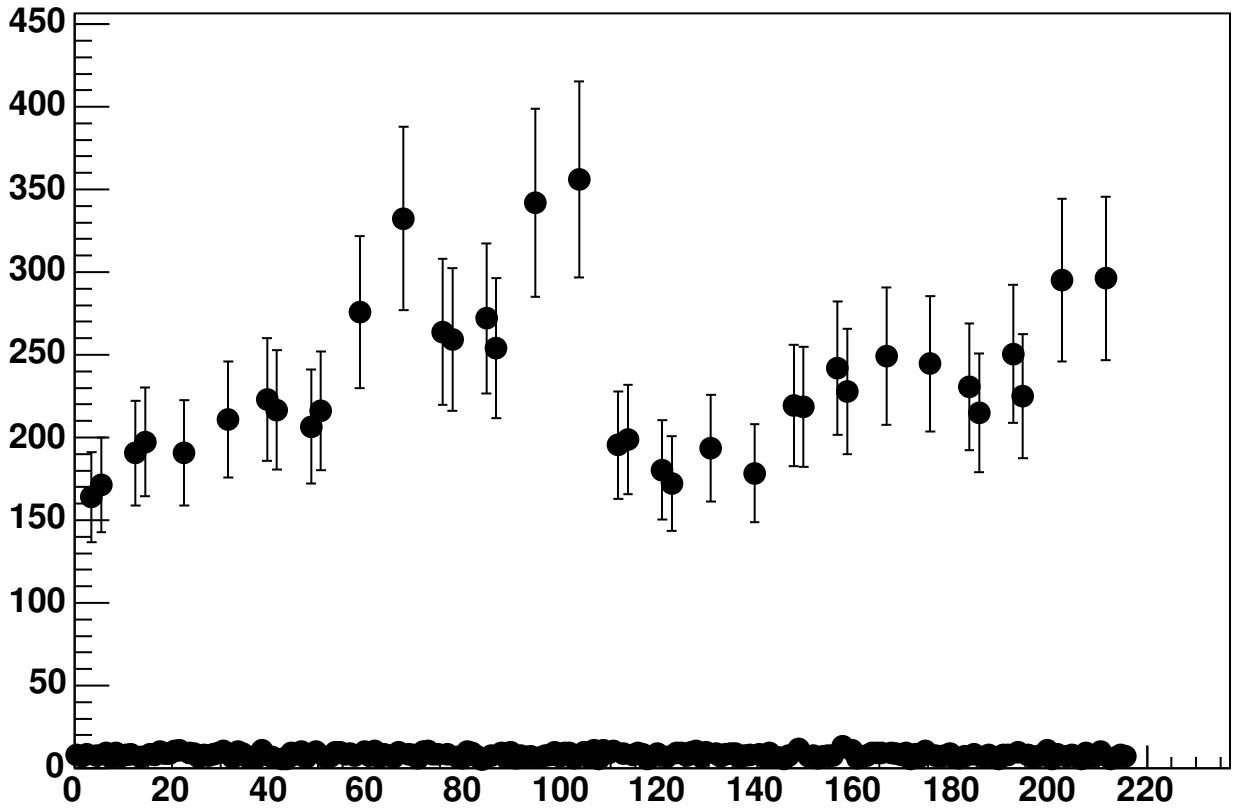
Enable 1, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



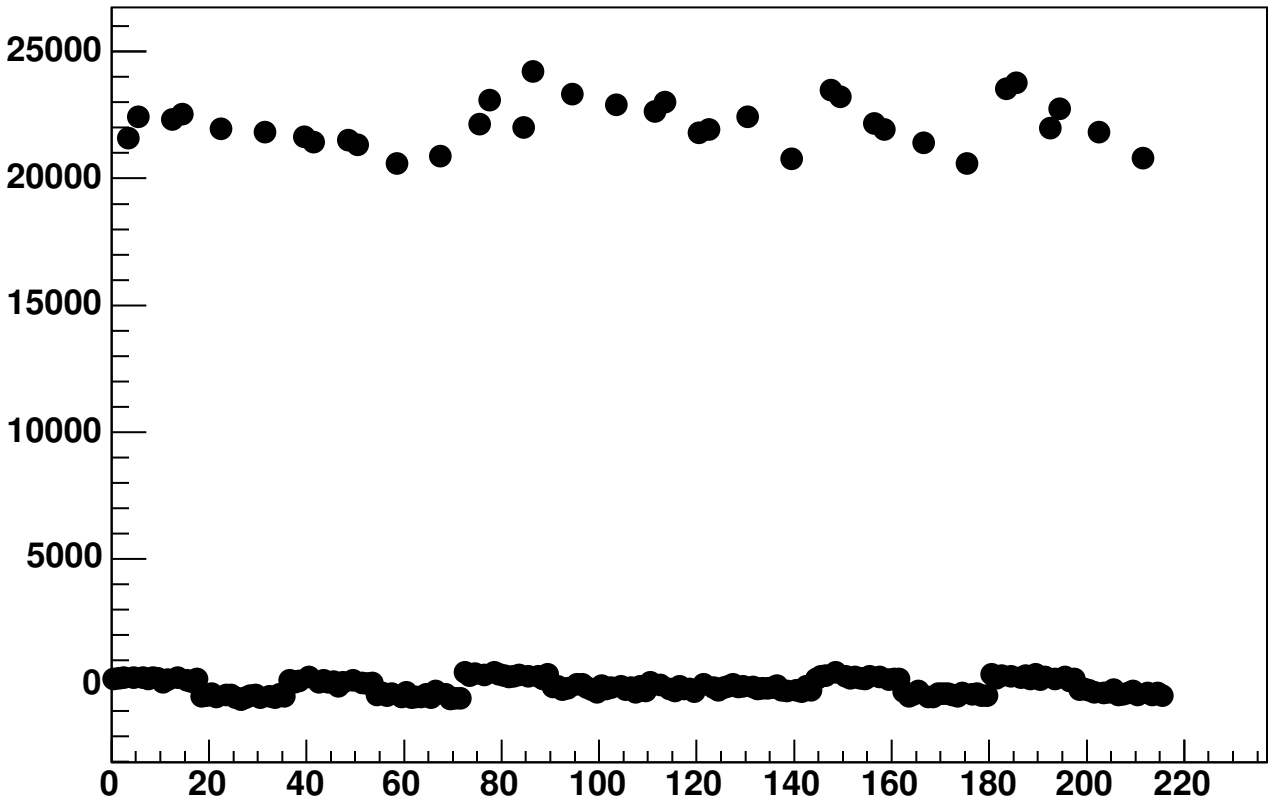
Enable 1, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



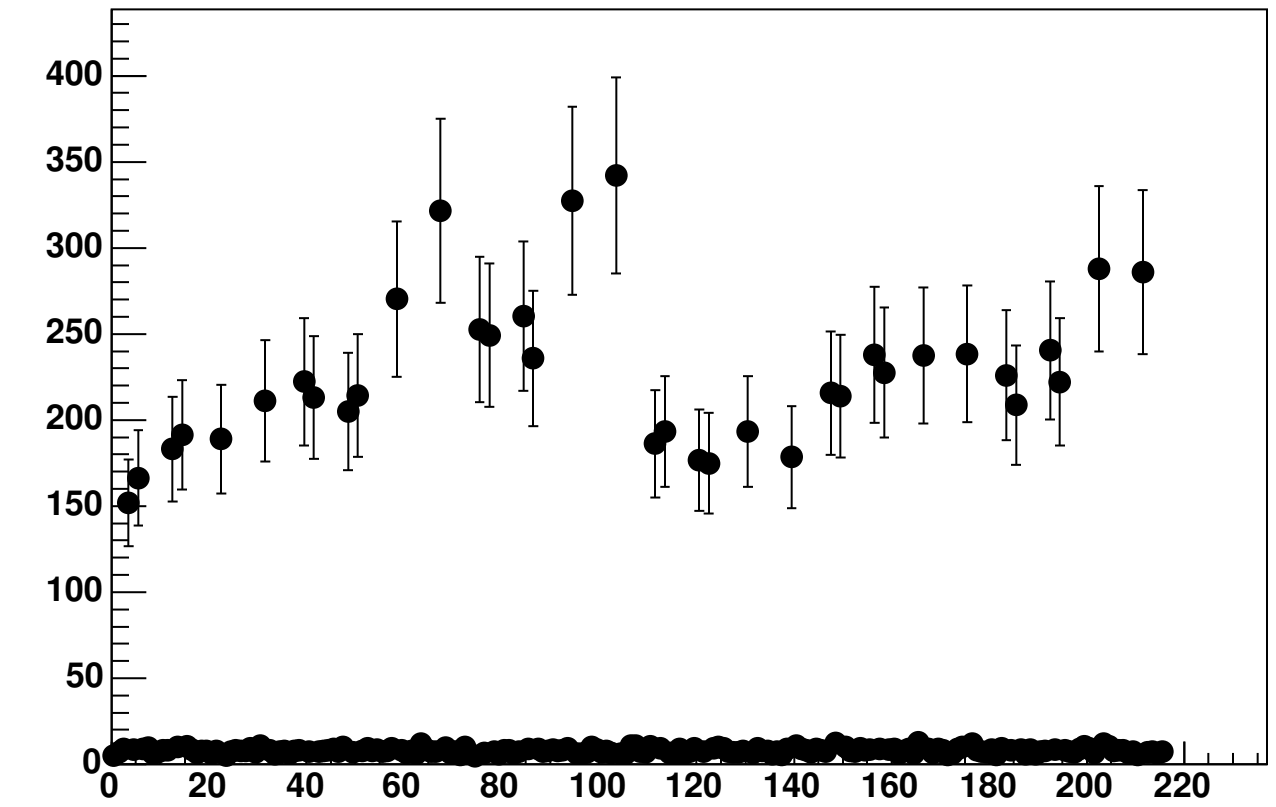
Enable 1, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



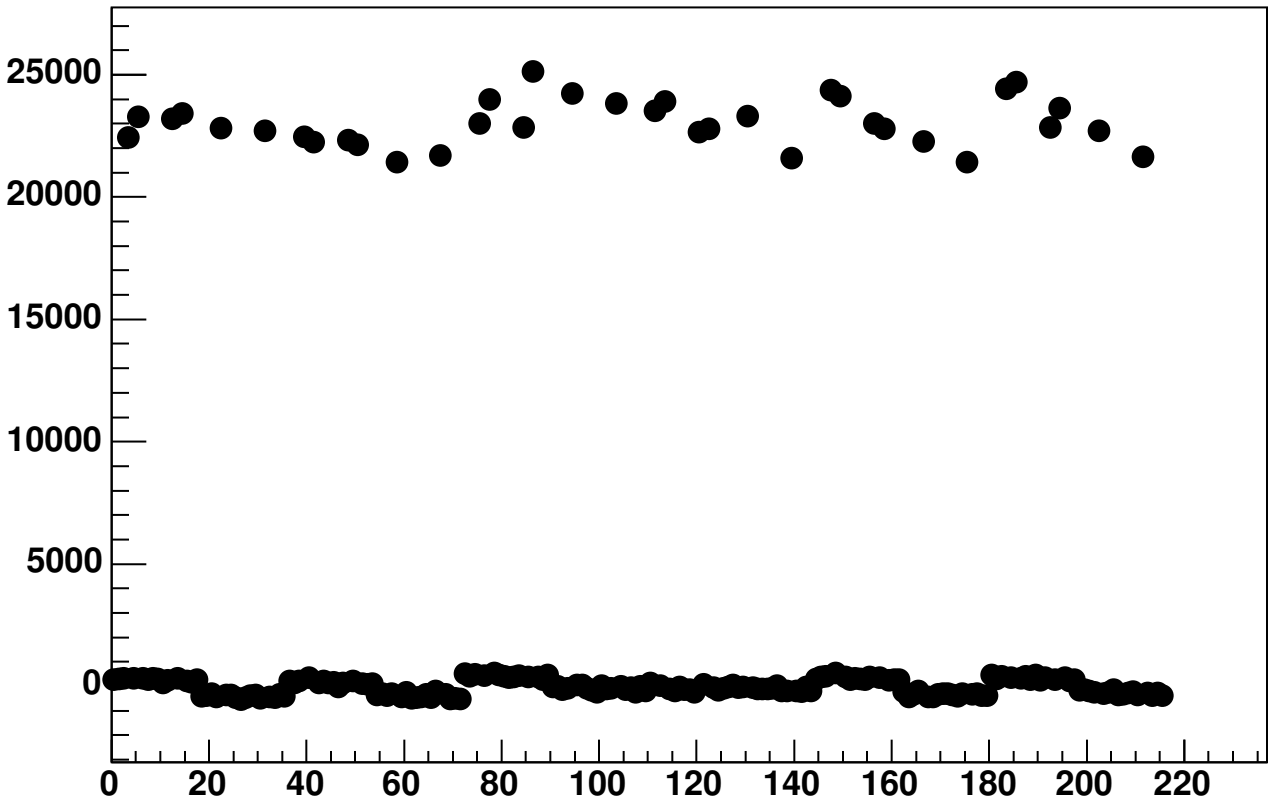
Enable 1, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



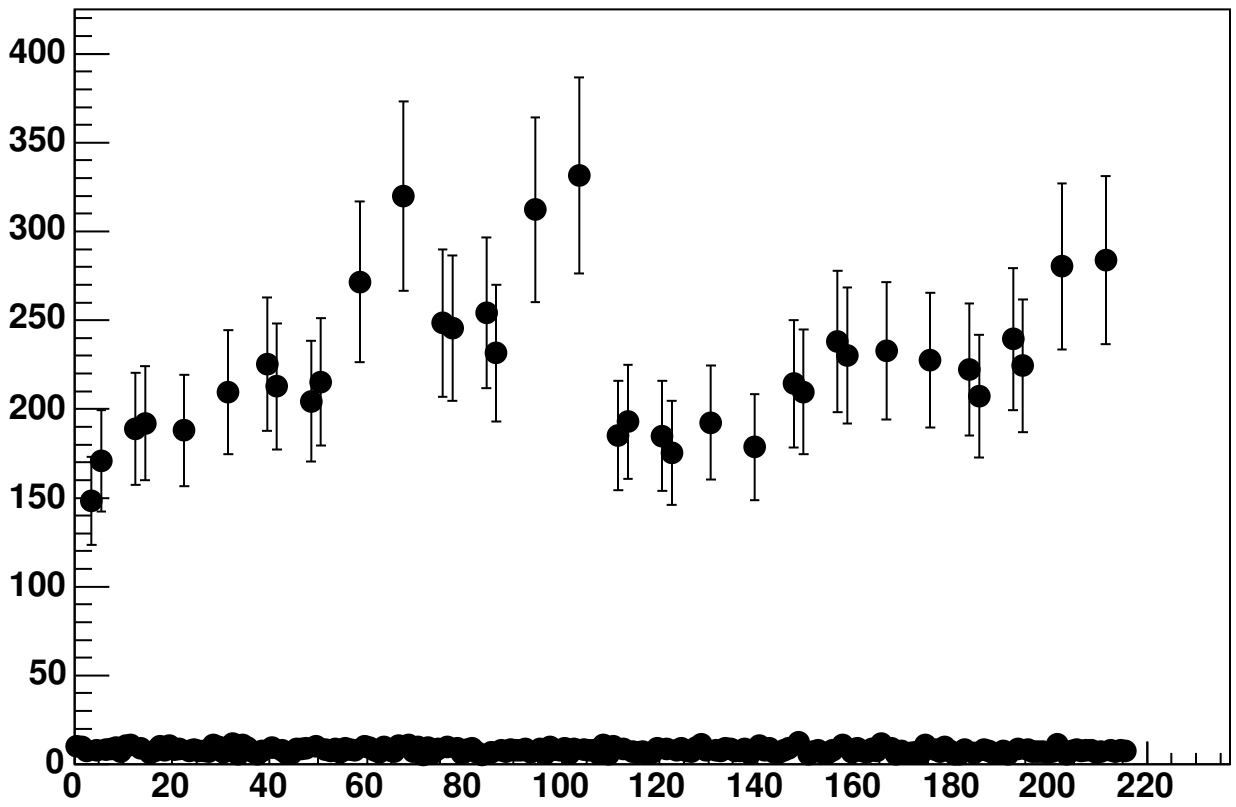
Enable 1, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



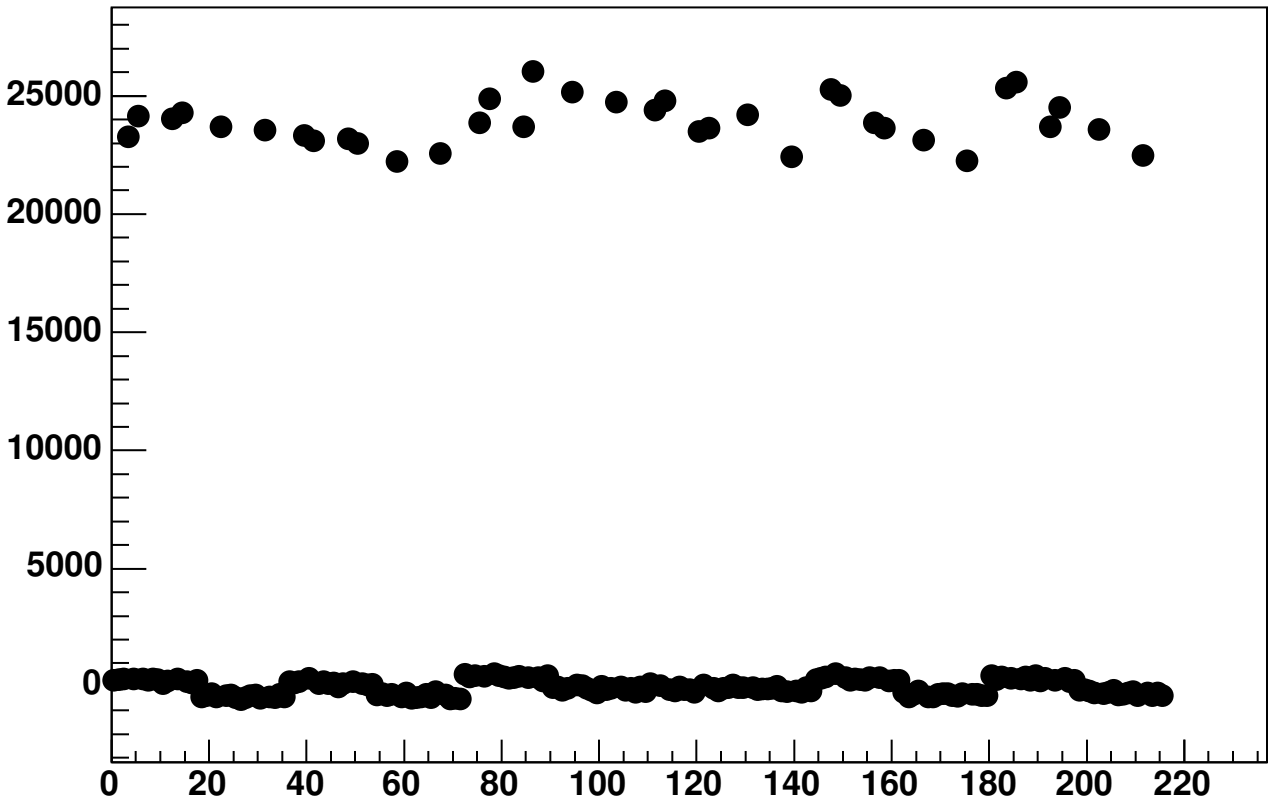
Enable 1, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



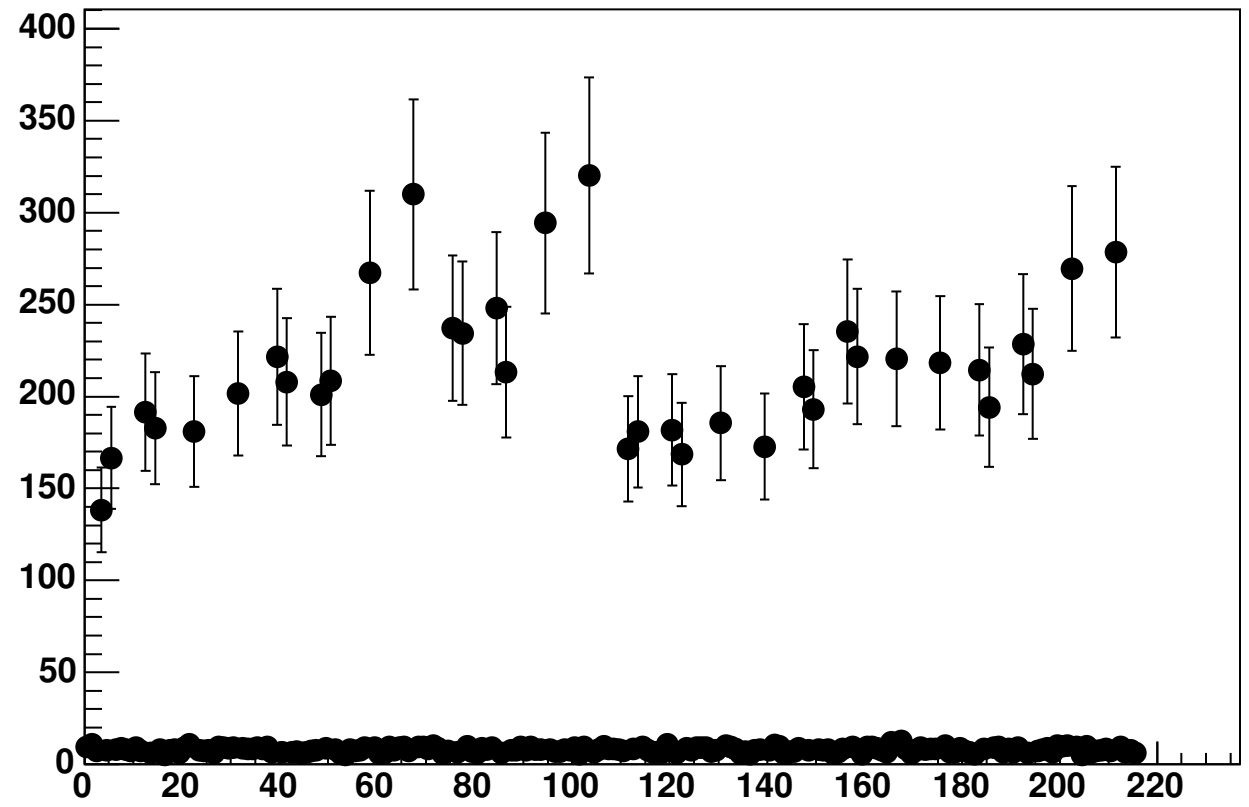
Enable 1, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



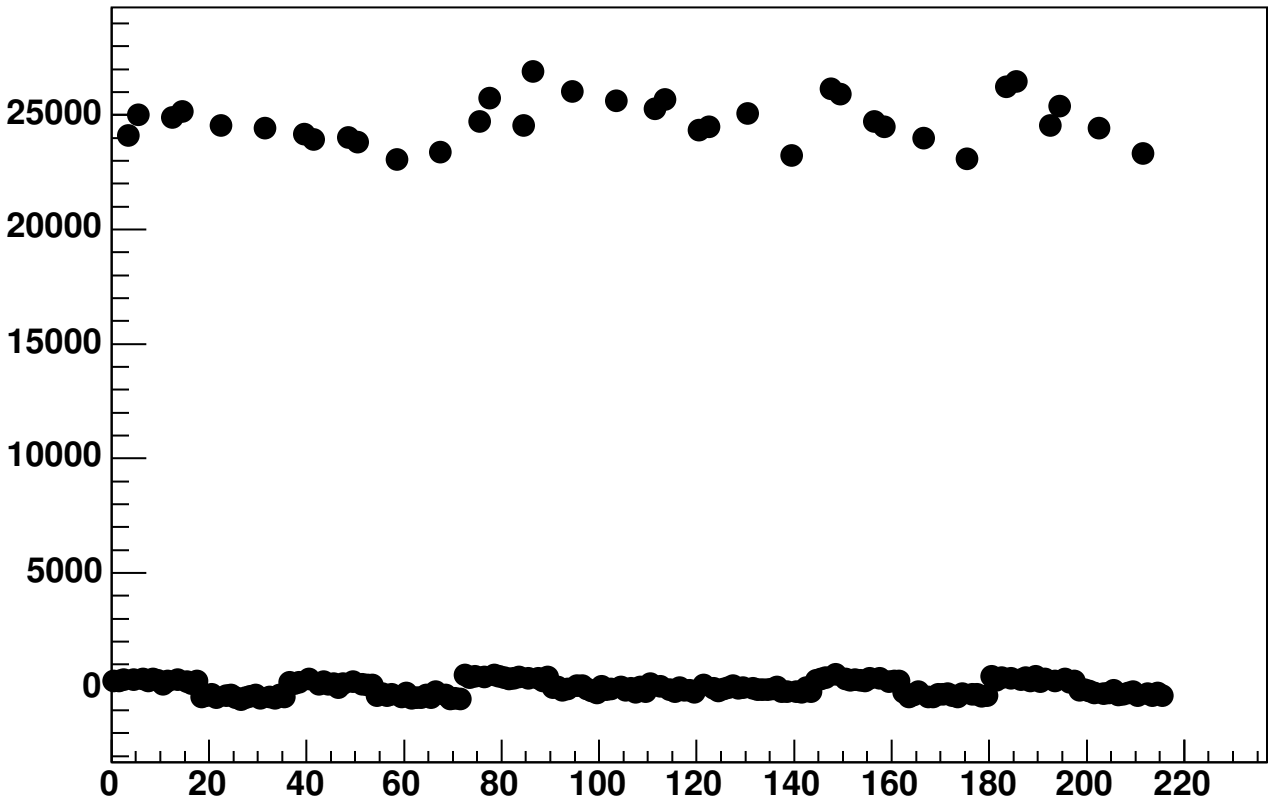
Enable 1, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



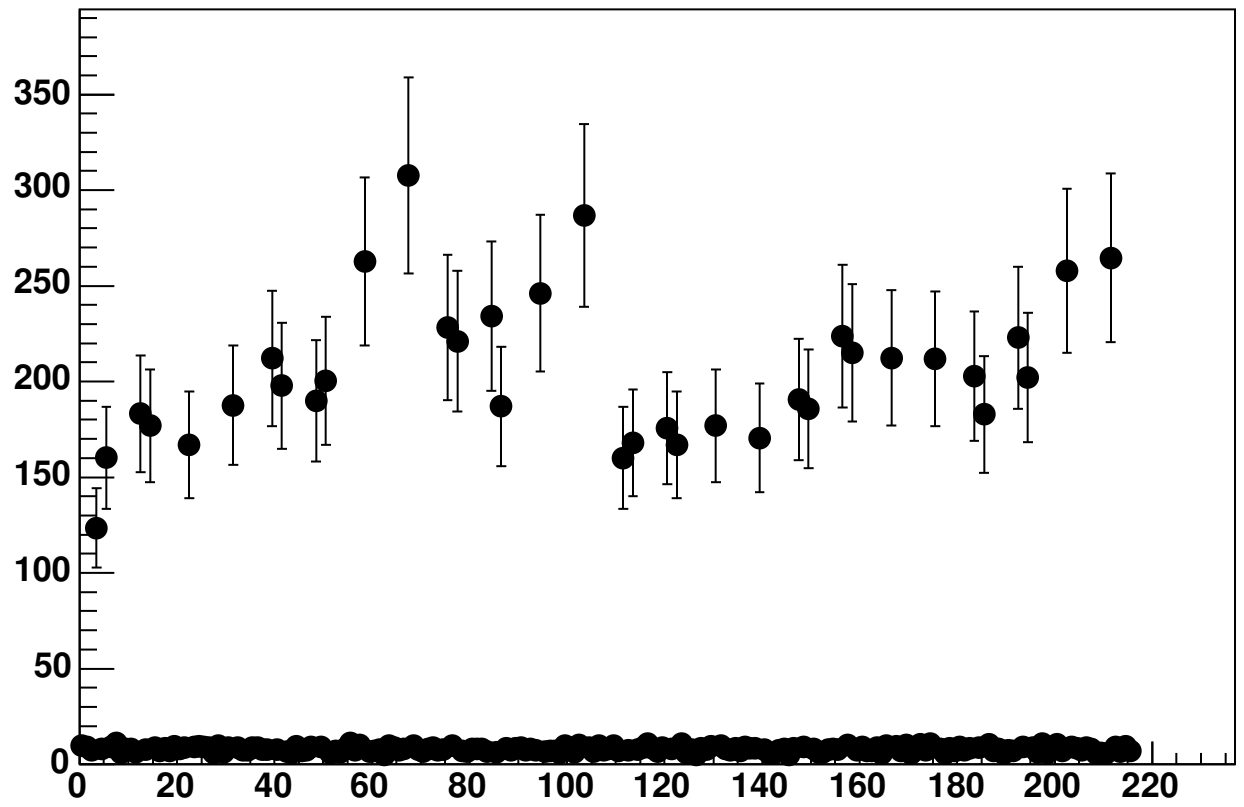
Enable 1, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



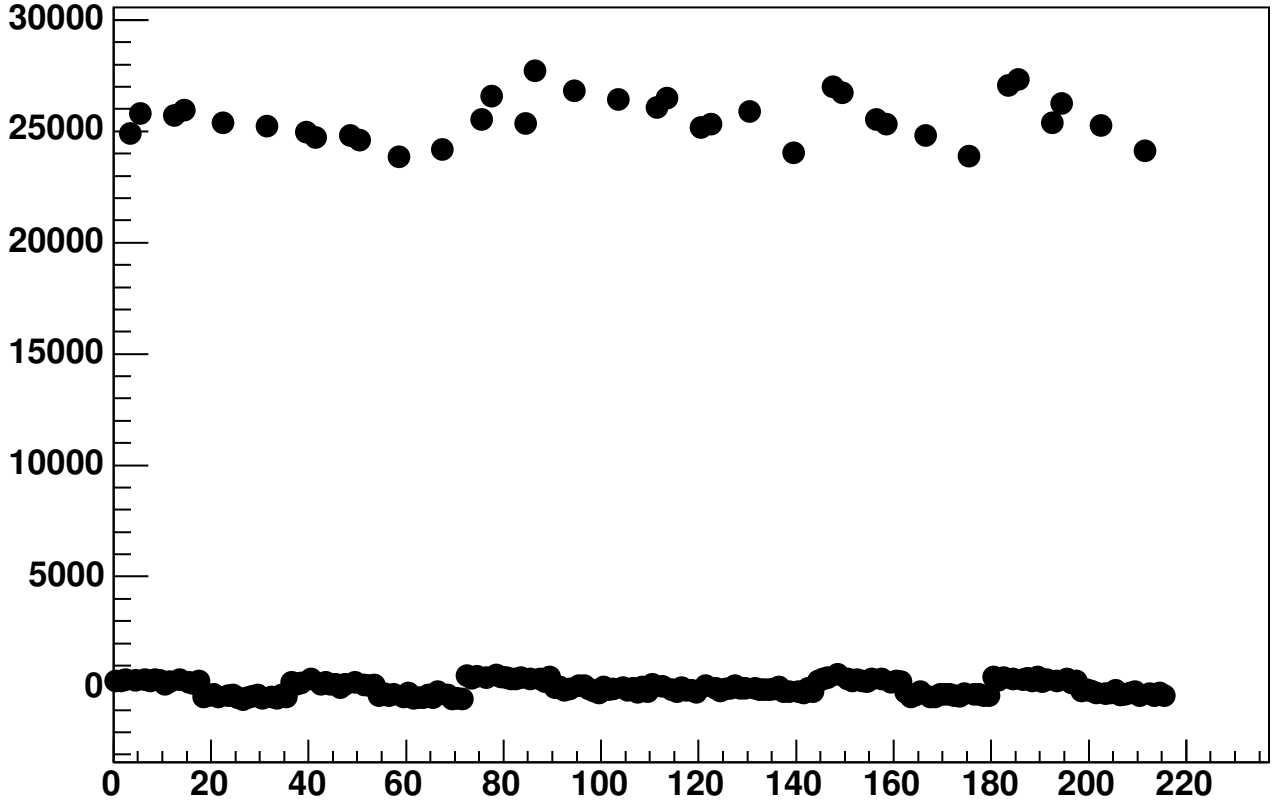
Enable 1, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



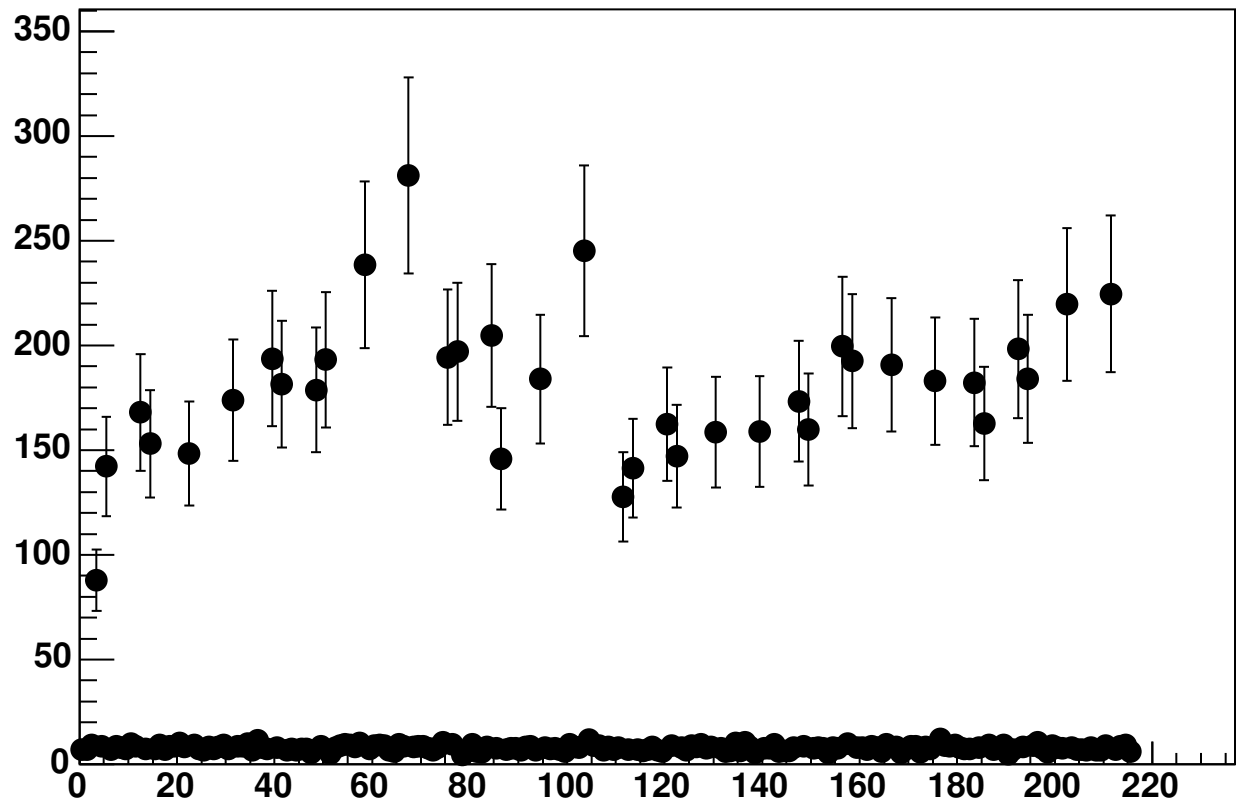
Enable 1, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



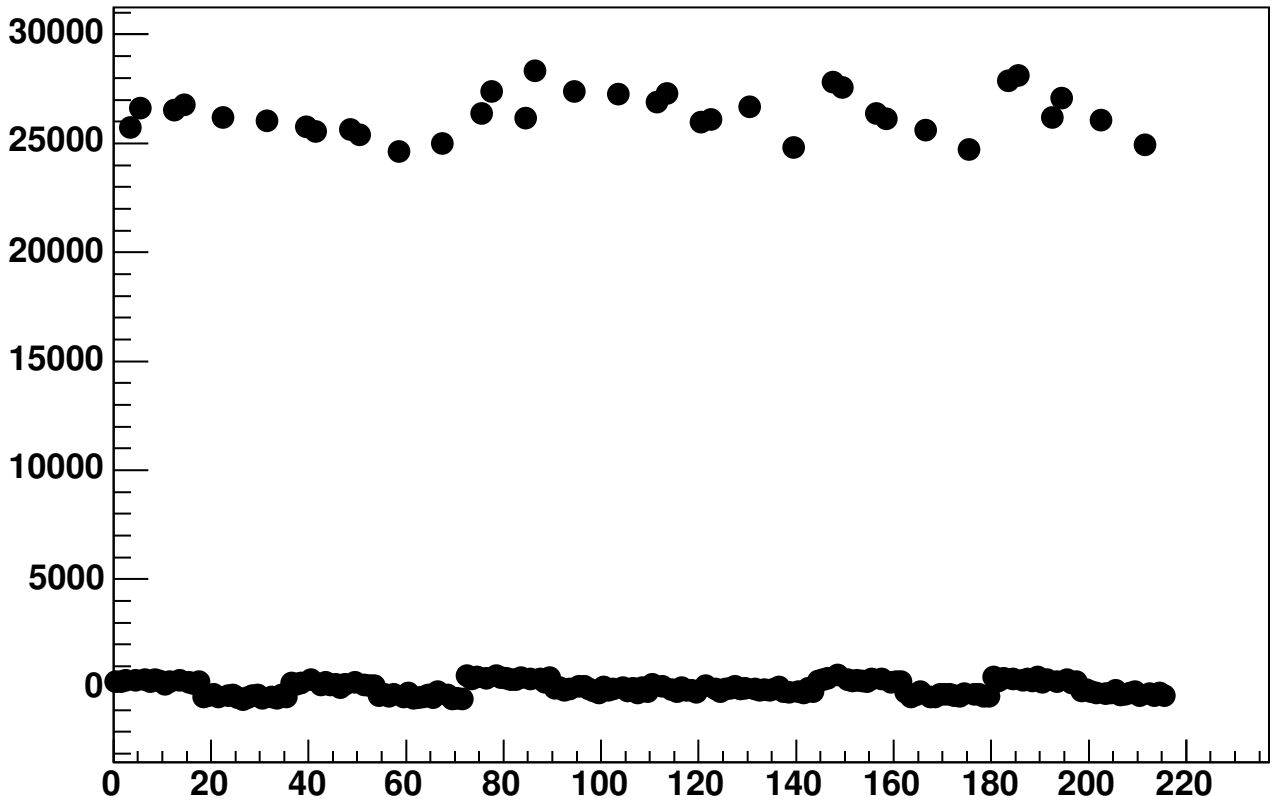
Enable 1, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



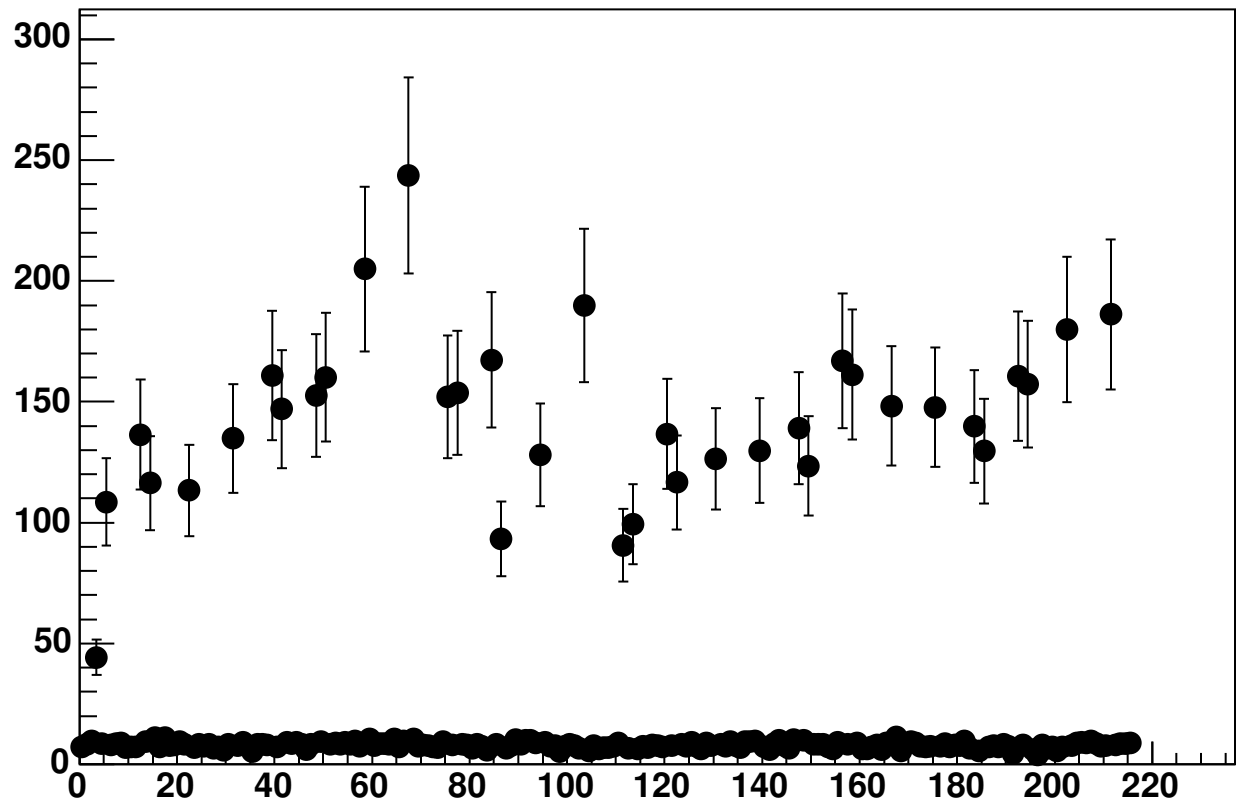
Enable 1, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



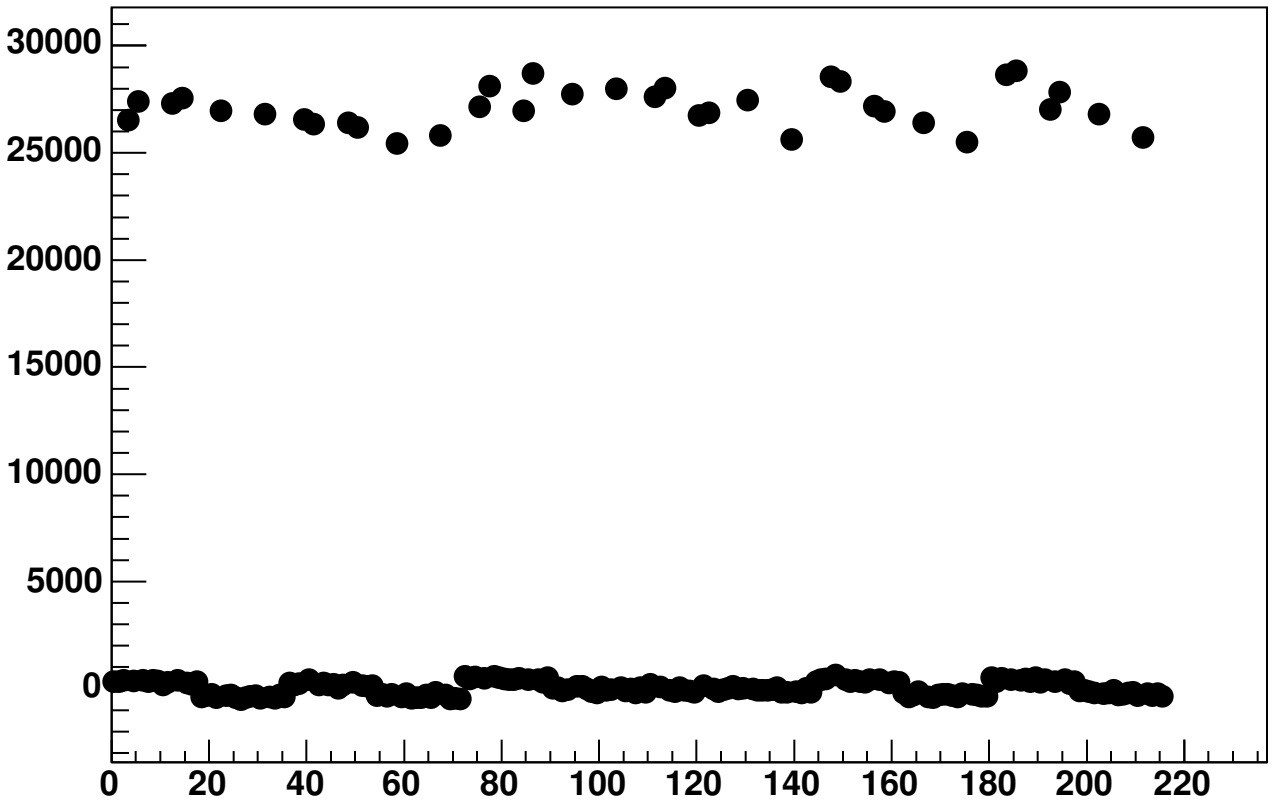
Enable 1, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



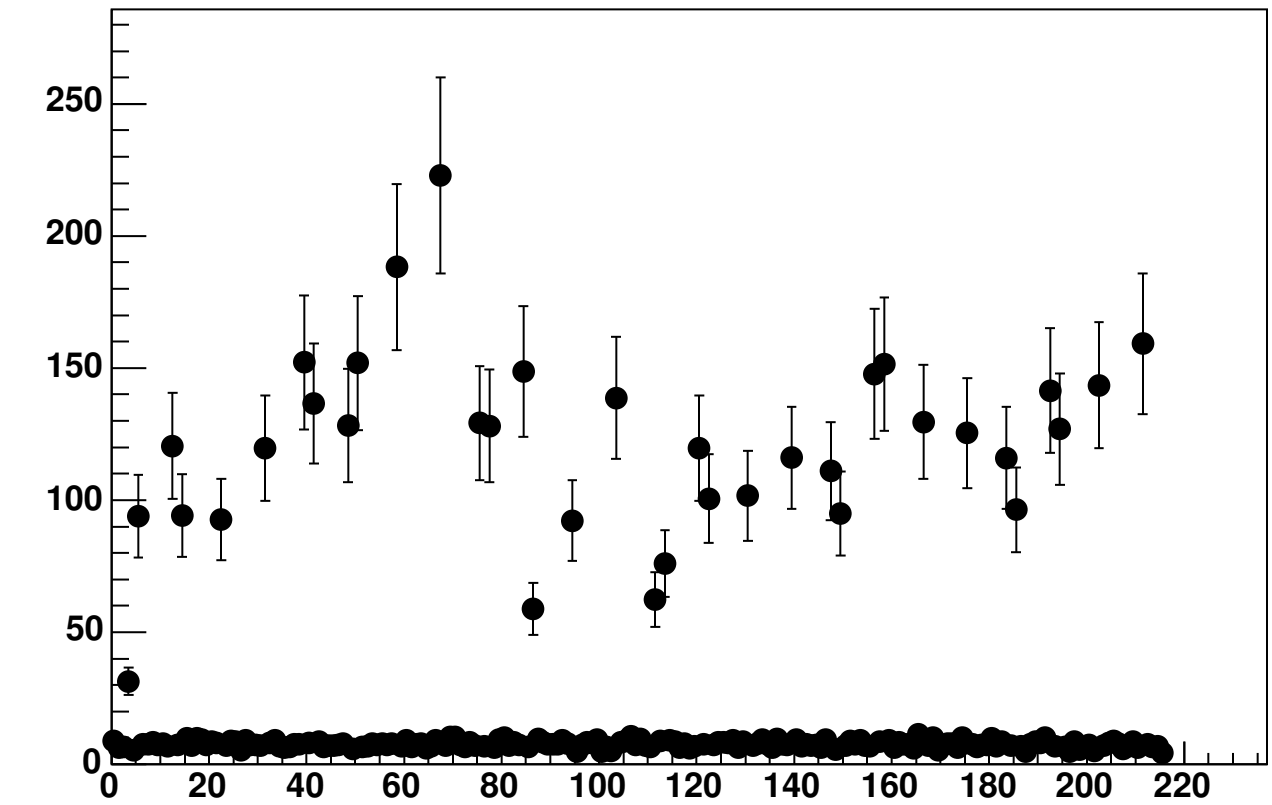
Enable 1, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



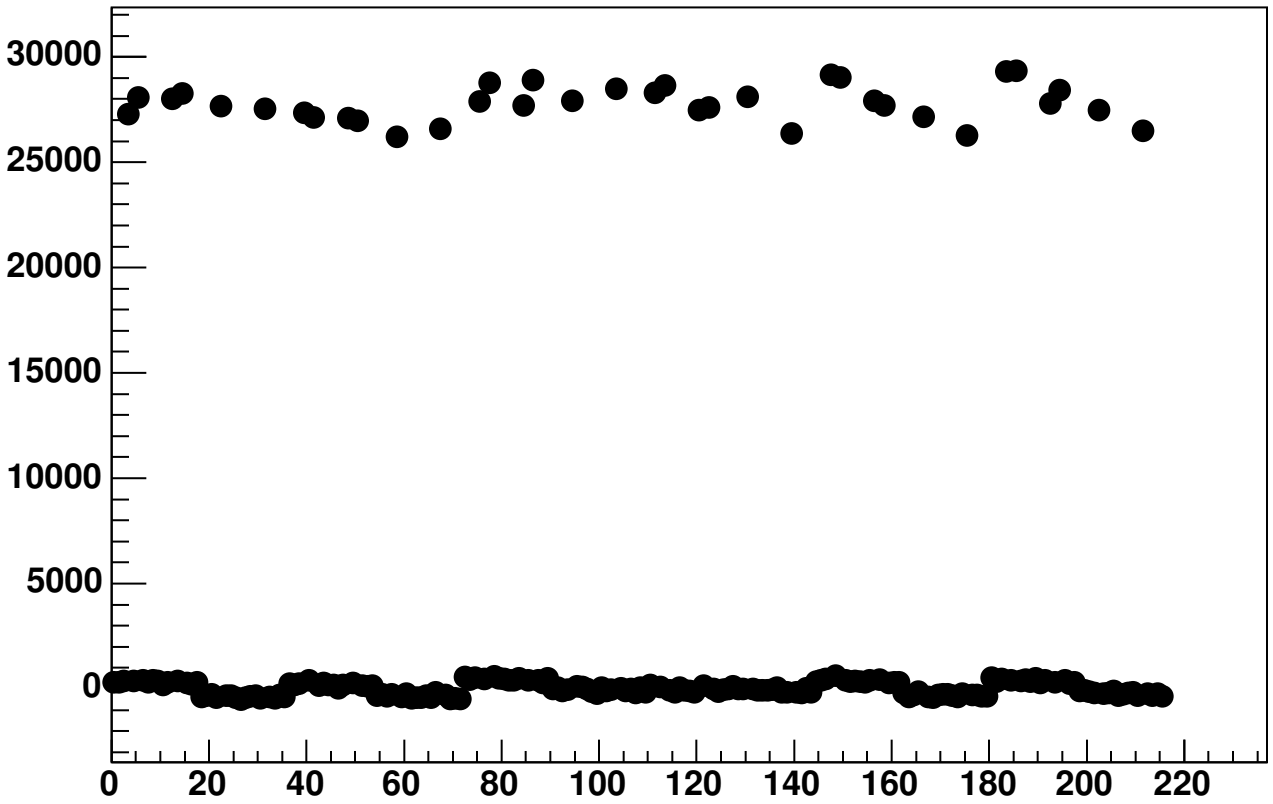
Enable 1, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



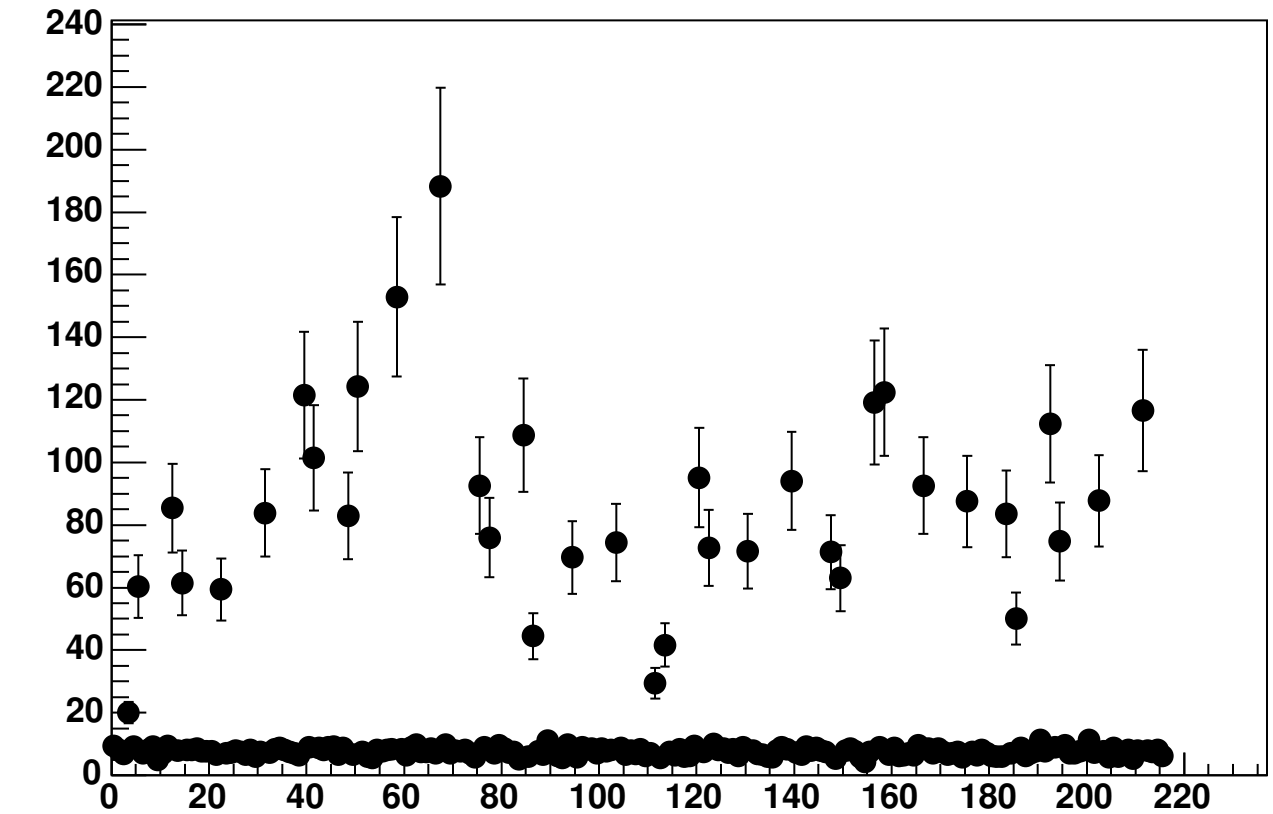
Enable 1, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



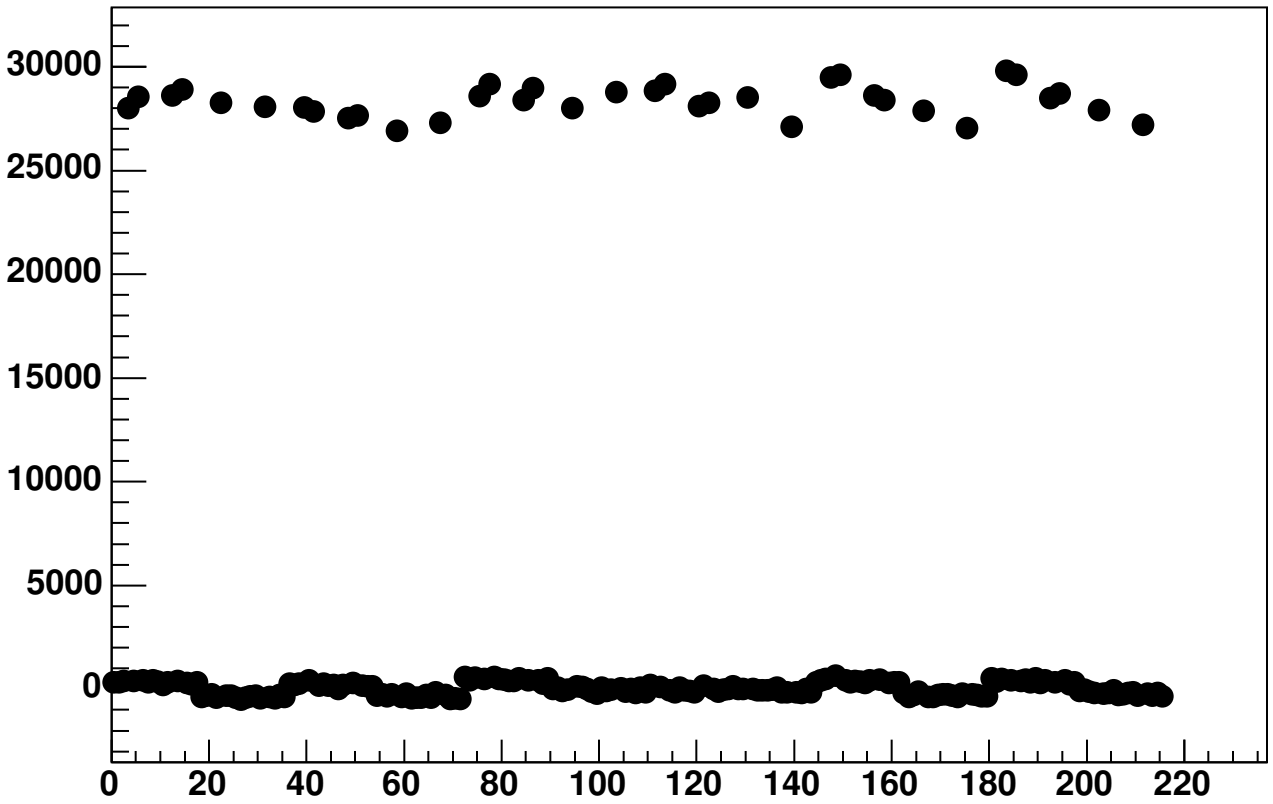
Enable 1, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



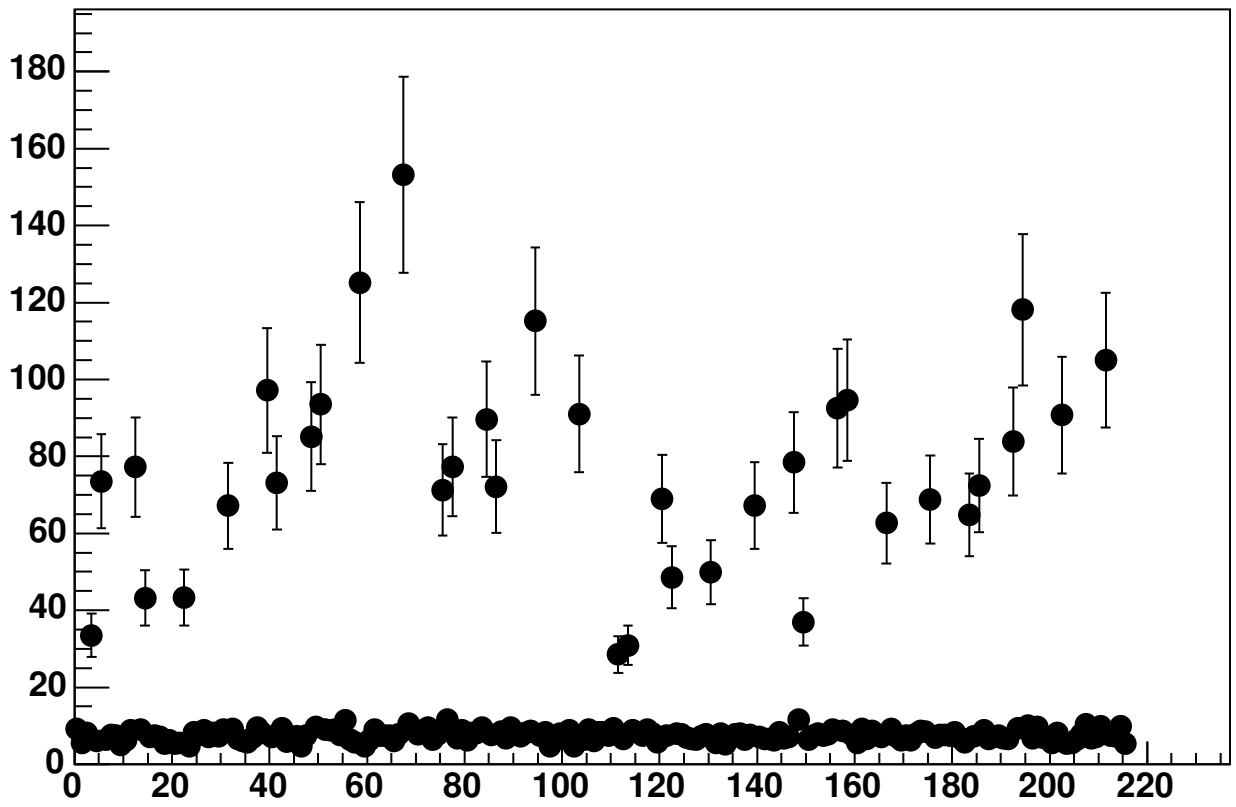
Enable 1, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



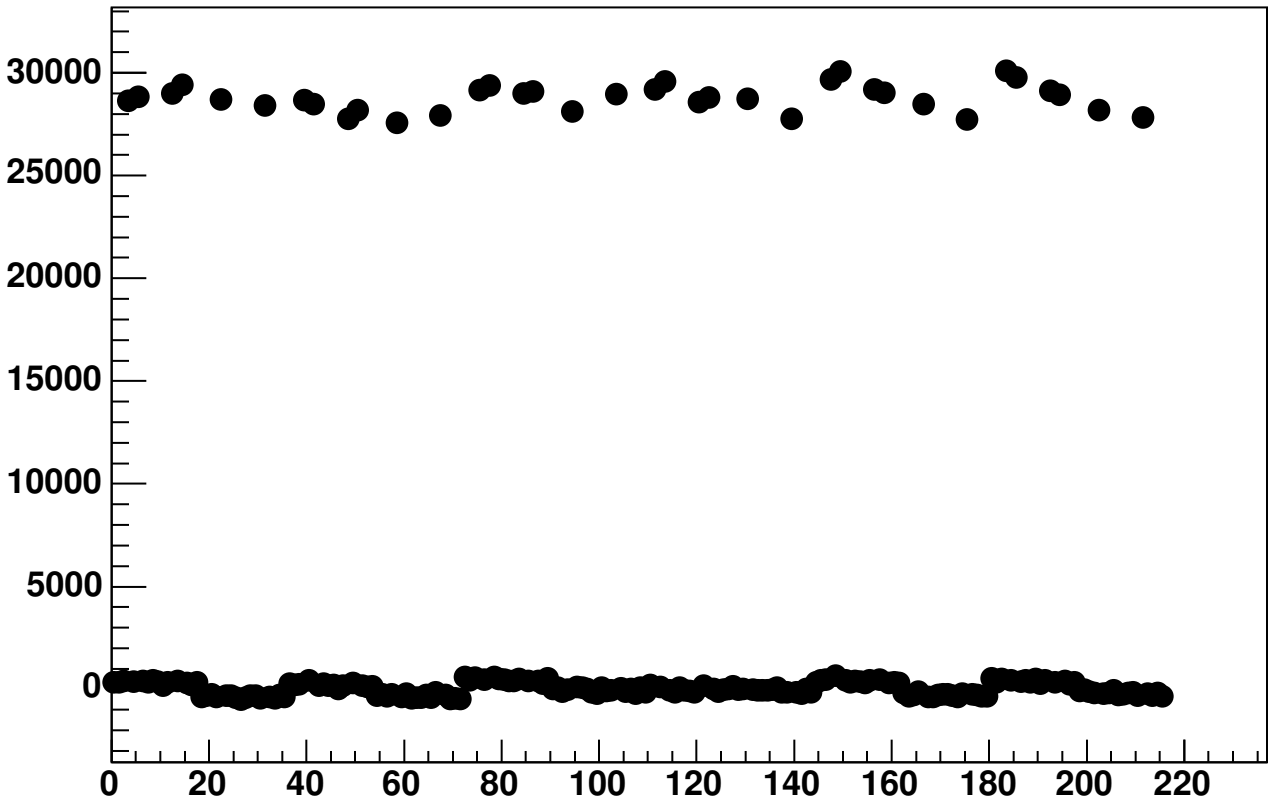
Enable 1, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



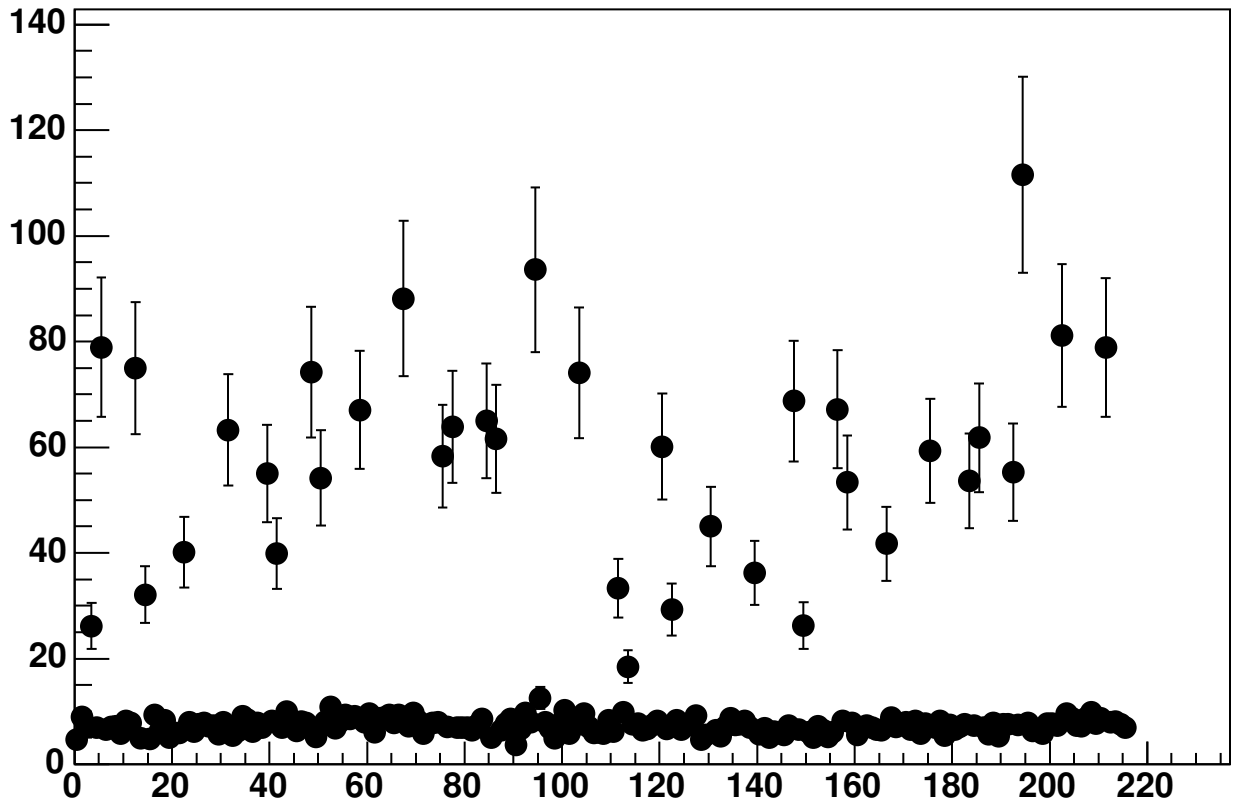
Enable 1, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



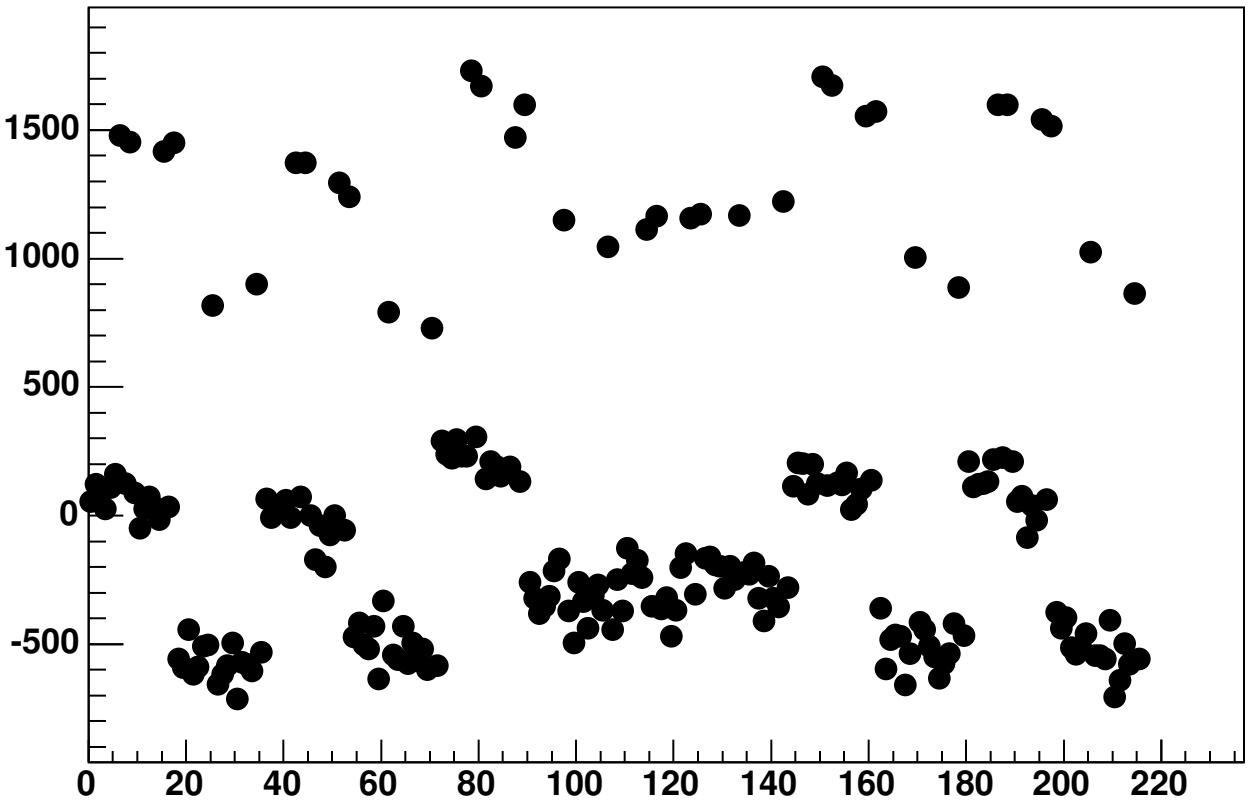
Enable 1, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



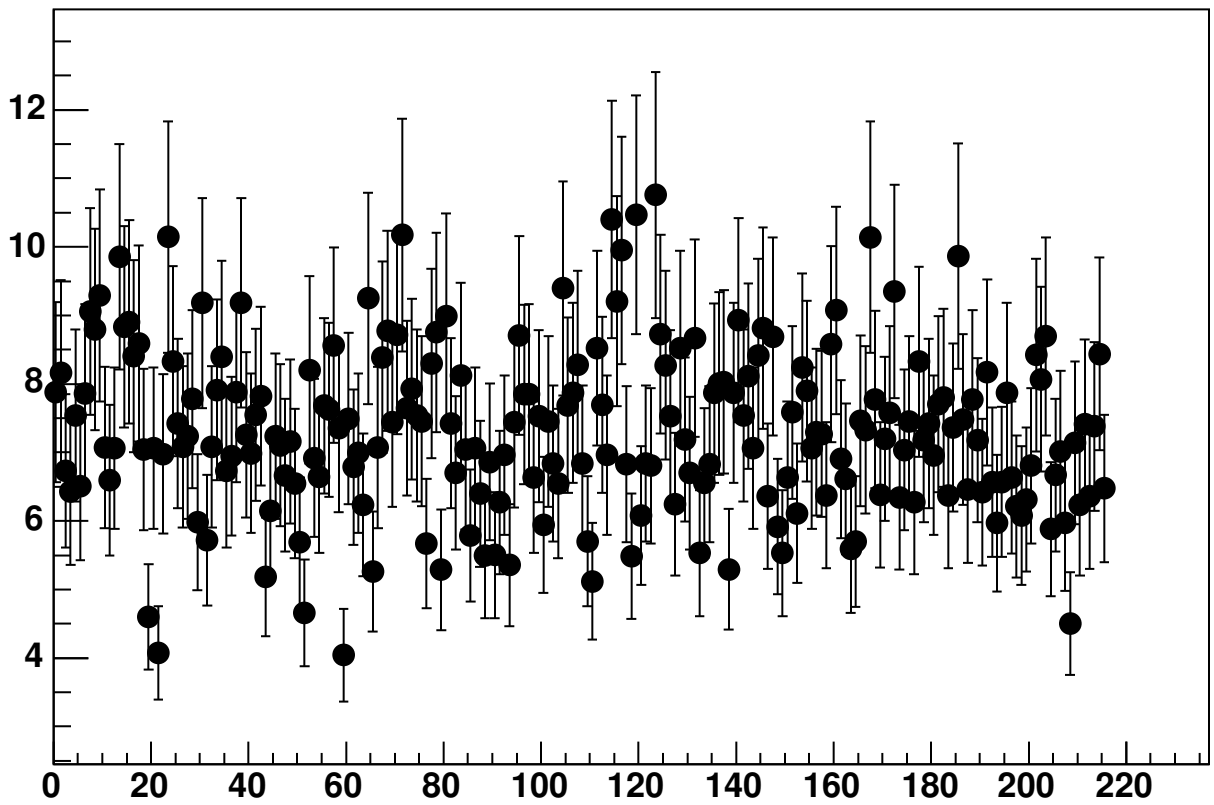
Enable 1, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



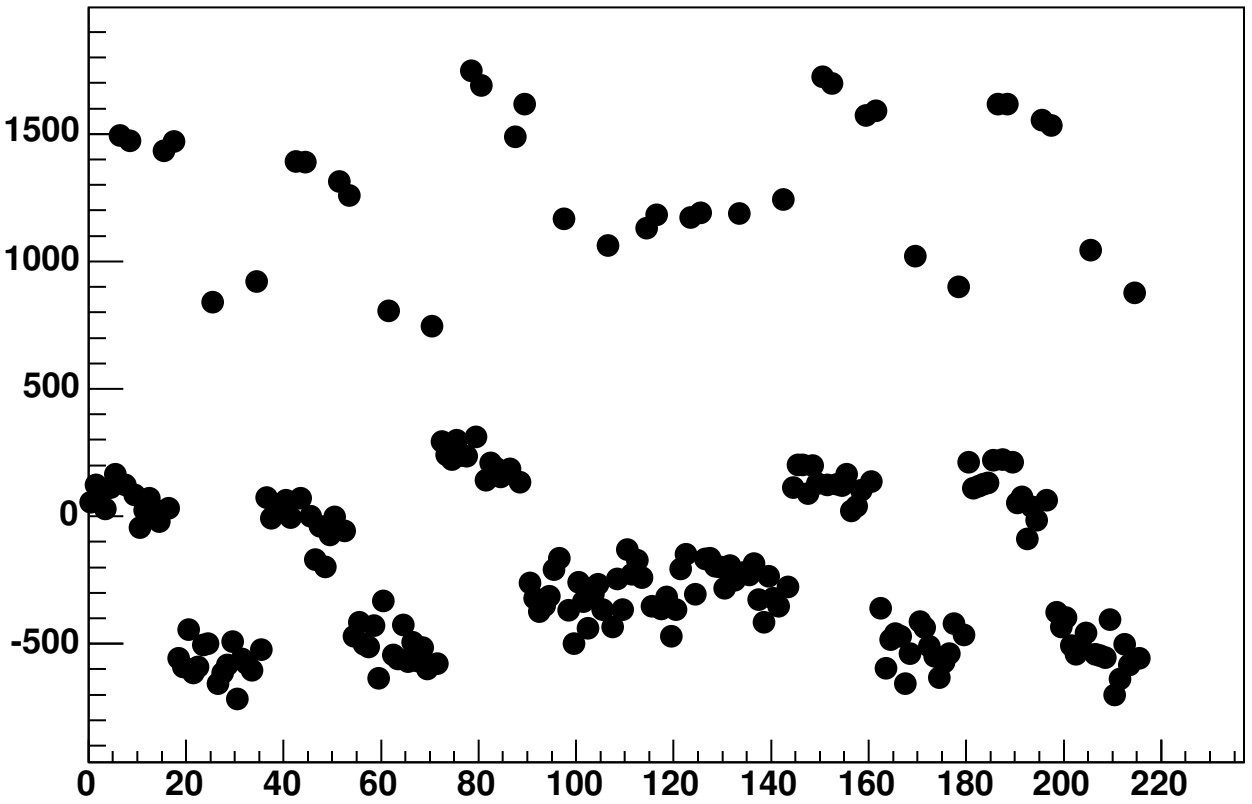
Enable 2, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



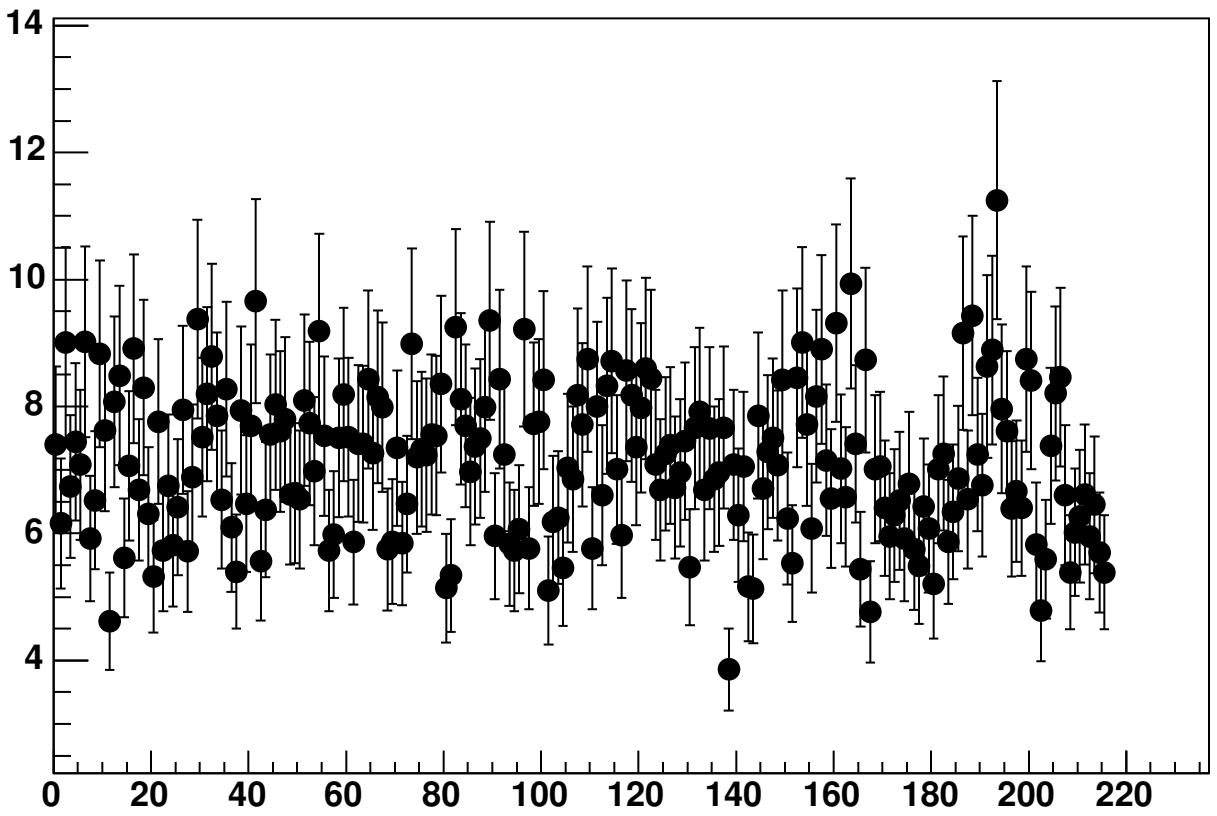
Enable 2, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



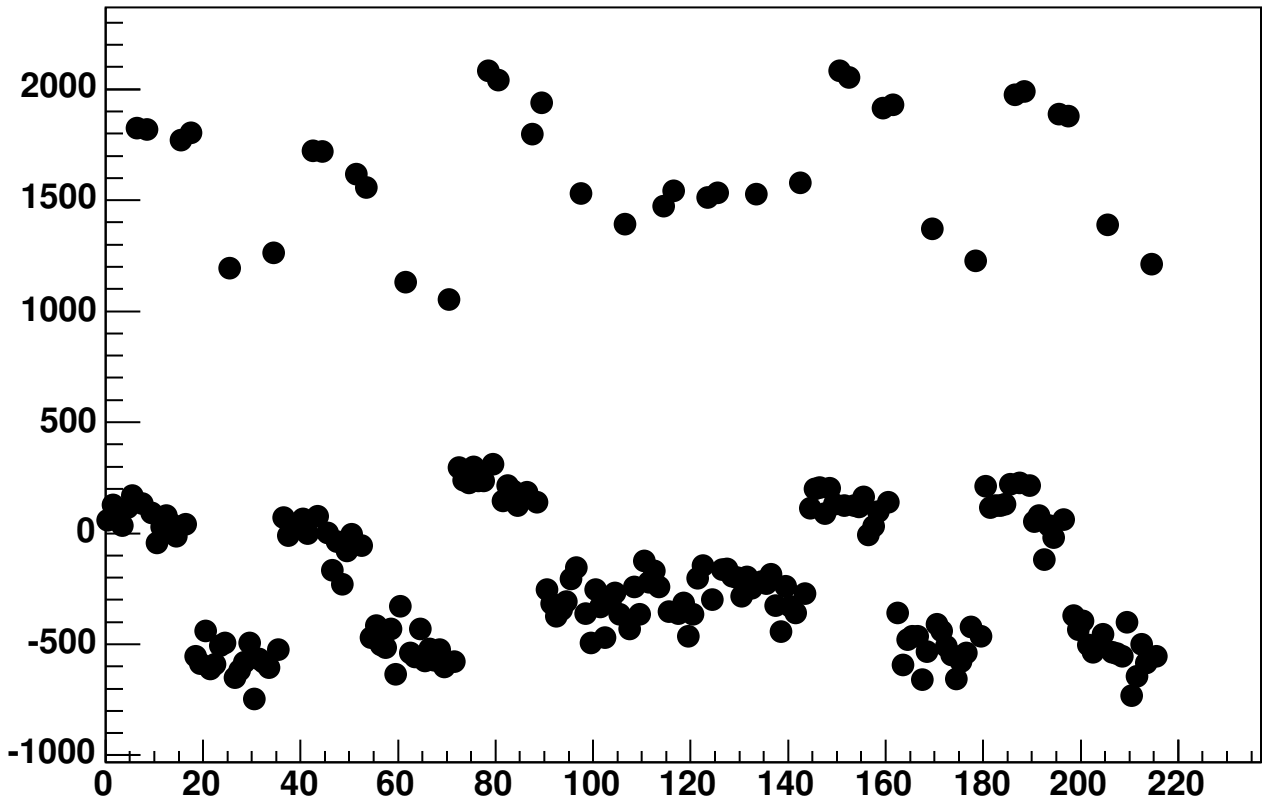
Enable 2, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



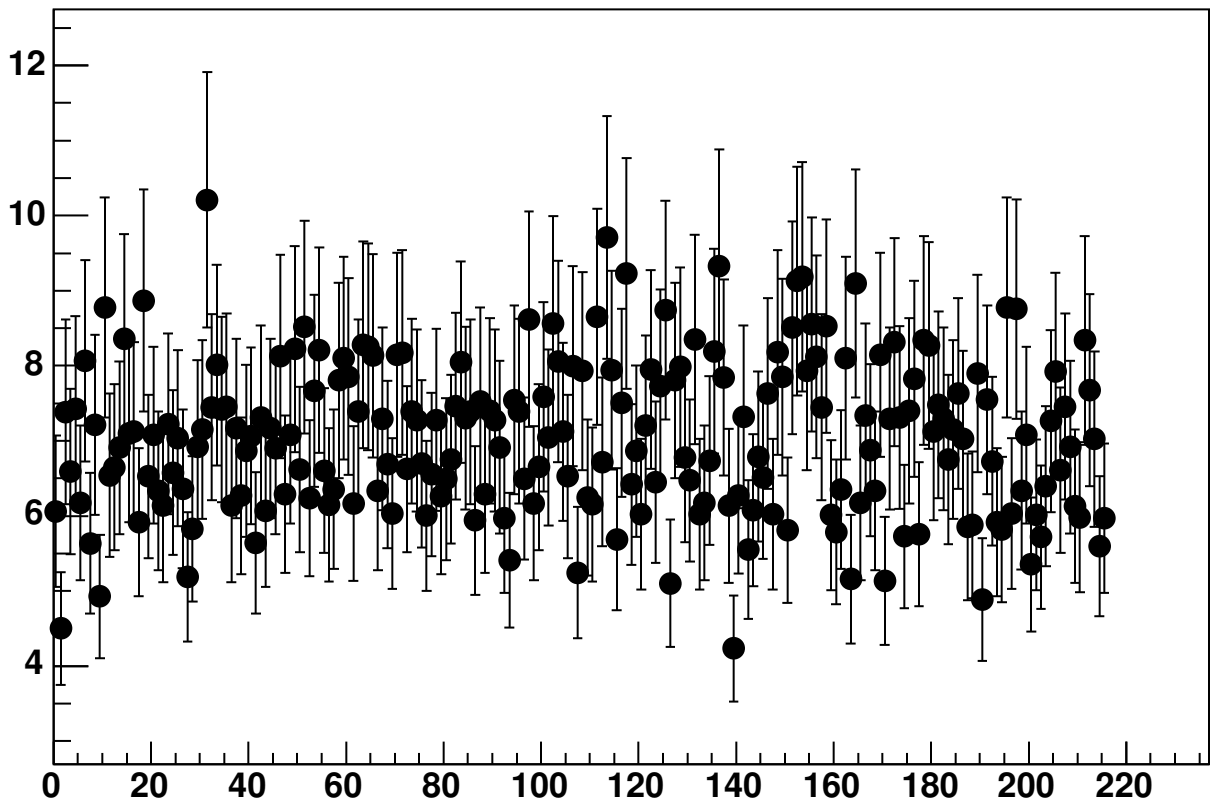
Enable 2, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



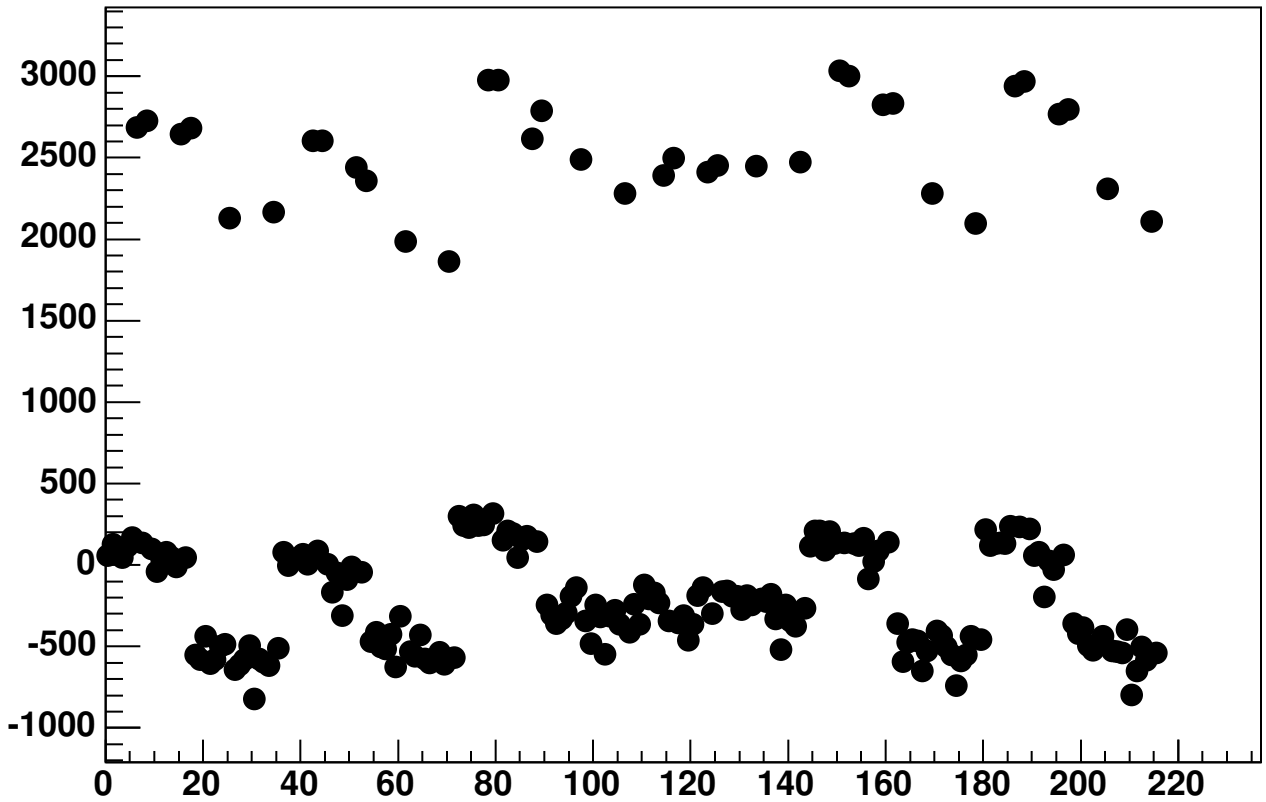
Enable 2, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



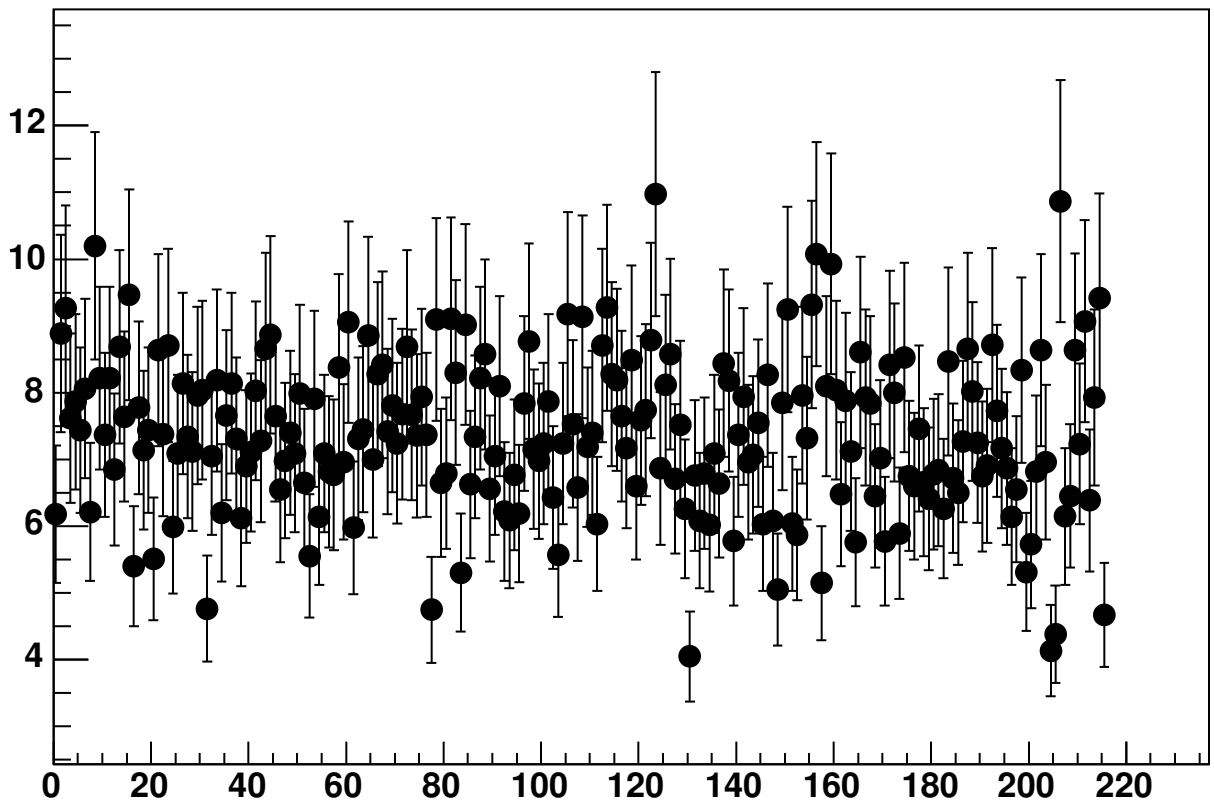
Enable 2, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



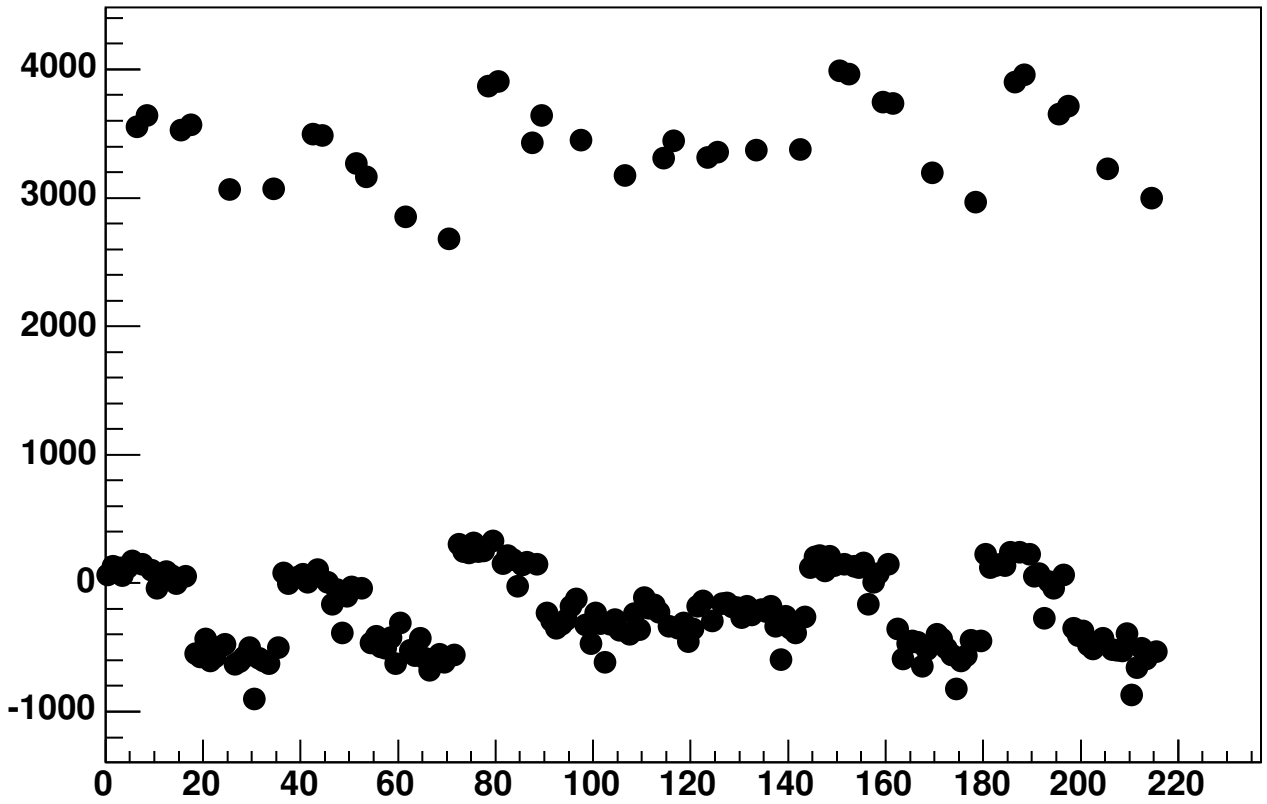
Enable 2, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



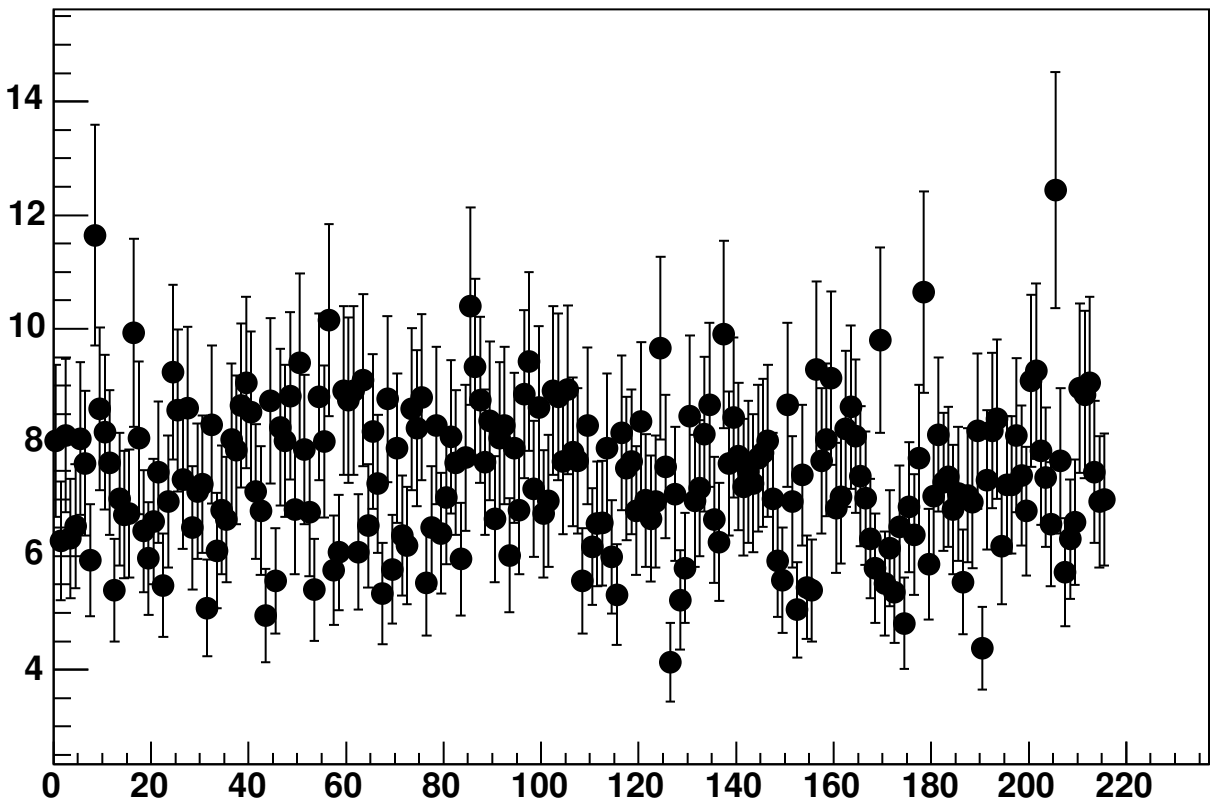
Enable 2, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



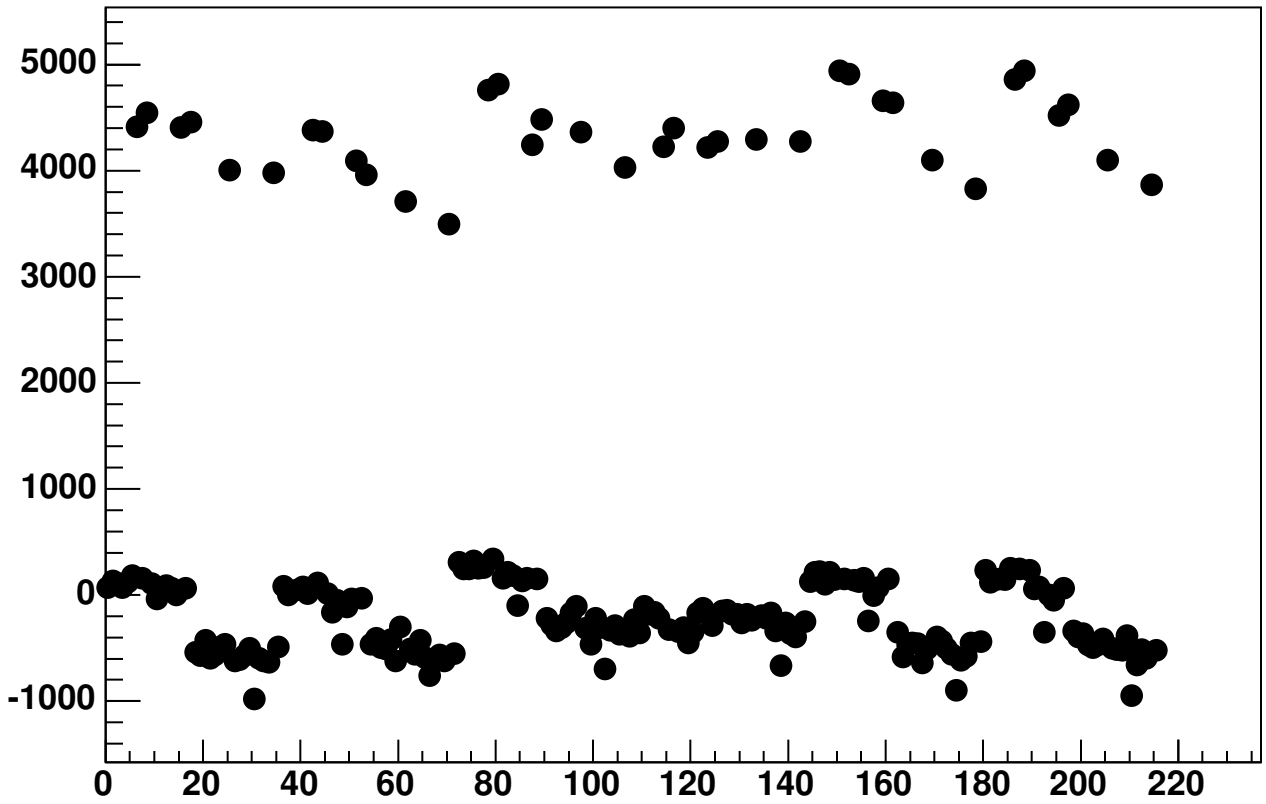
Enable 2, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



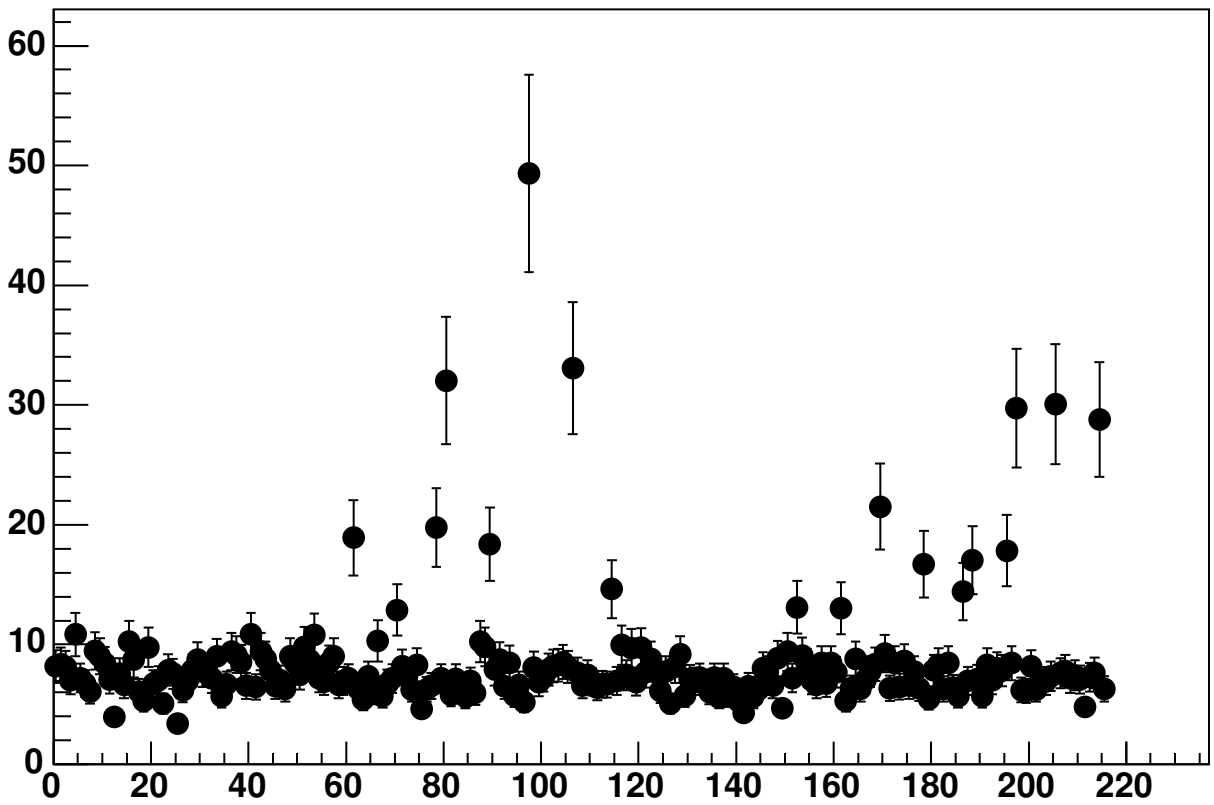
Enable 2, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



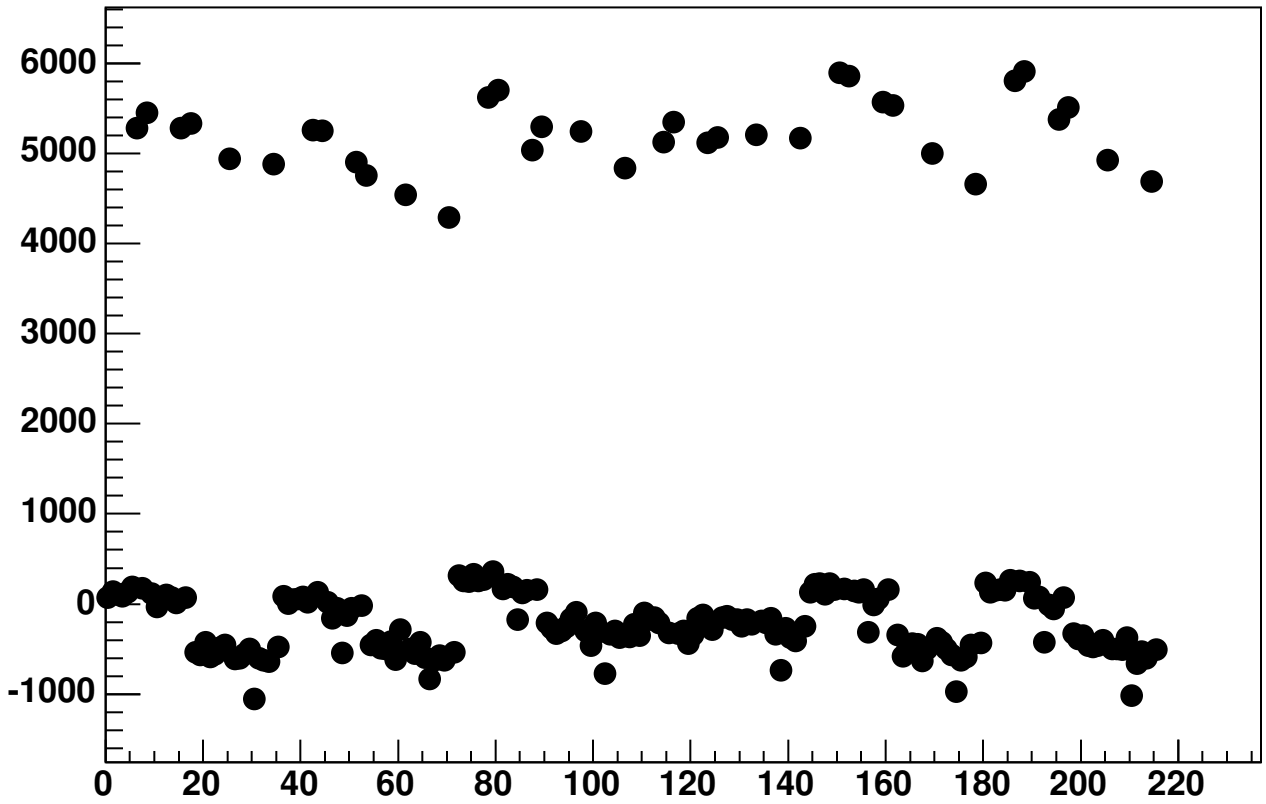
Enable 2, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



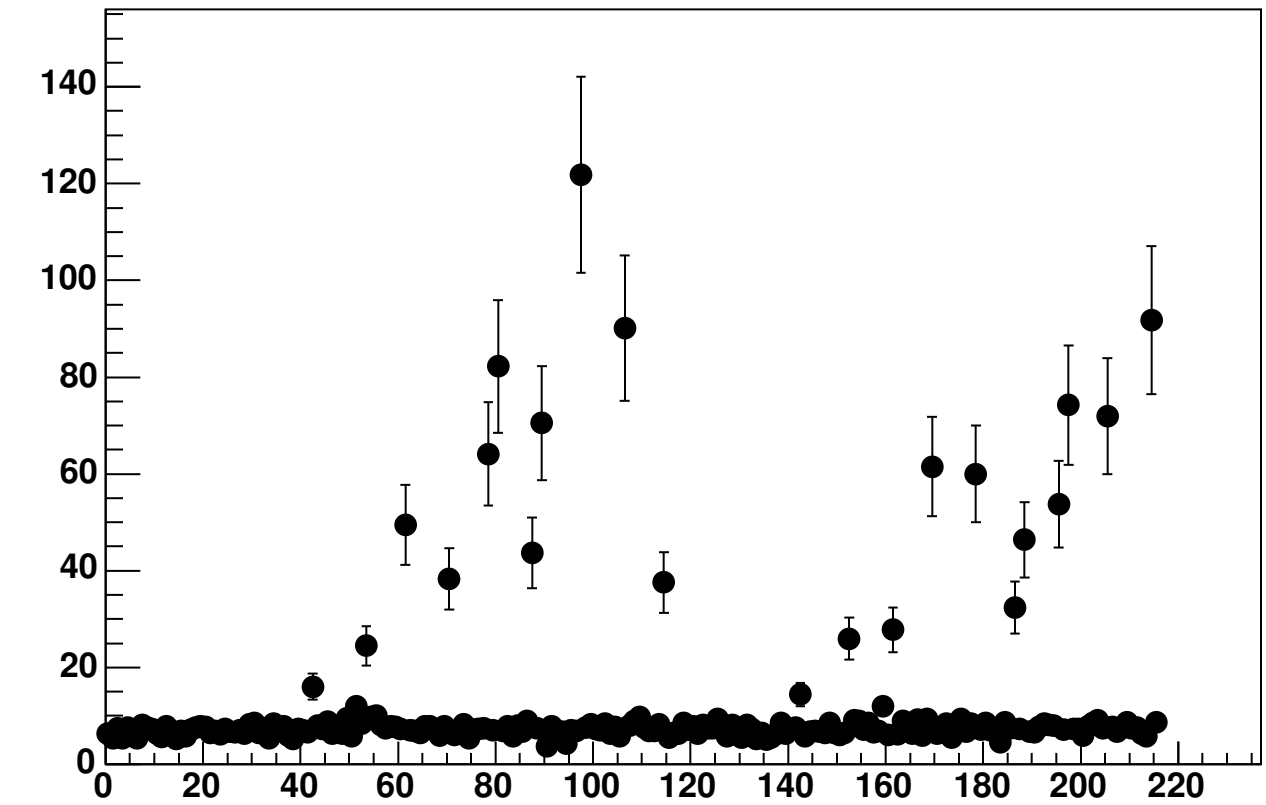
Enable 2, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



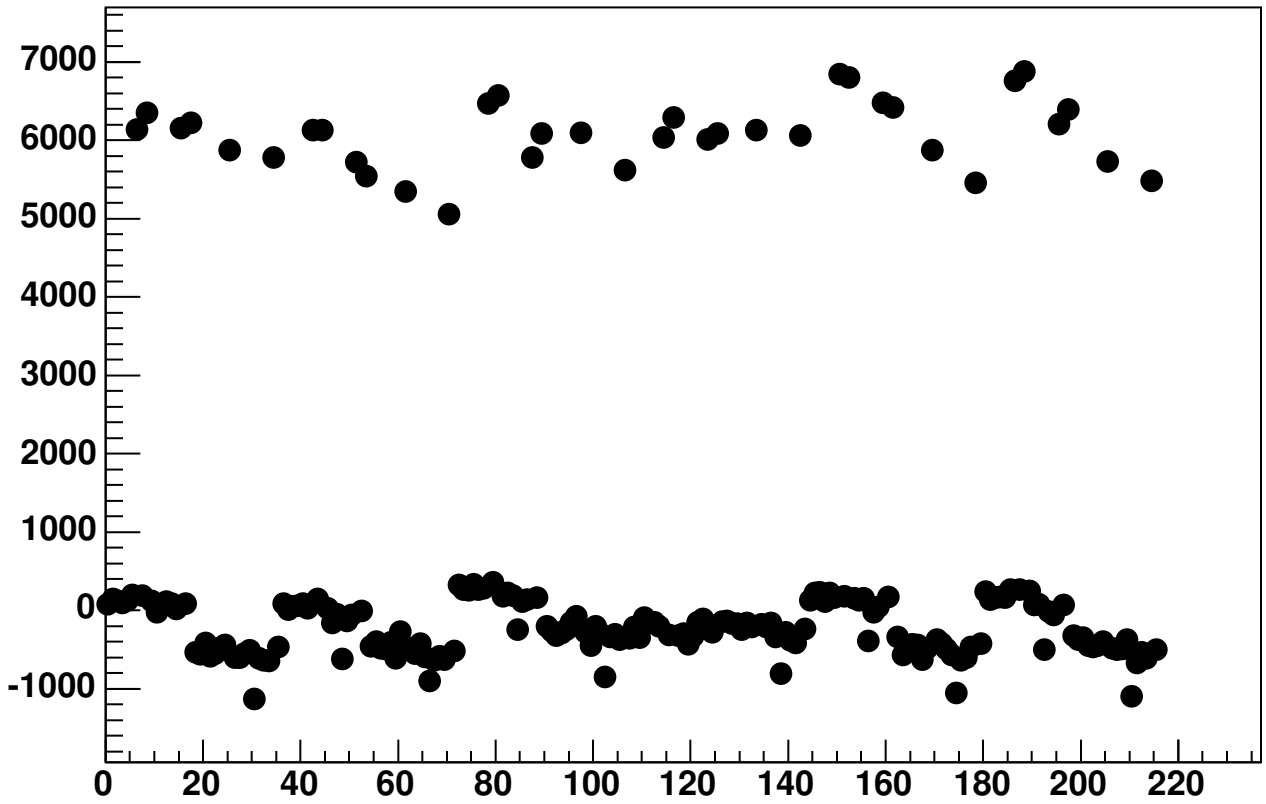
Enable 2, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



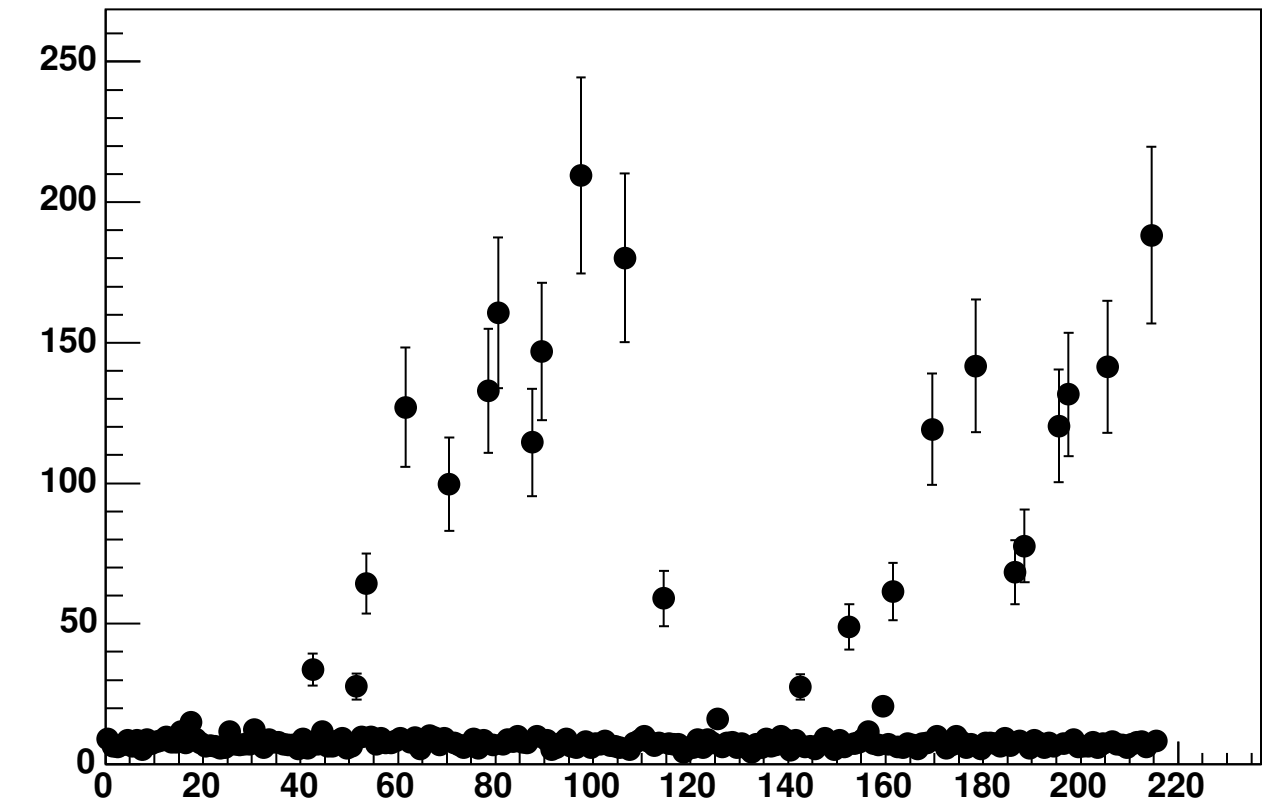
Enable 2, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



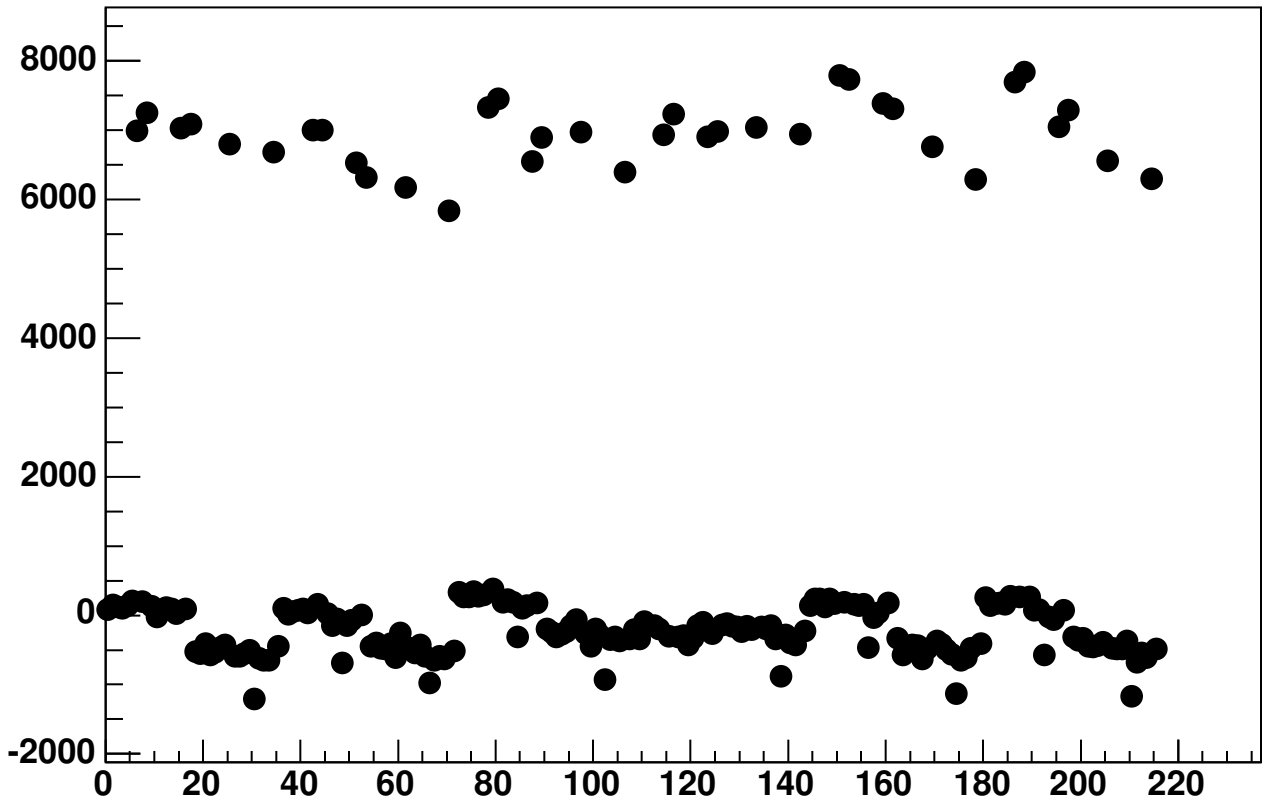
Enable 2, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



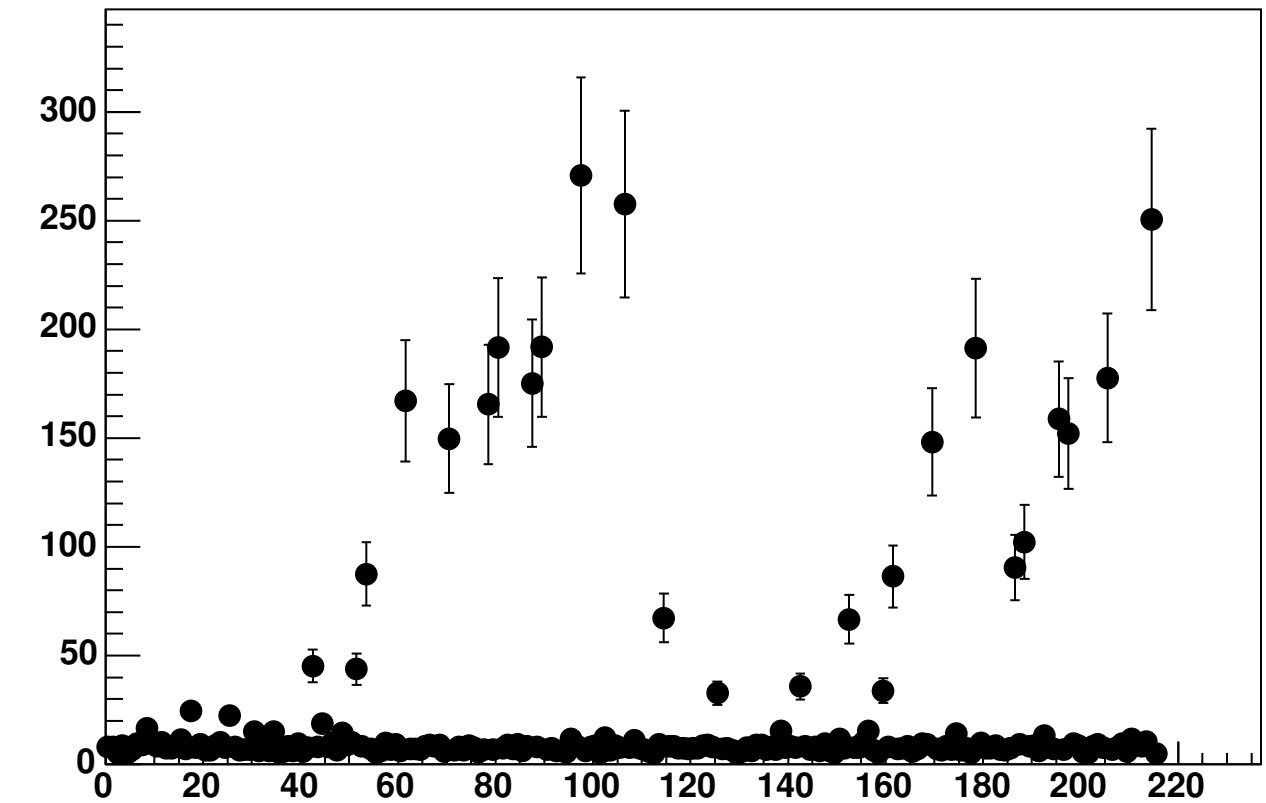
Enable 2, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



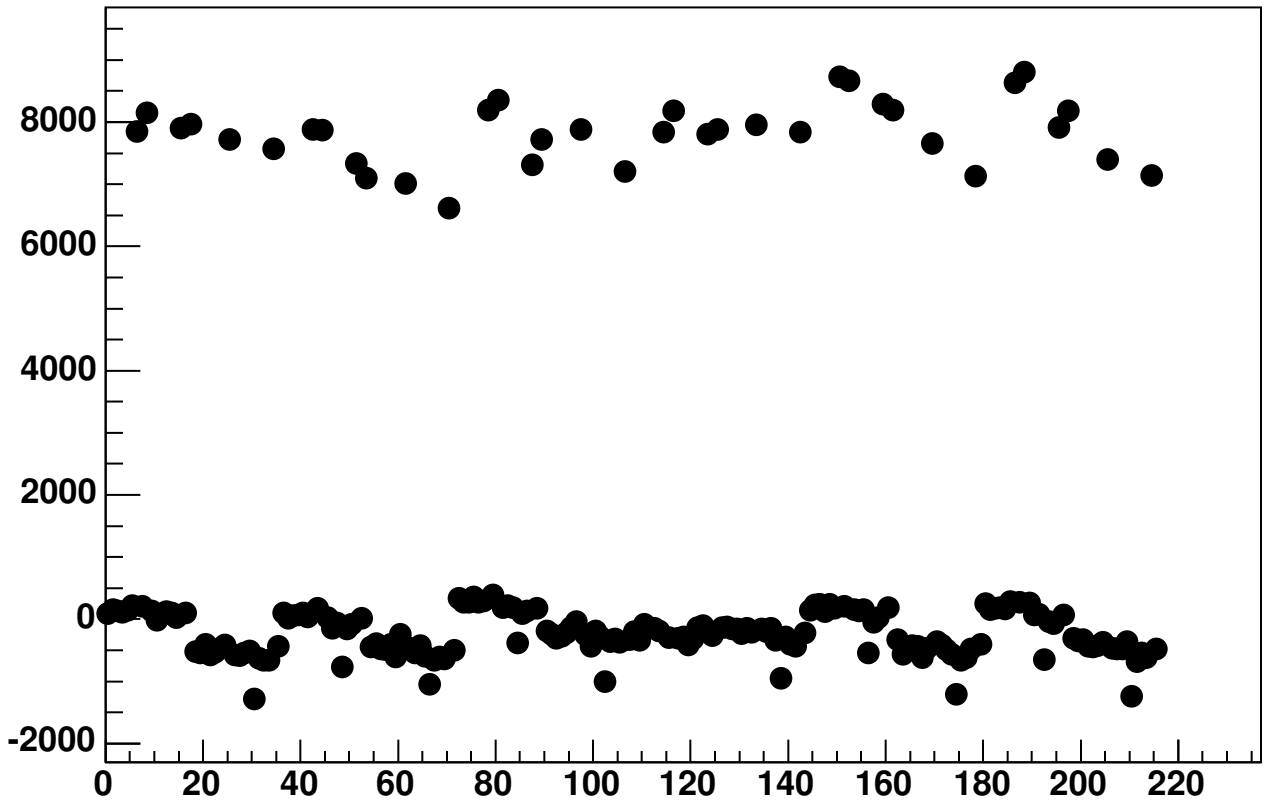
Enable 2, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



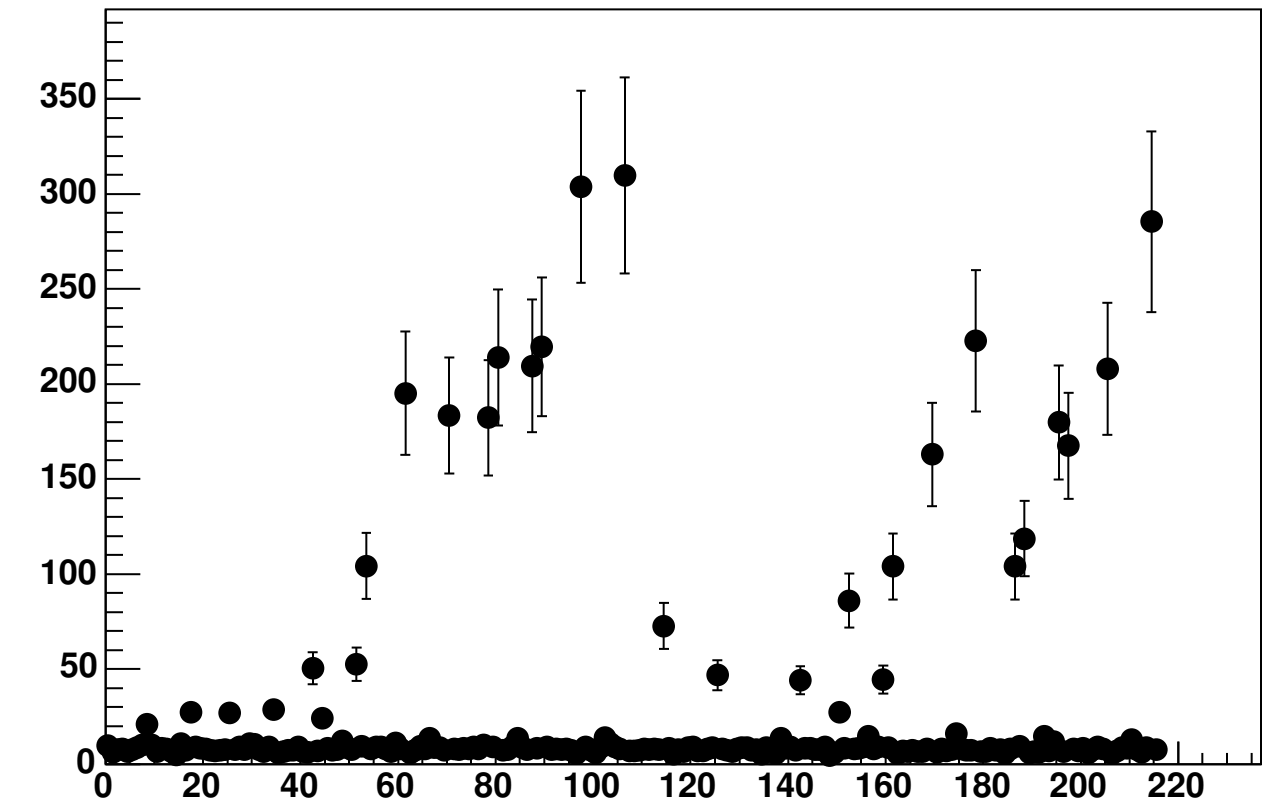
Enable 2, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



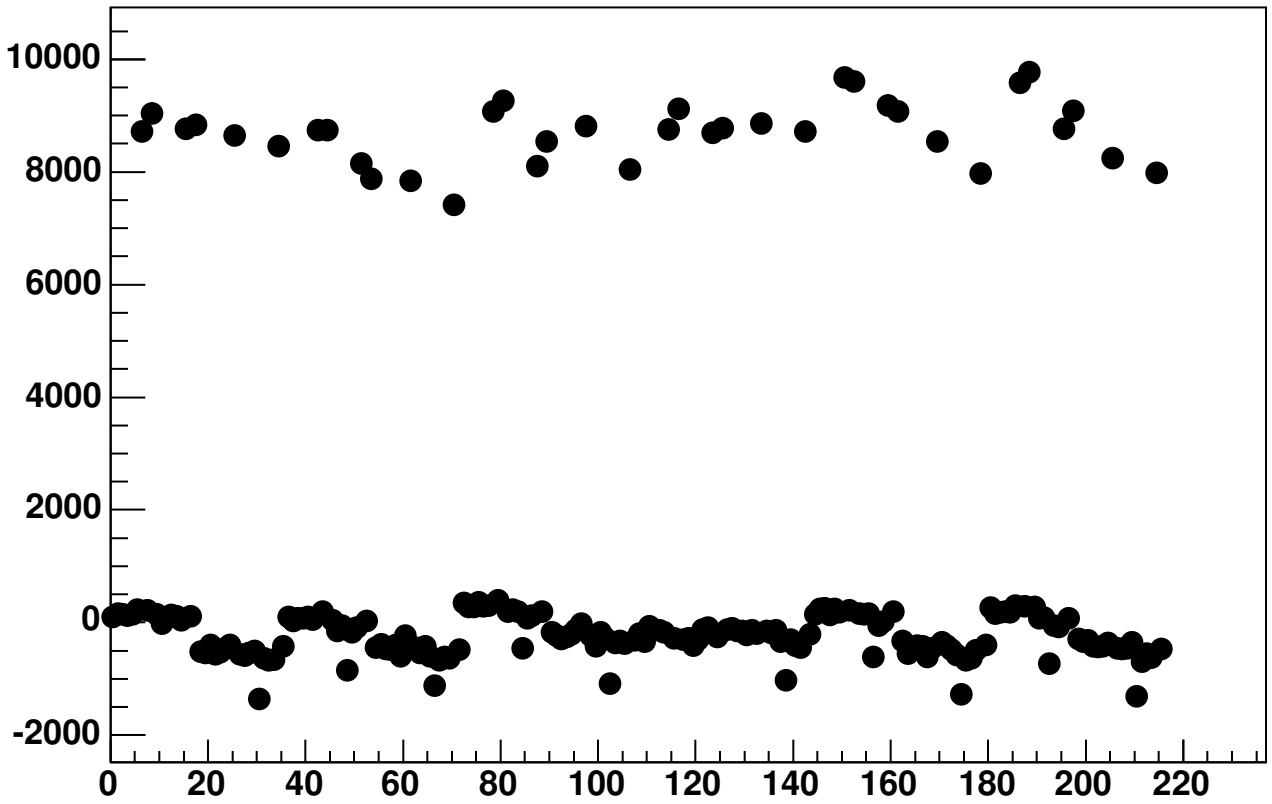
Enable 2, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



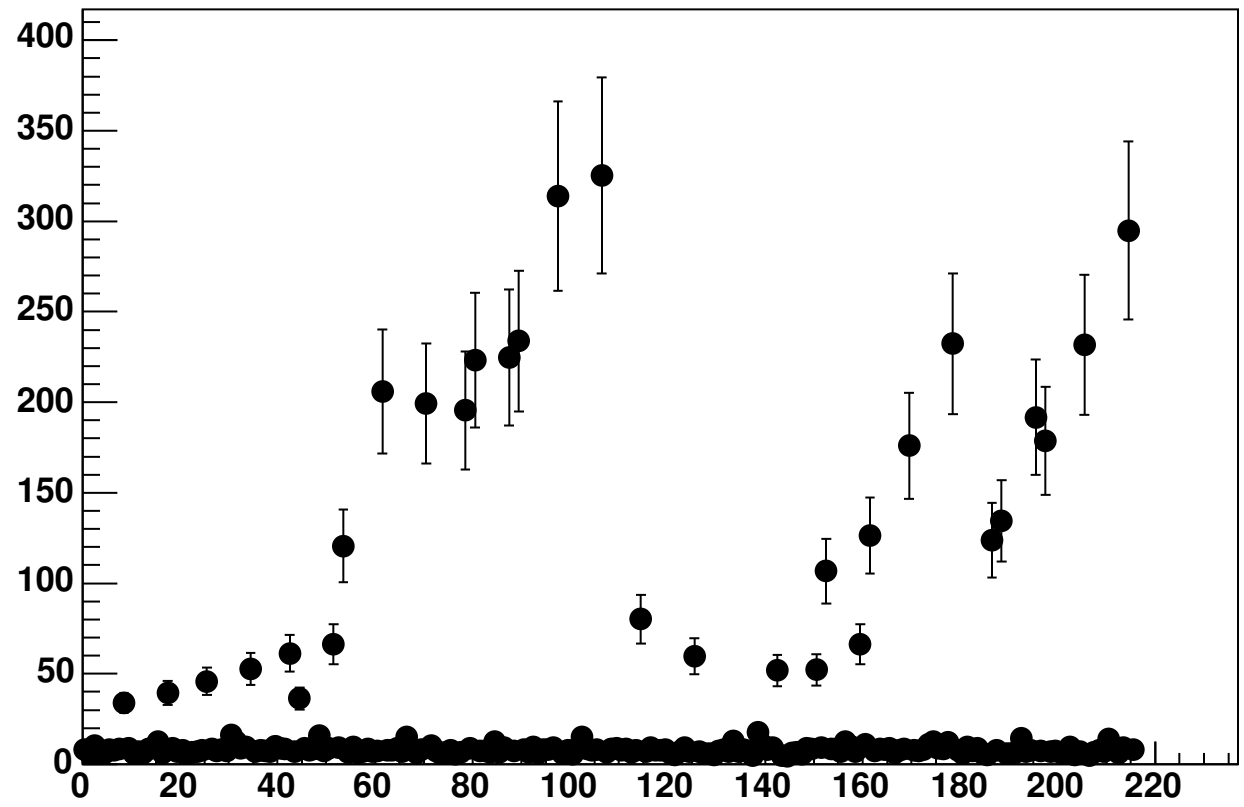
Enable 2, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



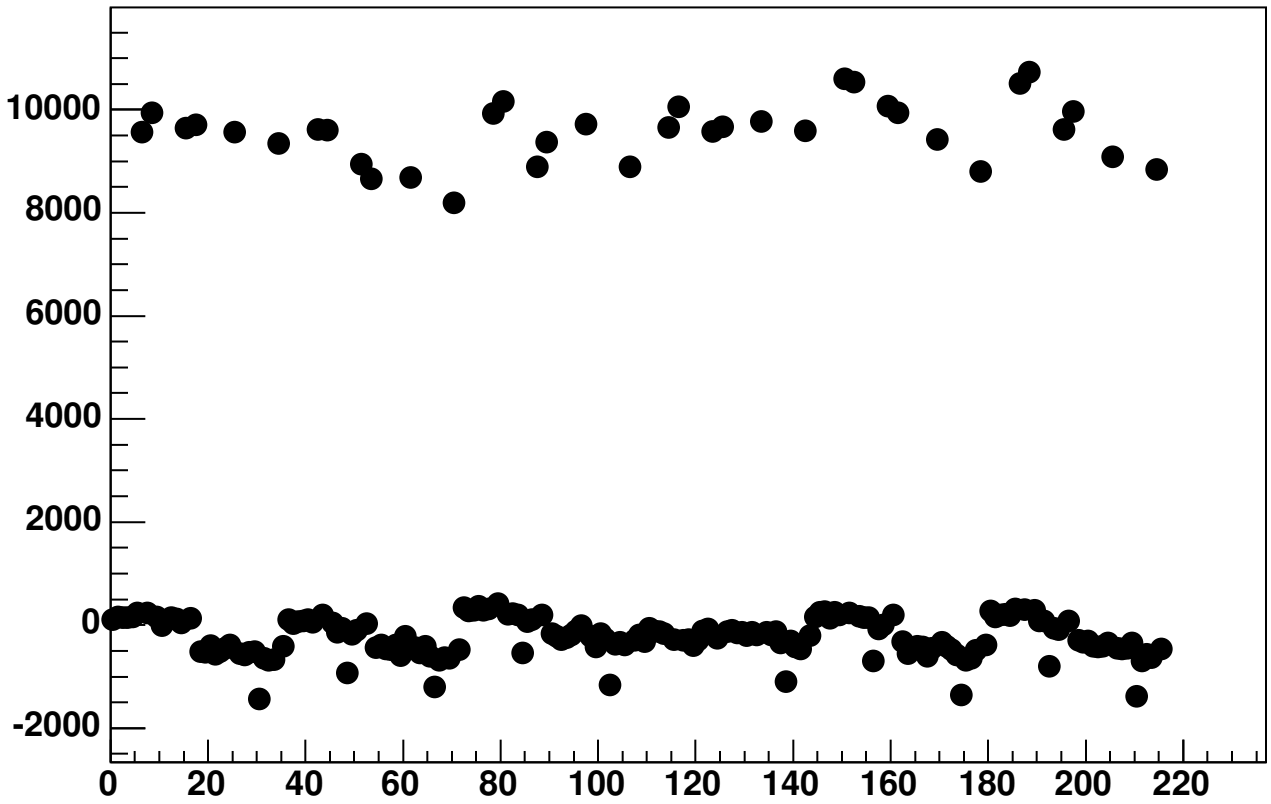
Enable 2, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



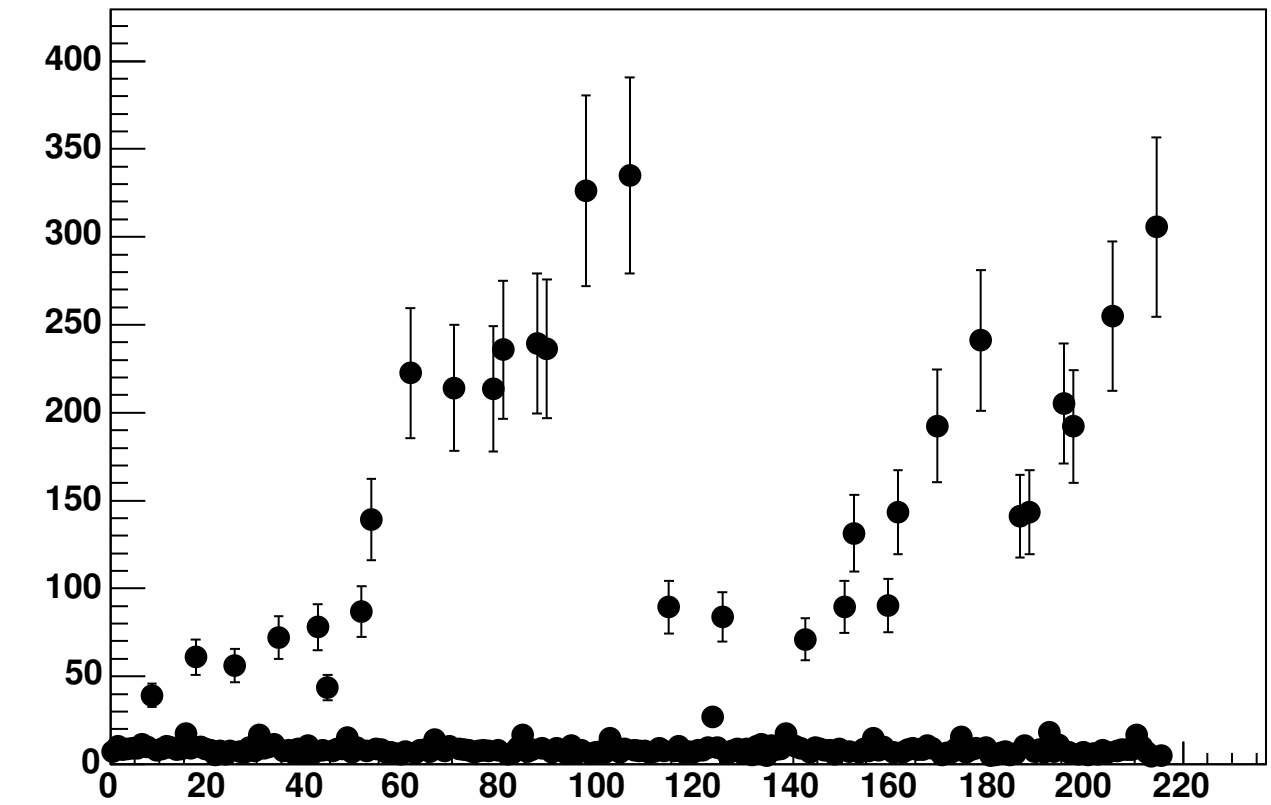
Enable 2, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



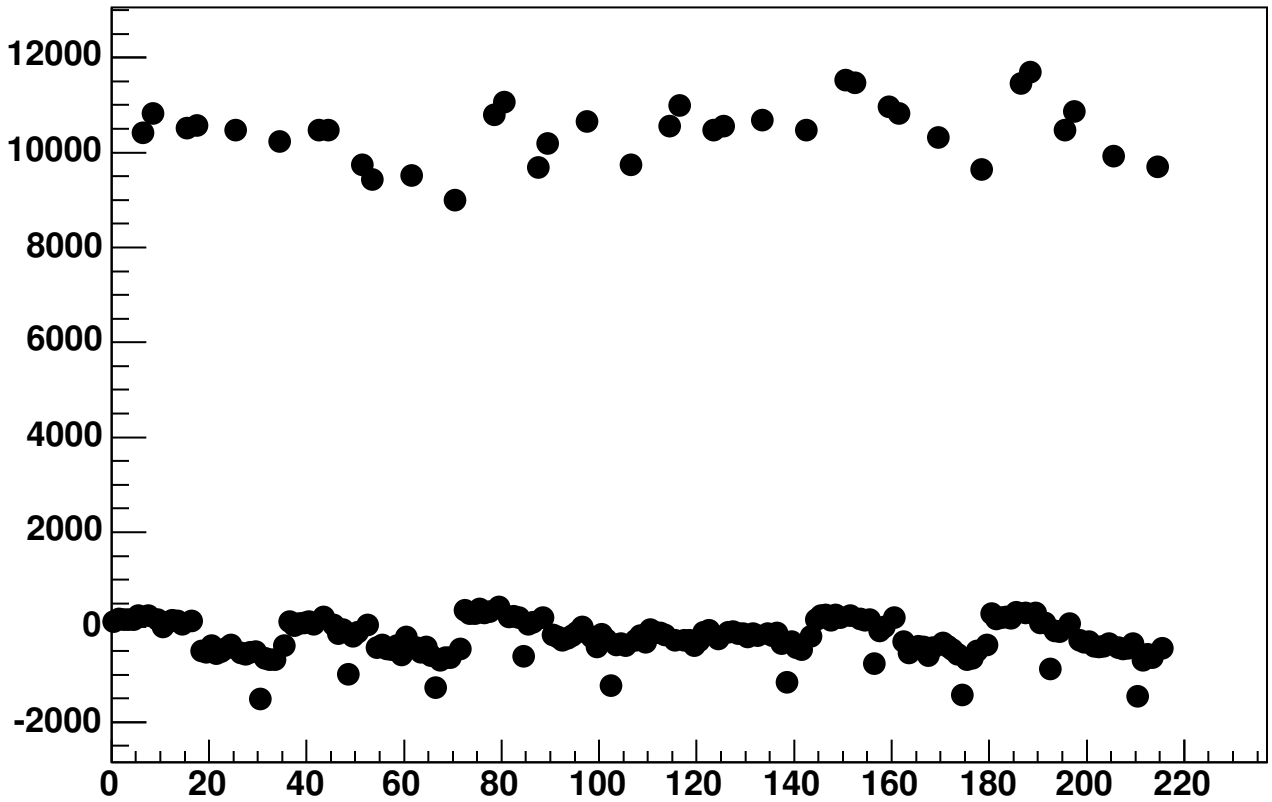
Enable 2, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



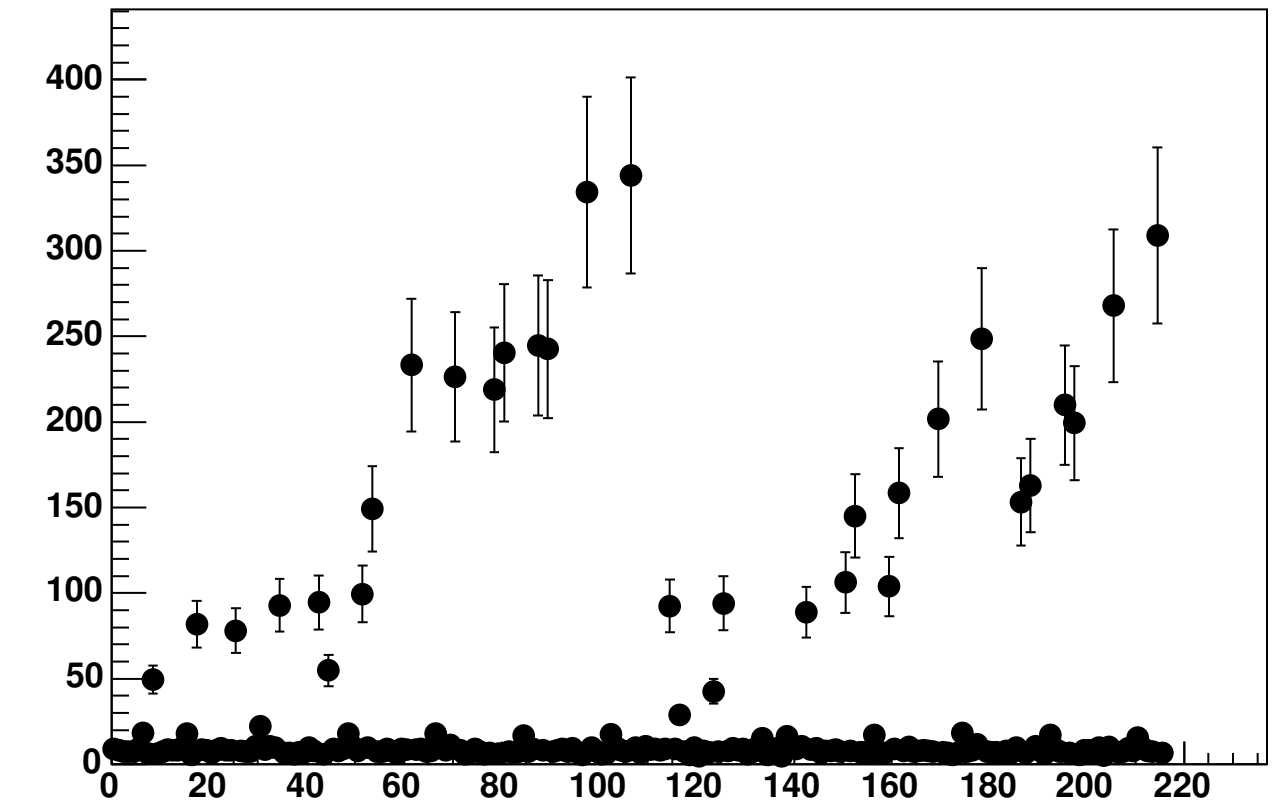
Enable 2, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



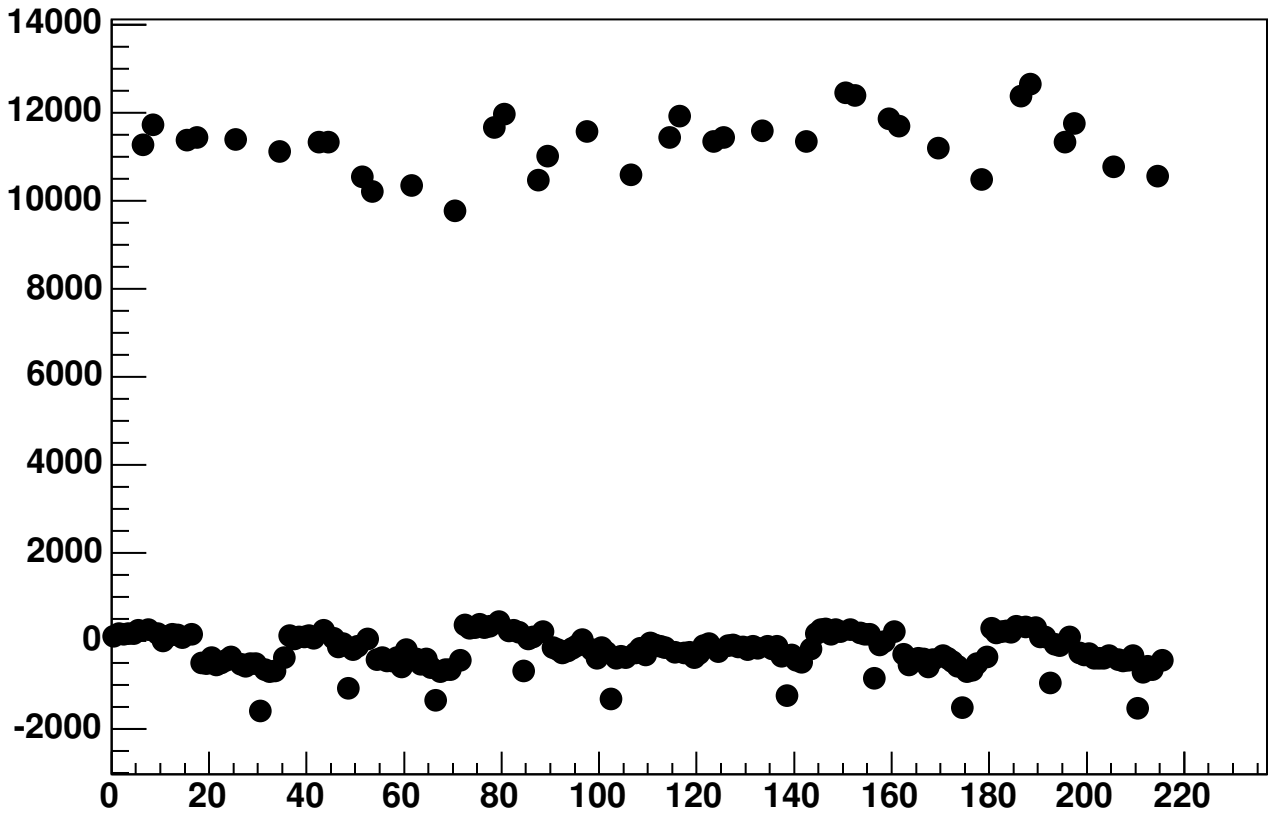
Enable 2, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



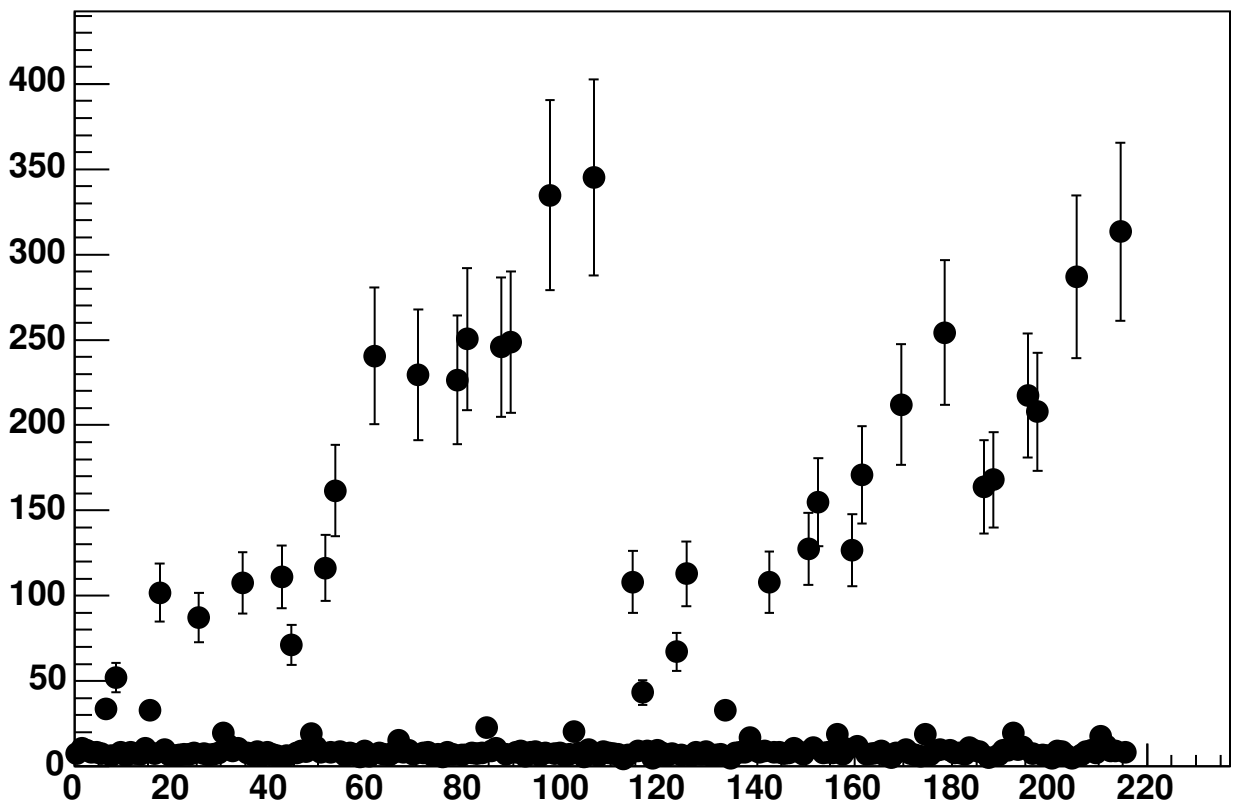
Enable 2, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



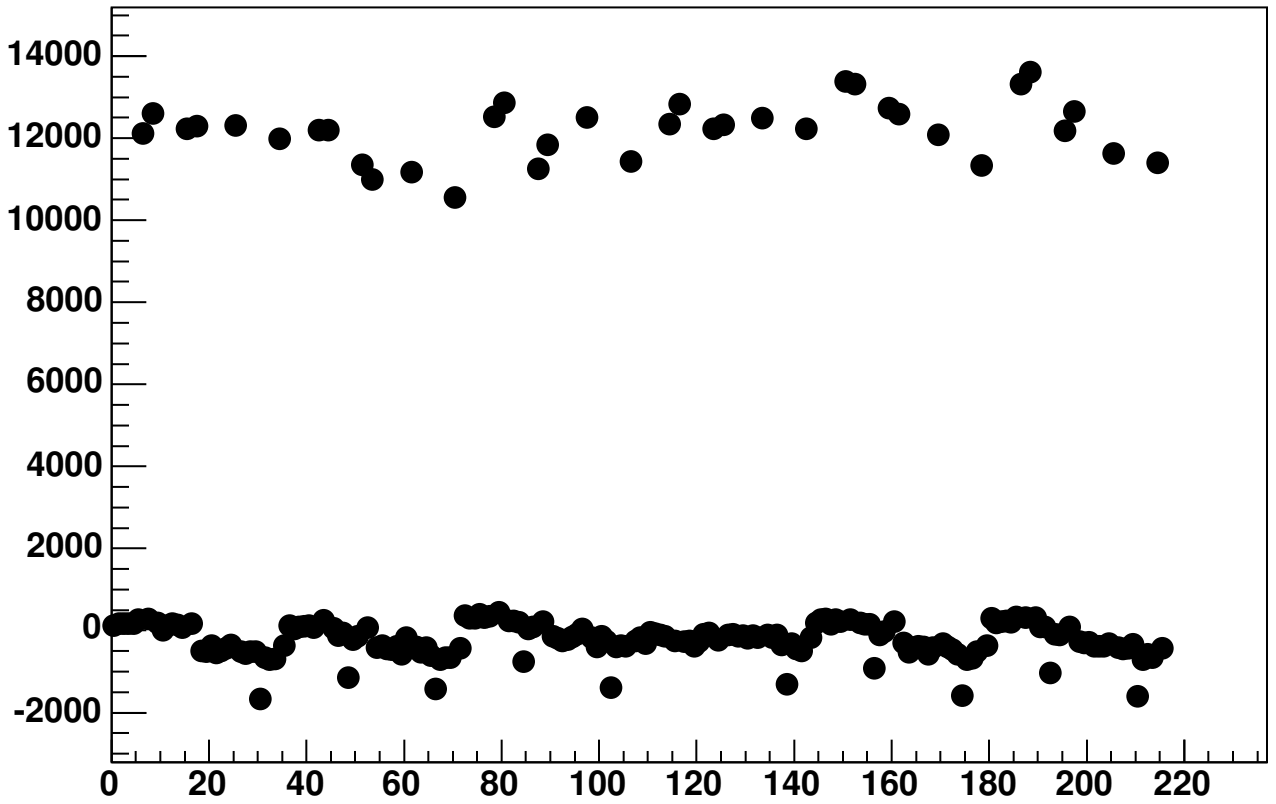
Enable 2, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



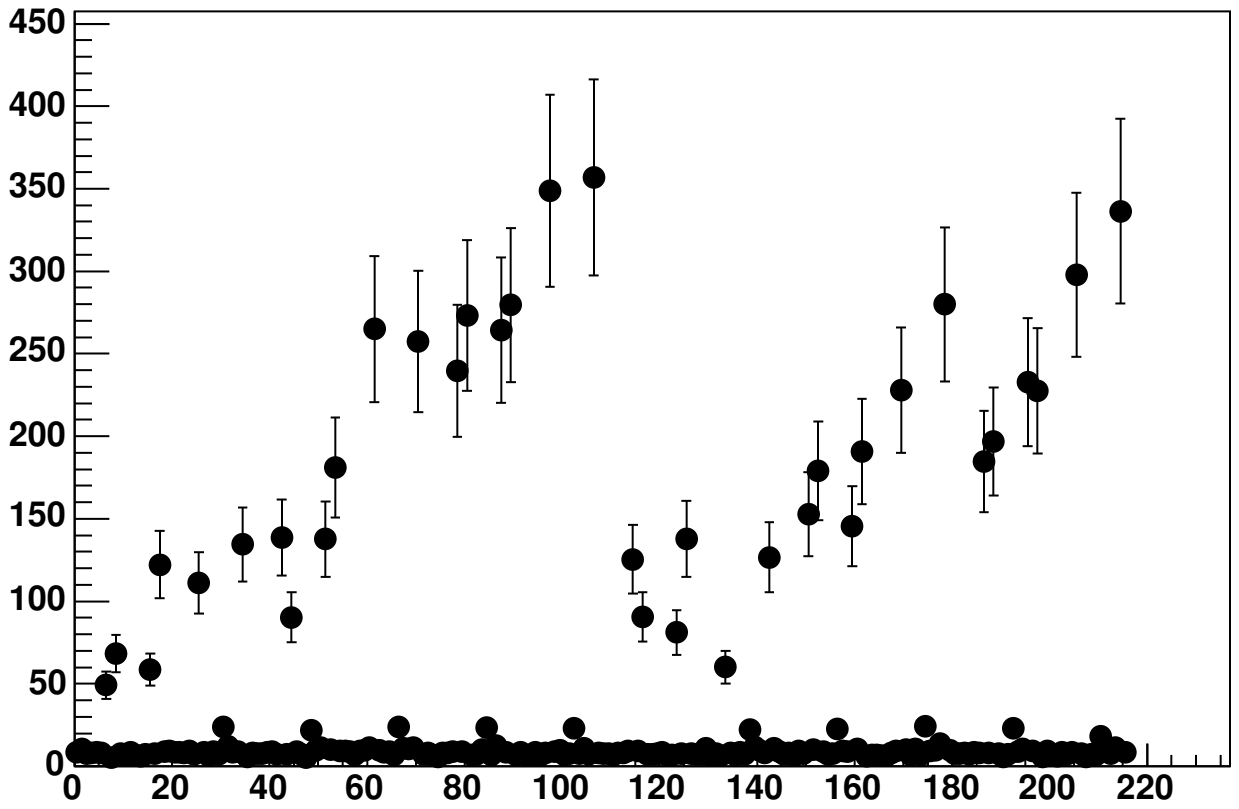
Enable 2, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



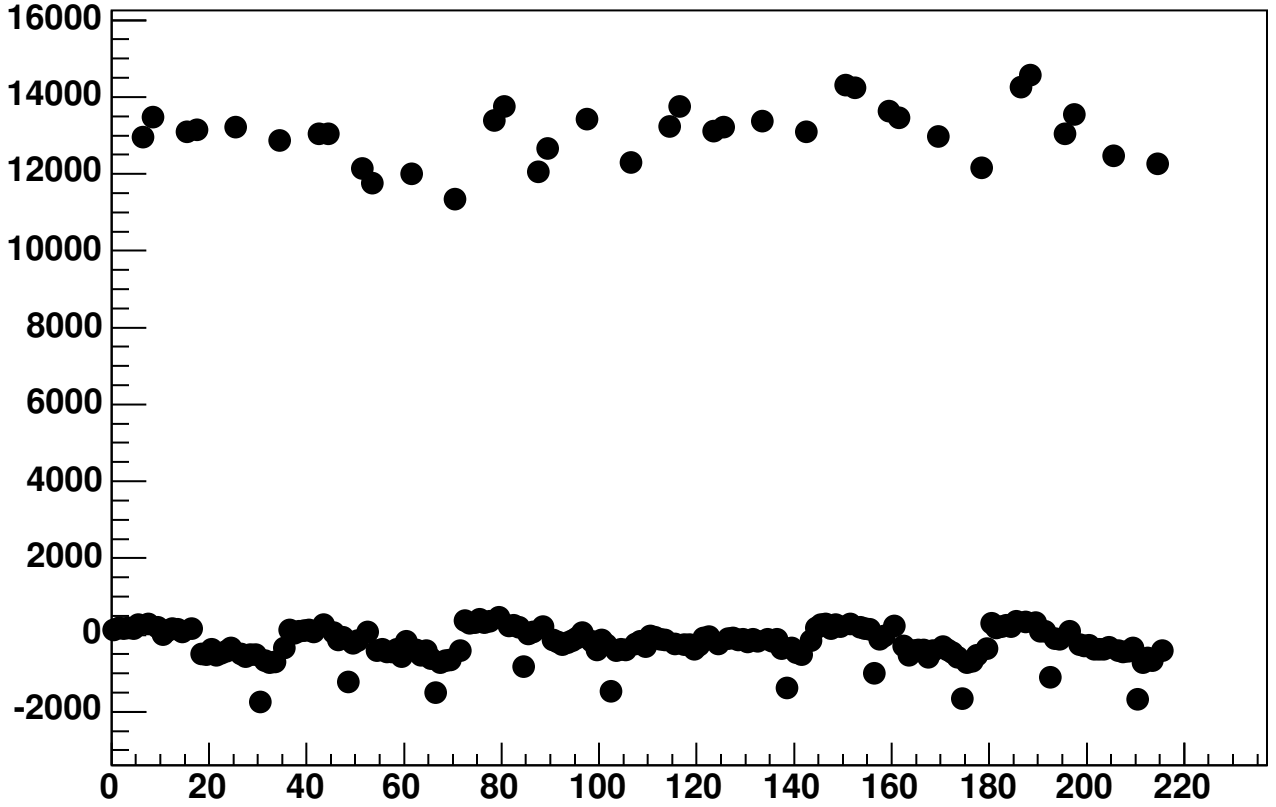
Enable 2, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



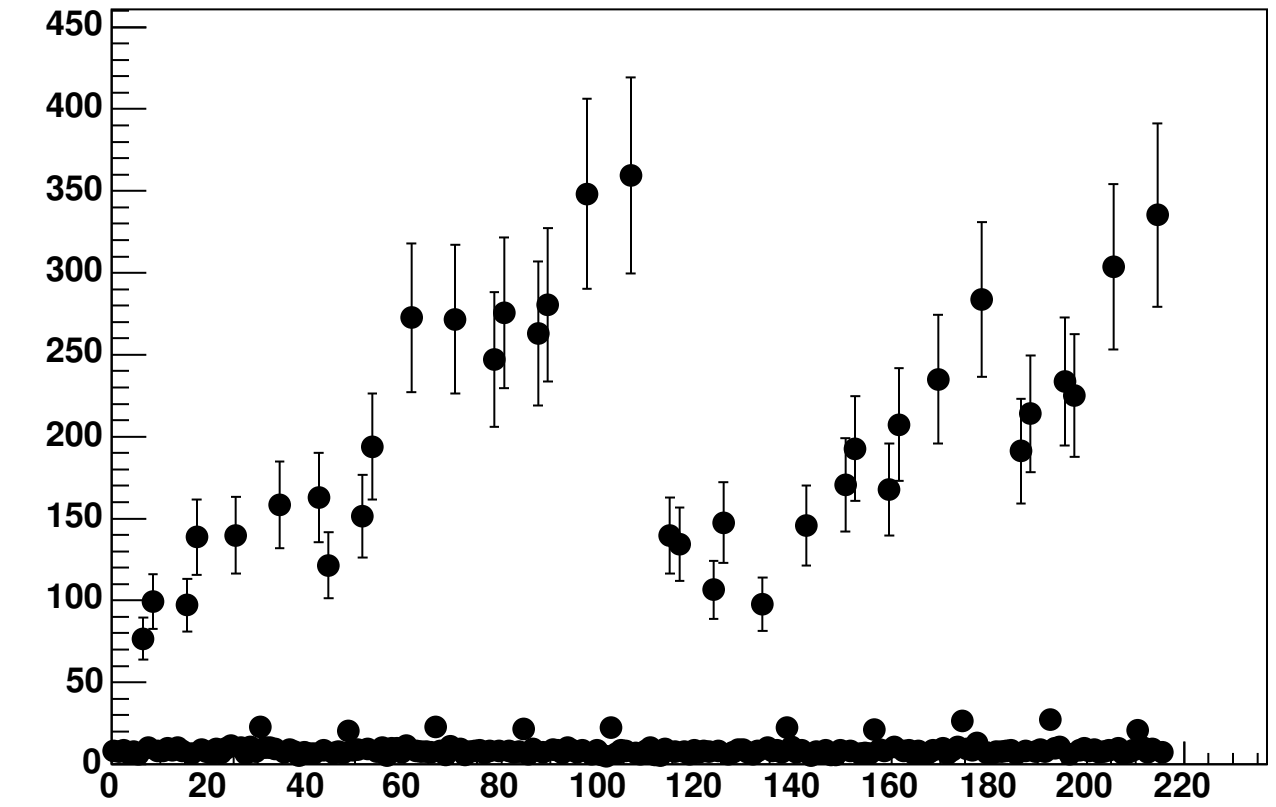
Enable 2, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



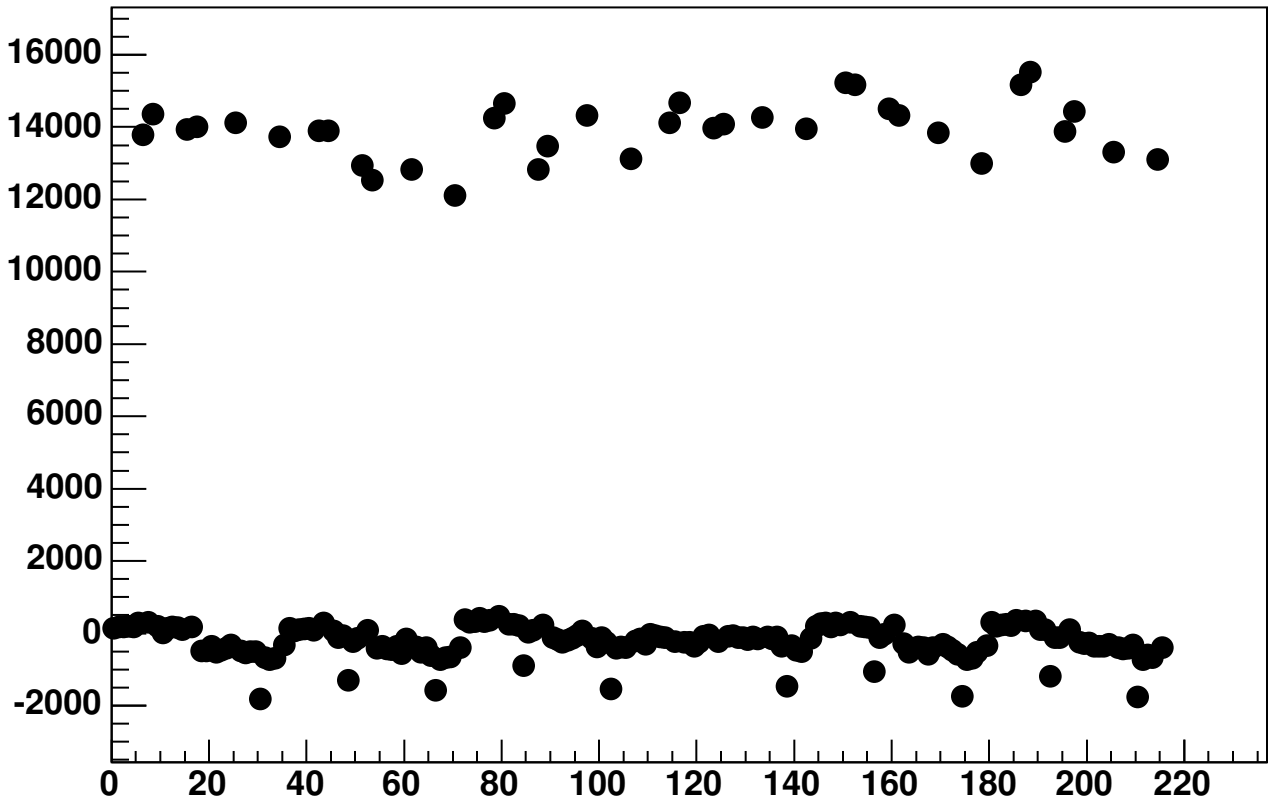
Enable 2, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



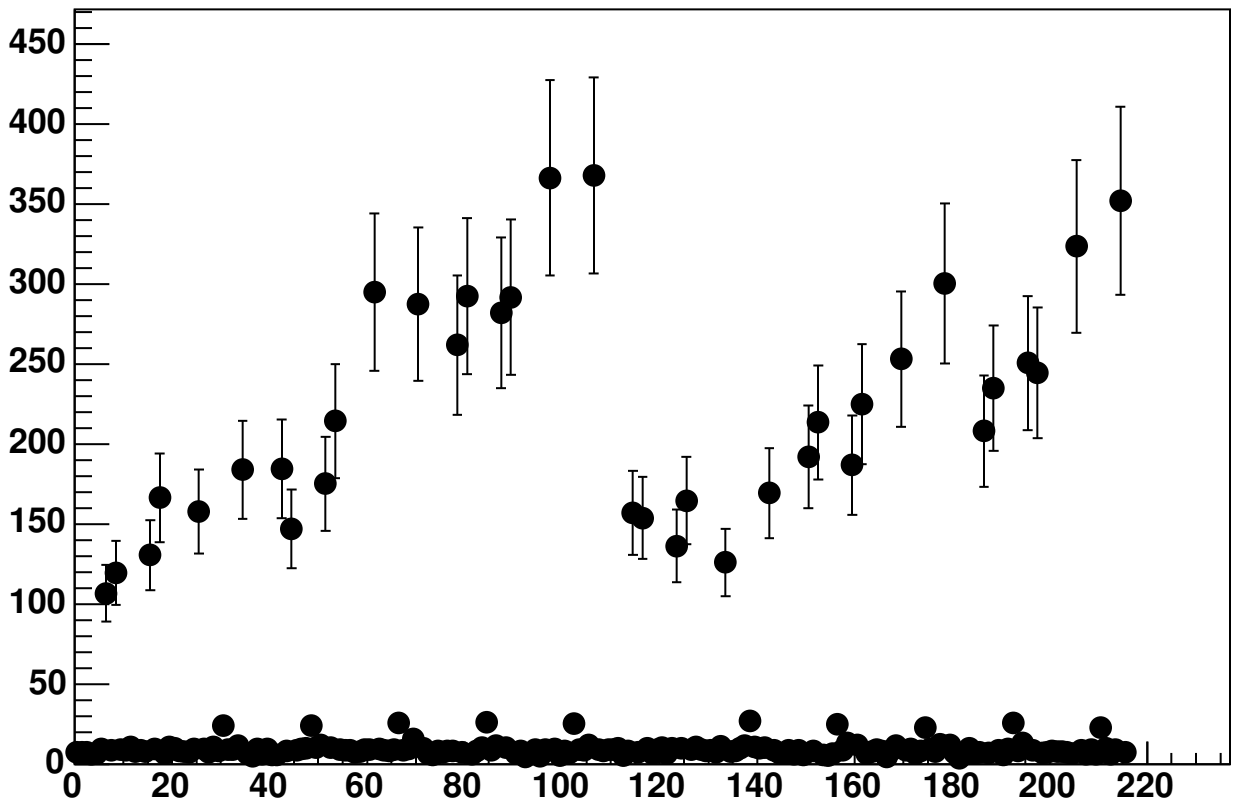
Enable 2, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



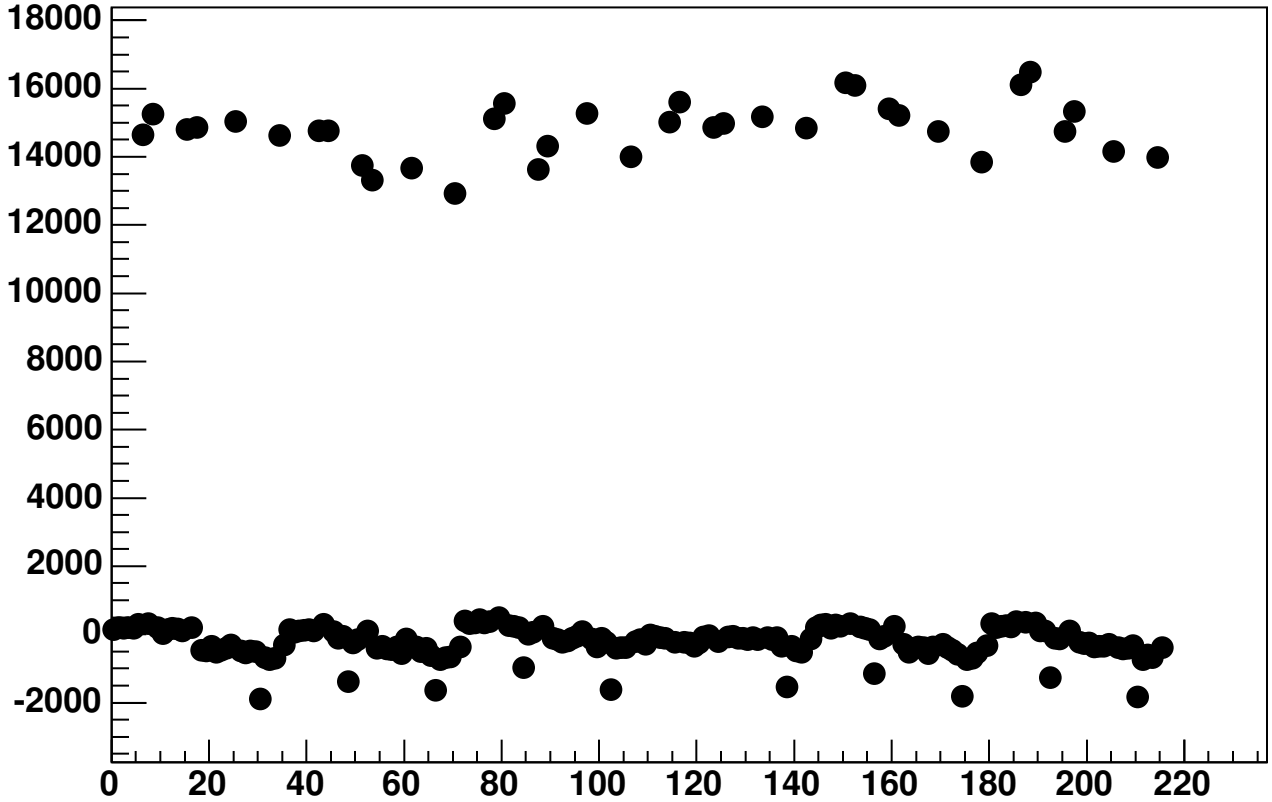
Enable 2, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



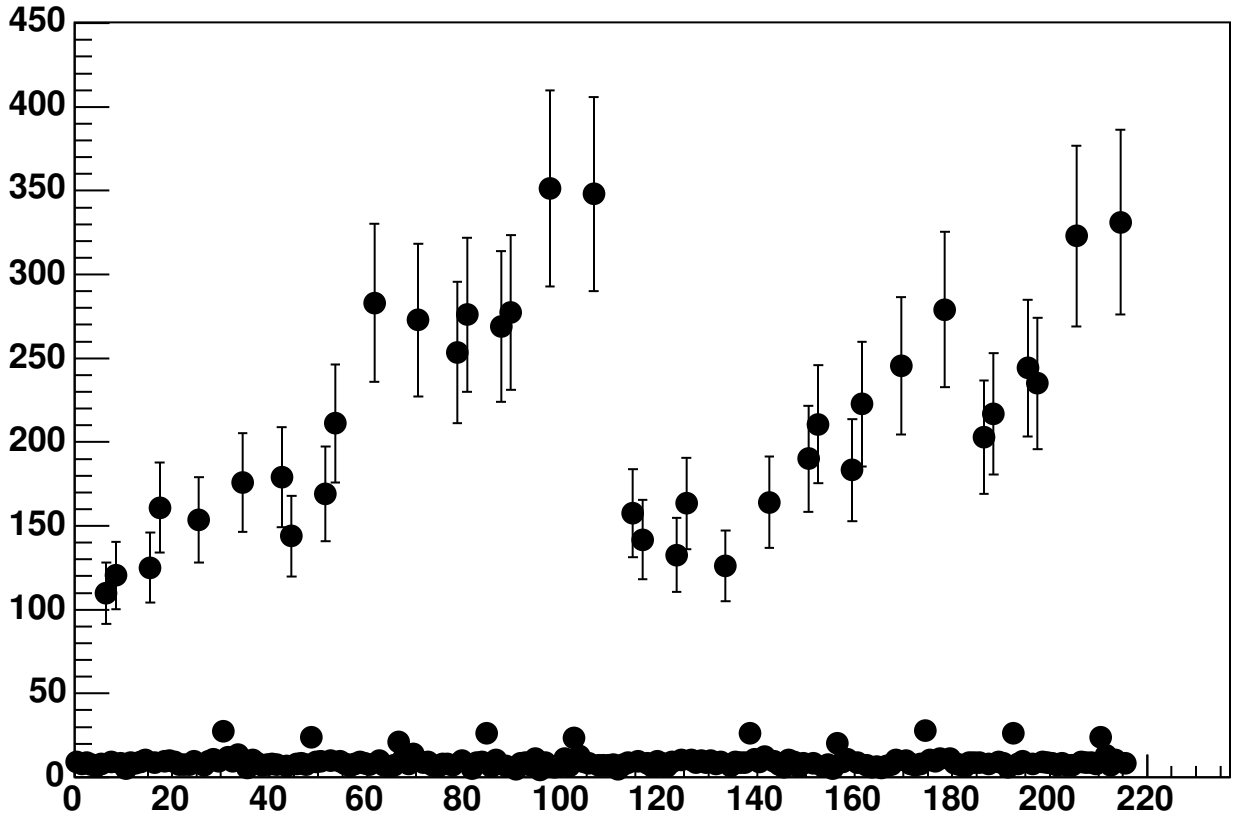
Enable 2, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



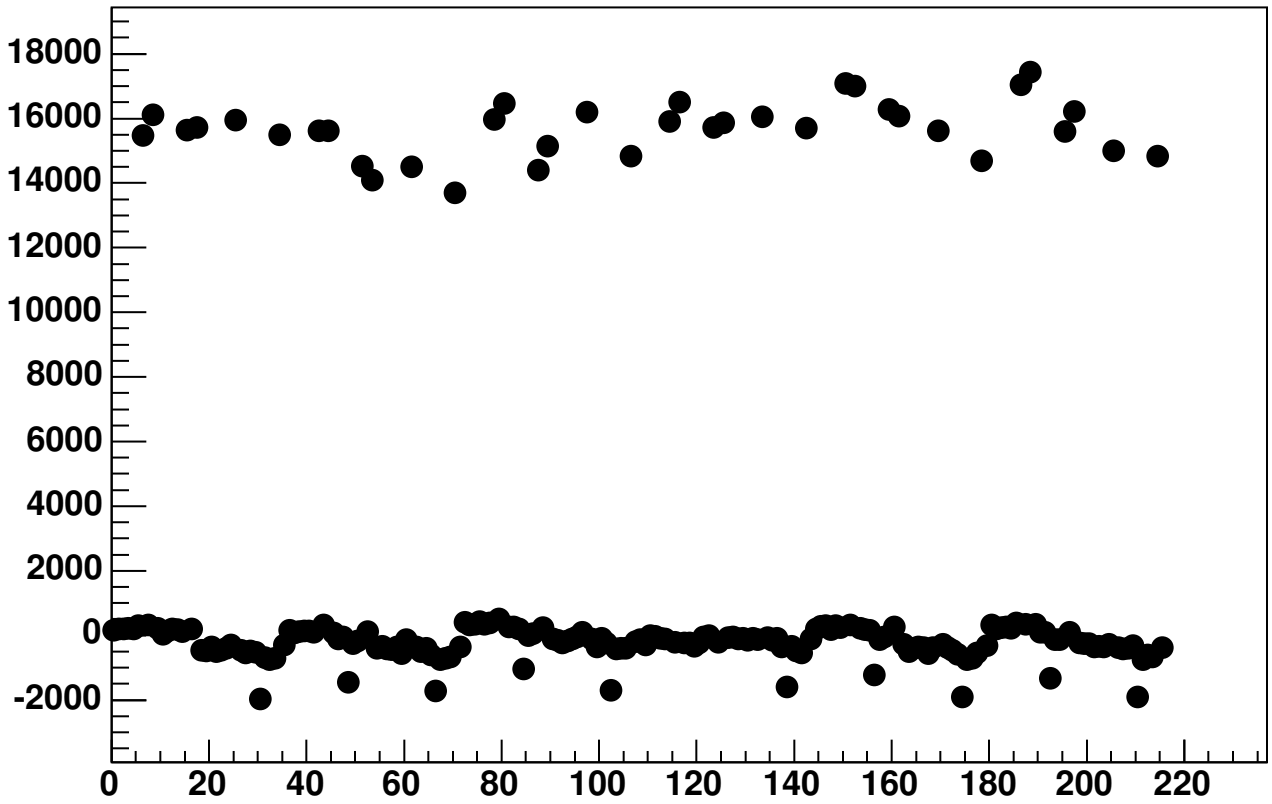
Enable 2, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



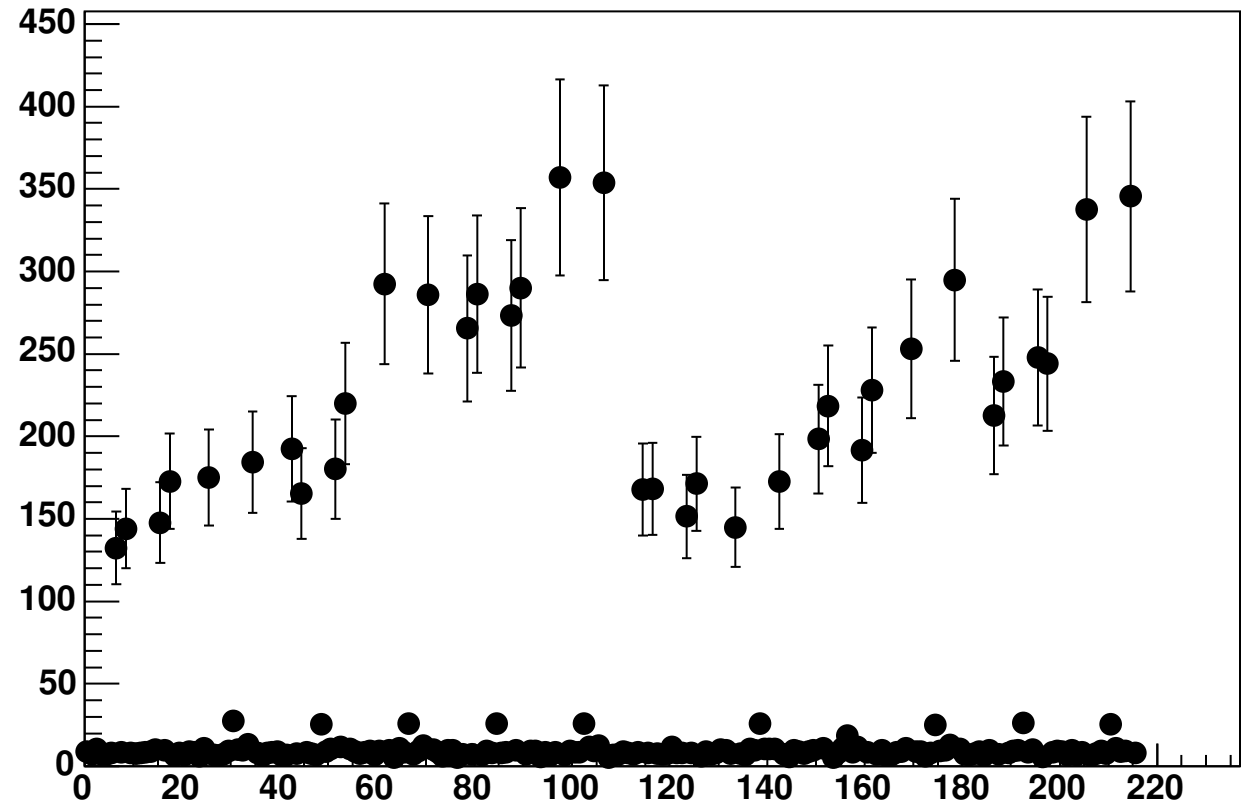
Enable 2, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



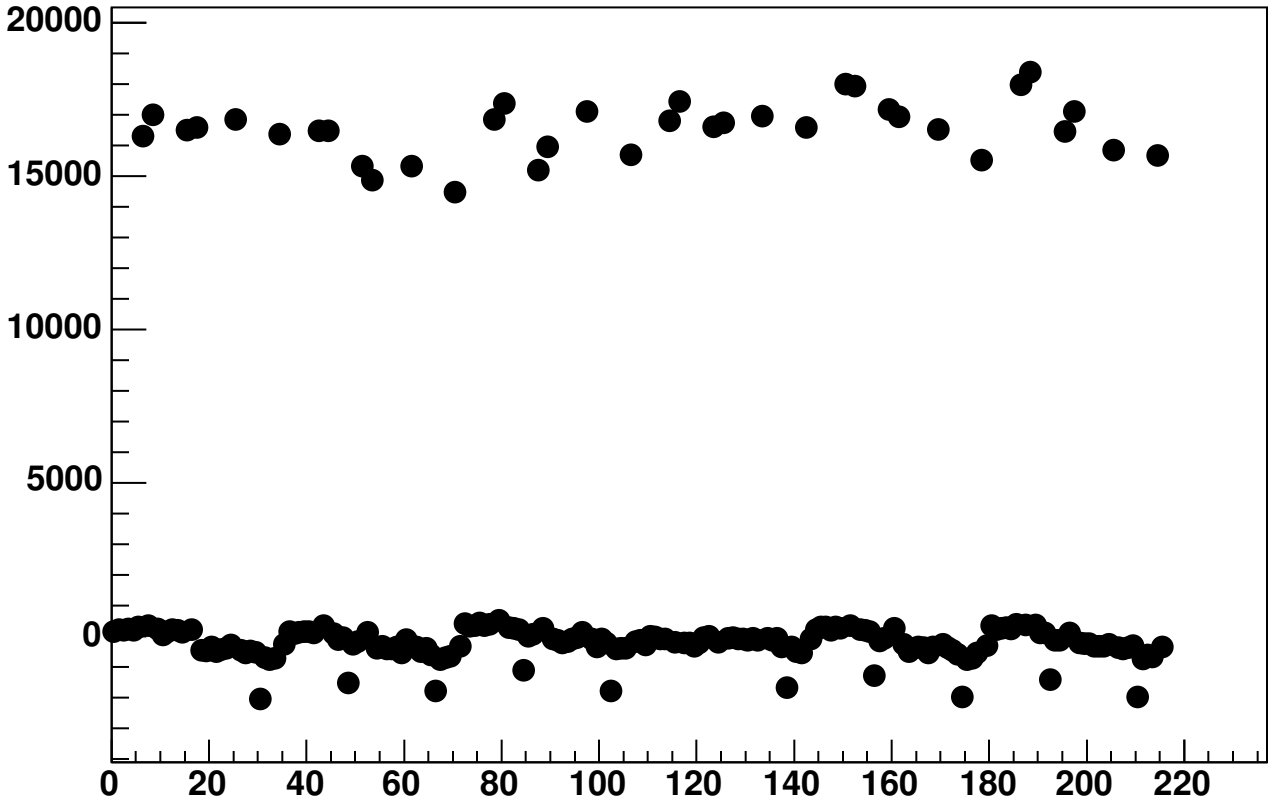
Enable 2, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



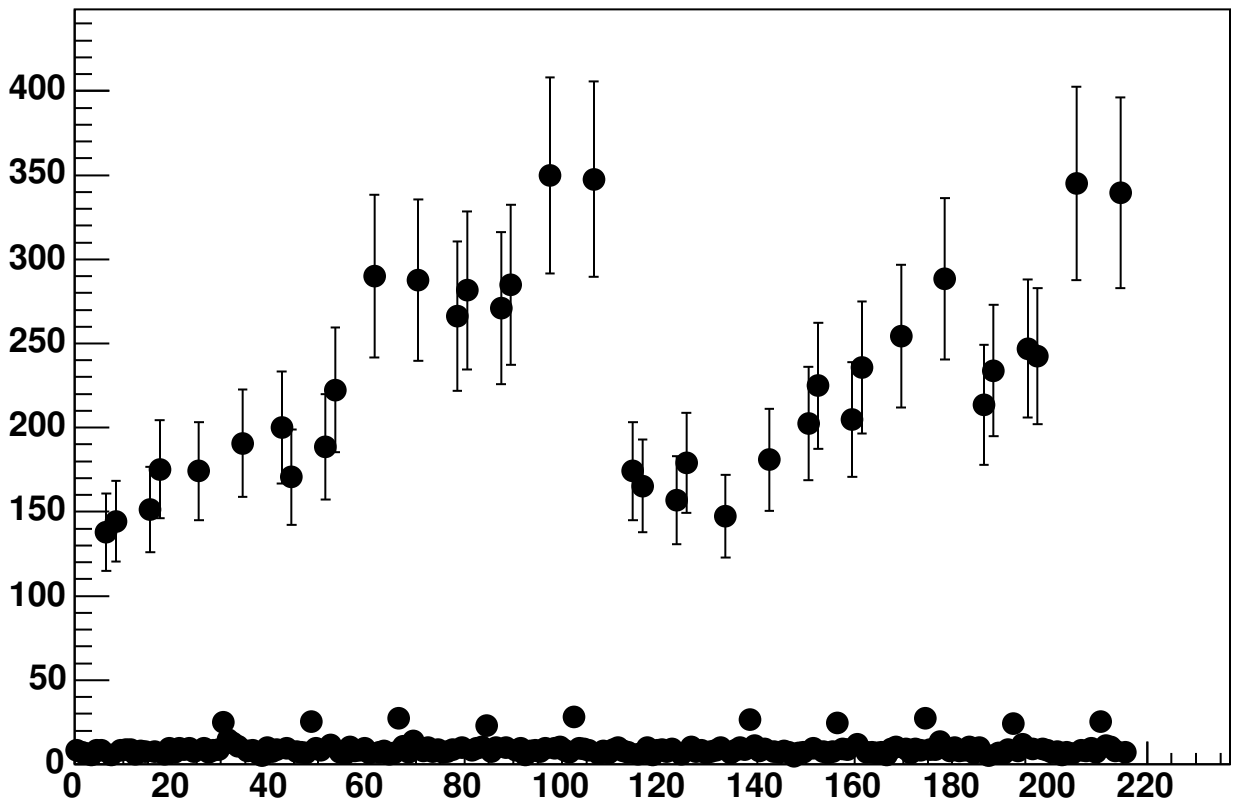
Enable 2, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



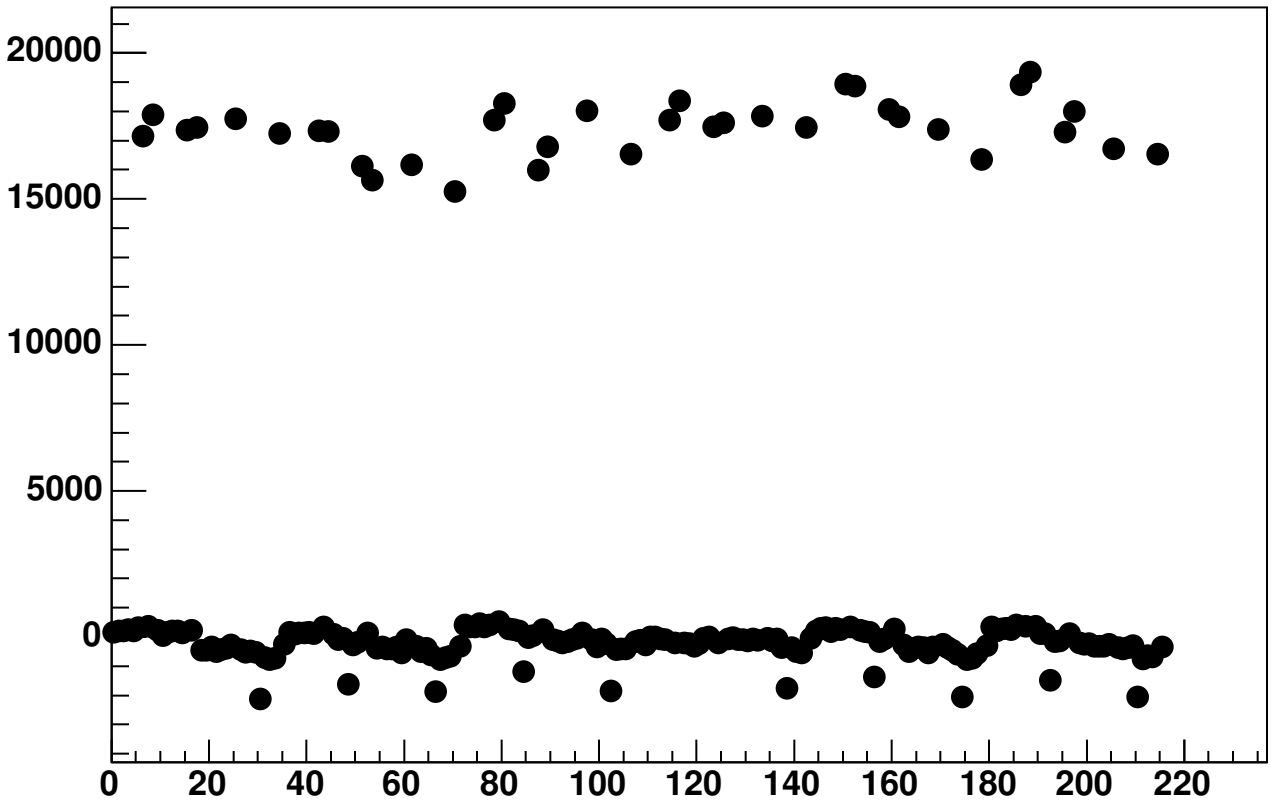
Enable 2, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



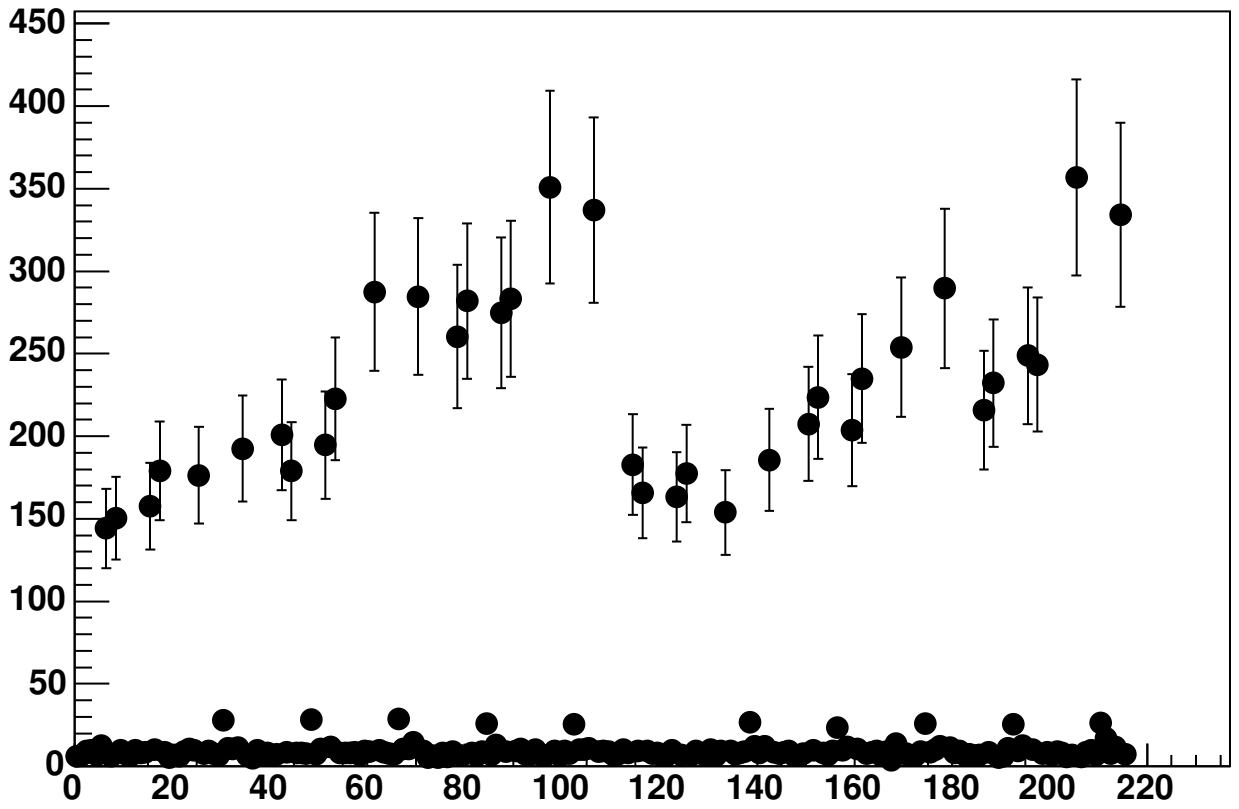
Enable 2, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



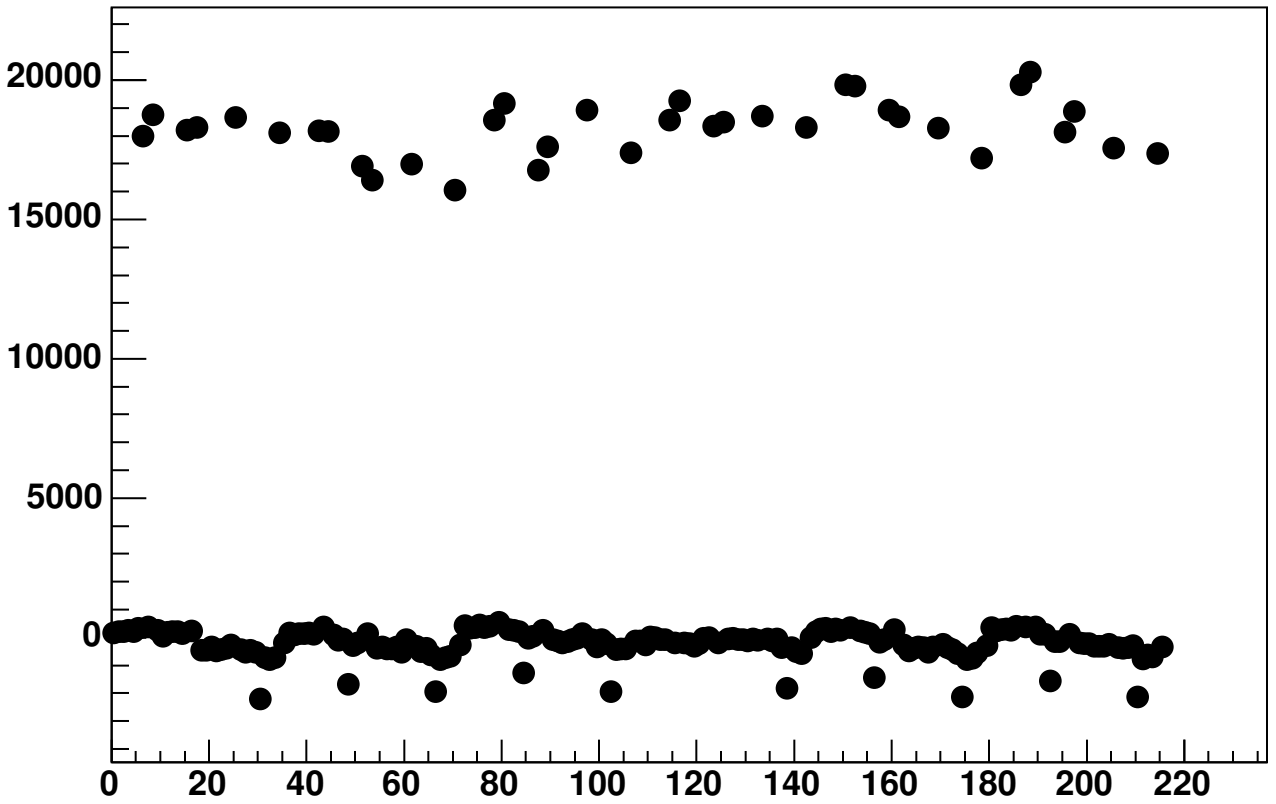
Enable 2, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



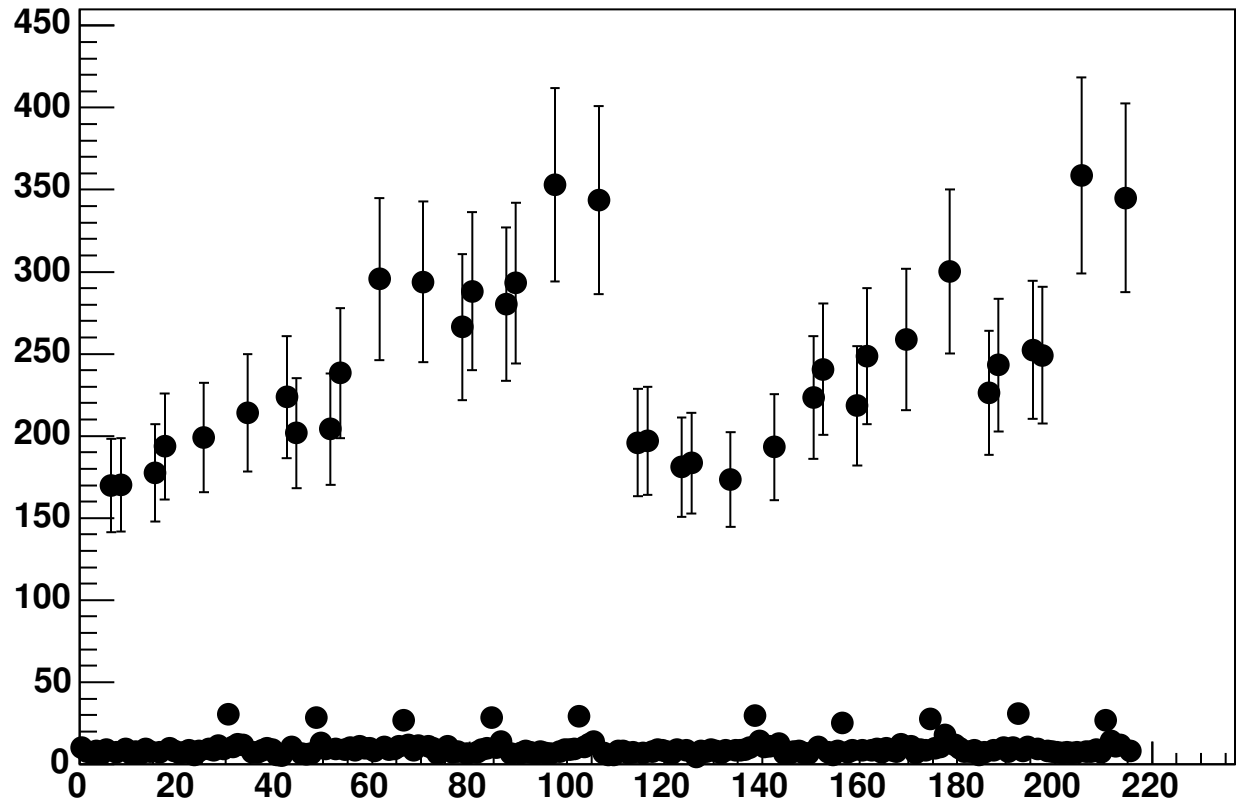
Enable 2, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



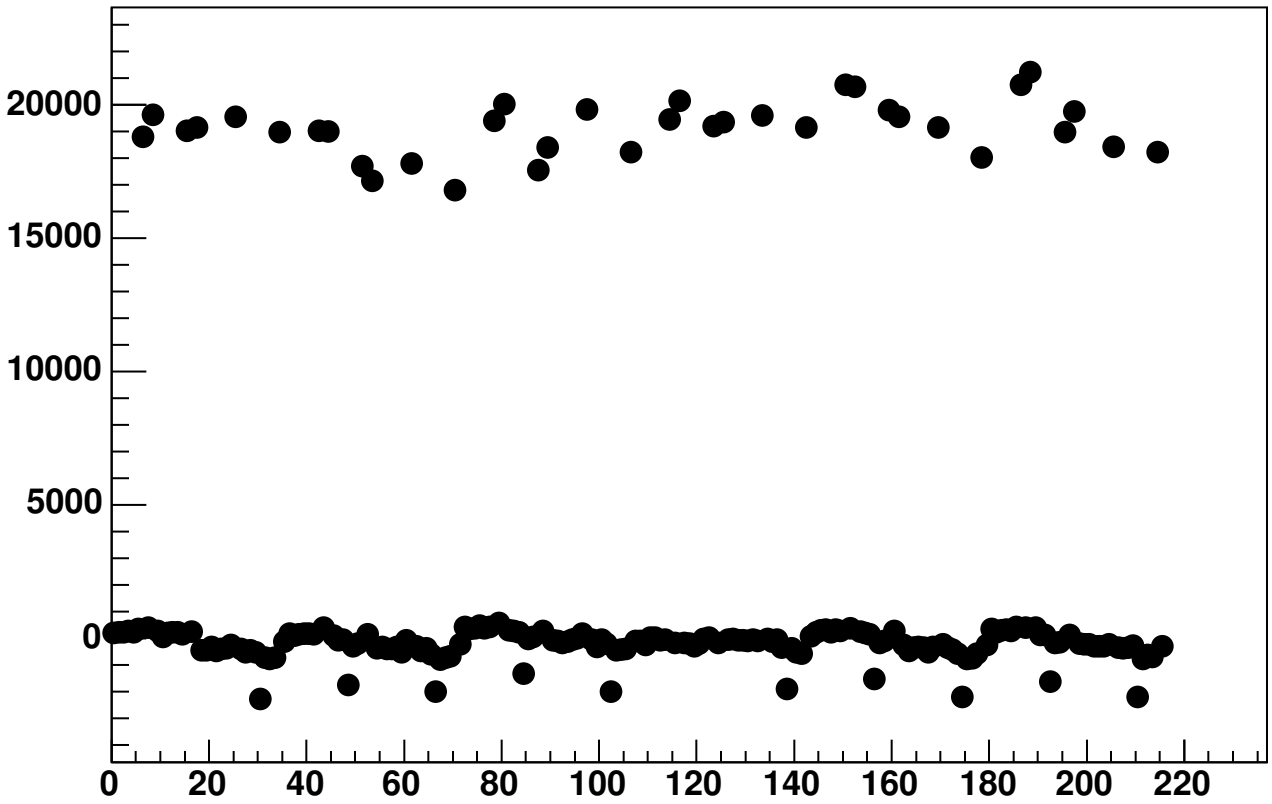
Enable 2, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



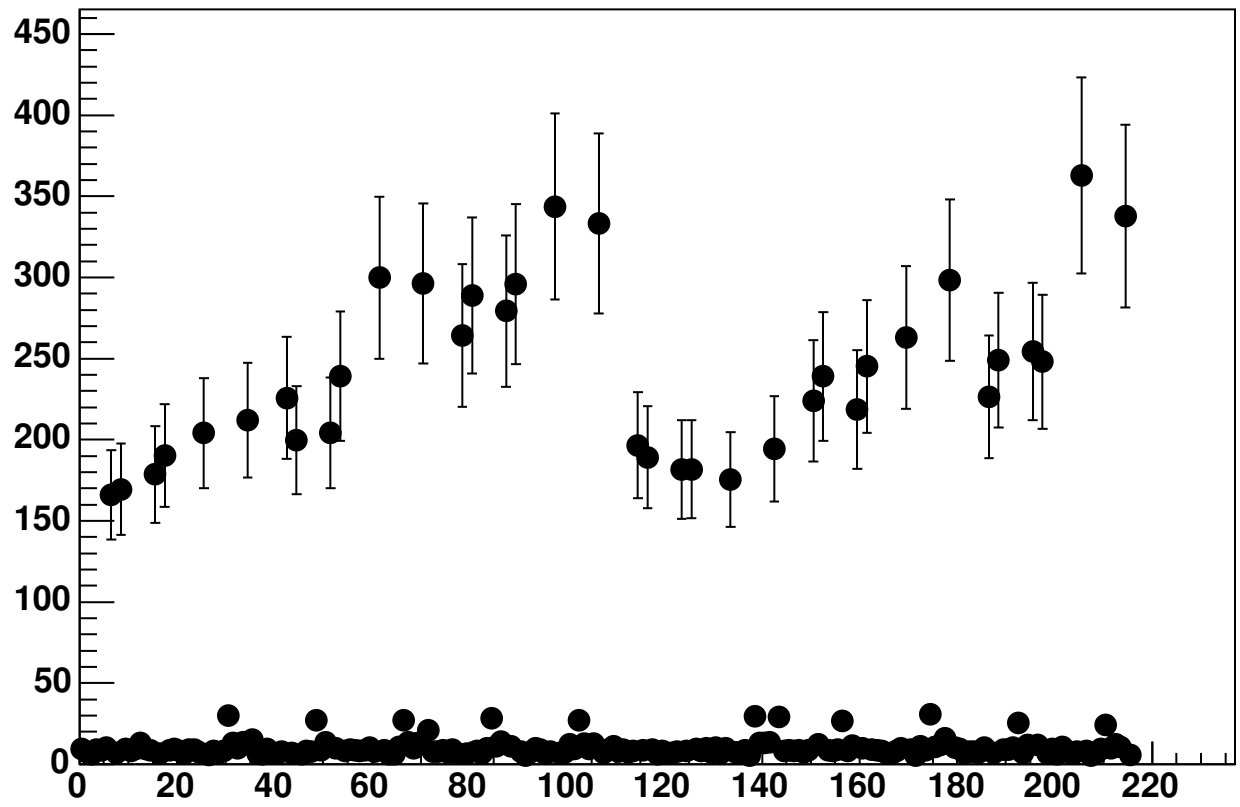
Enable 2, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



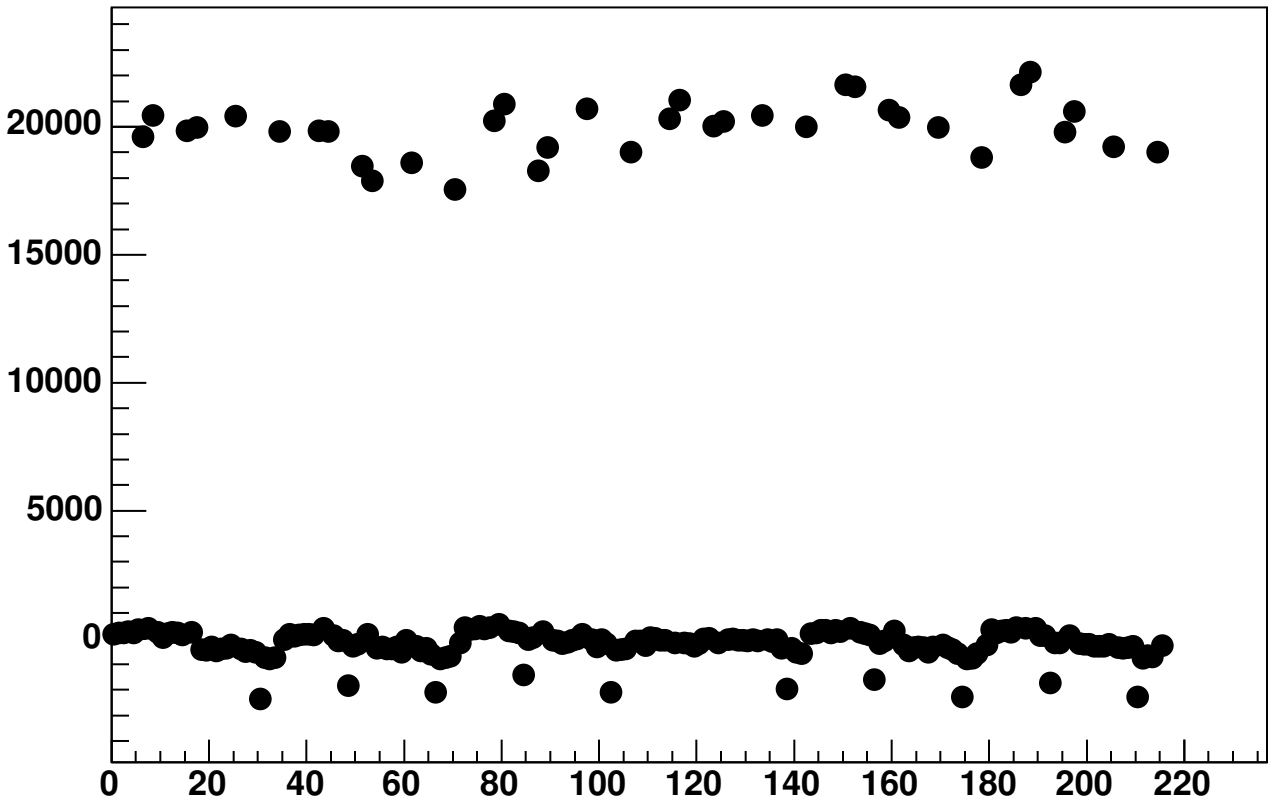
Enable 2, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



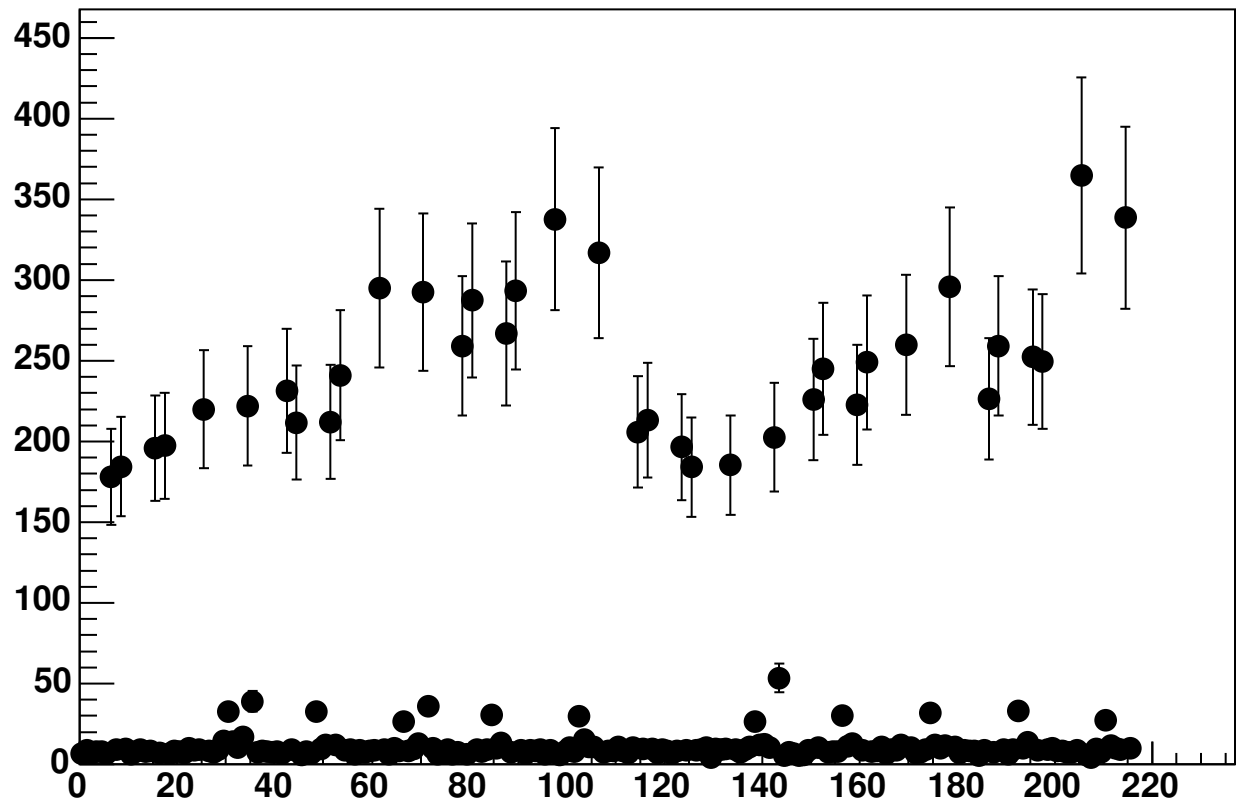
Enable 2, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



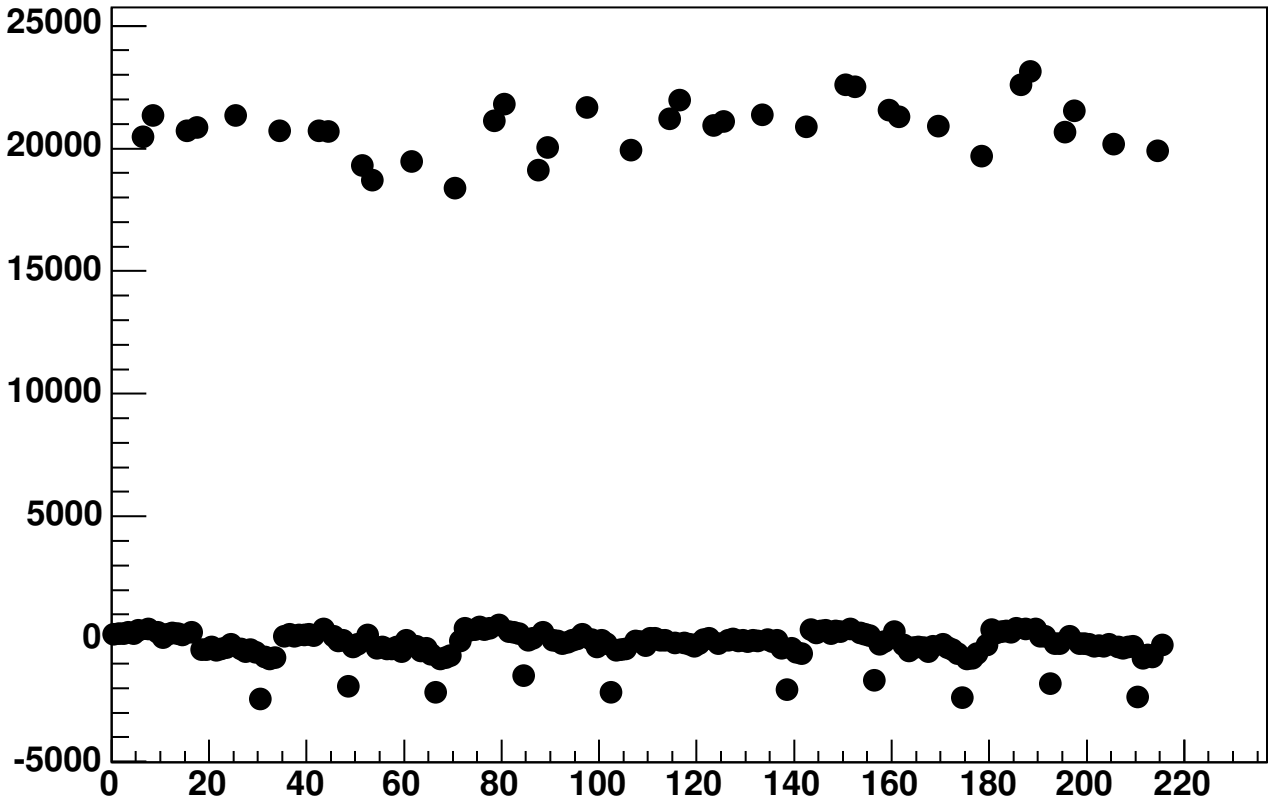
Enable 2, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



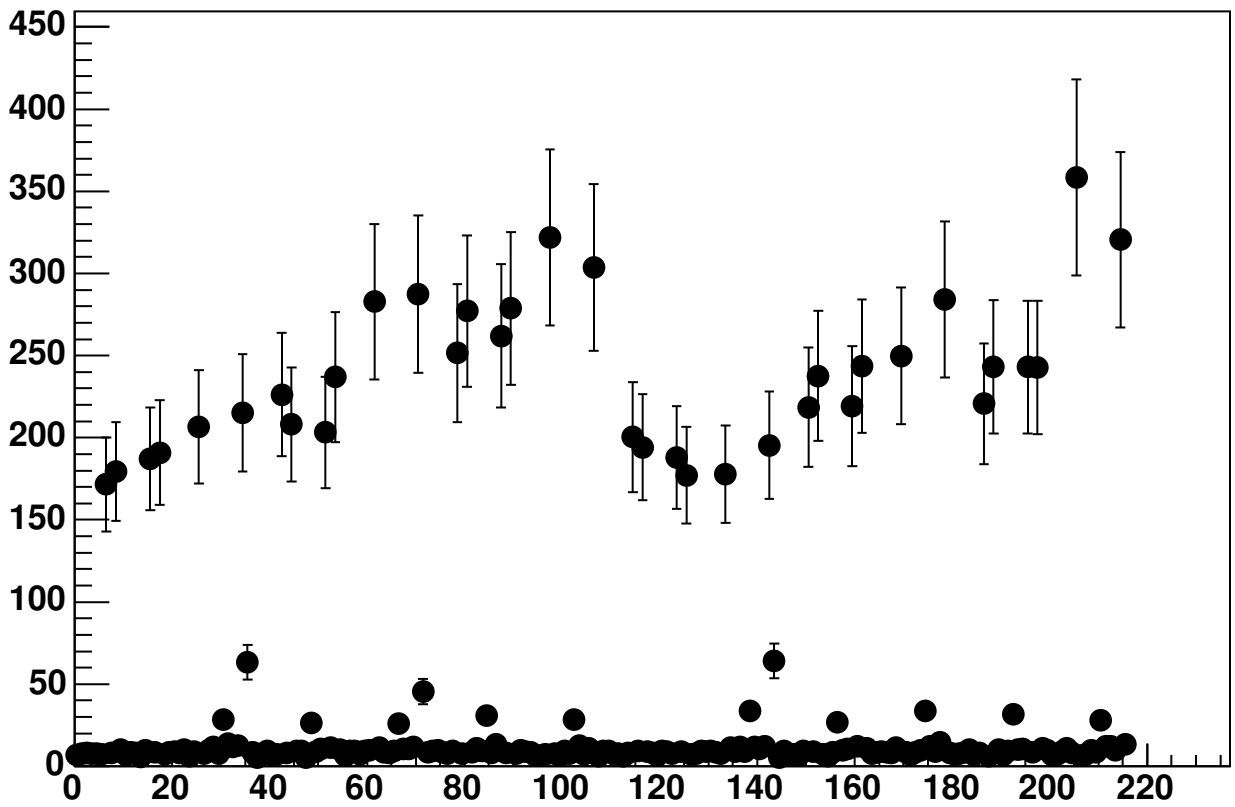
Enable 2, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



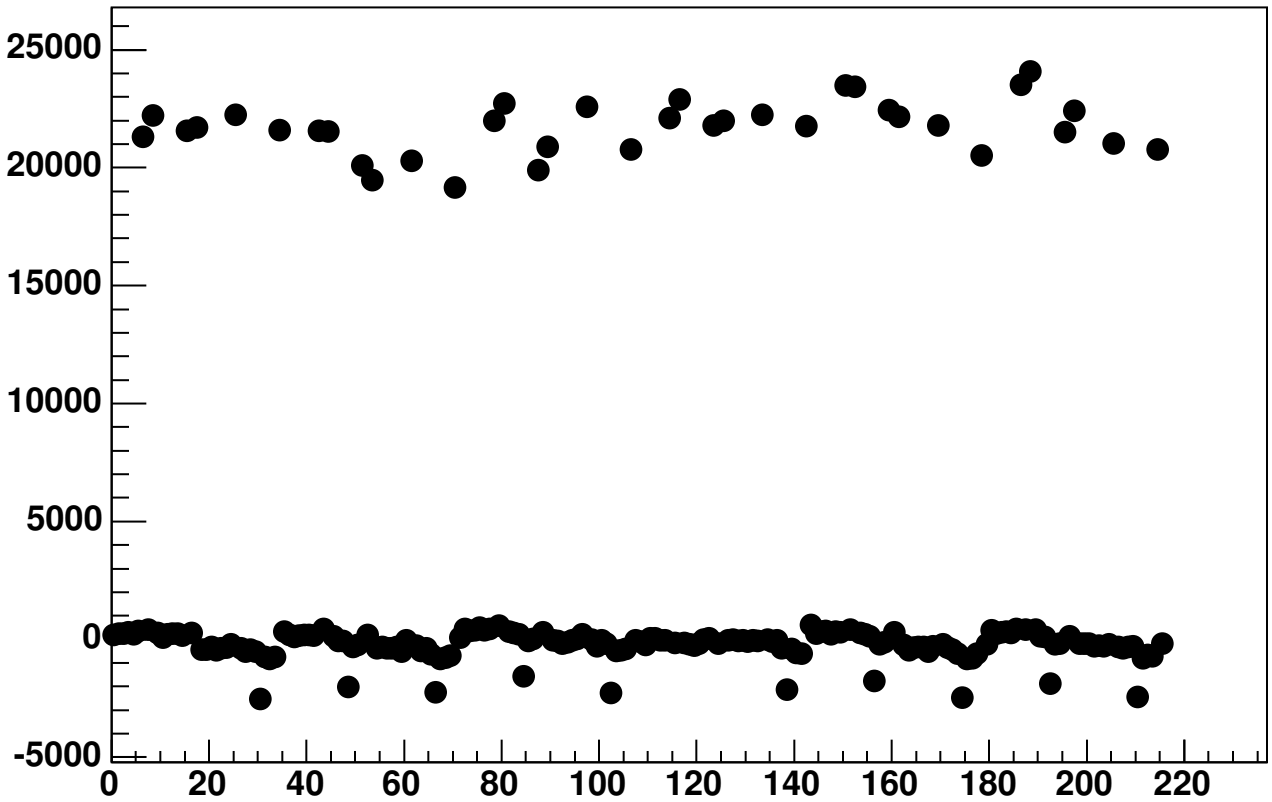
Enable 2, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



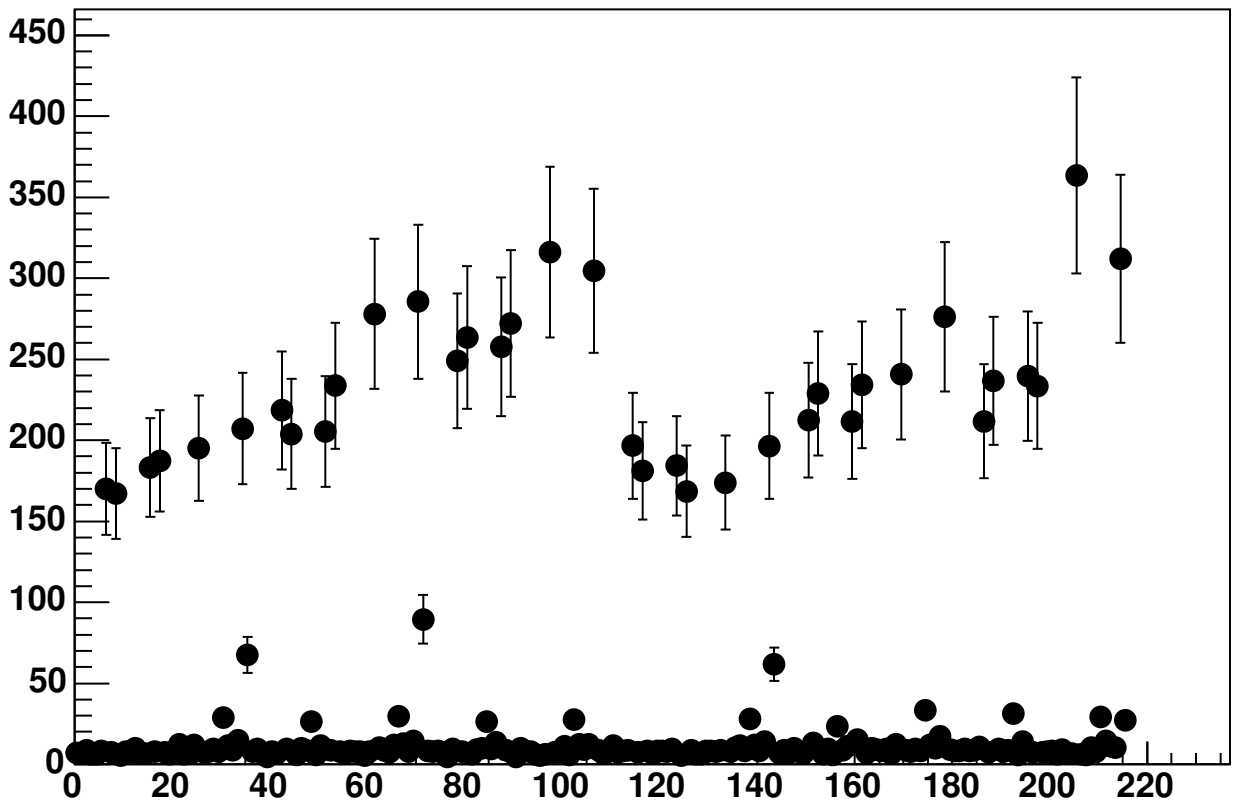
Enable 2, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



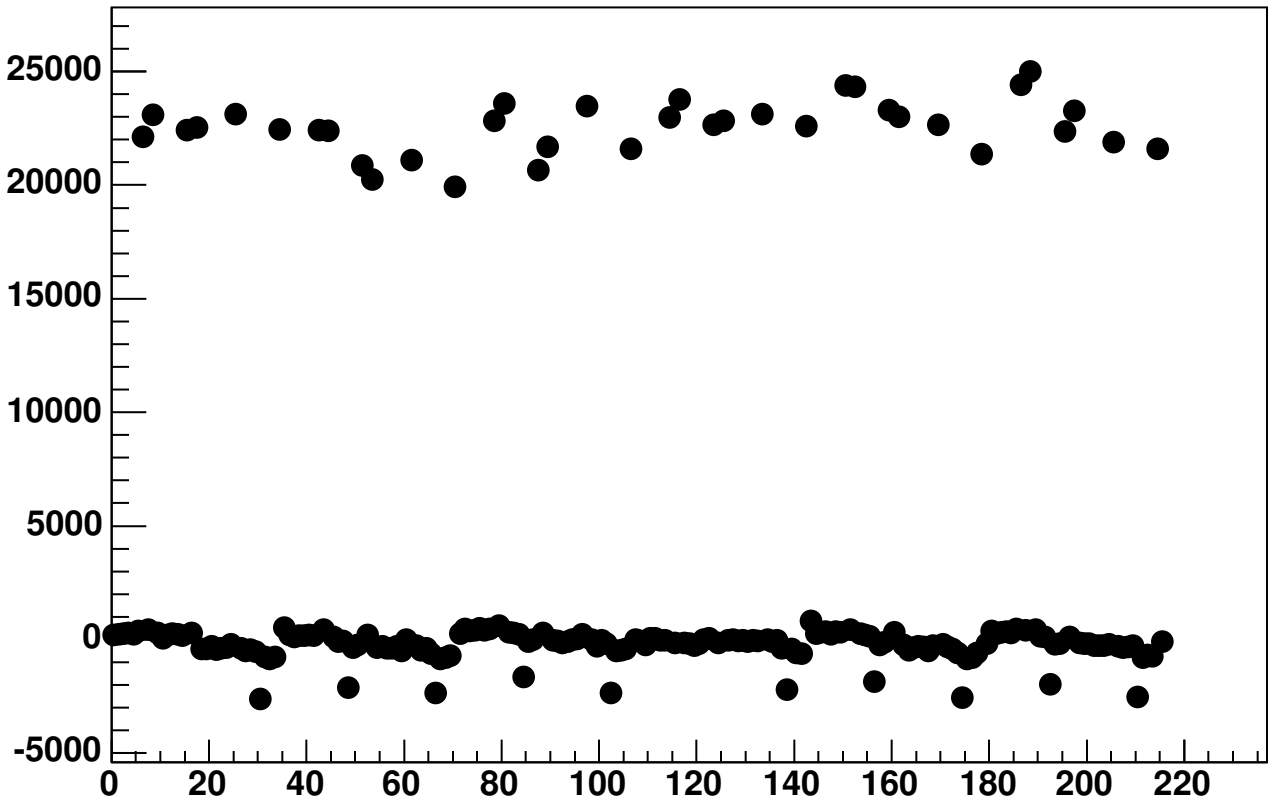
Enable 2, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



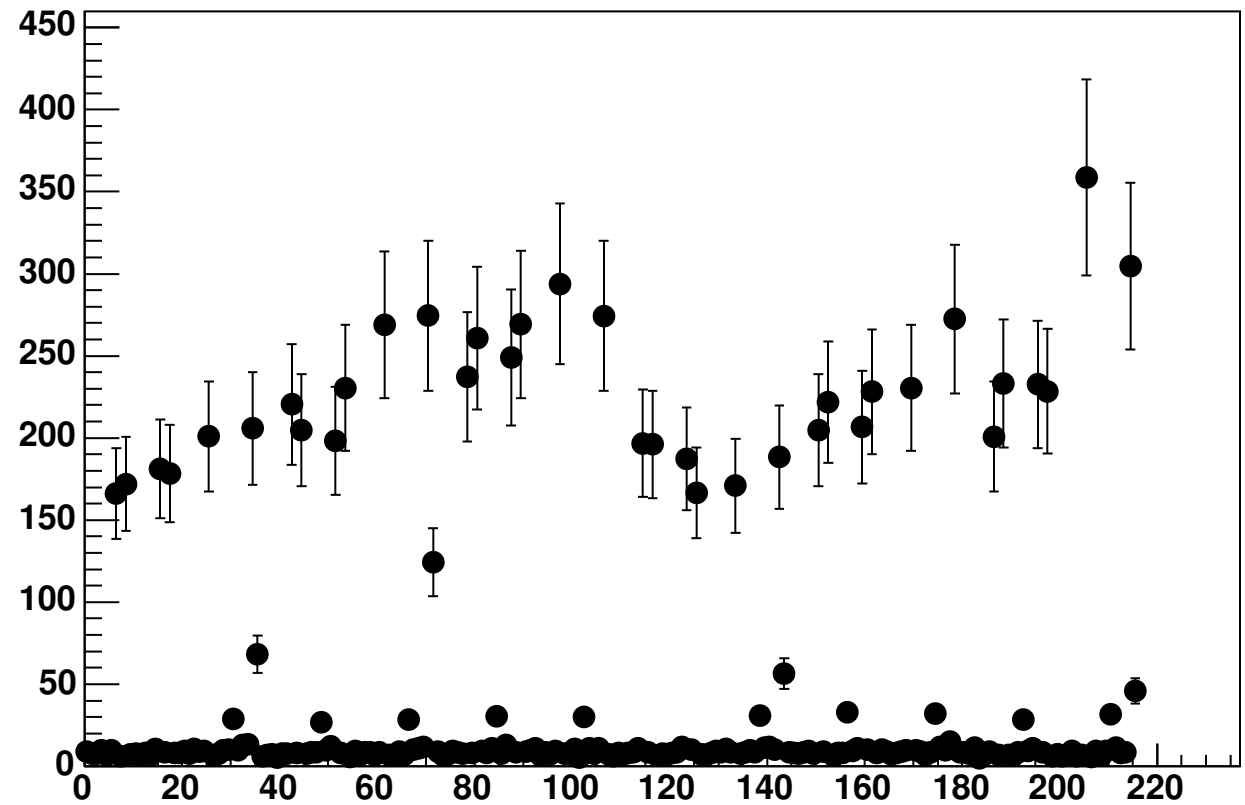
Enable 2, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



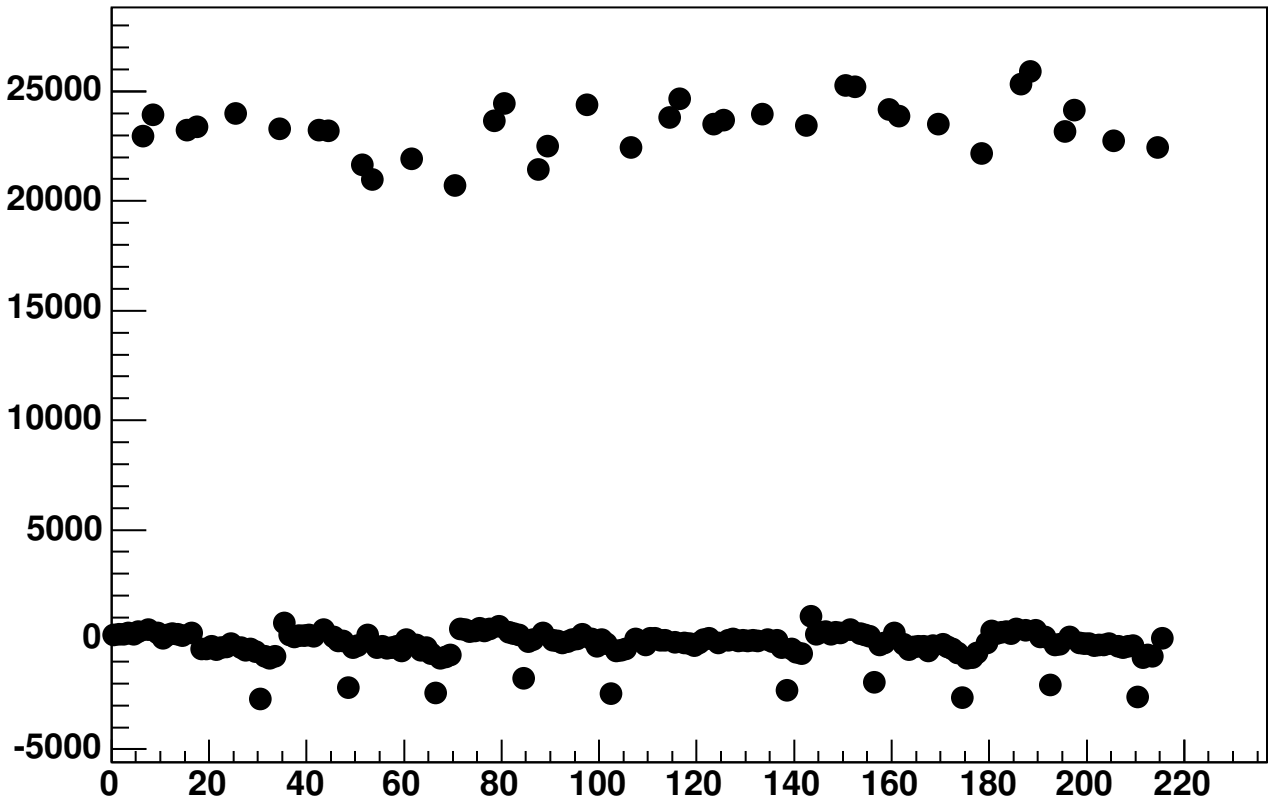
Enable 2, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



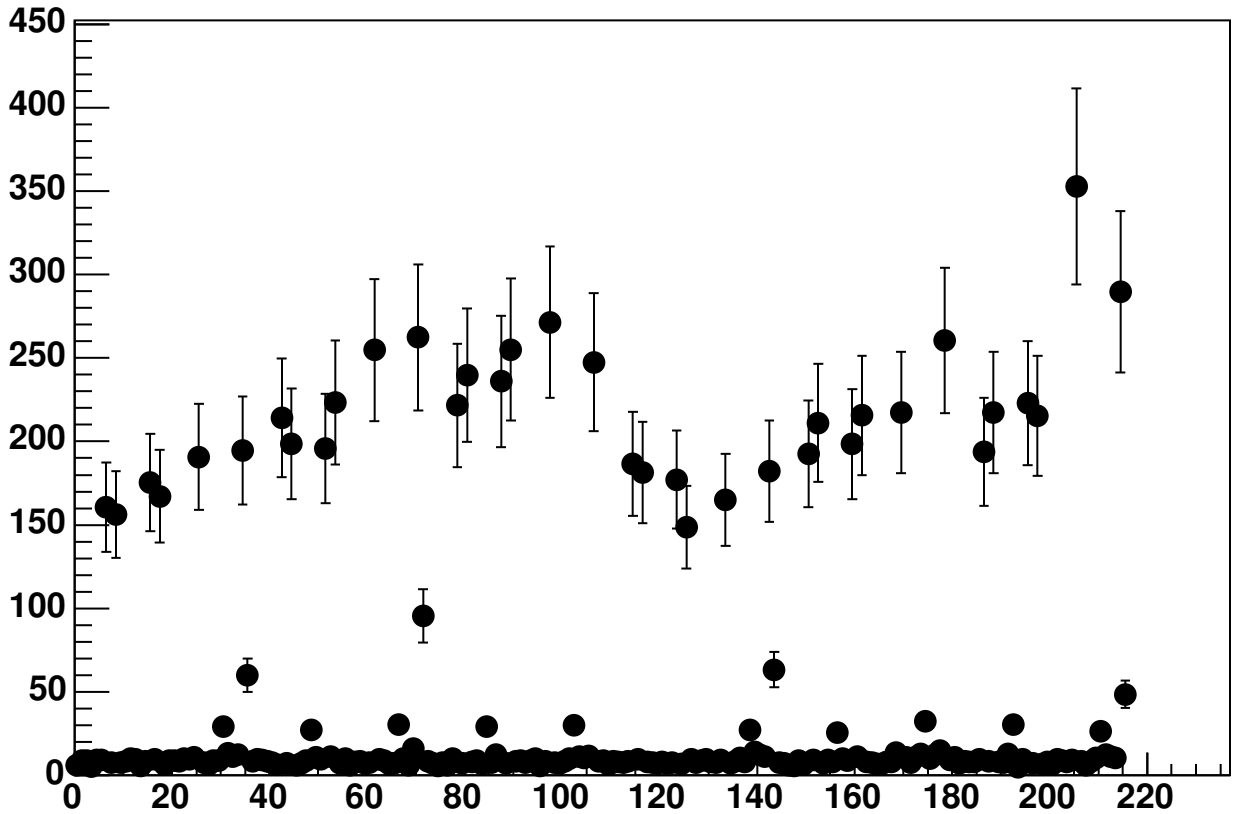
Enable 2, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



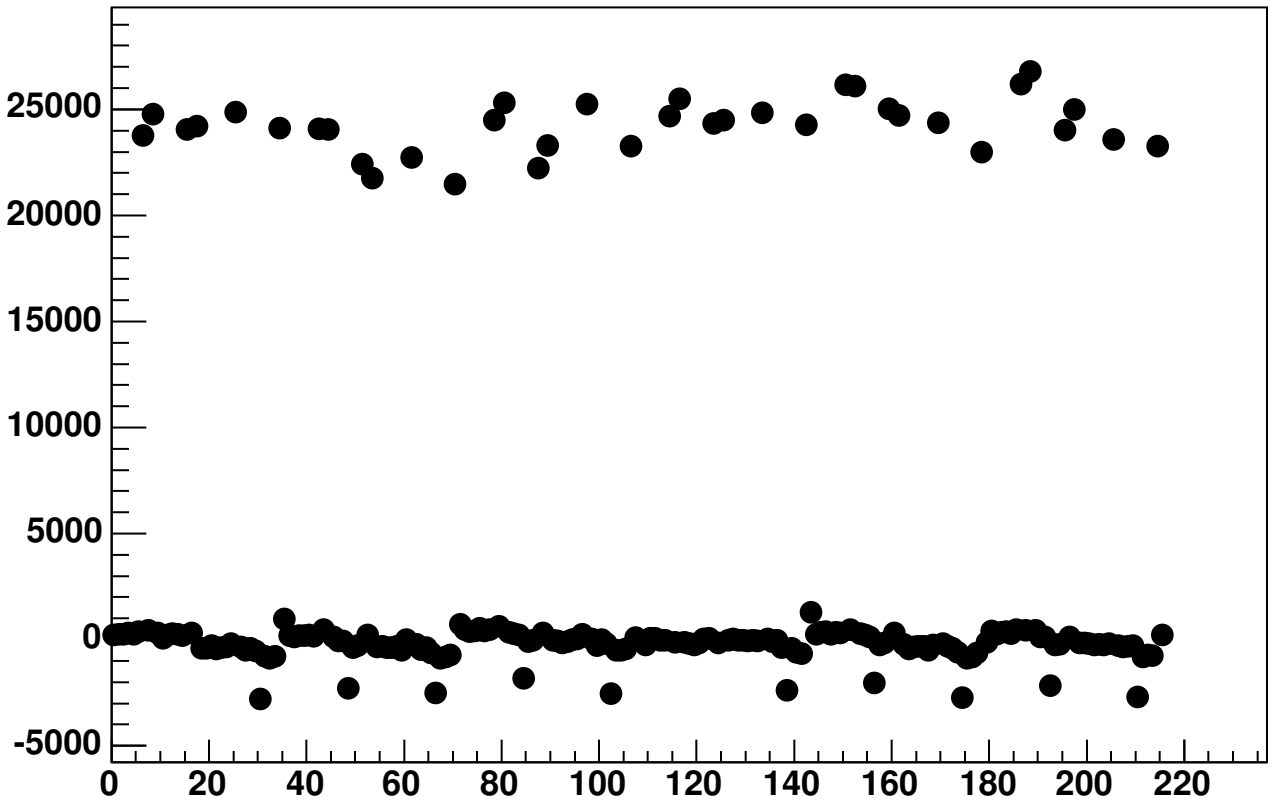
Enable 2, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



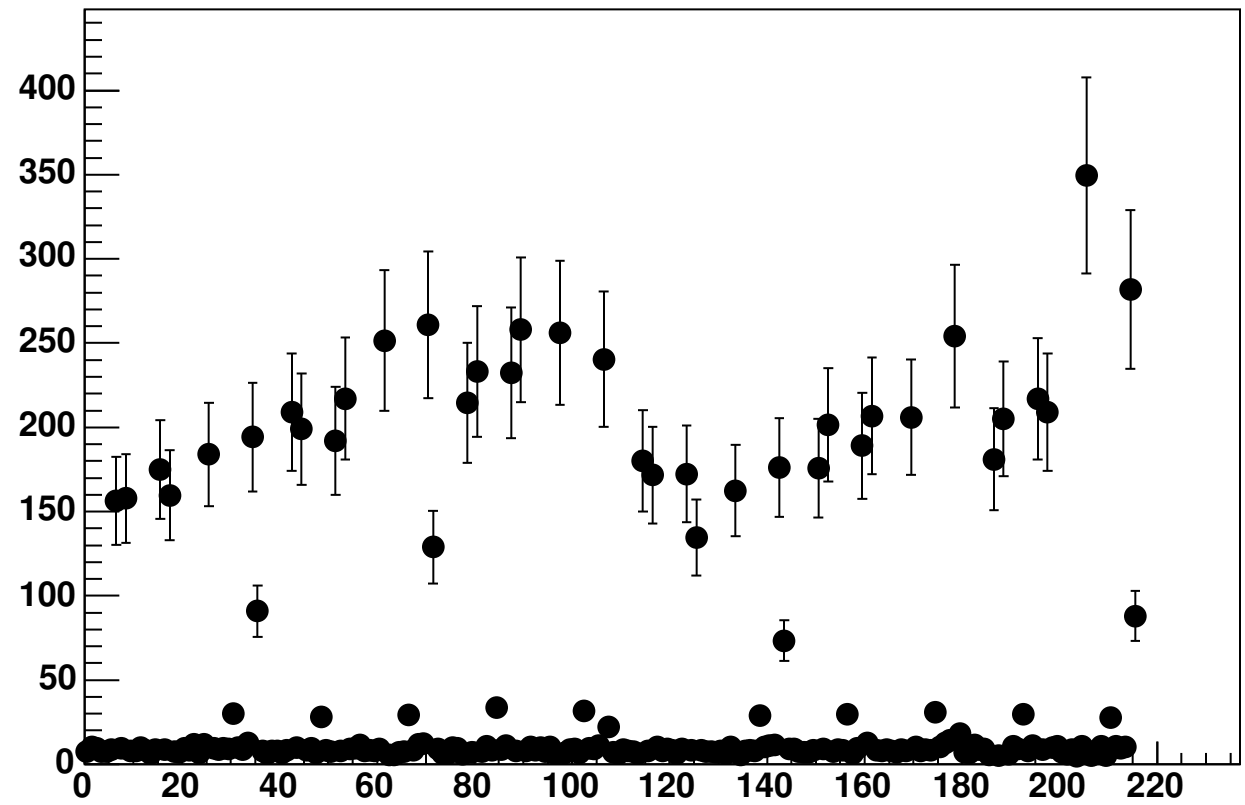
Enable 2, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



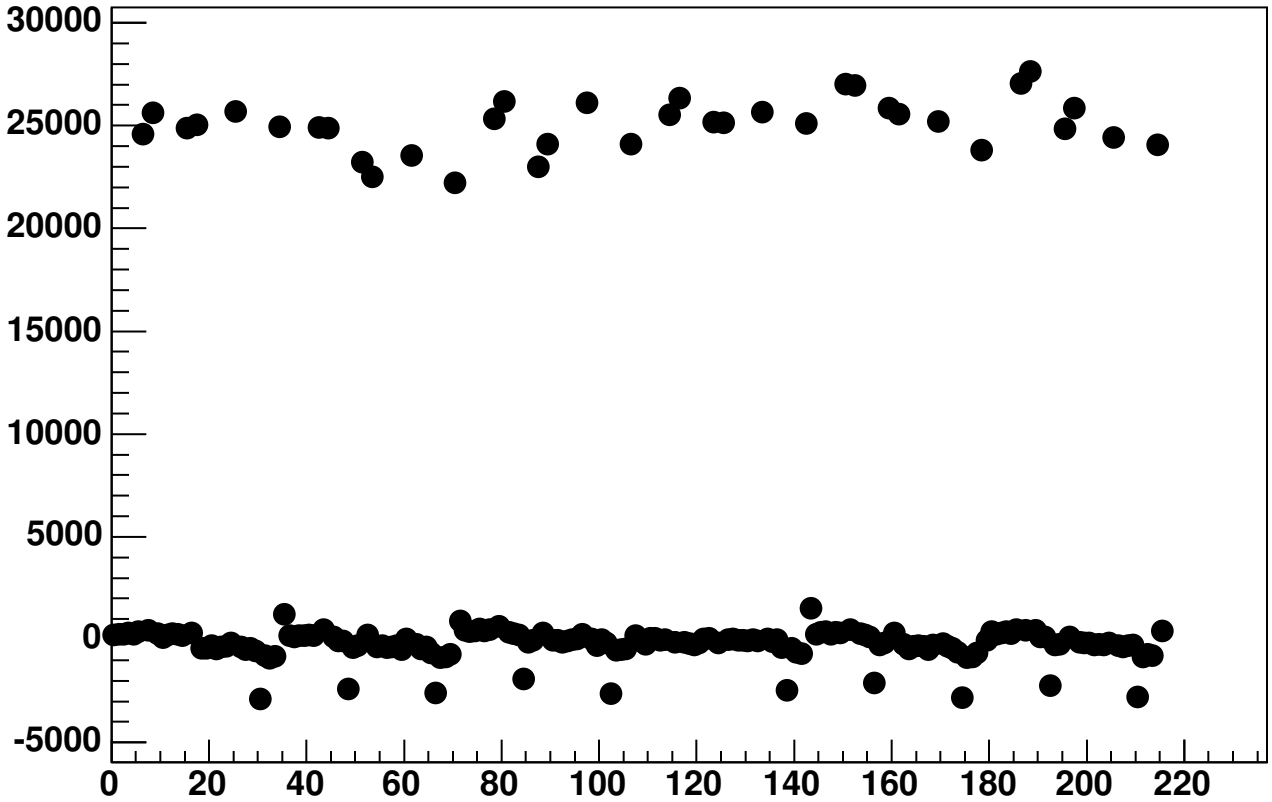
Enable 2, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



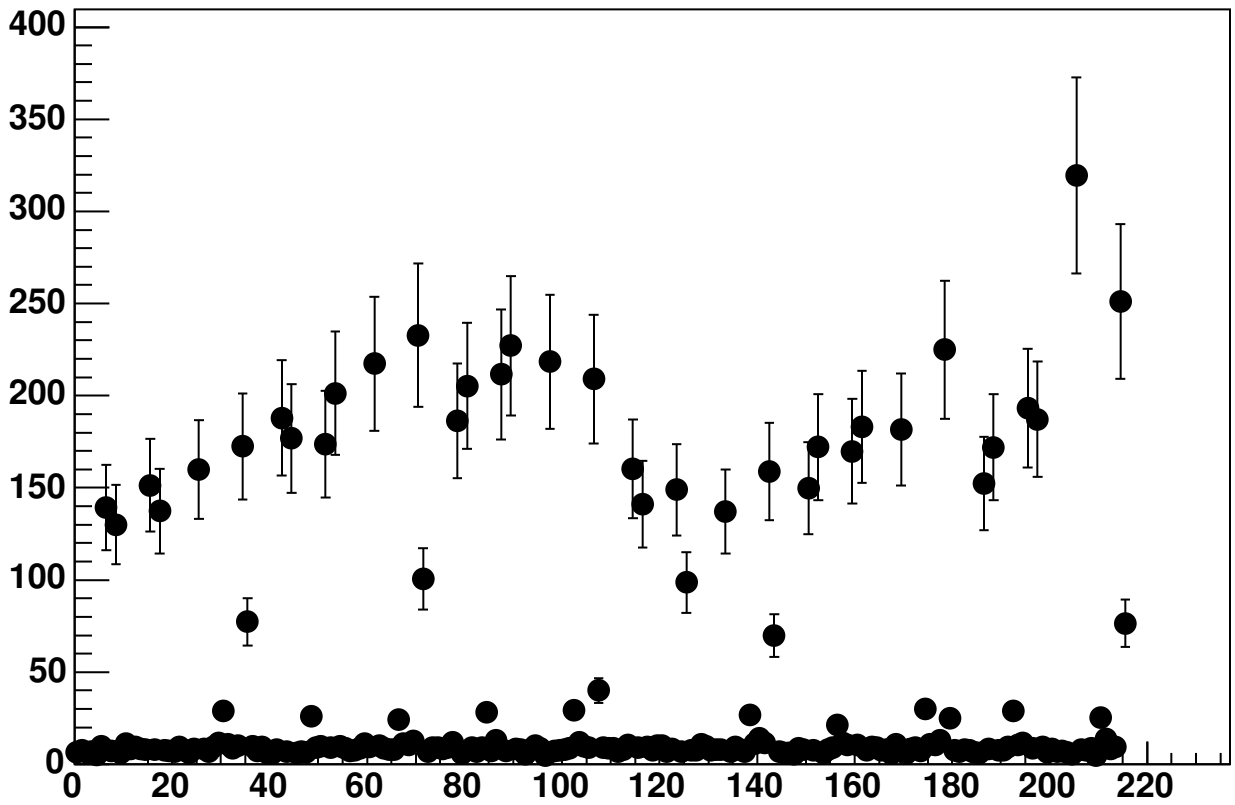
Enable 2, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



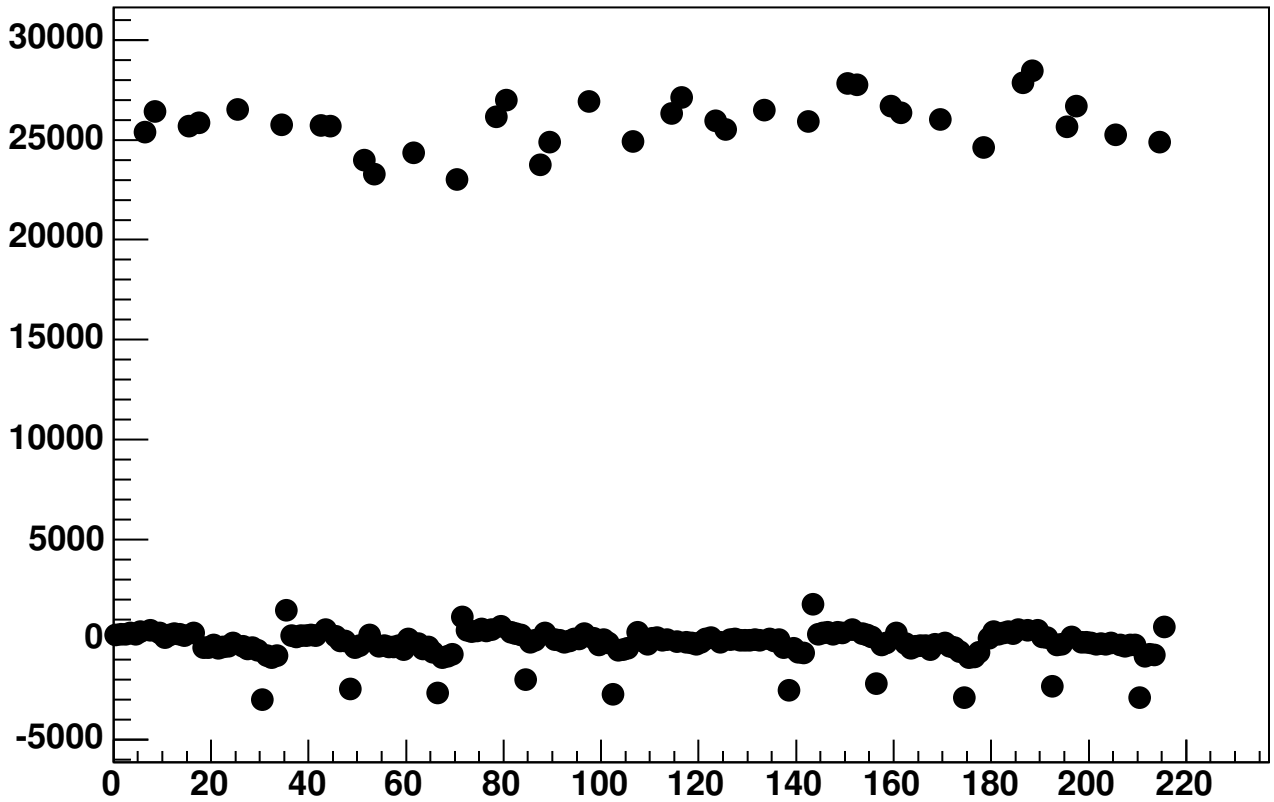
Enable 2, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



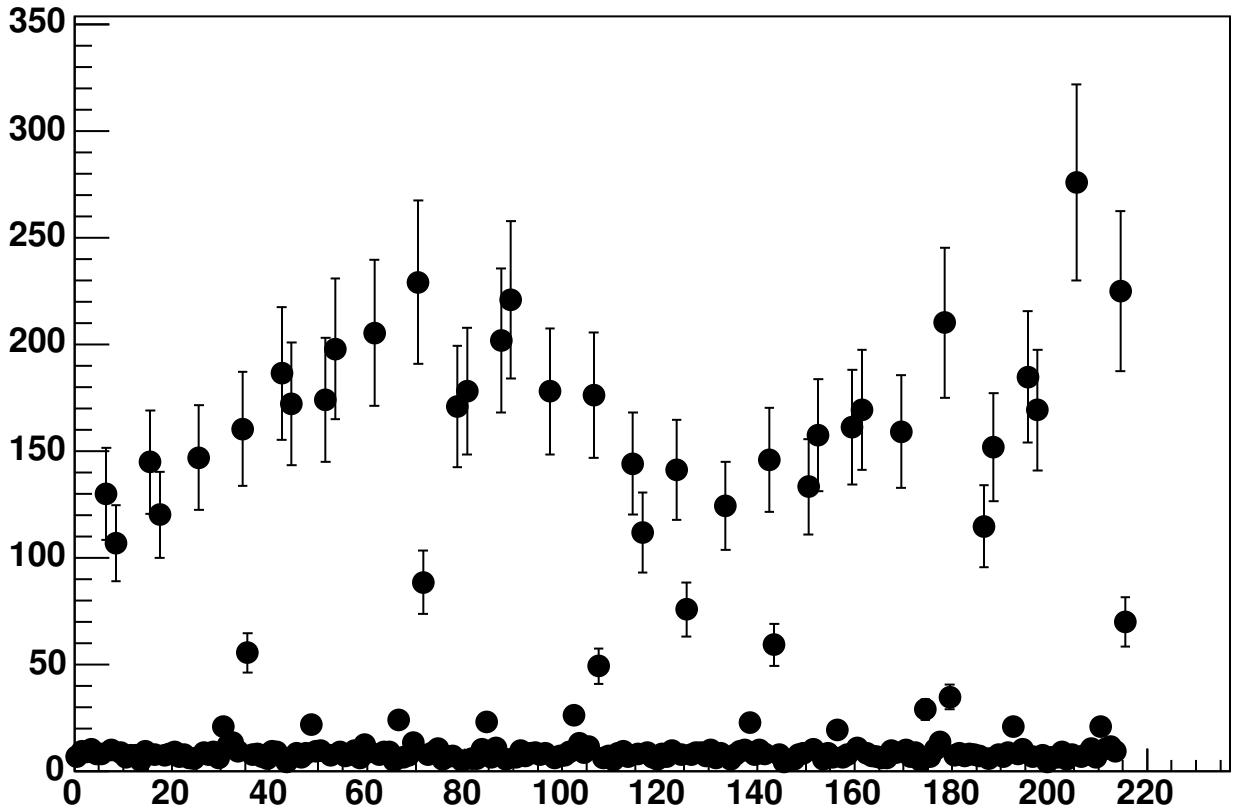
Enable 2, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



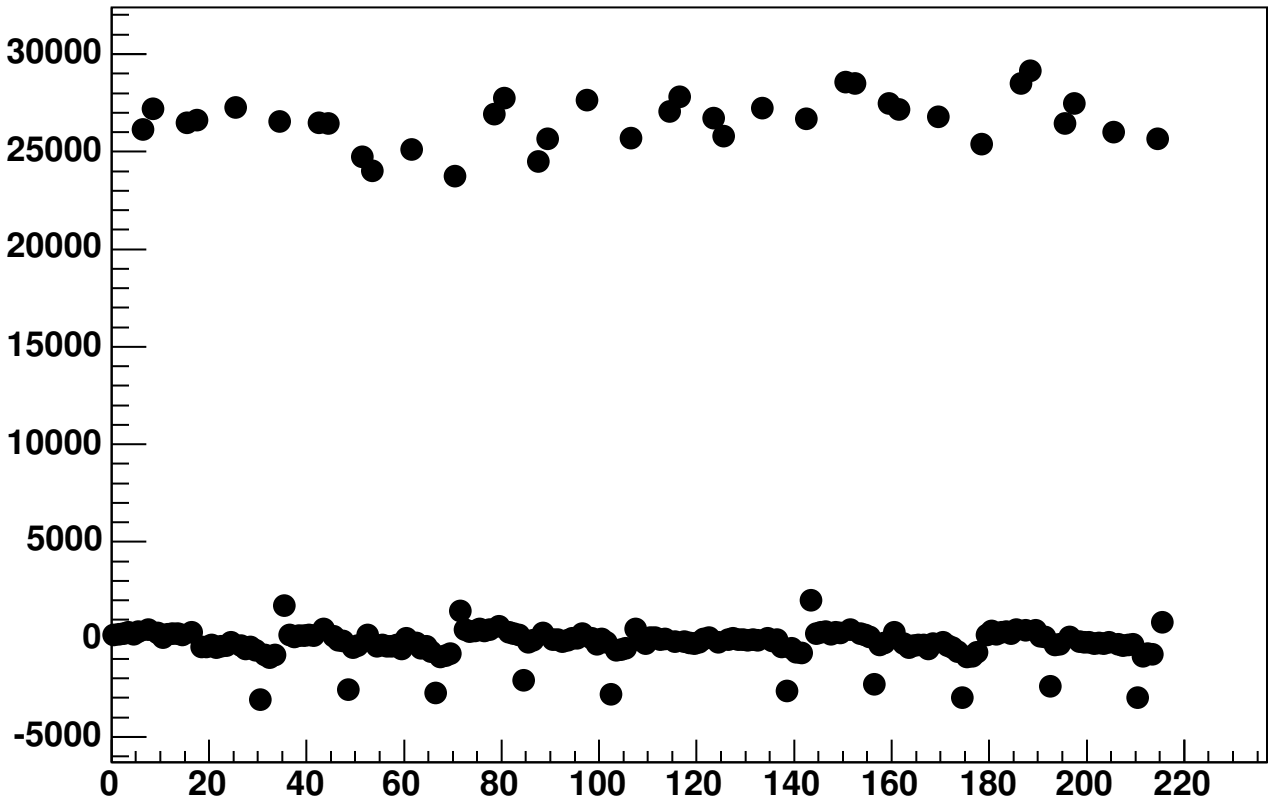
Enable 2, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



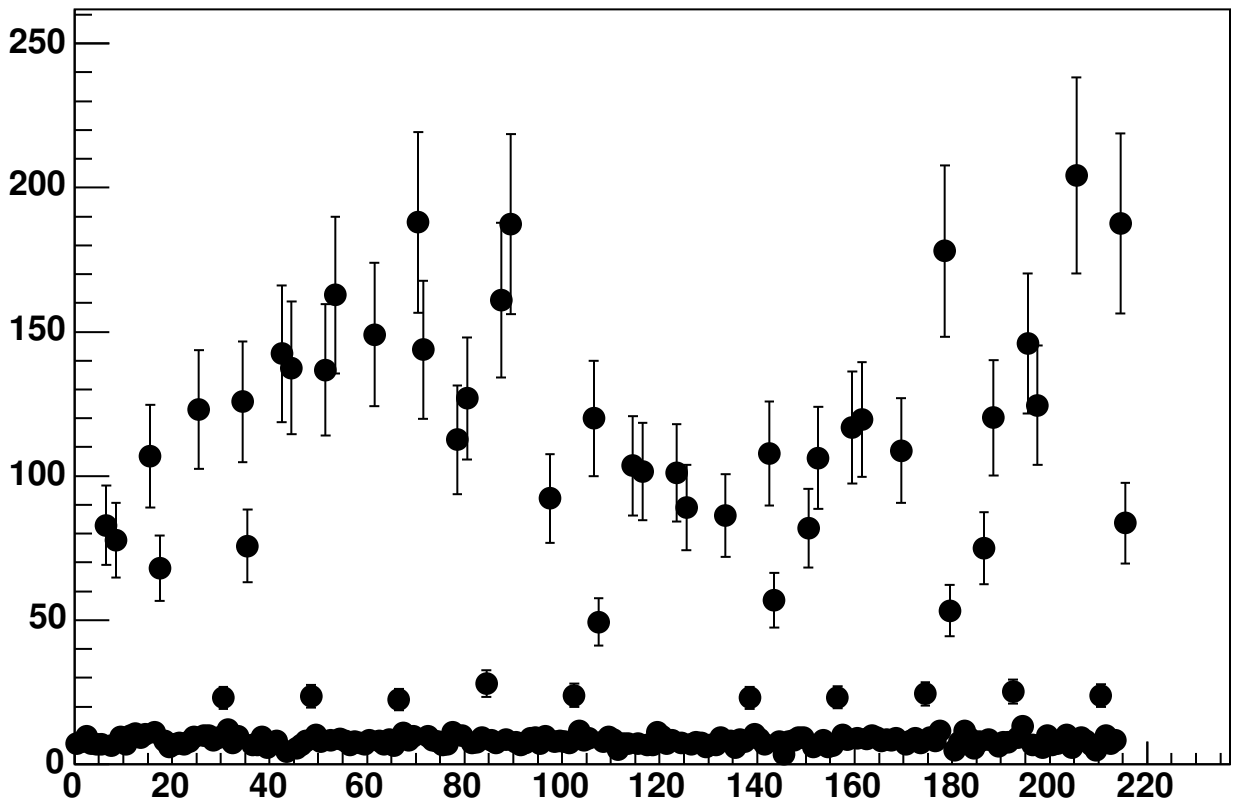
Enable 2, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



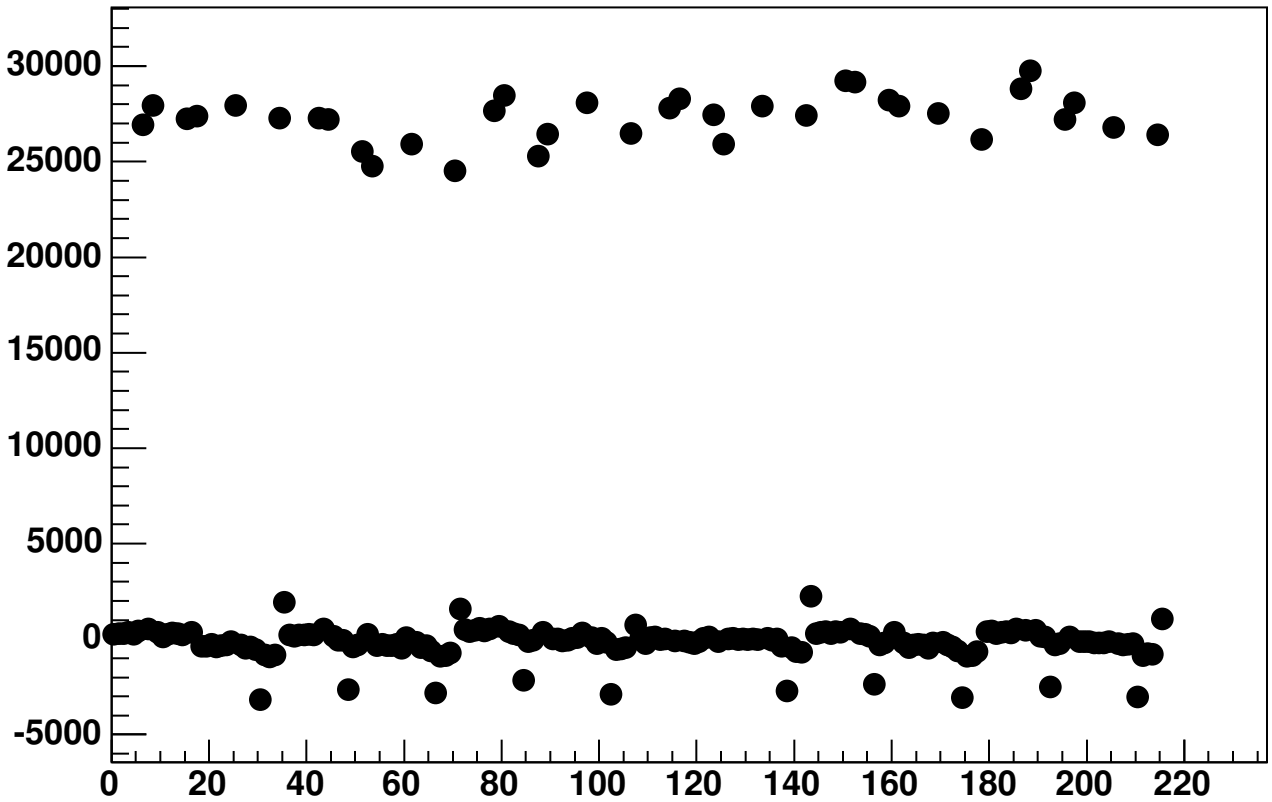
Enable 2, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



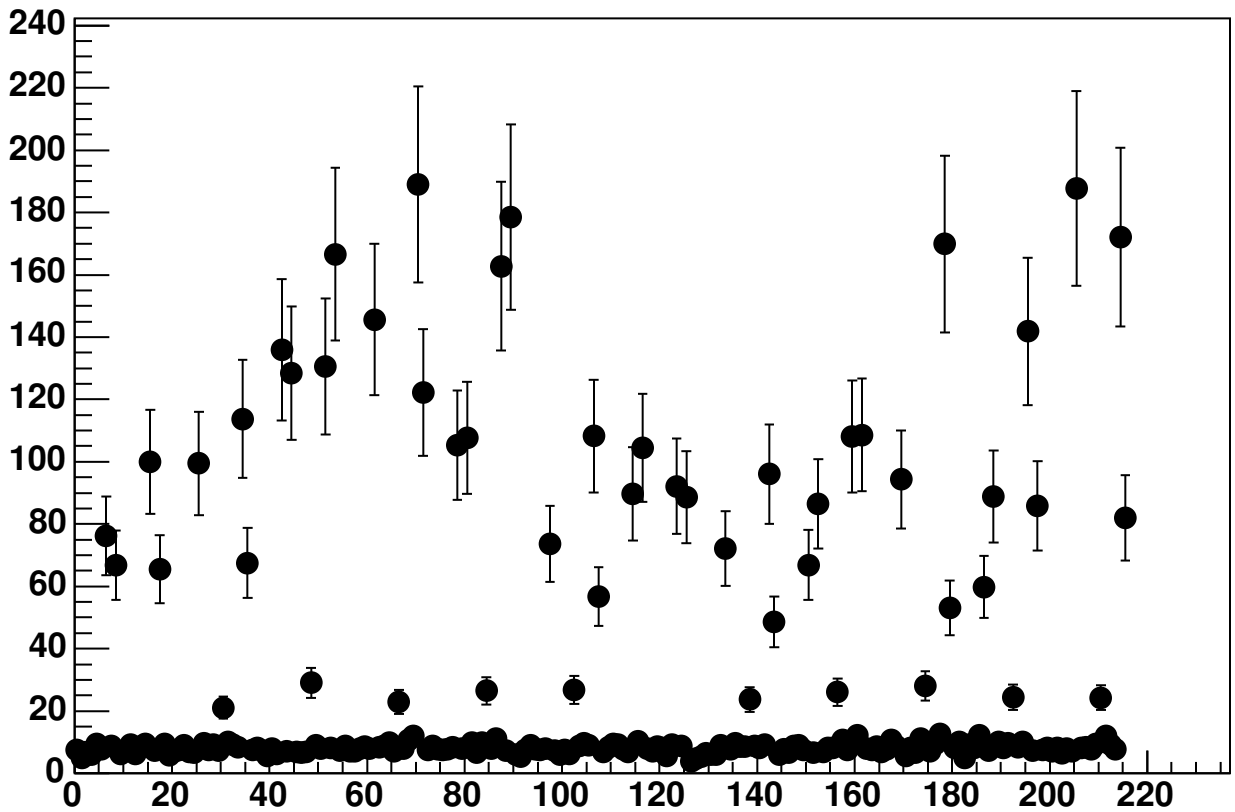
Enable 2, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



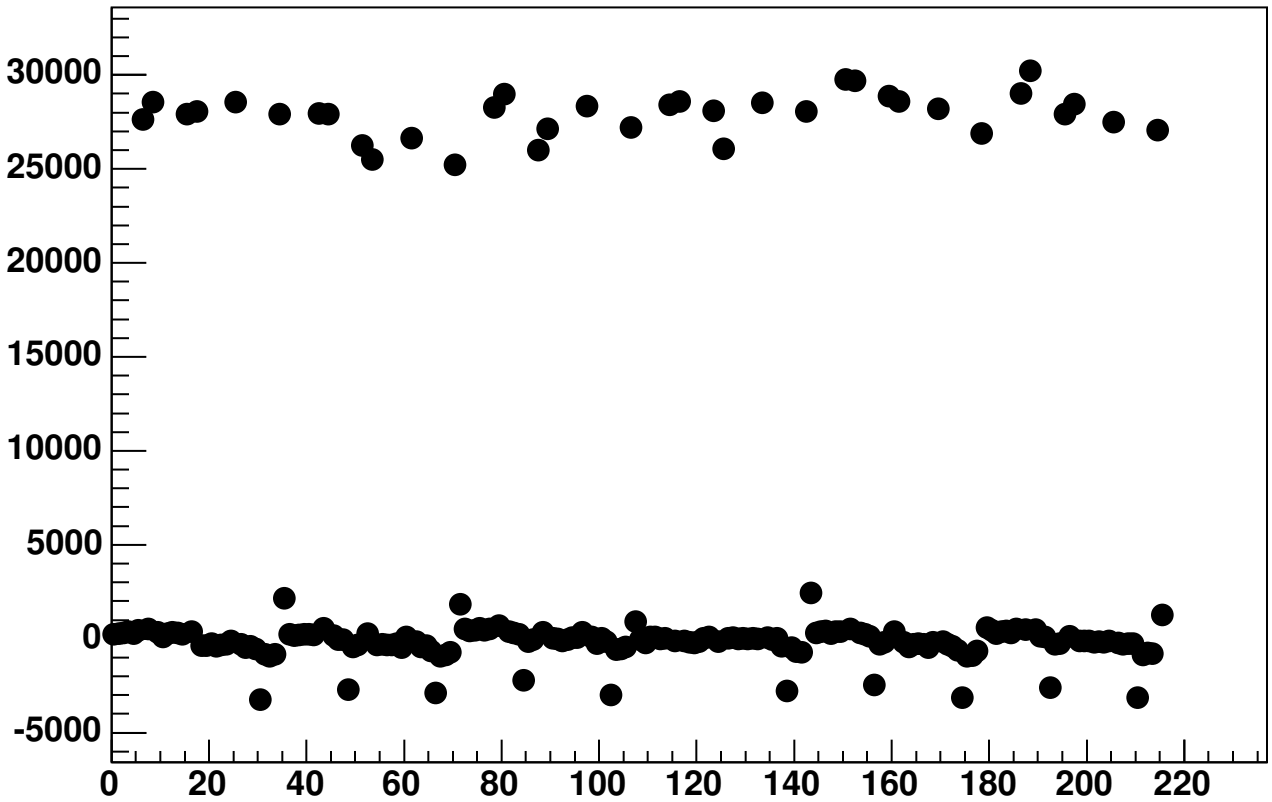
Enable 2, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



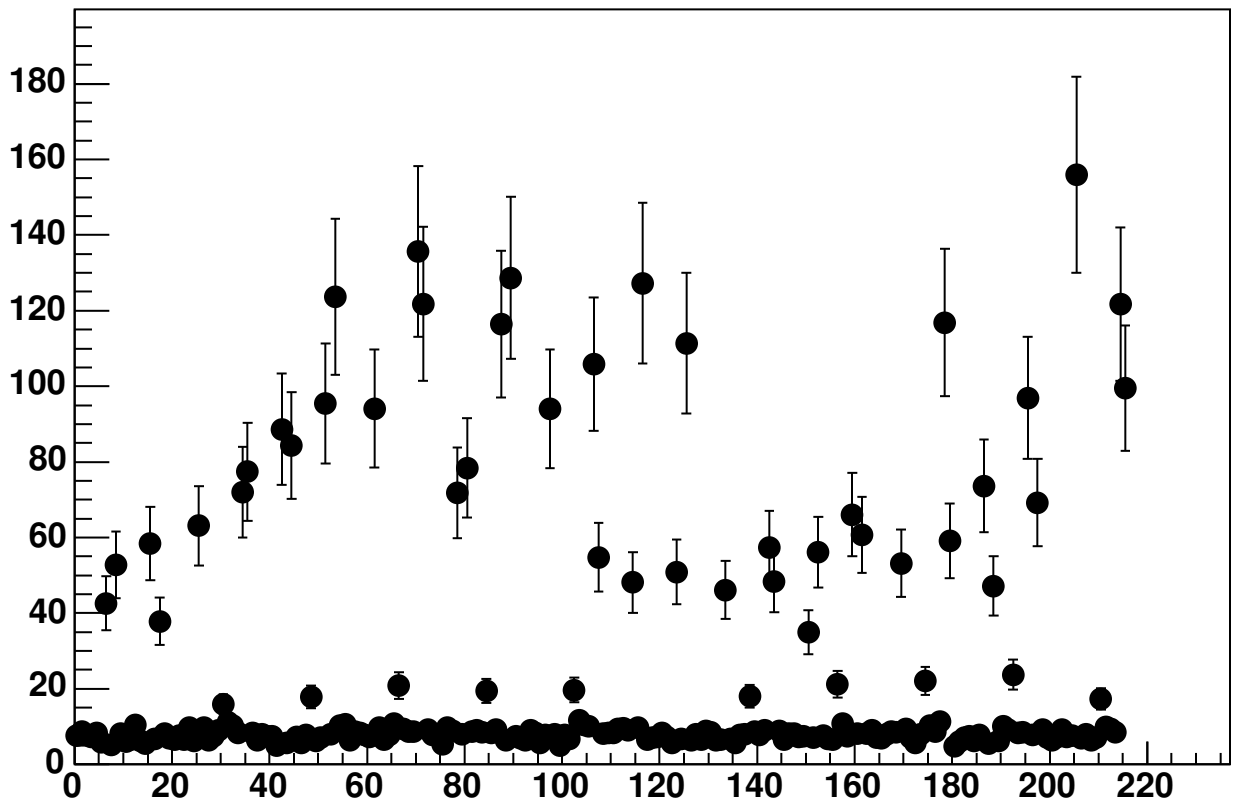
Enable 2, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



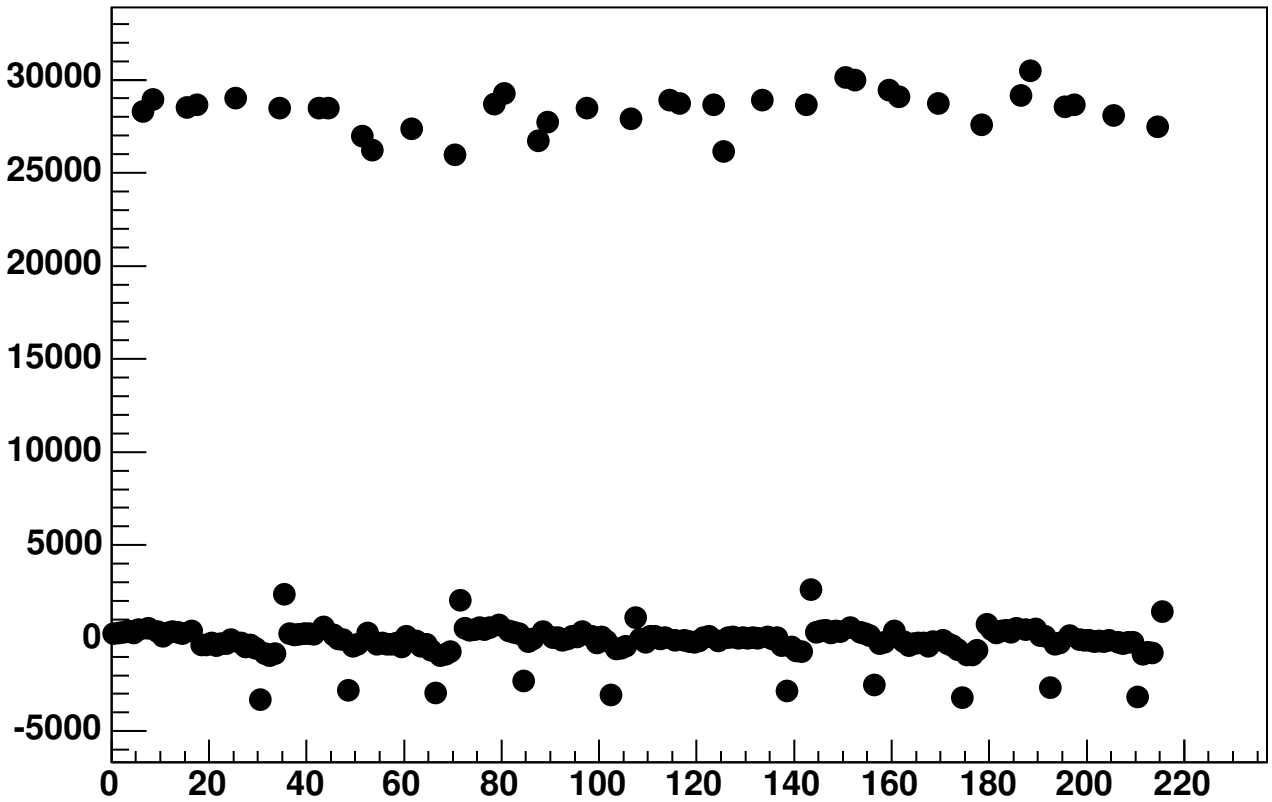
Enable 2, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



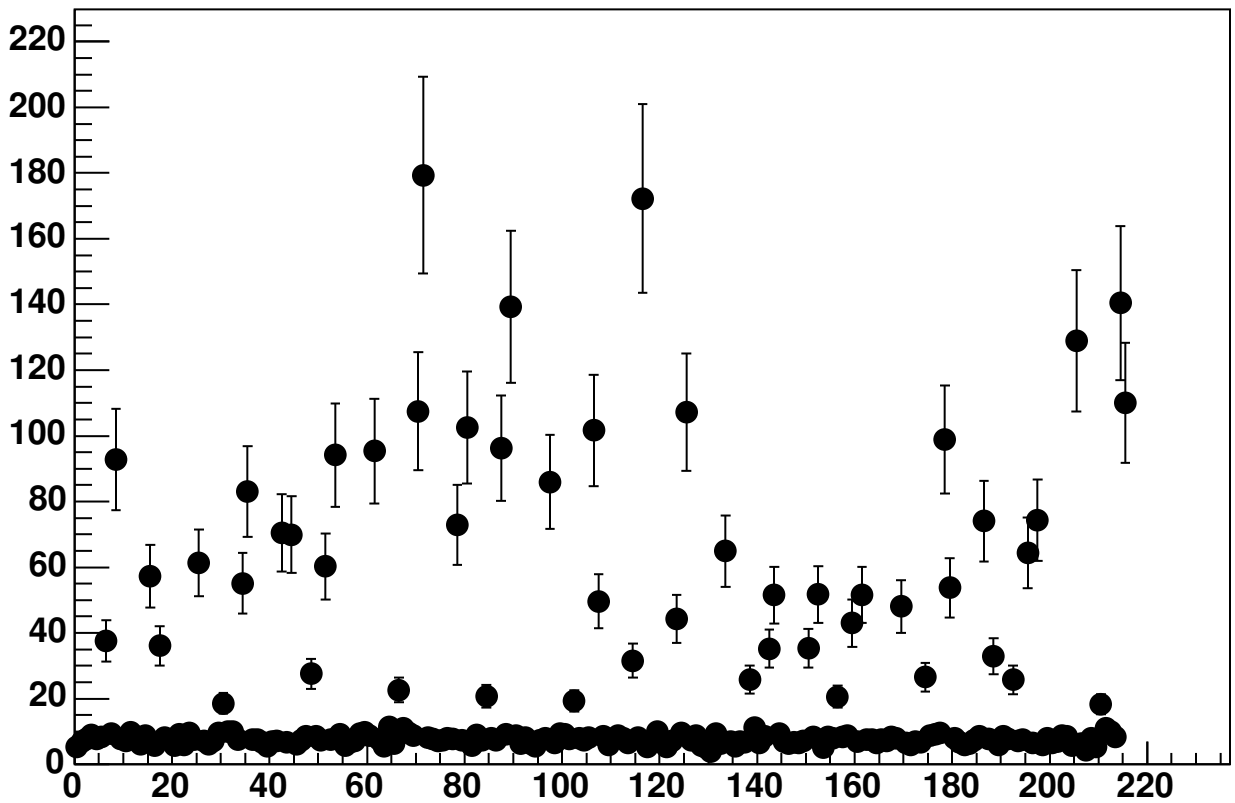
Enable 2, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



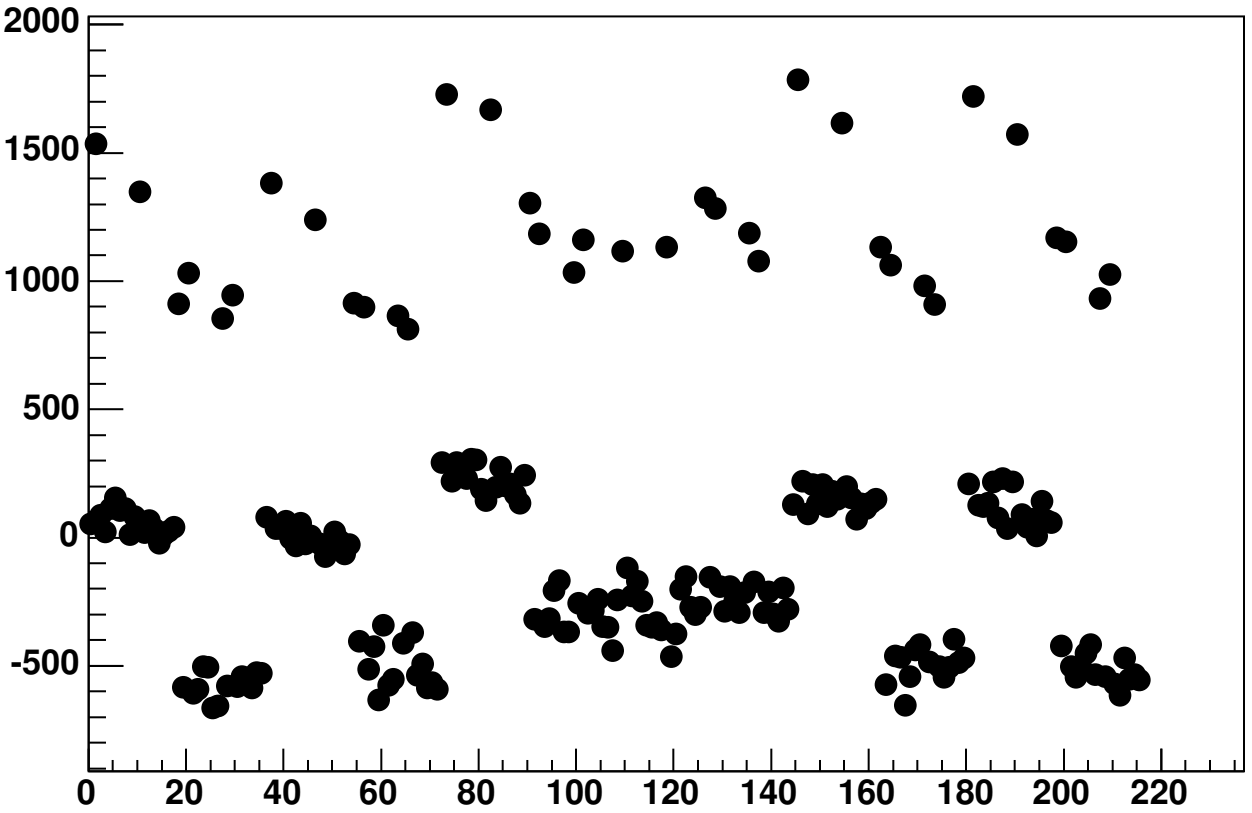
Enable 2, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



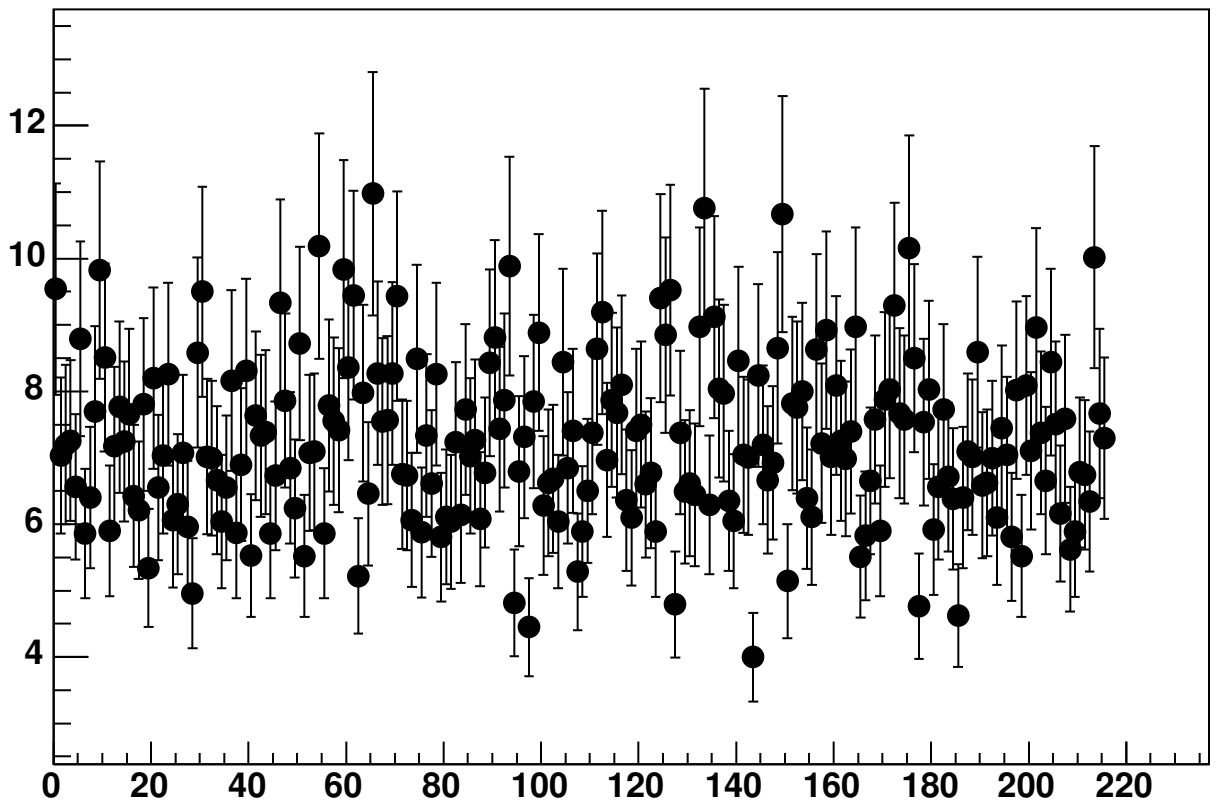
Enable 2, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



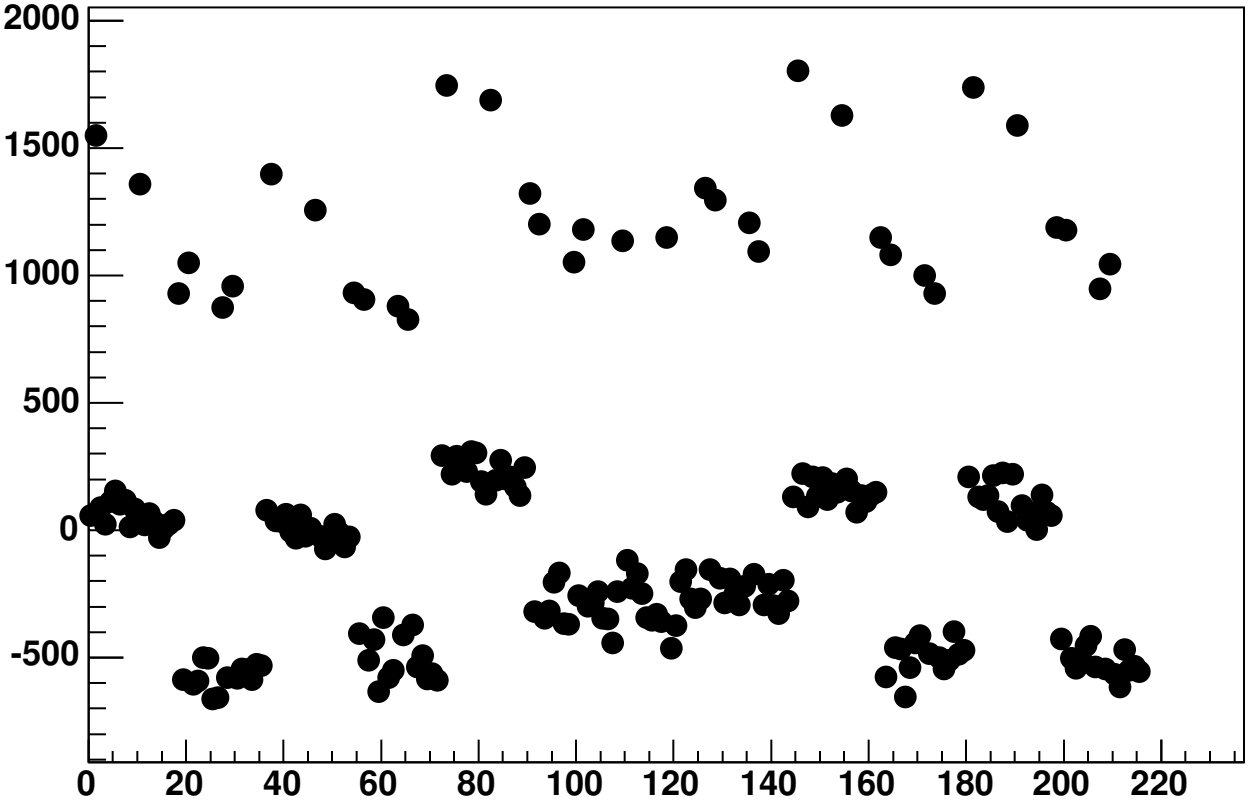
Enable 3, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



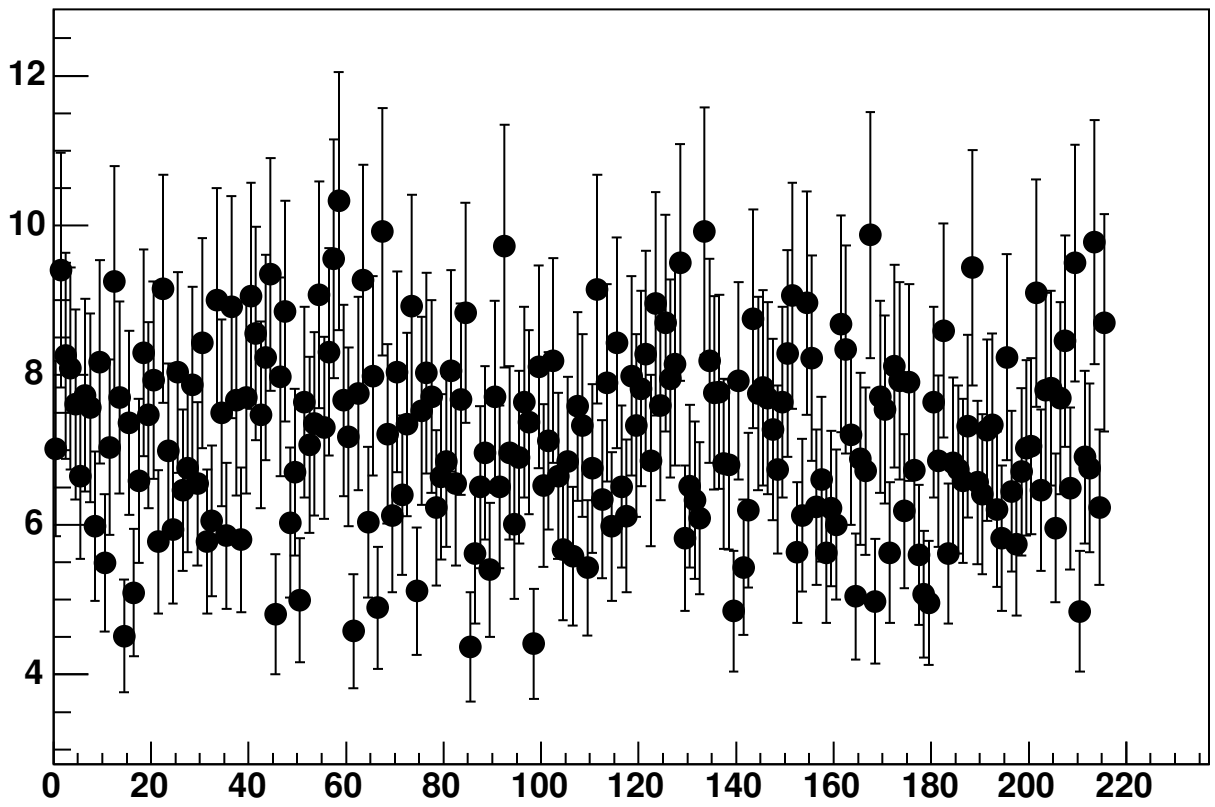
Enable 3, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



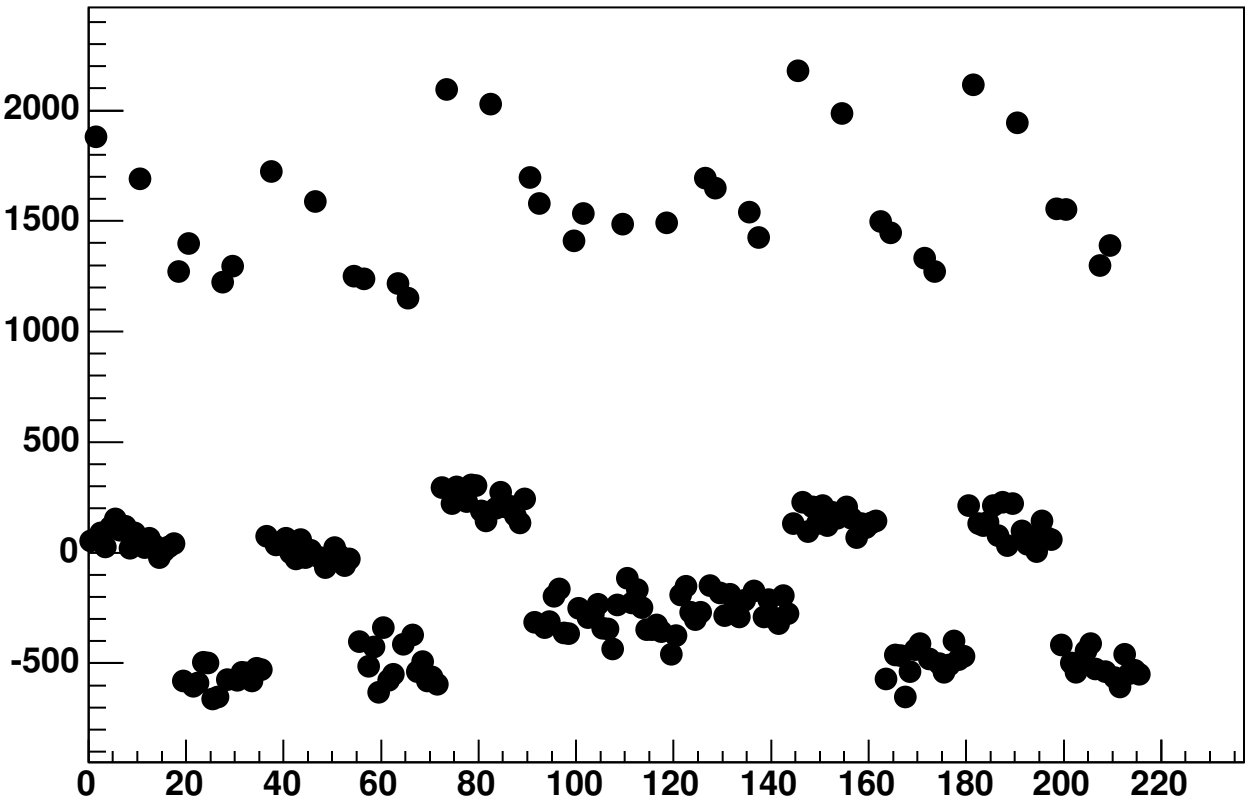
Enable 3, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



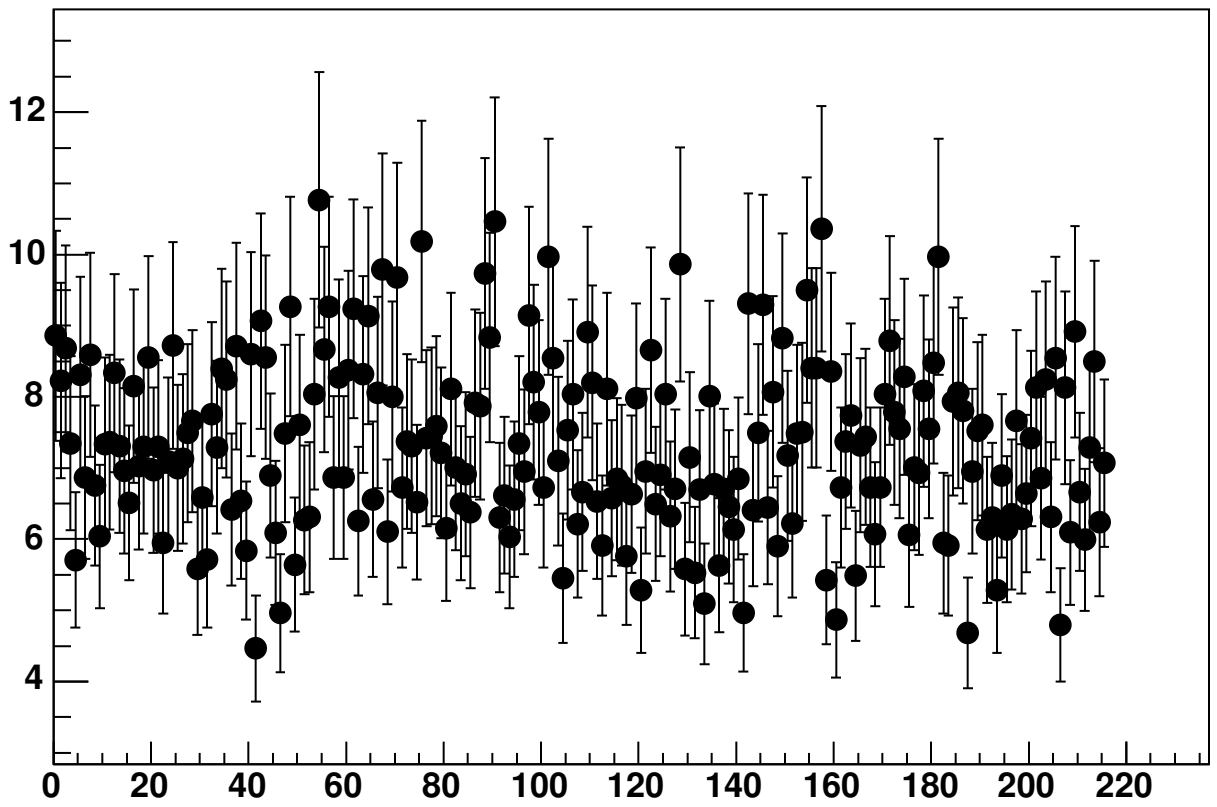
Enable 3, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



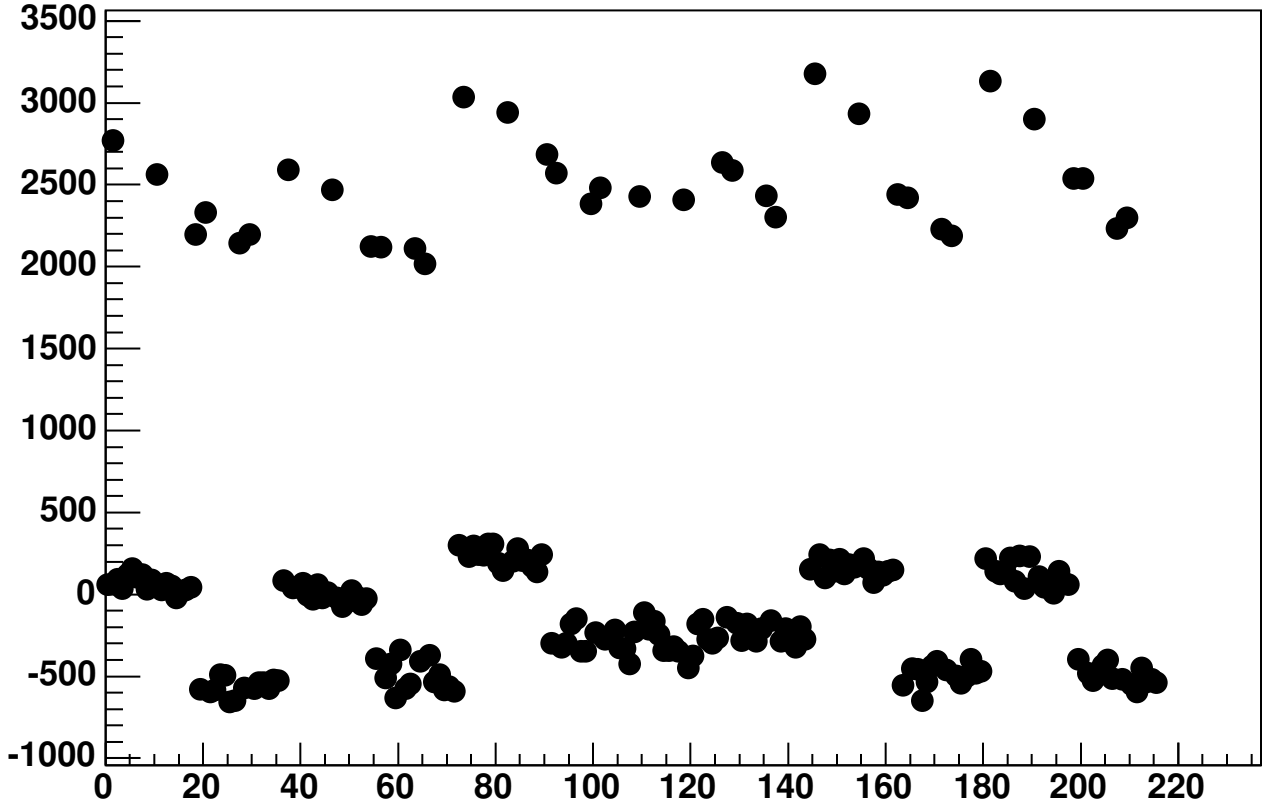
Enable 3, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



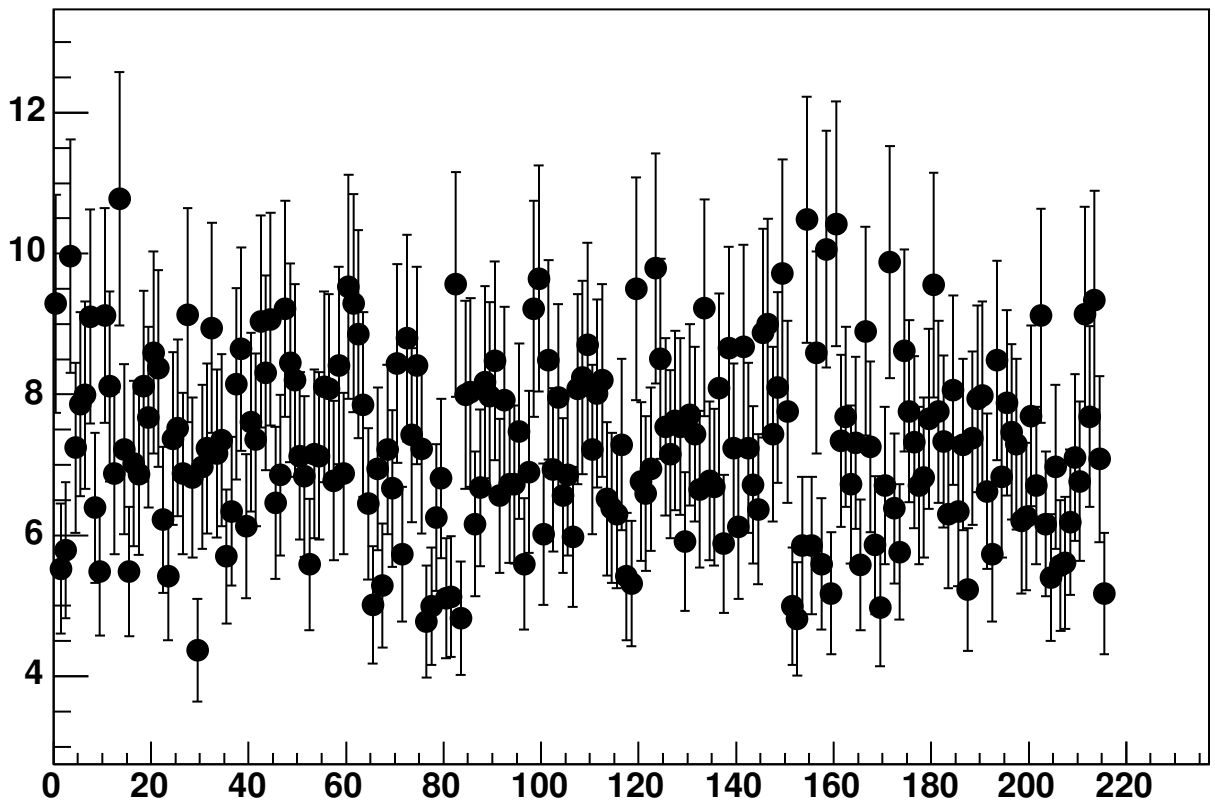
Enable 3, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



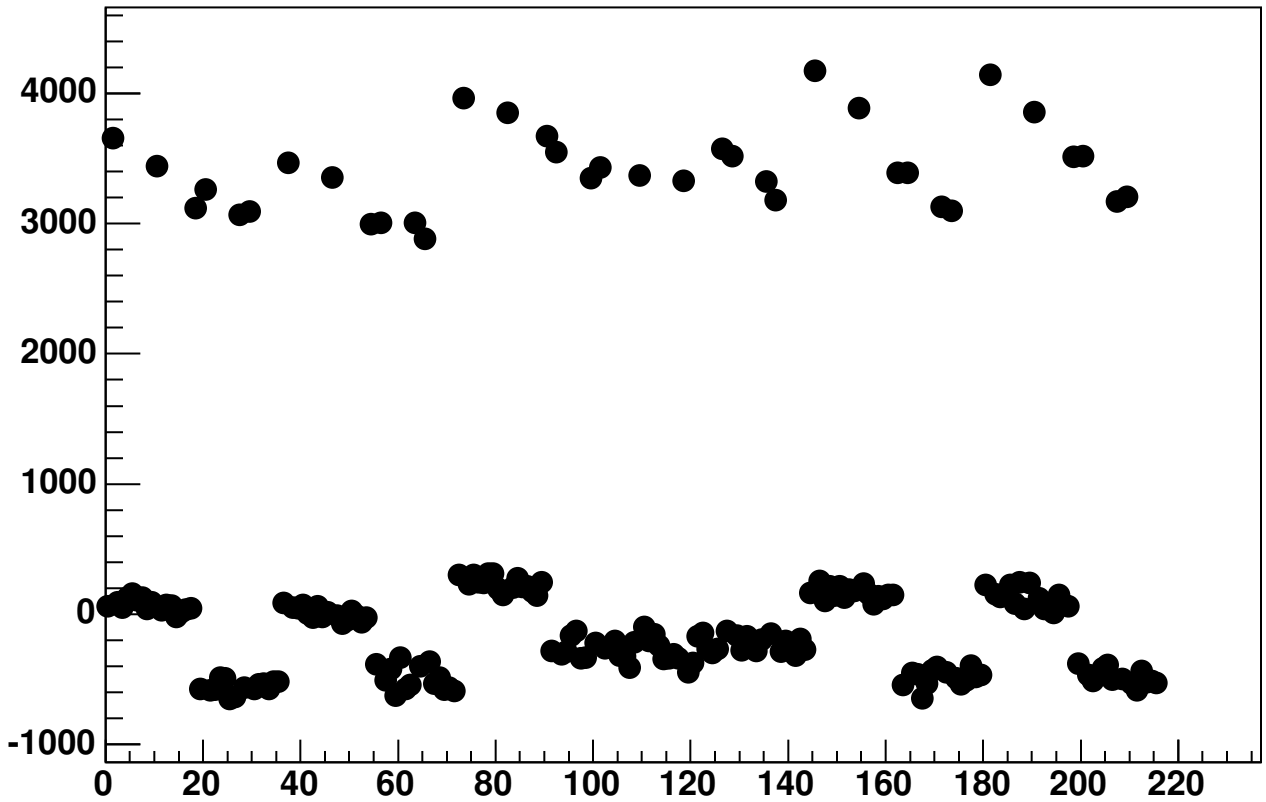
Enable 3, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



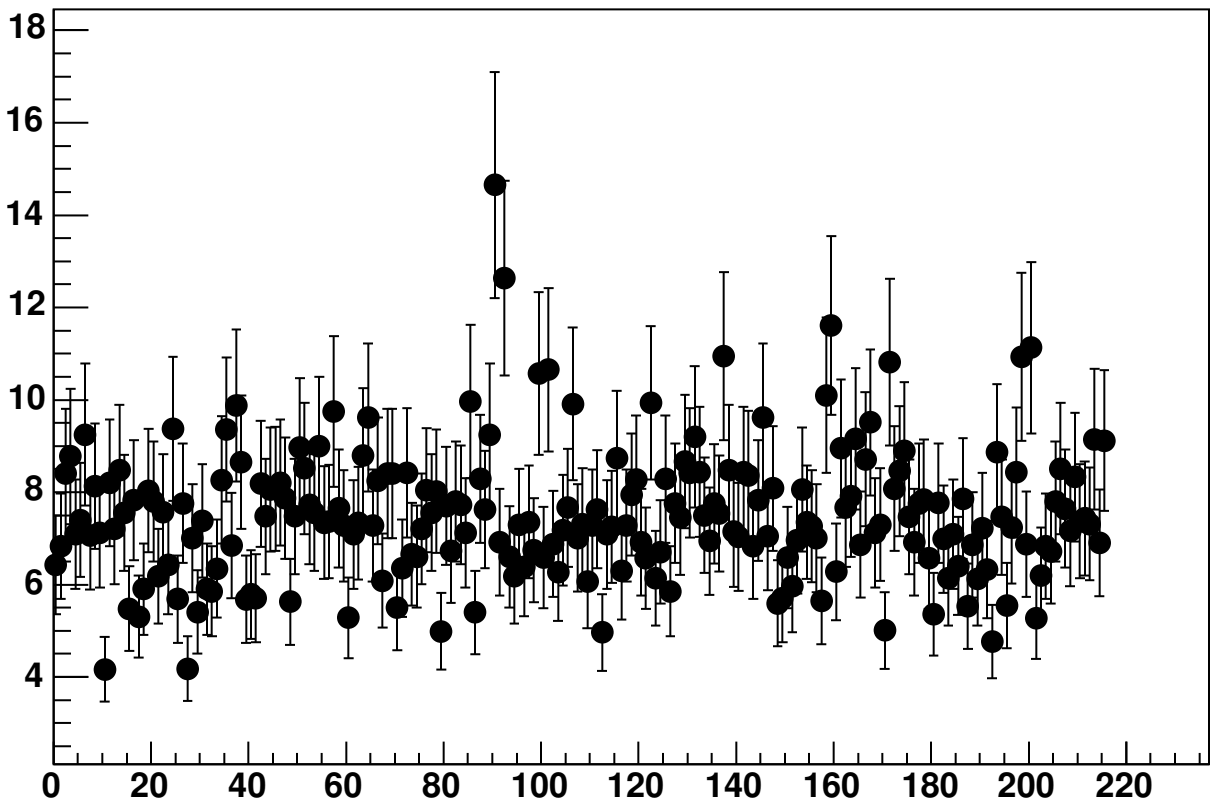
Enable 3, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



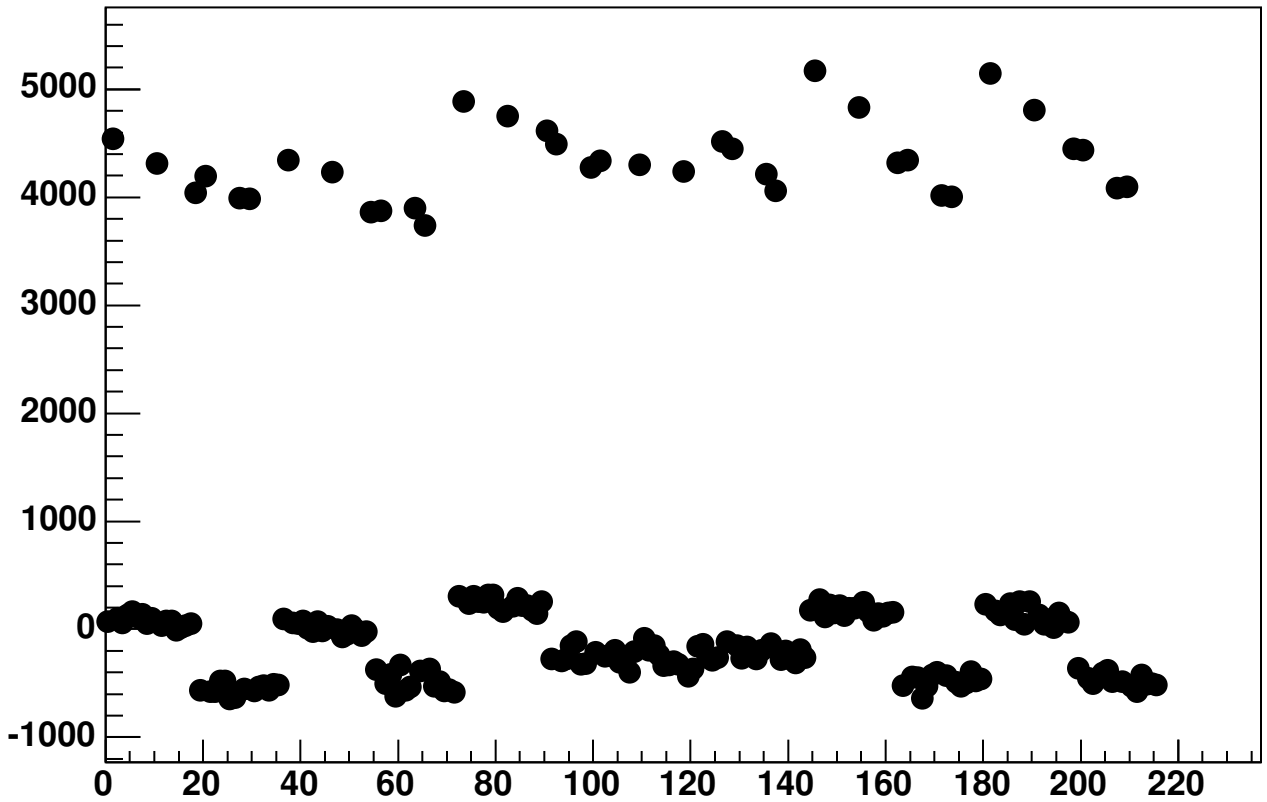
Enable 3, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



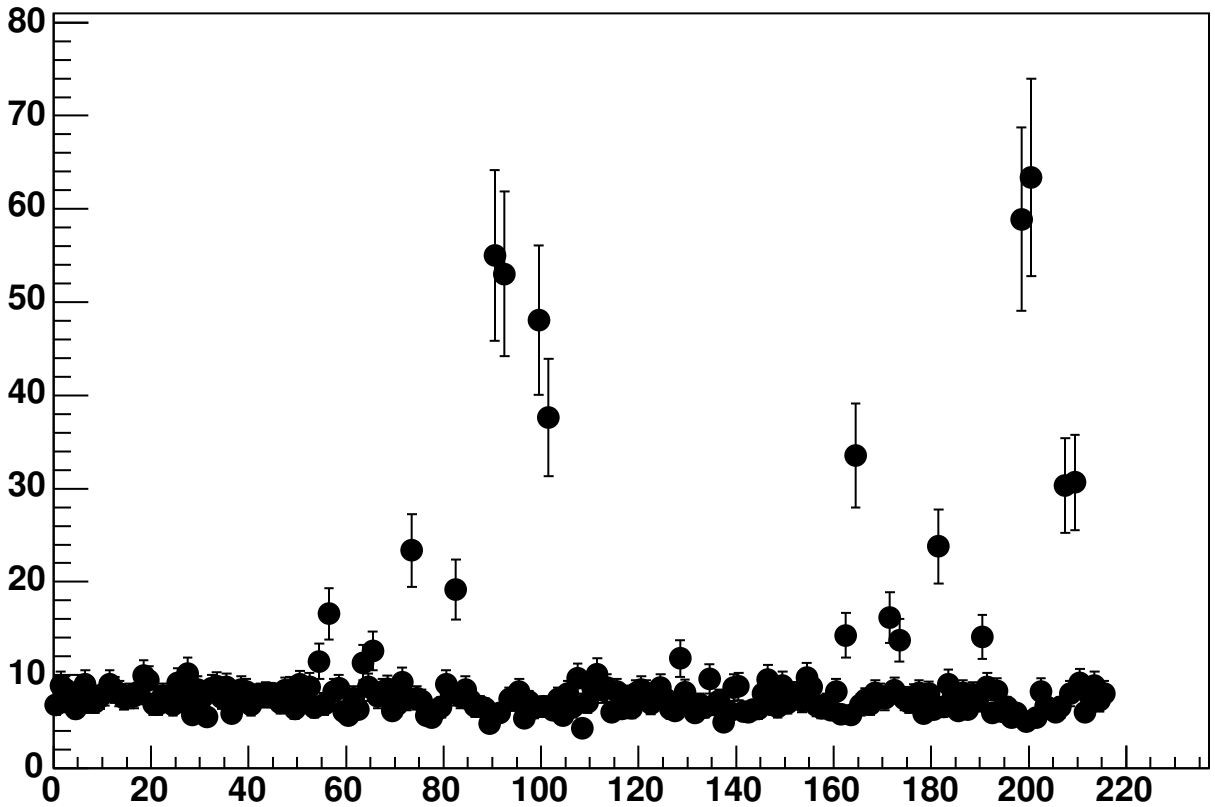
Enable 3, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



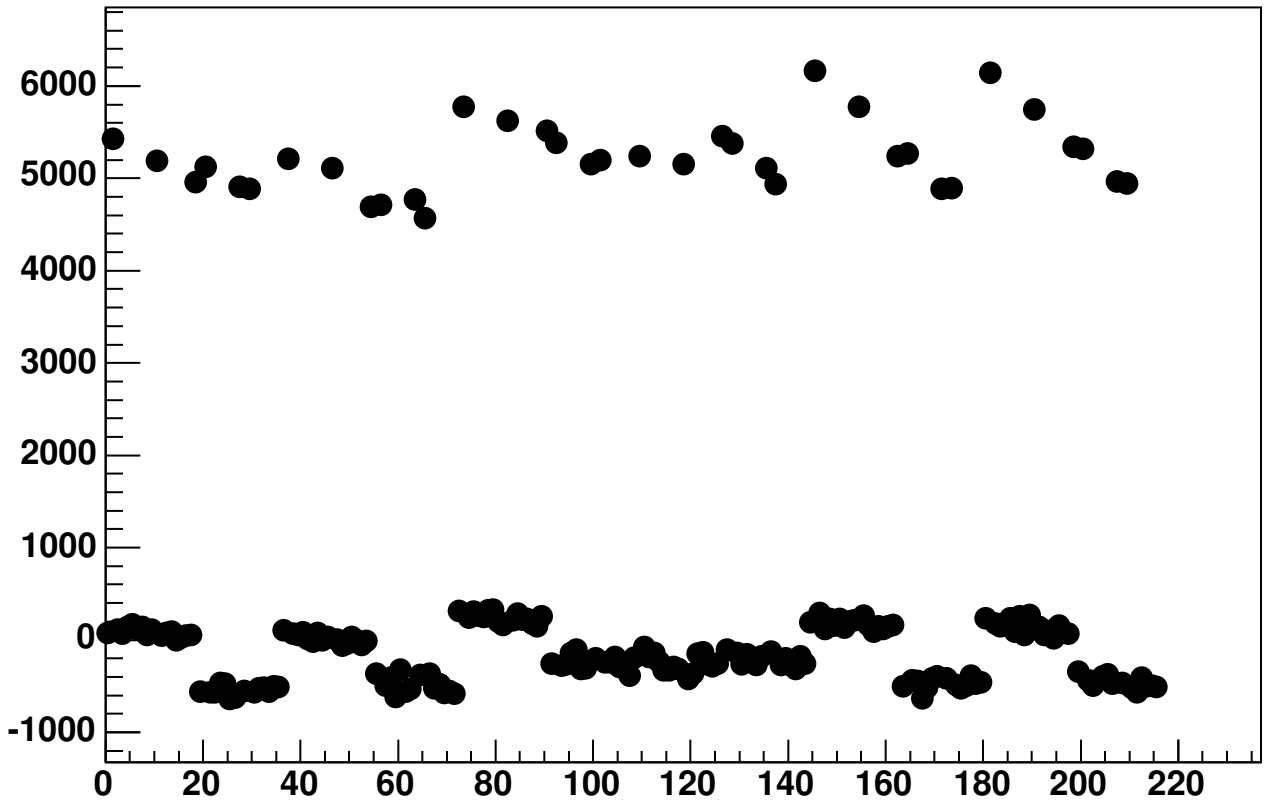
Enable 3, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



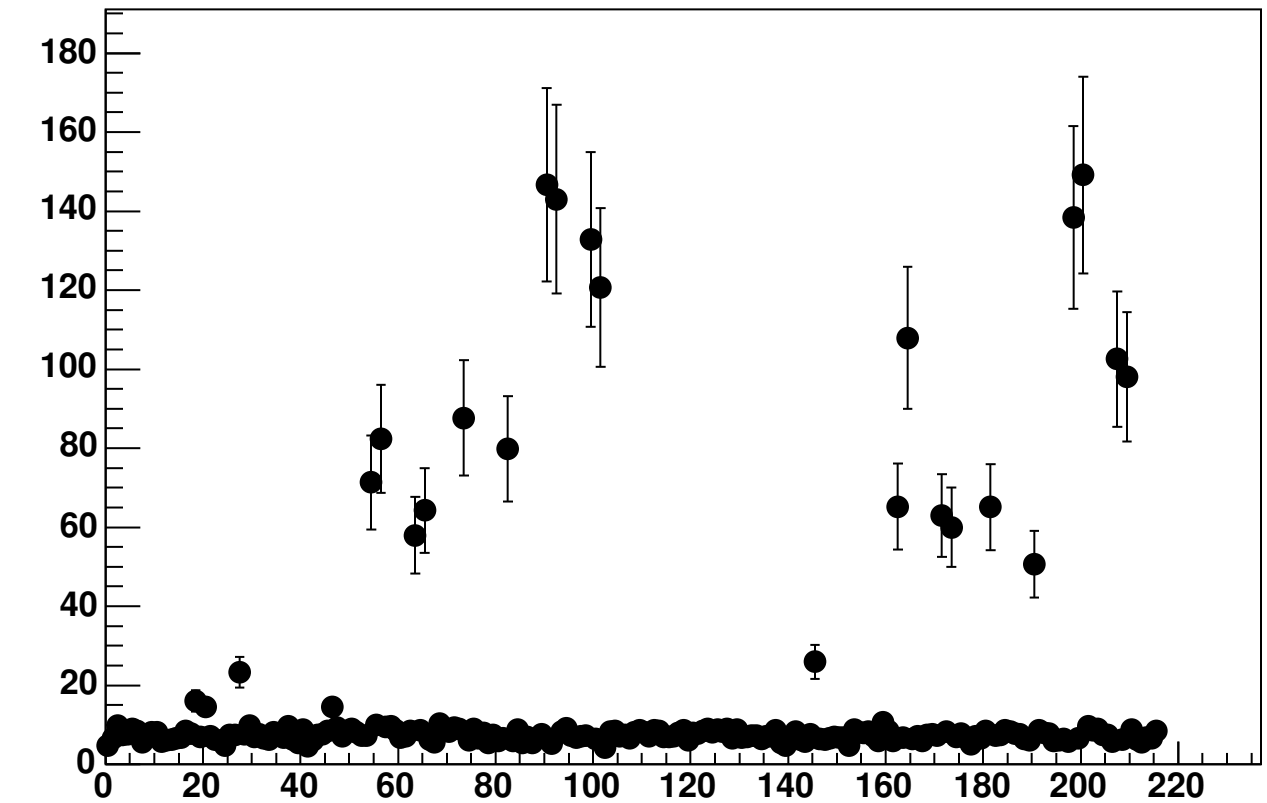
Enable 3, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



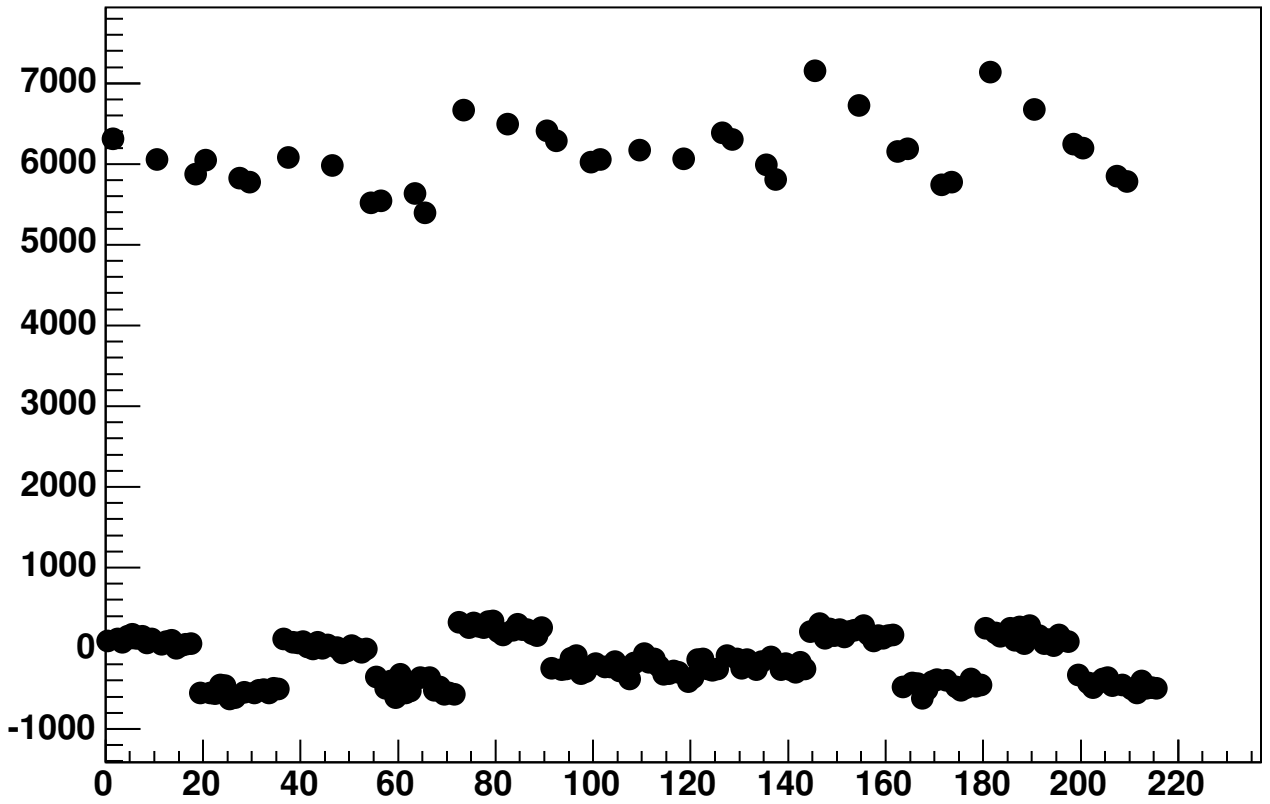
Enable 3, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



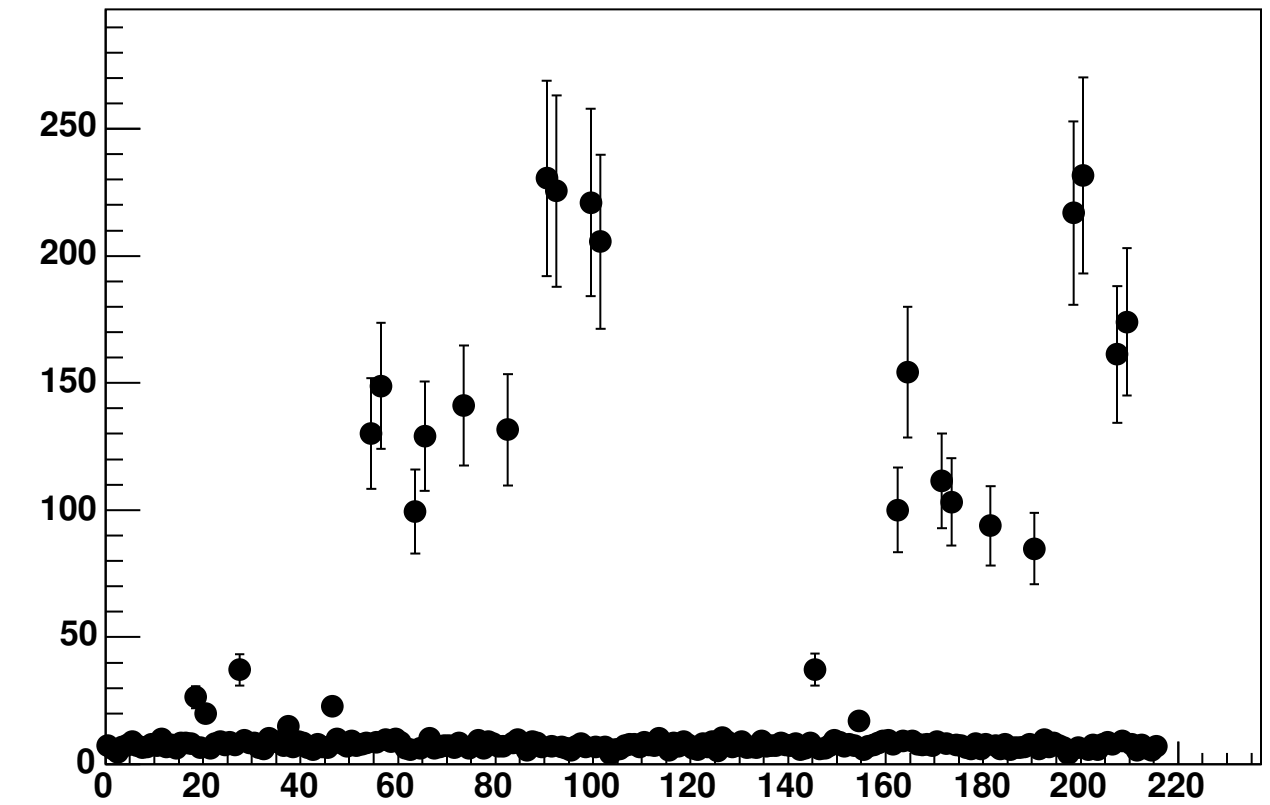
Enable 3, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



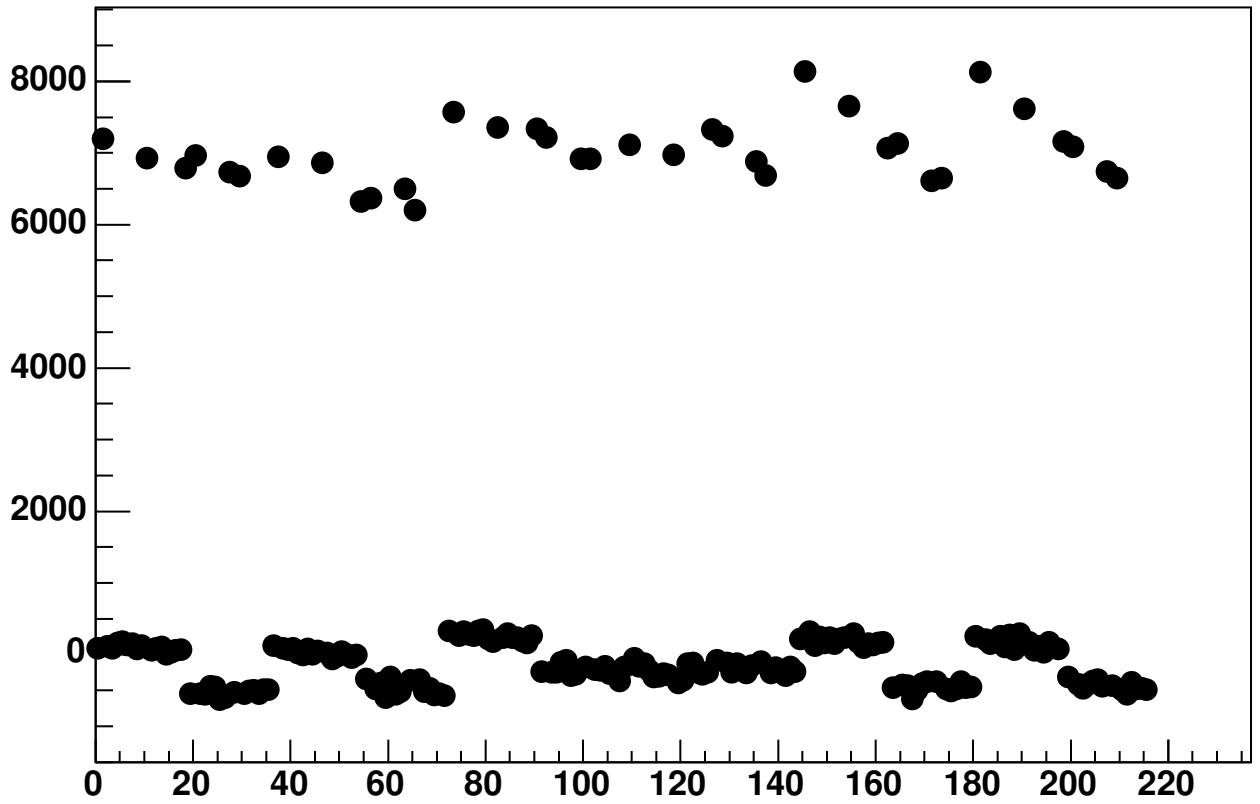
Enable 3, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



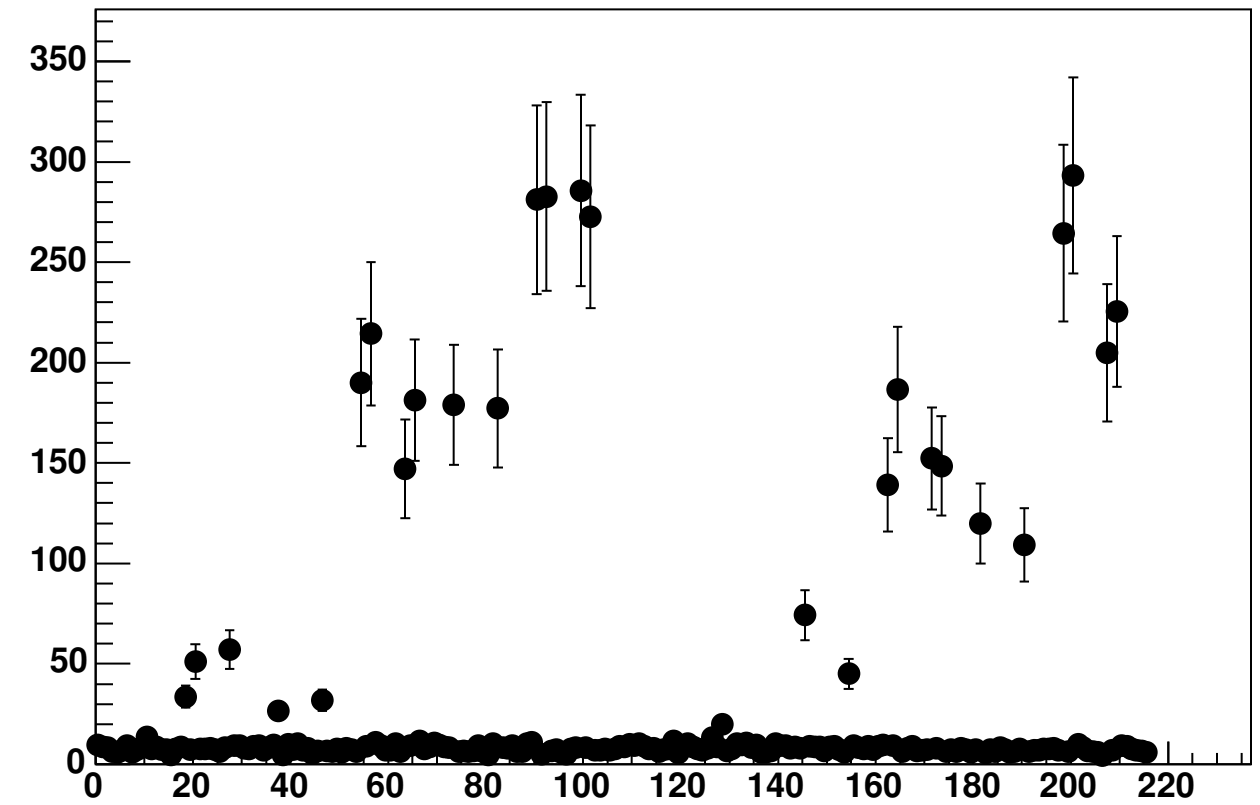
Enable 3, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



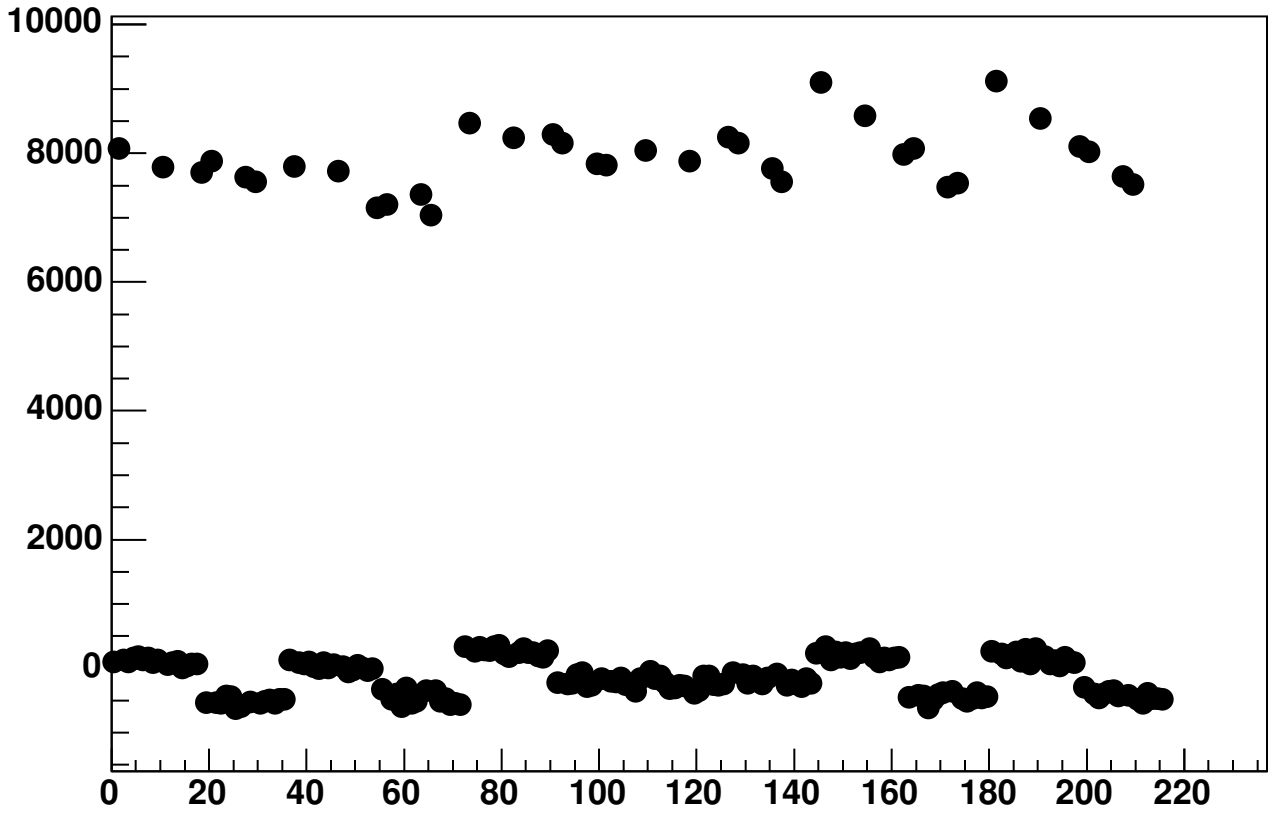
Enable 3, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



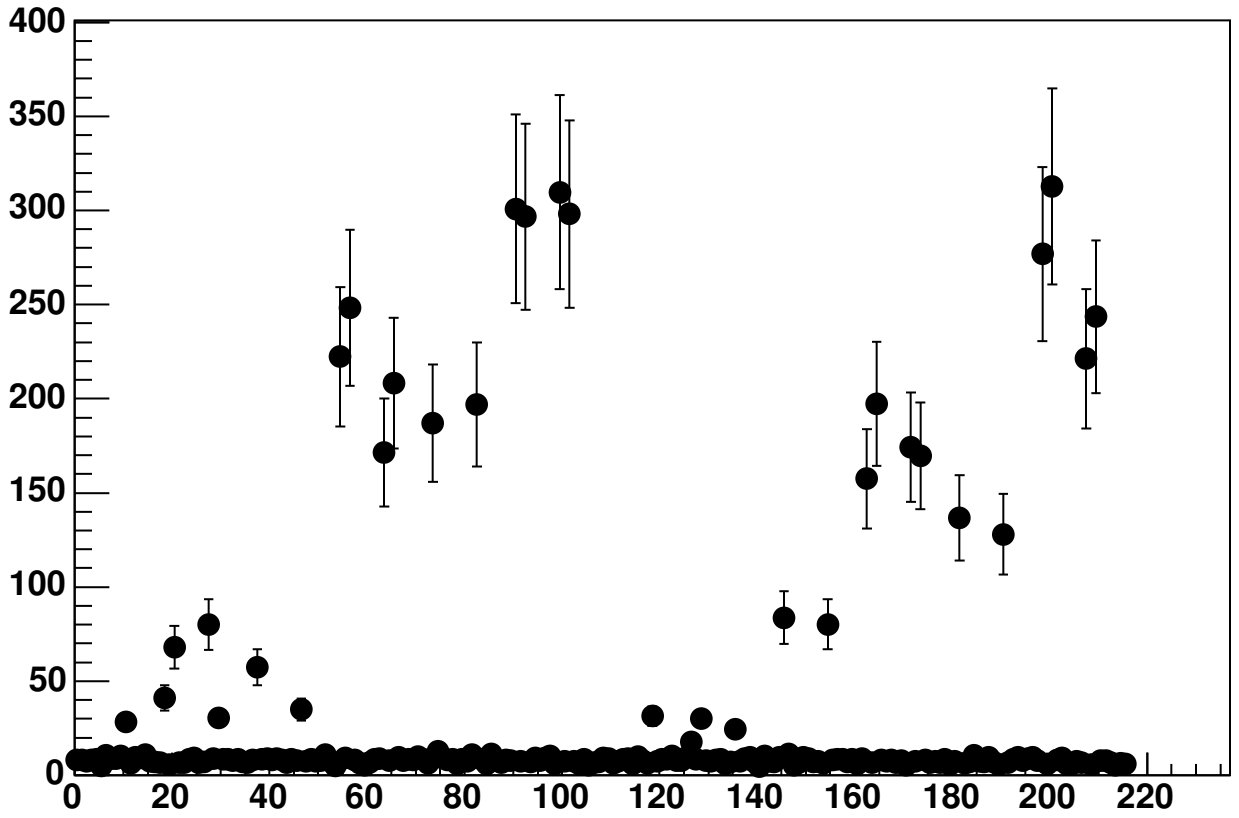
Enable 3, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



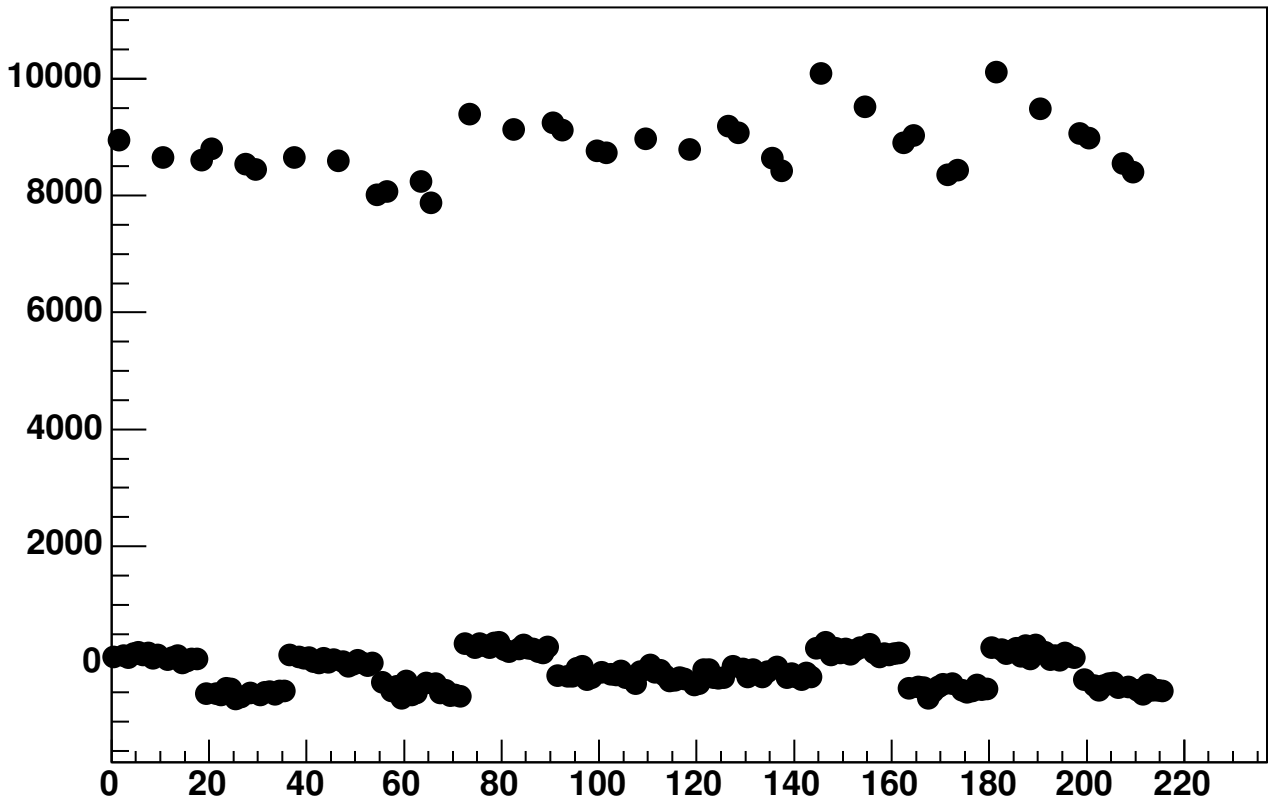
Enable 3, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



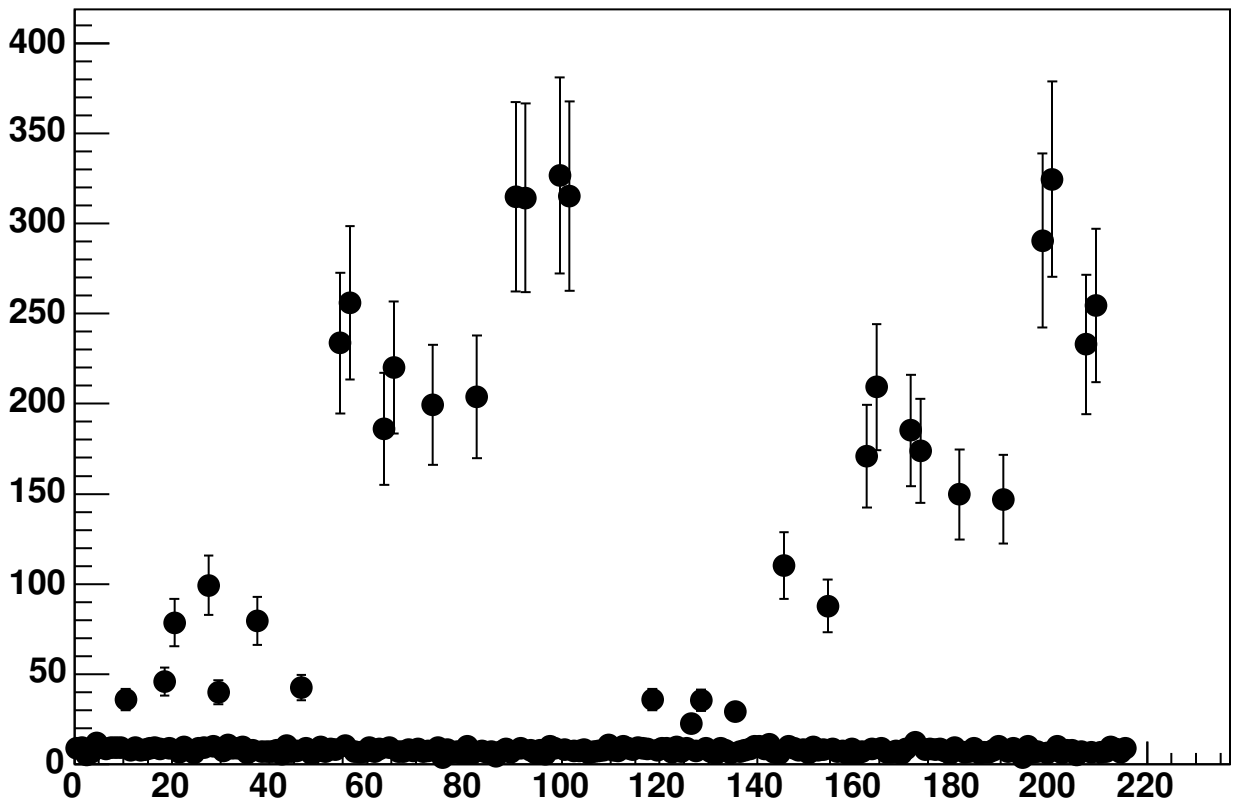
Enable 3, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



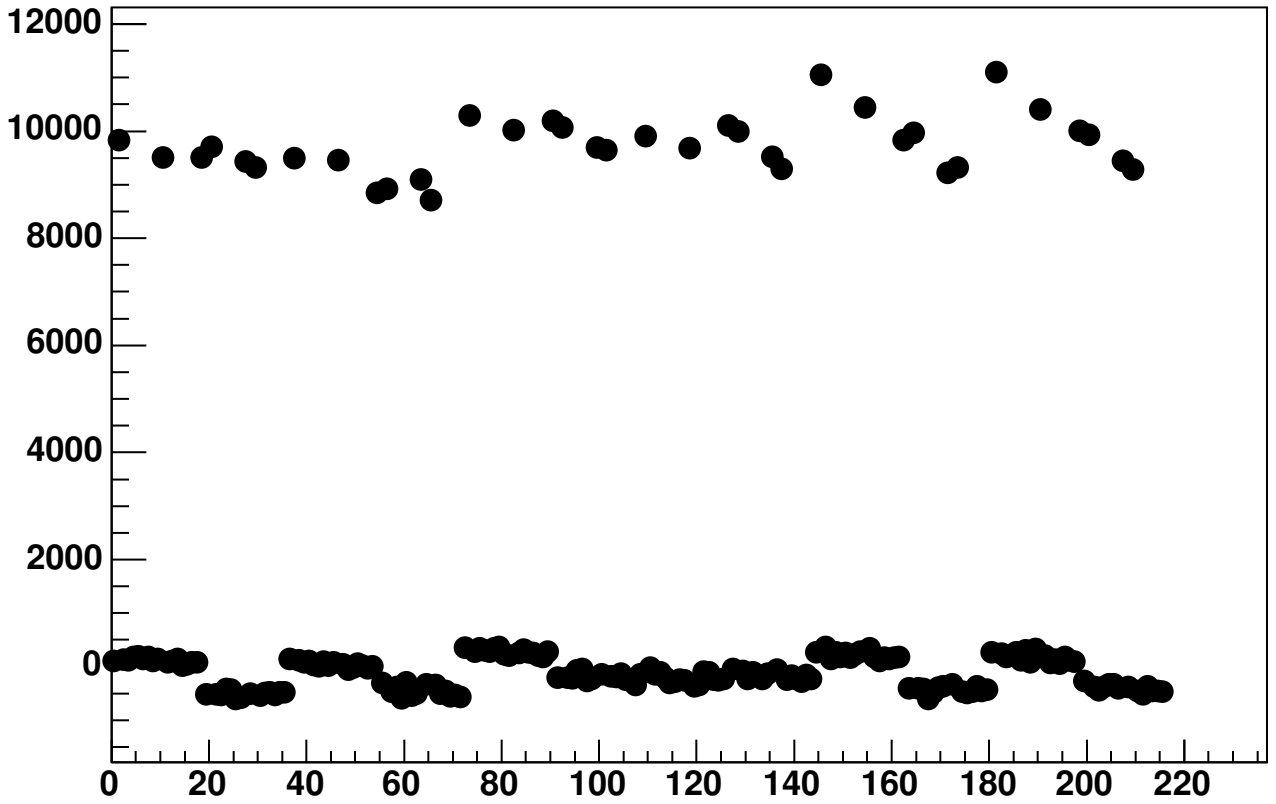
Enable 3, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



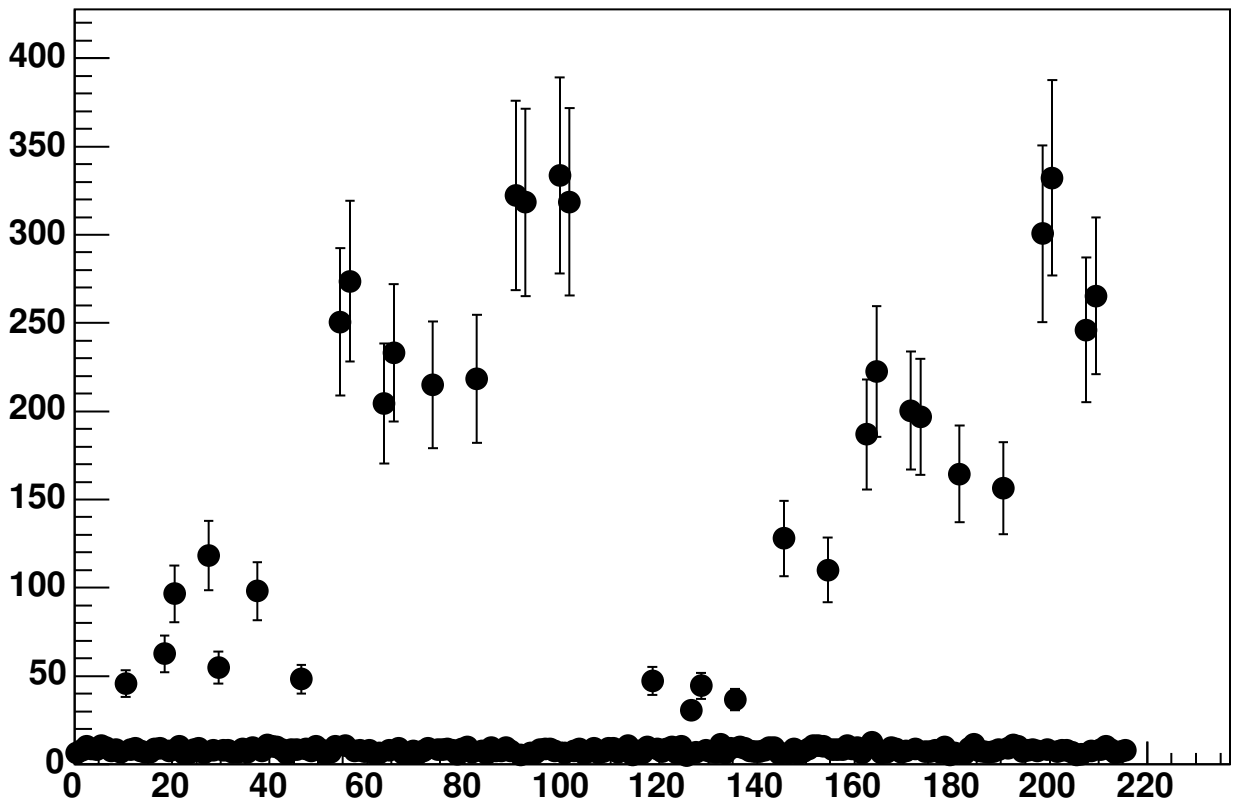
Enable 3, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



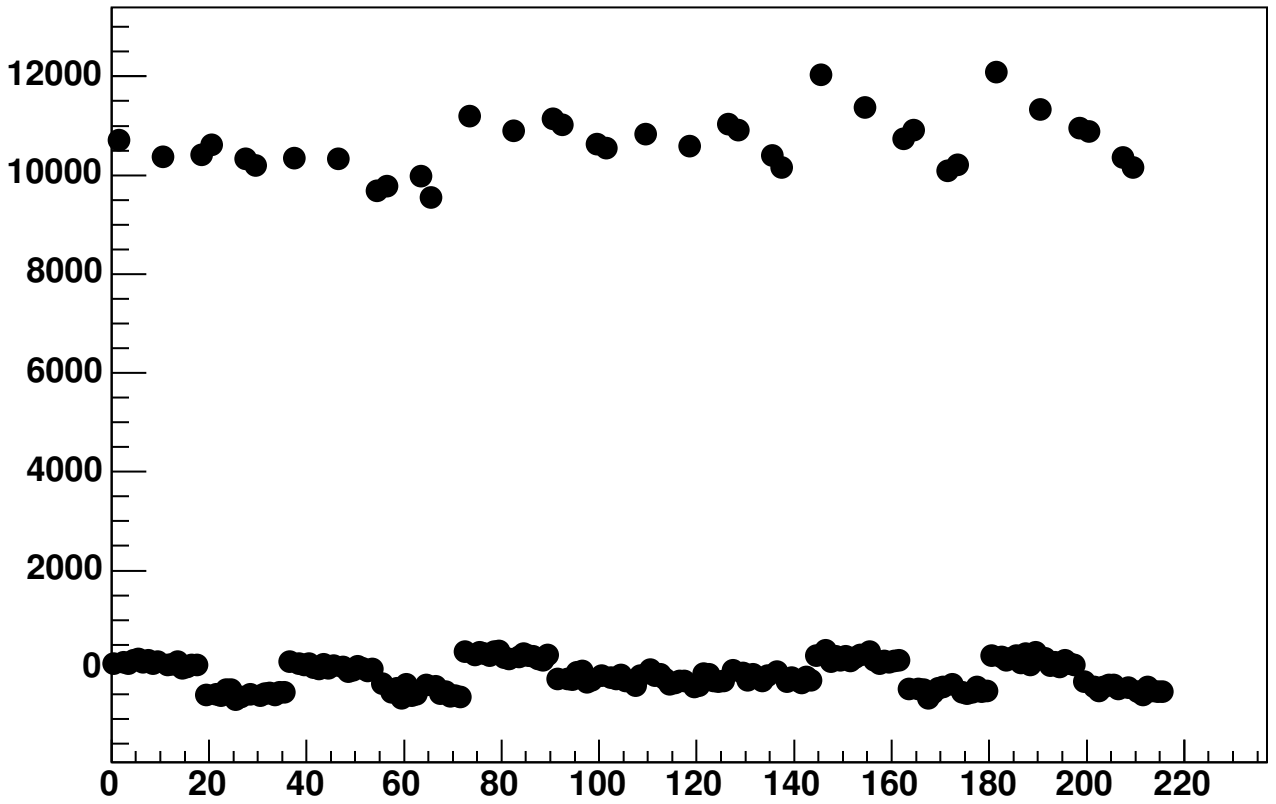
Enable 3, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



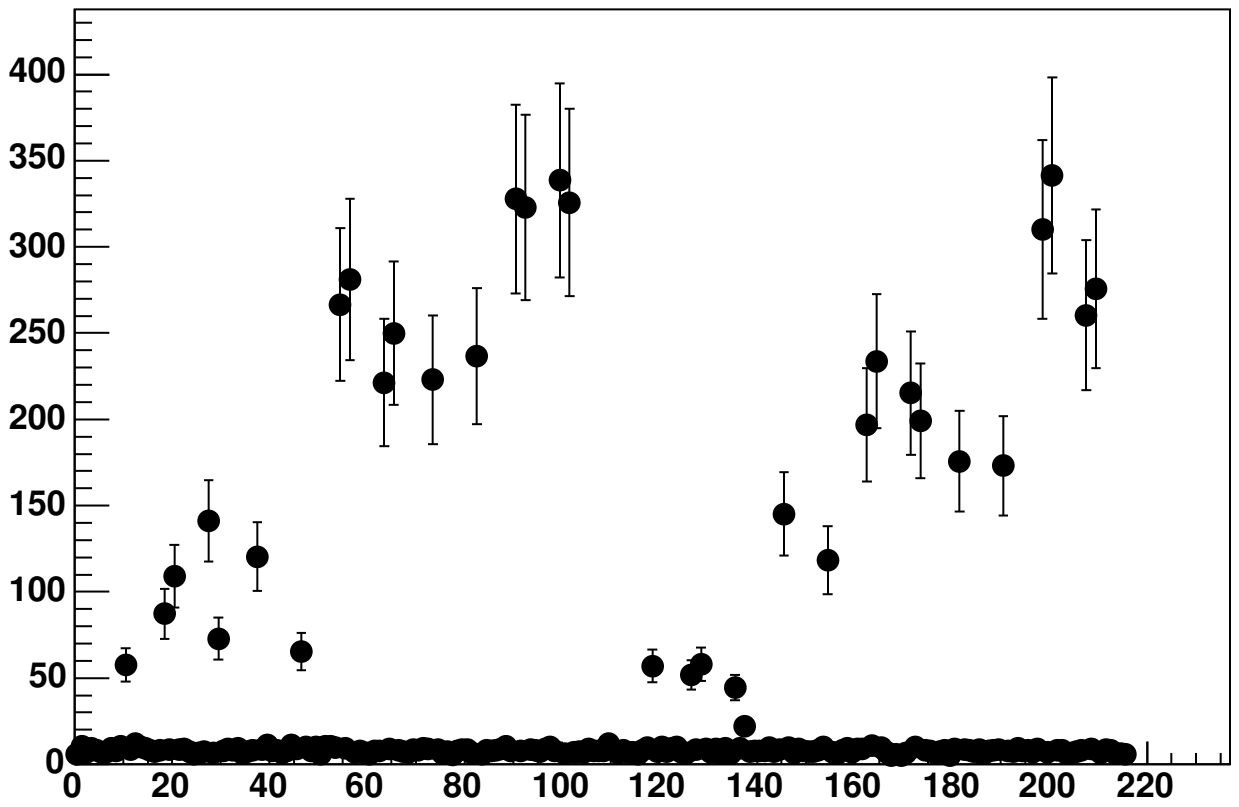
Enable 3, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



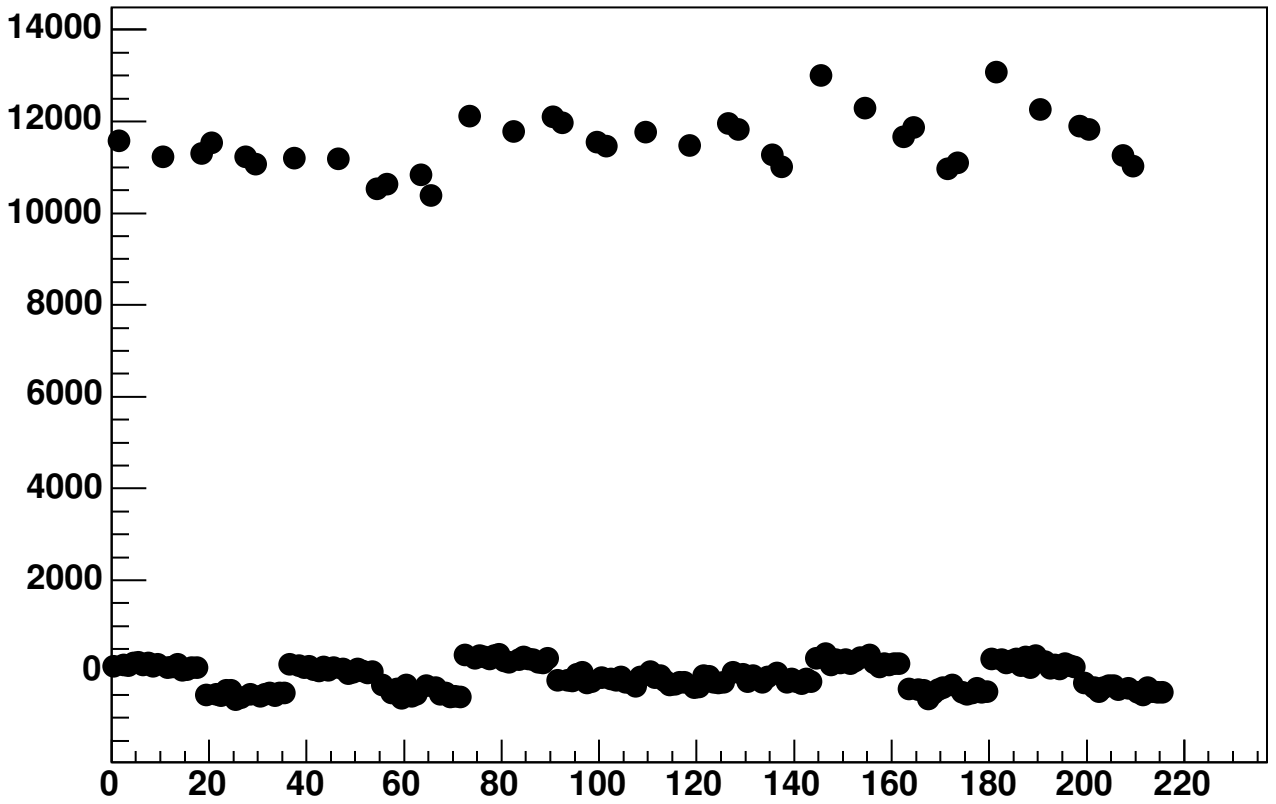
Enable 3, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



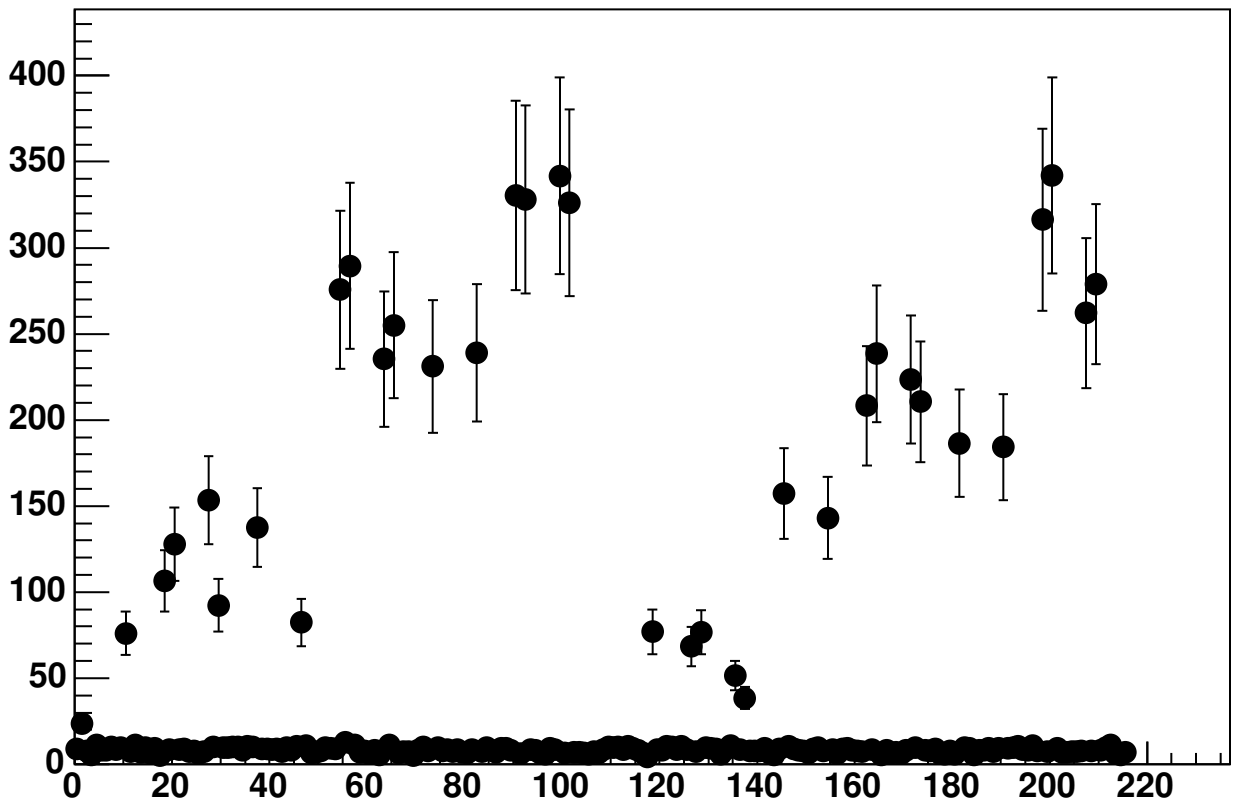
Enable 3, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



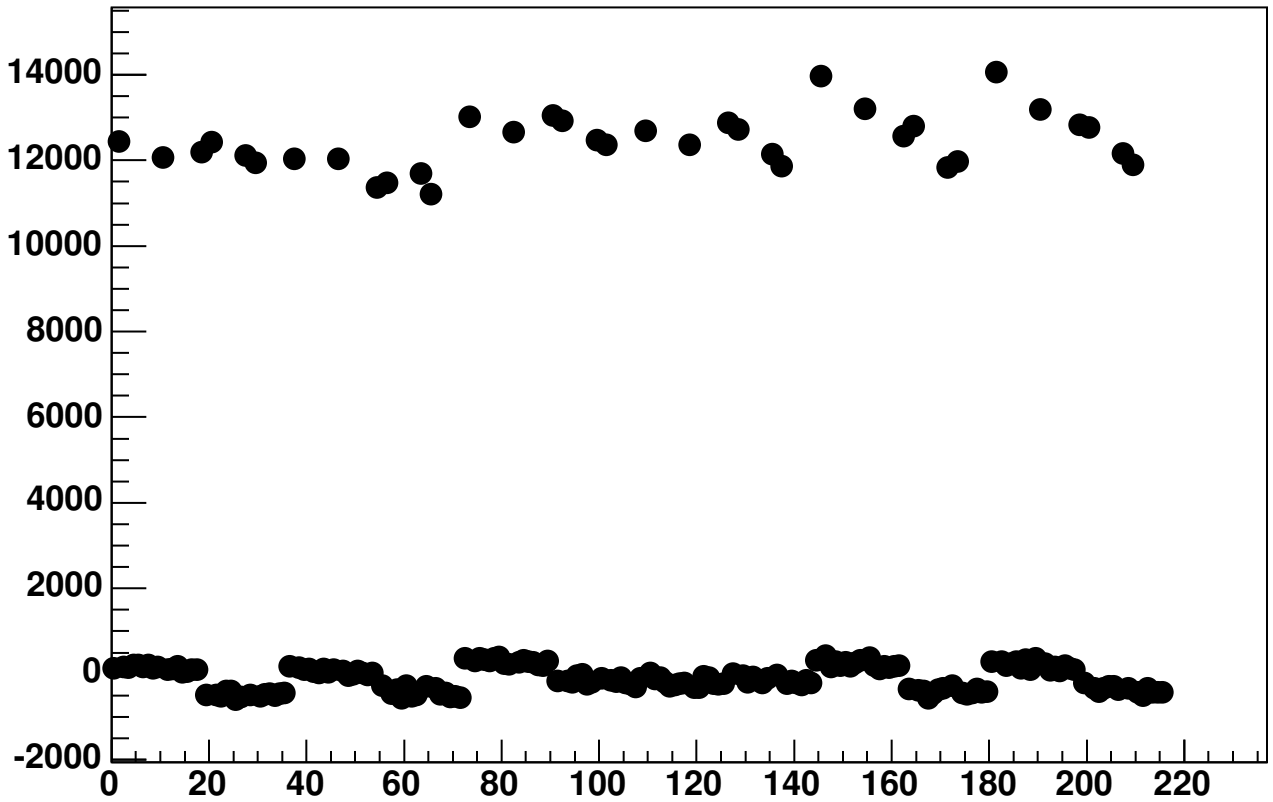
Enable 3, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



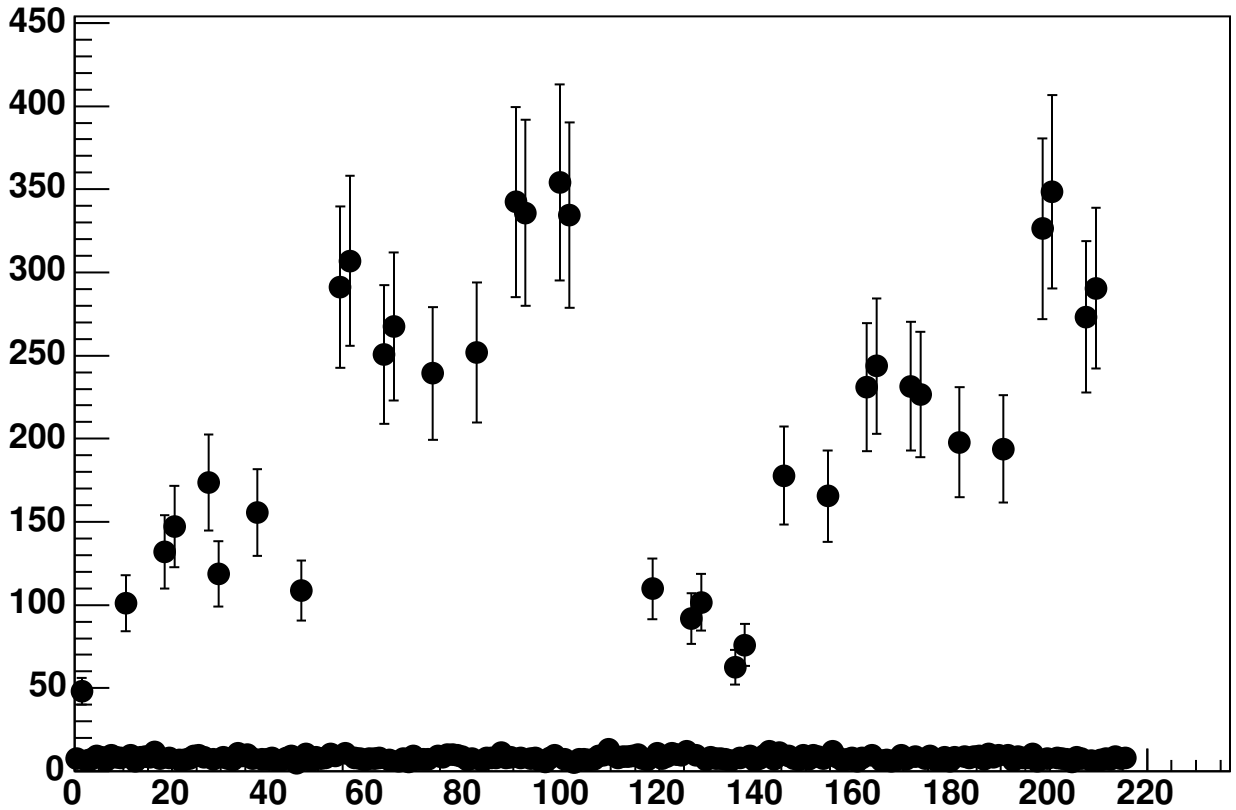
Enable 3, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



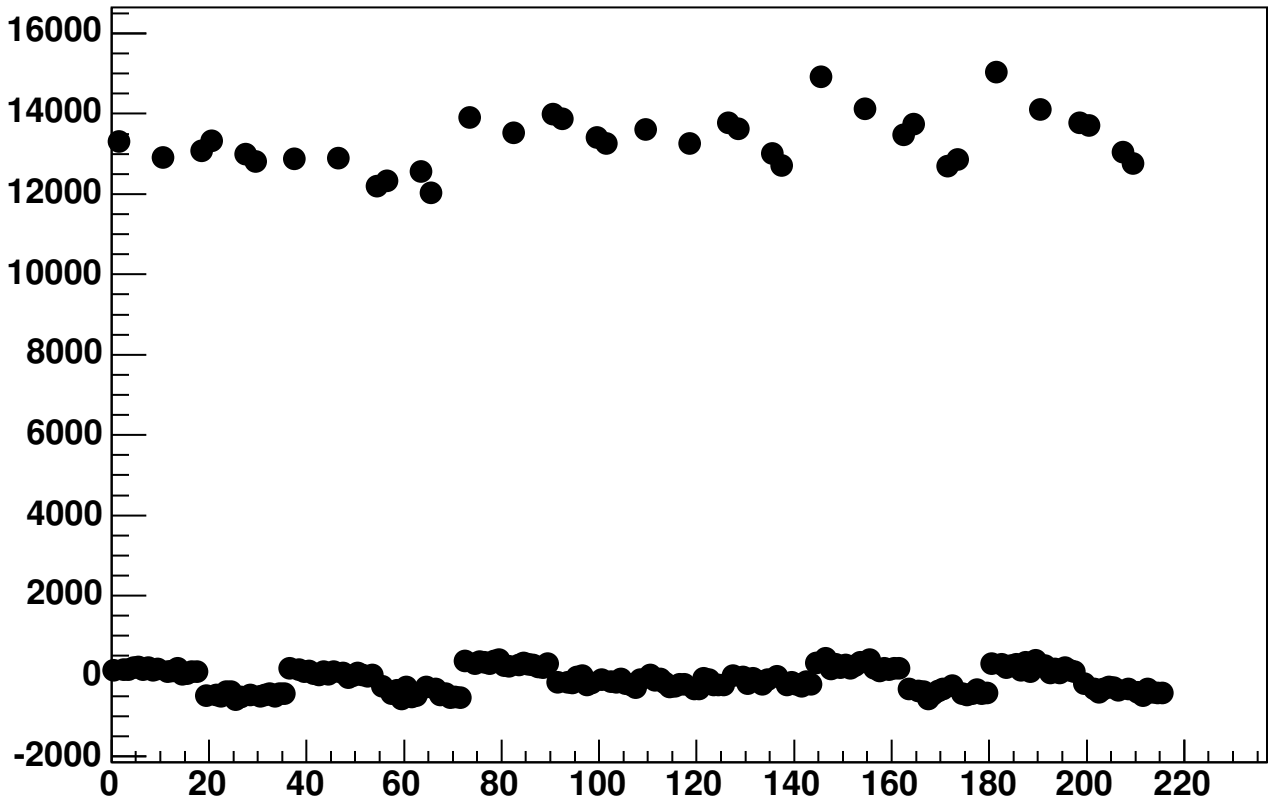
Enable 3, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



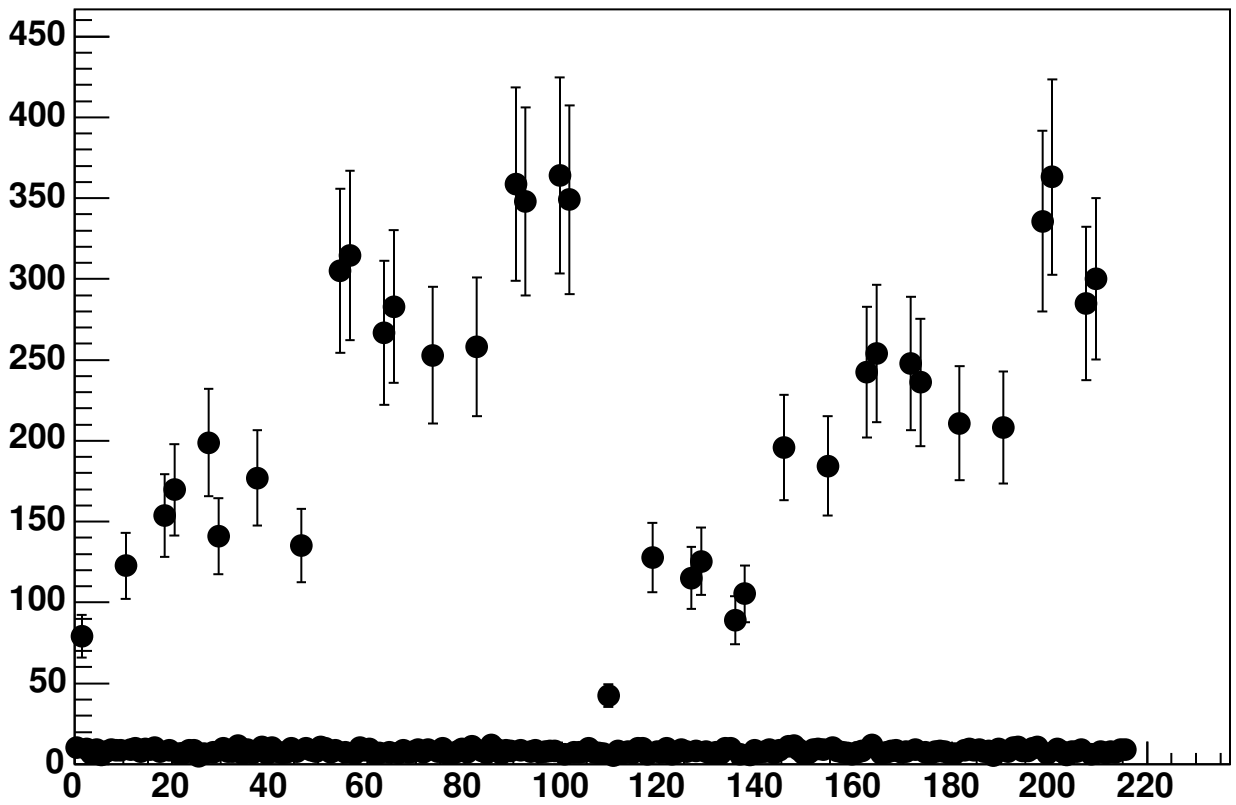
Enable 3, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



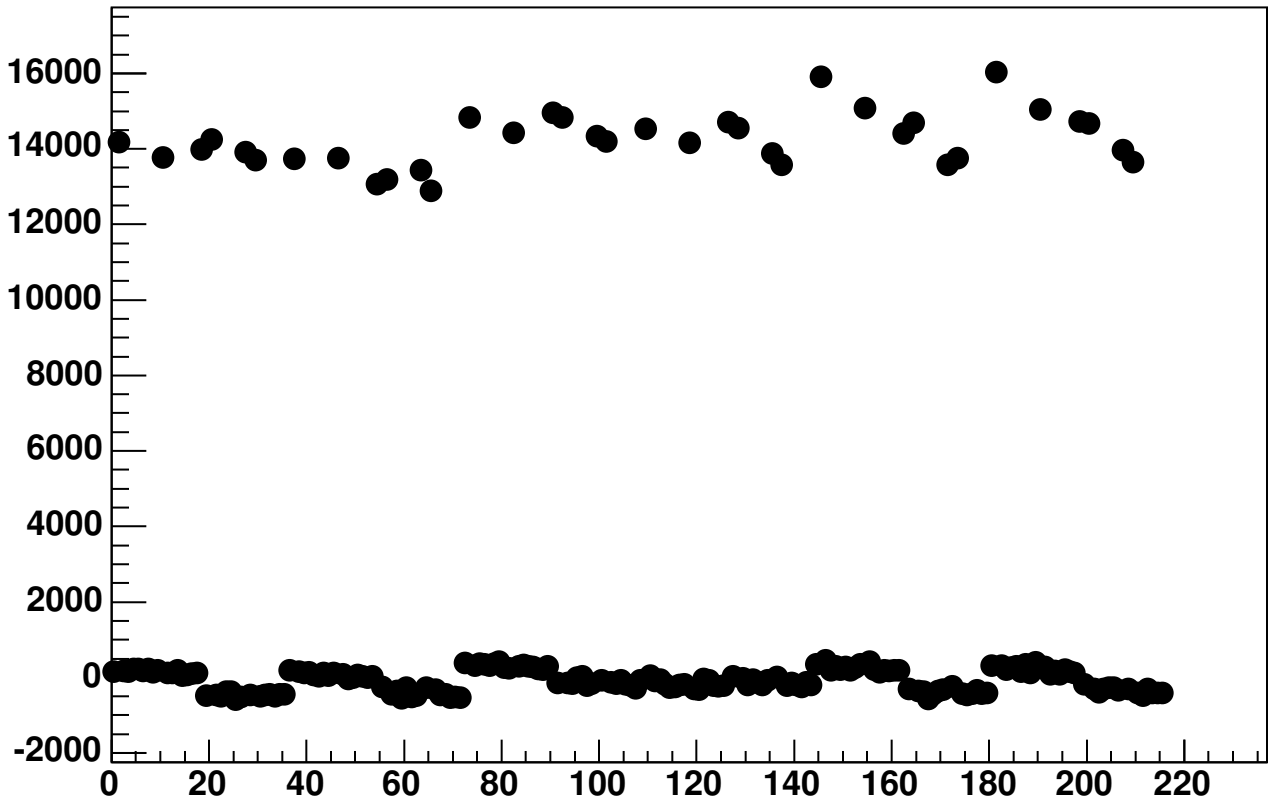
Enable 3, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



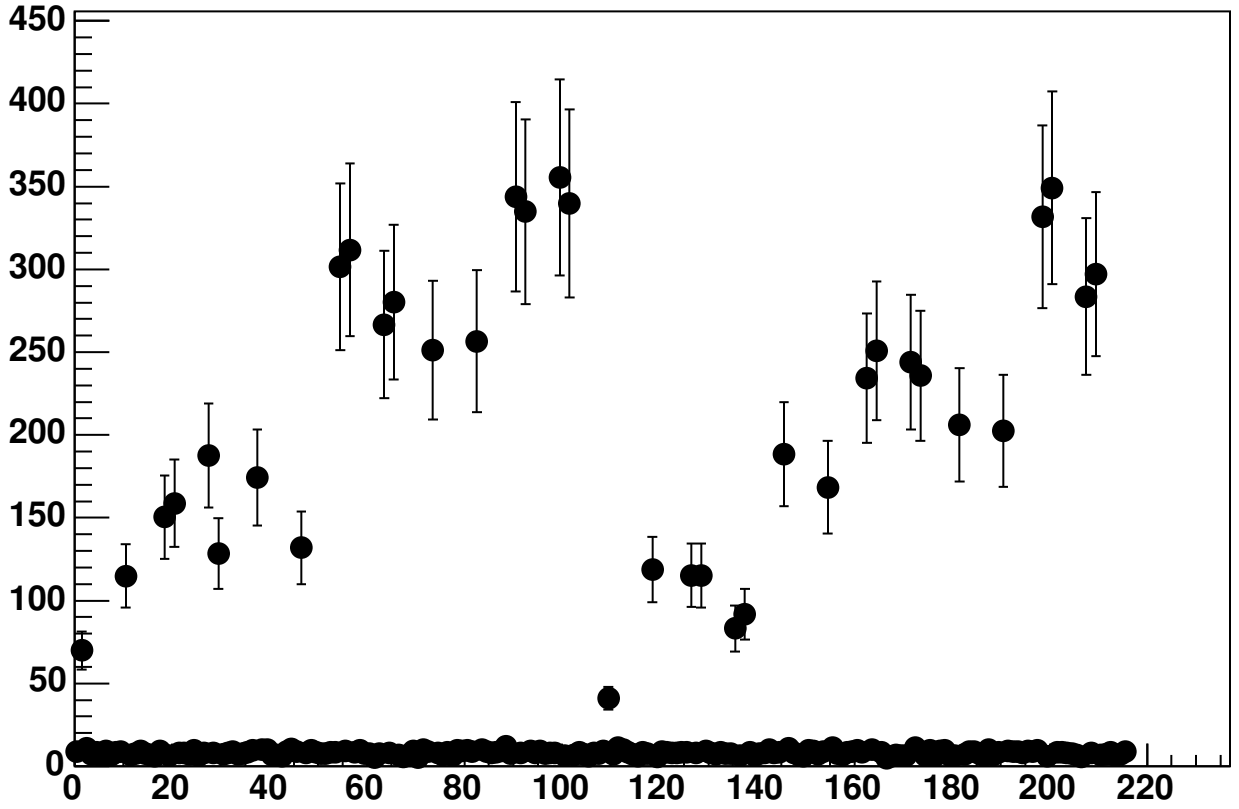
Enable 3, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



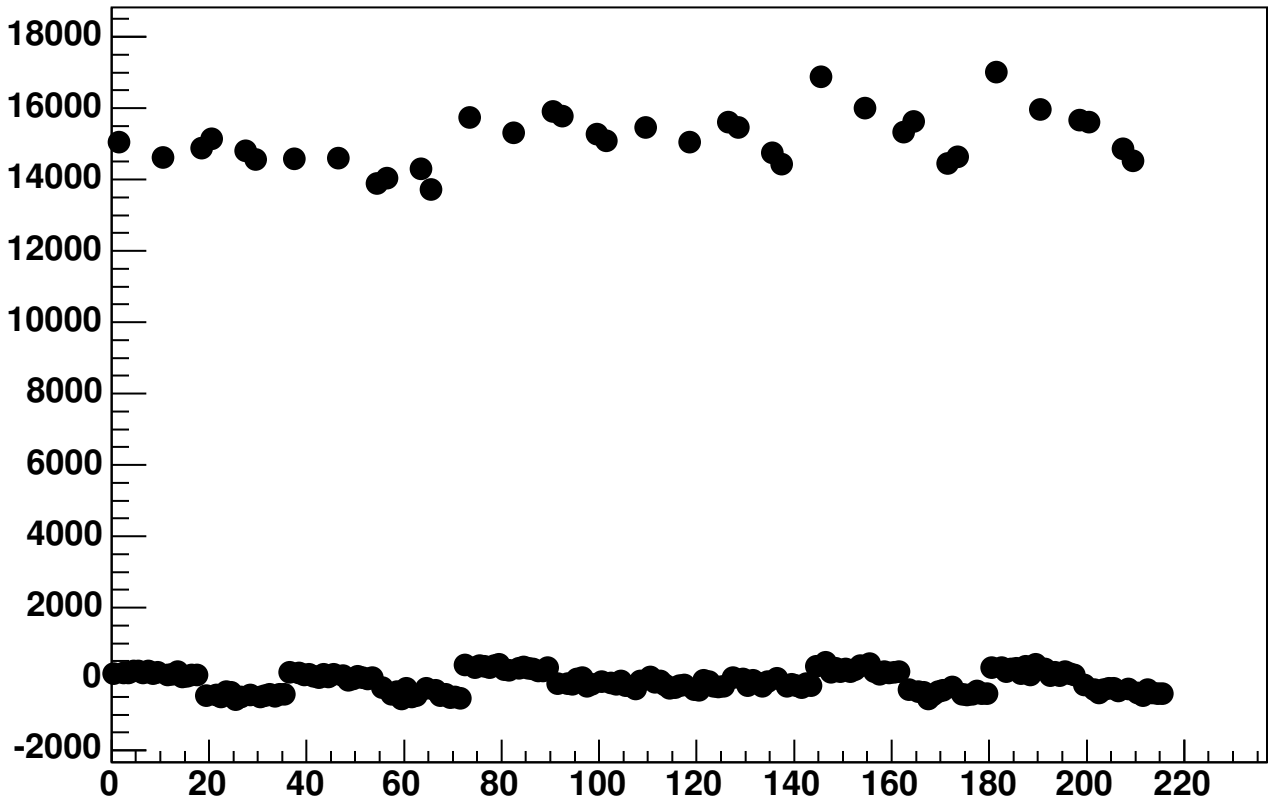
Enable 3, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



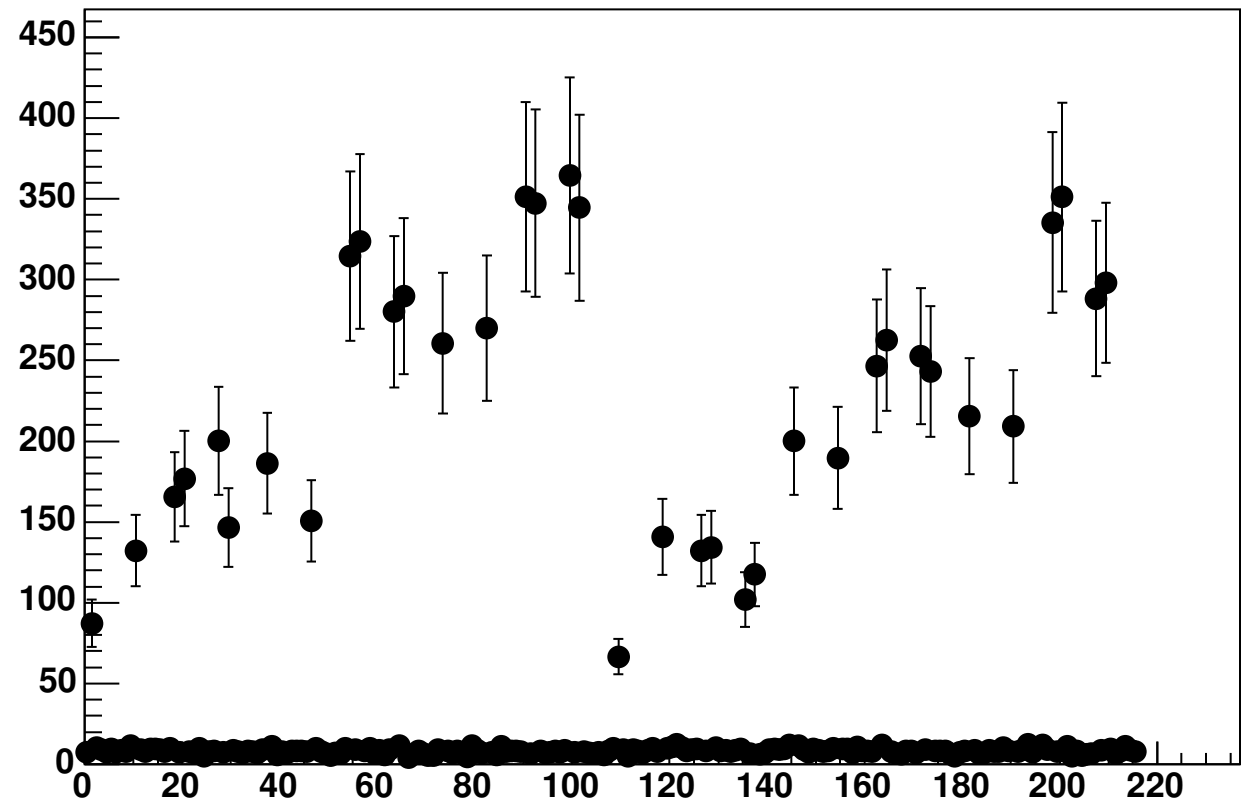
Enable 3, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



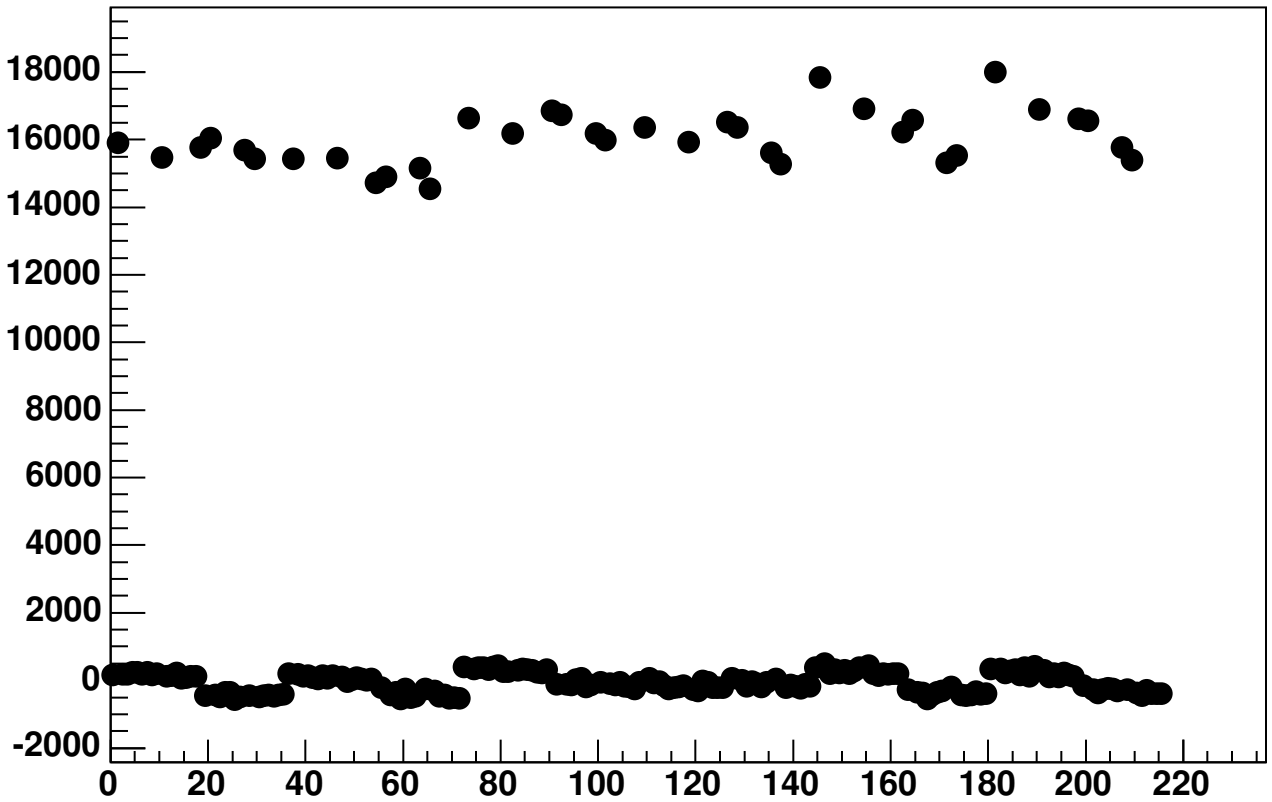
Enable 3, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



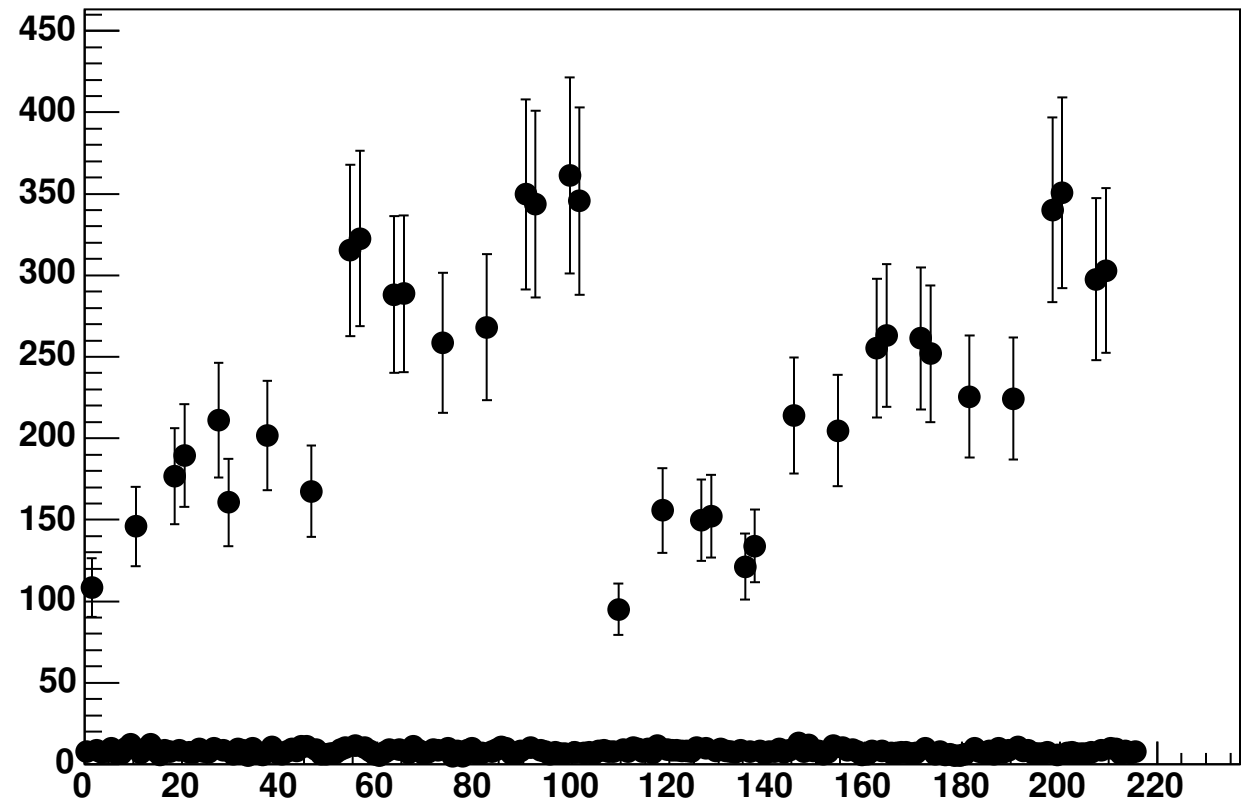
Enable 3, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



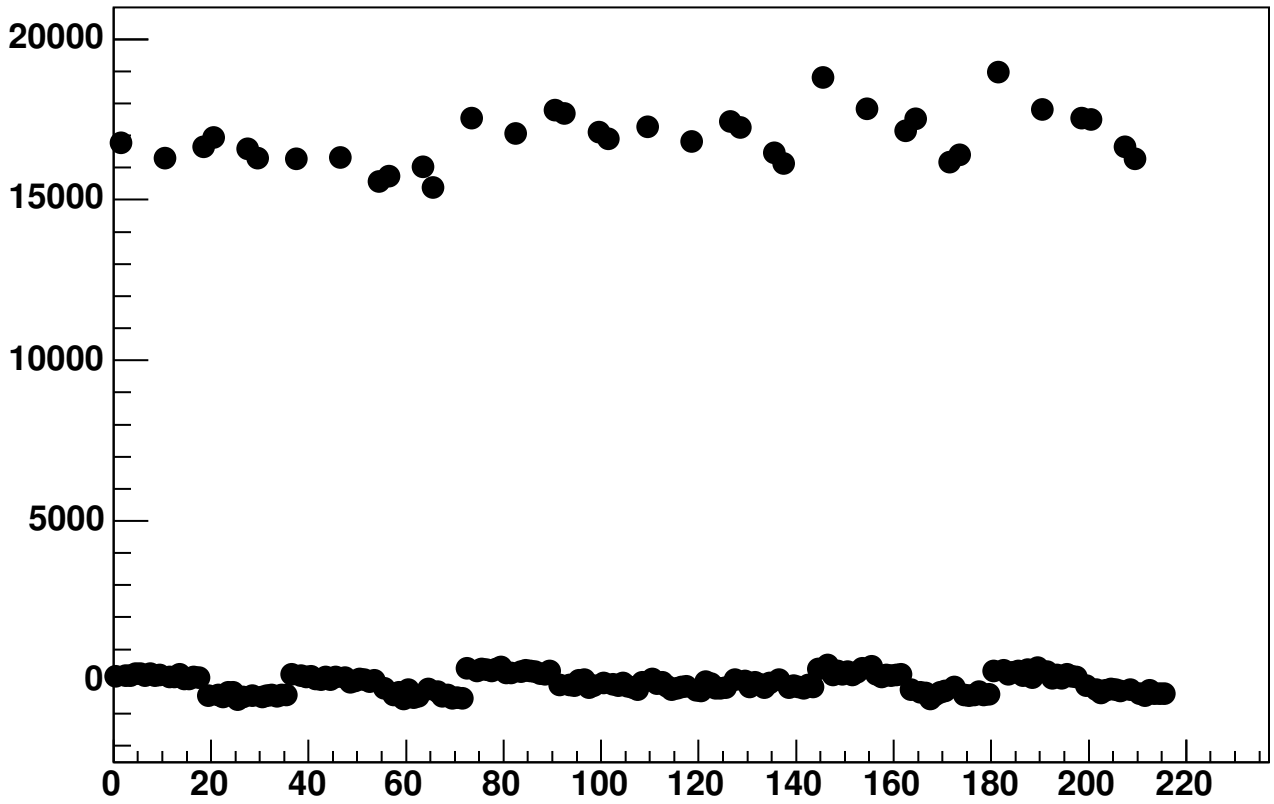
Enable 3, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



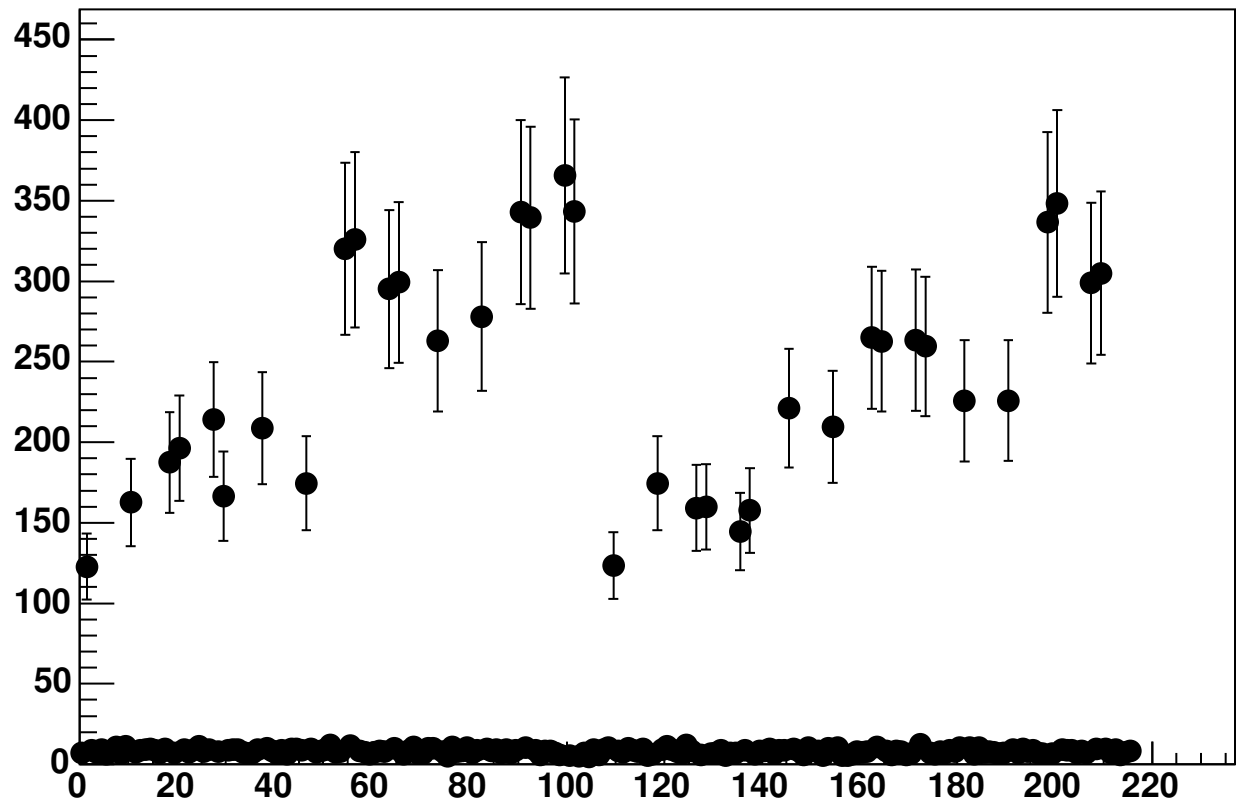
Enable 3, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



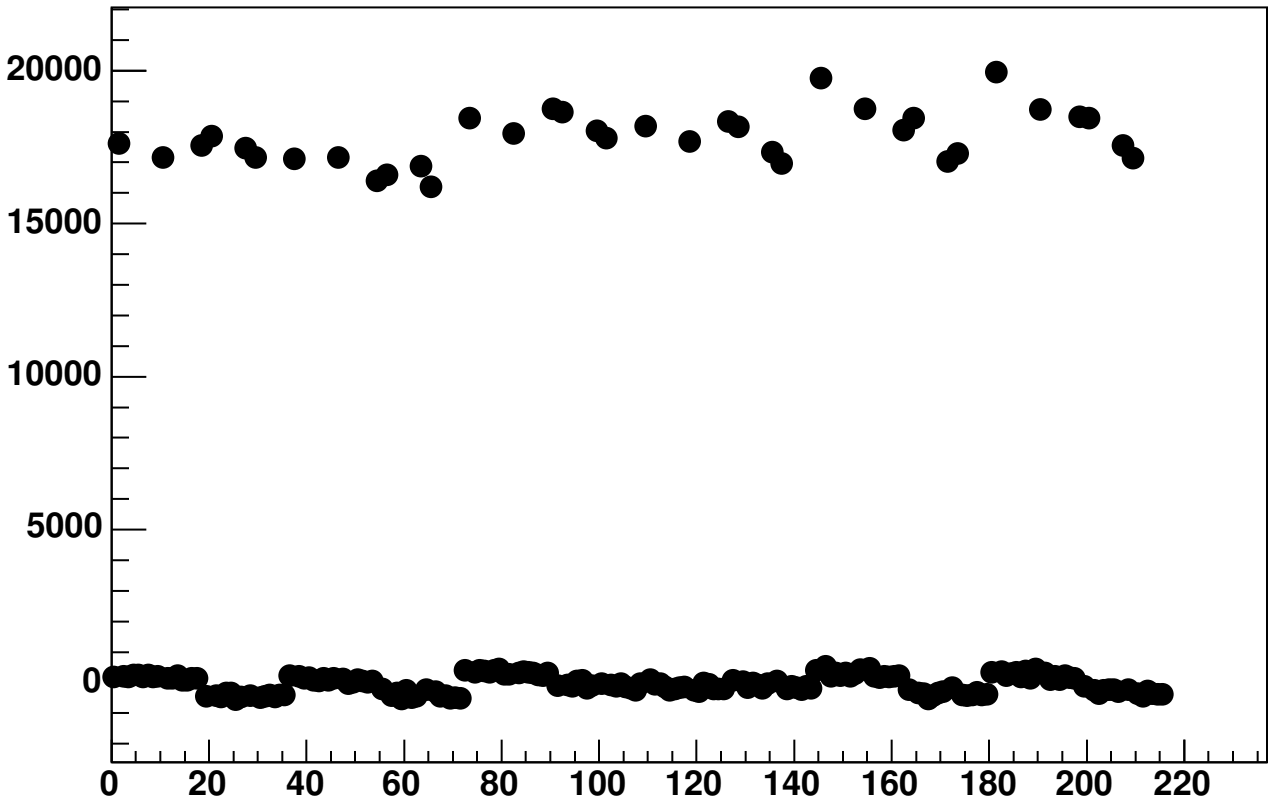
Enable 3, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



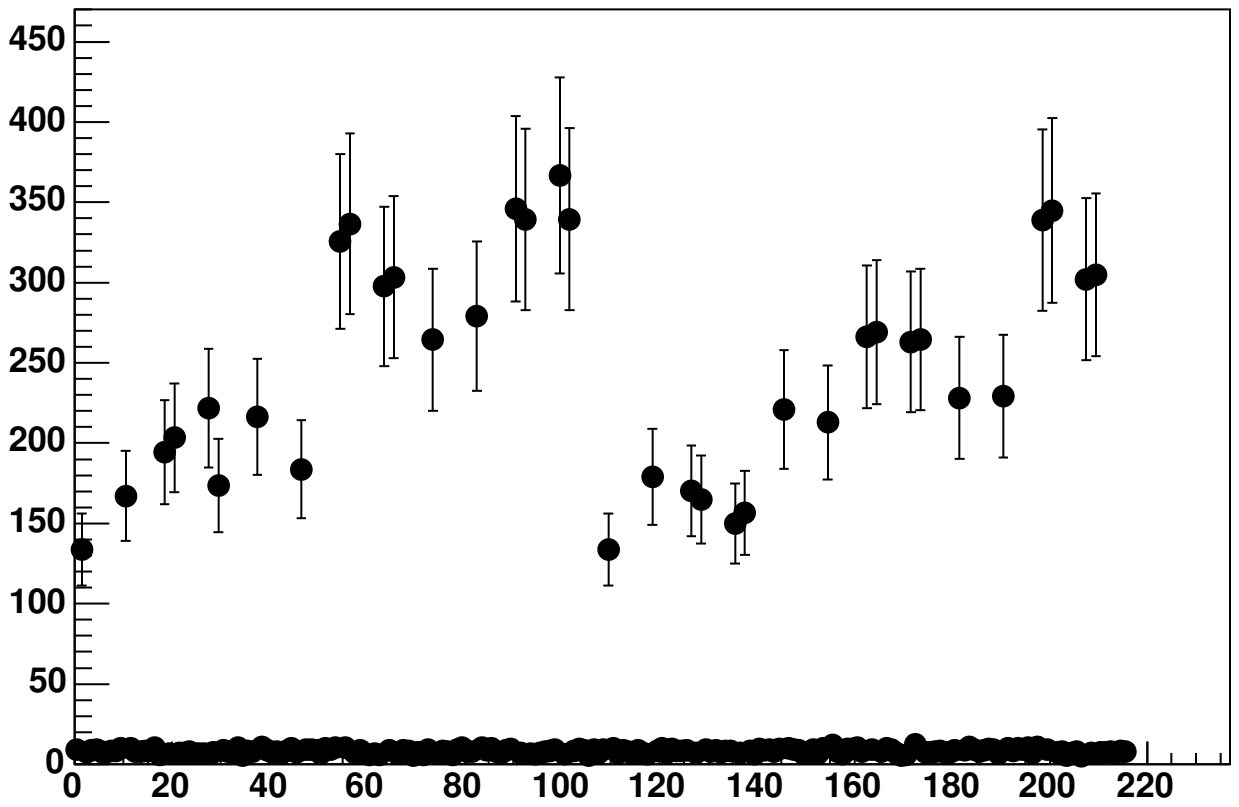
Enable 3, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



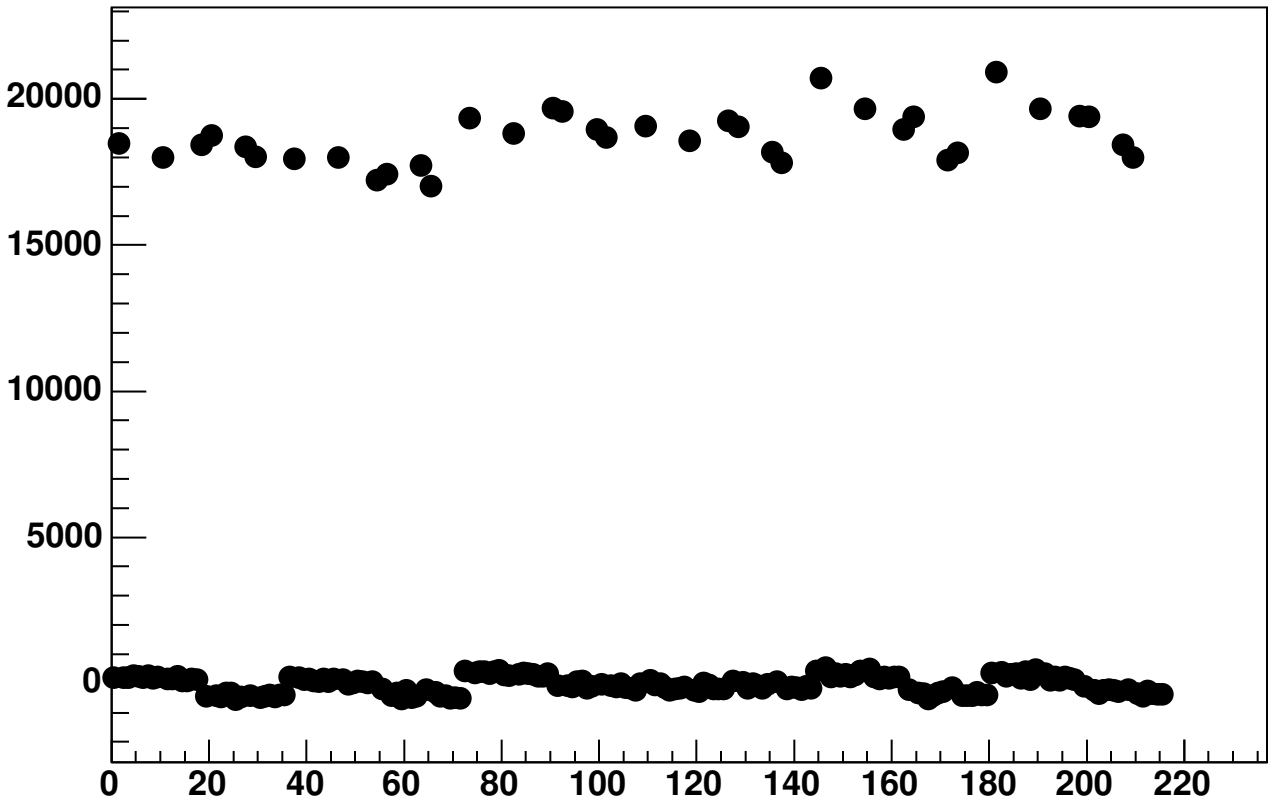
Enable 3, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



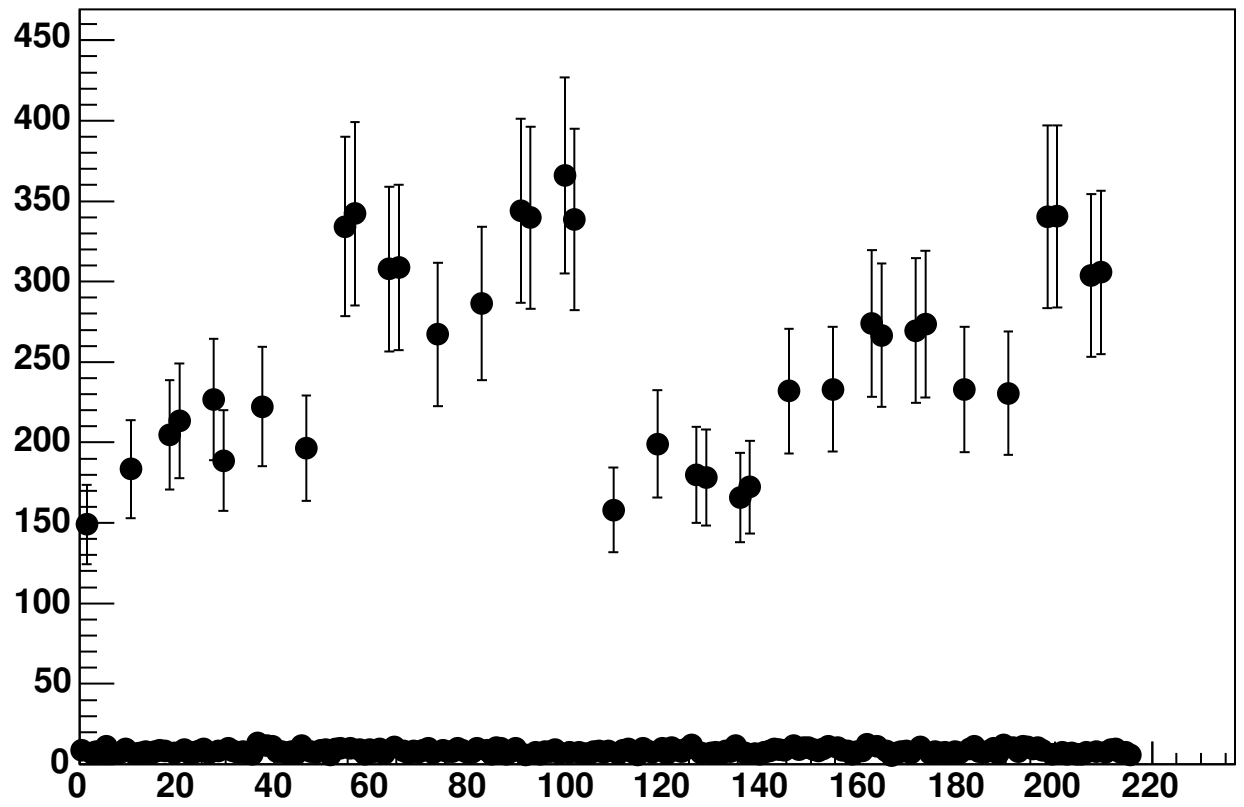
Enable 3, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



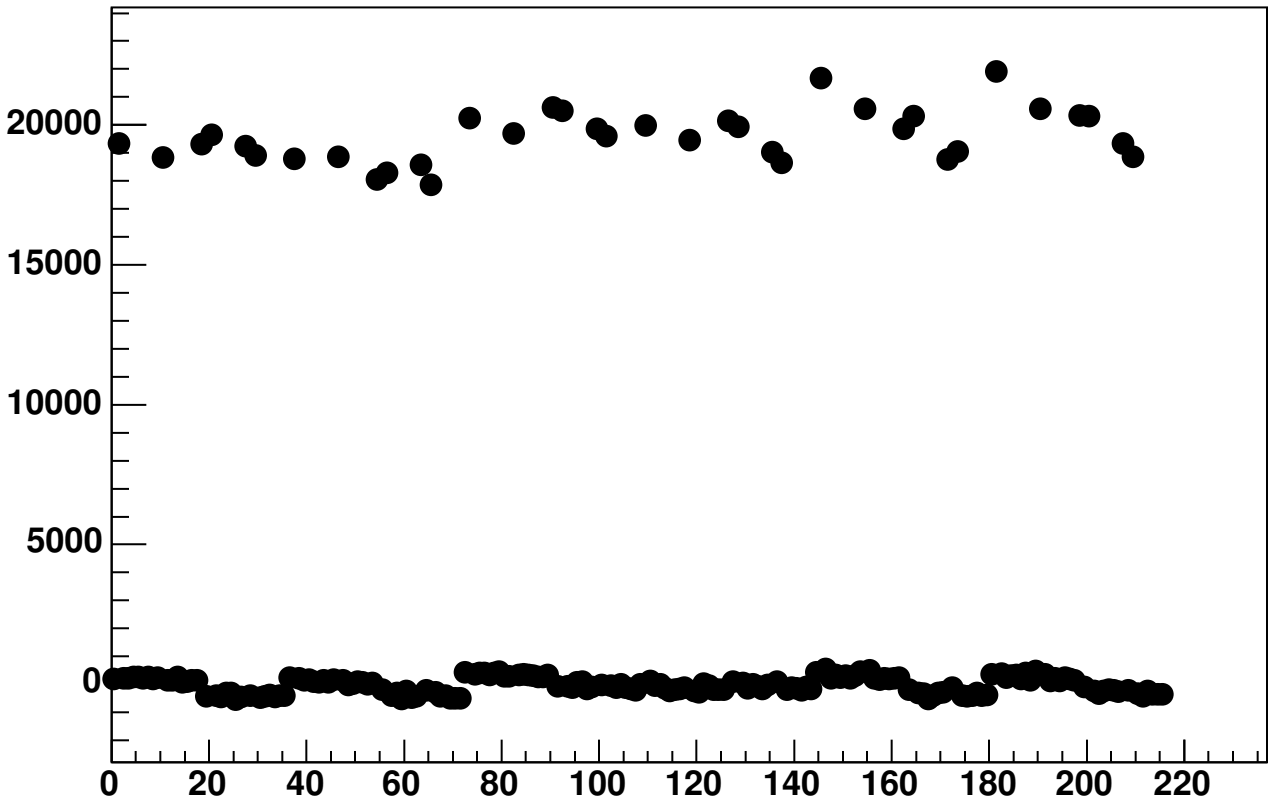
Enable 3, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



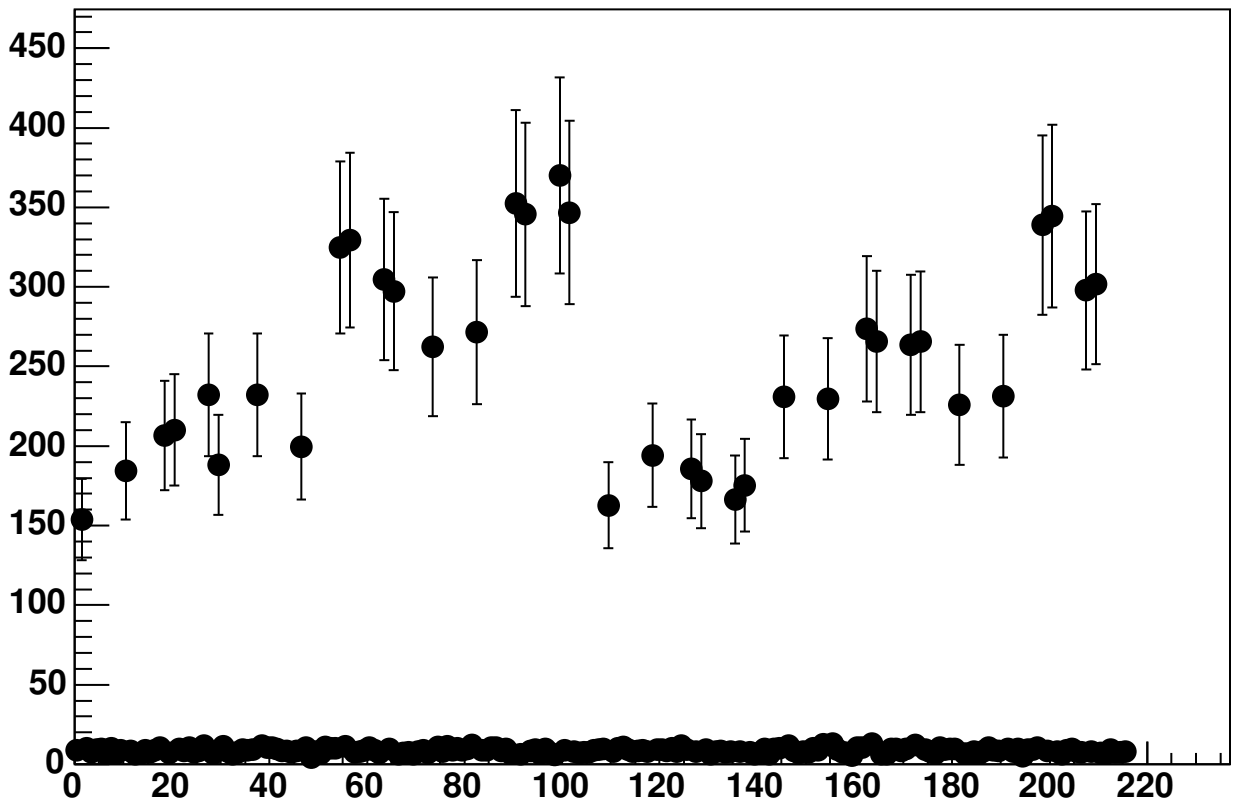
Enable 3, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



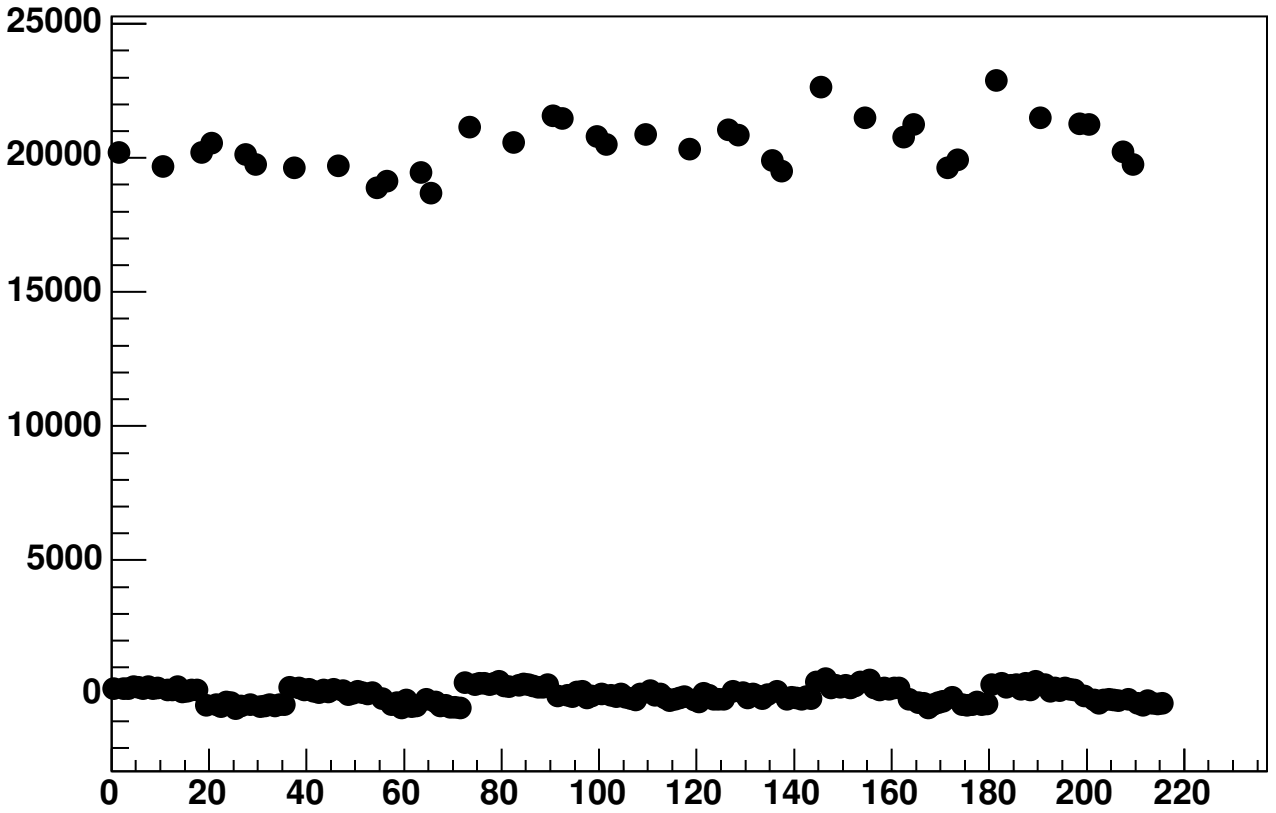
Enable 3, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



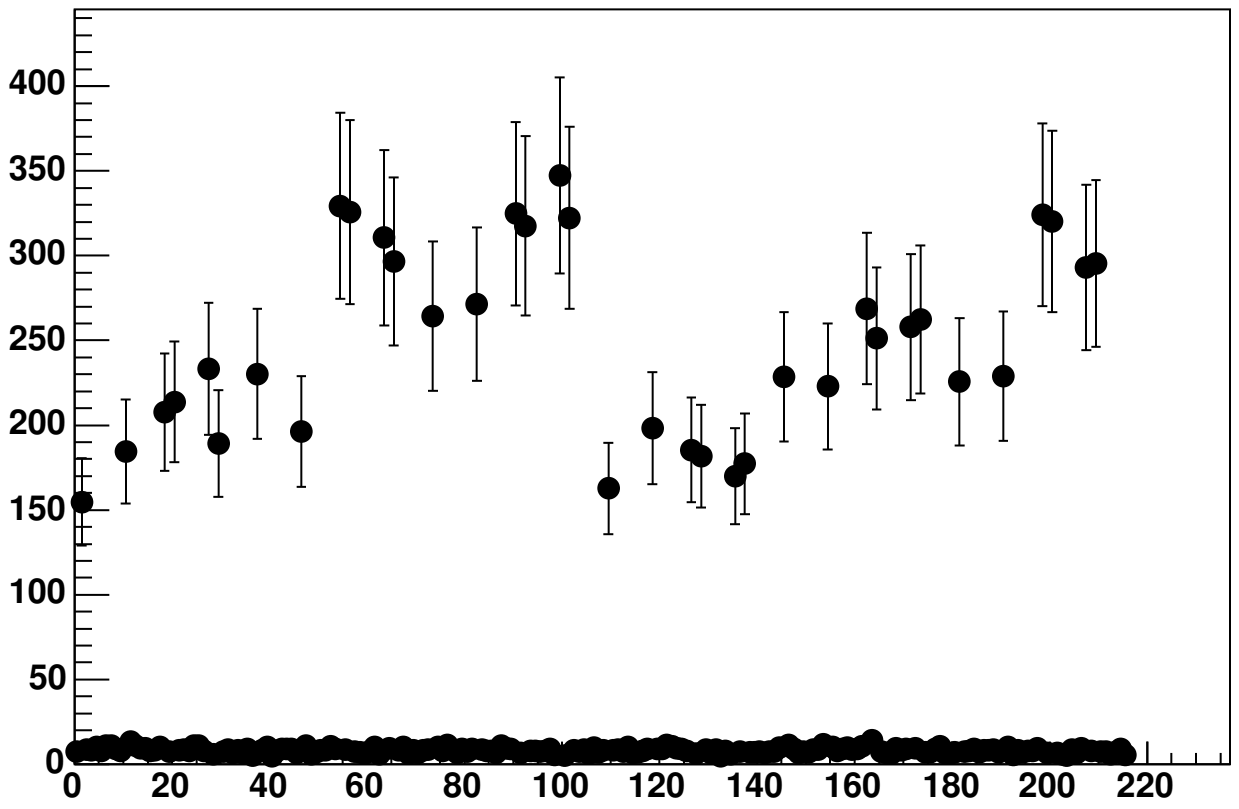
Enable 3, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



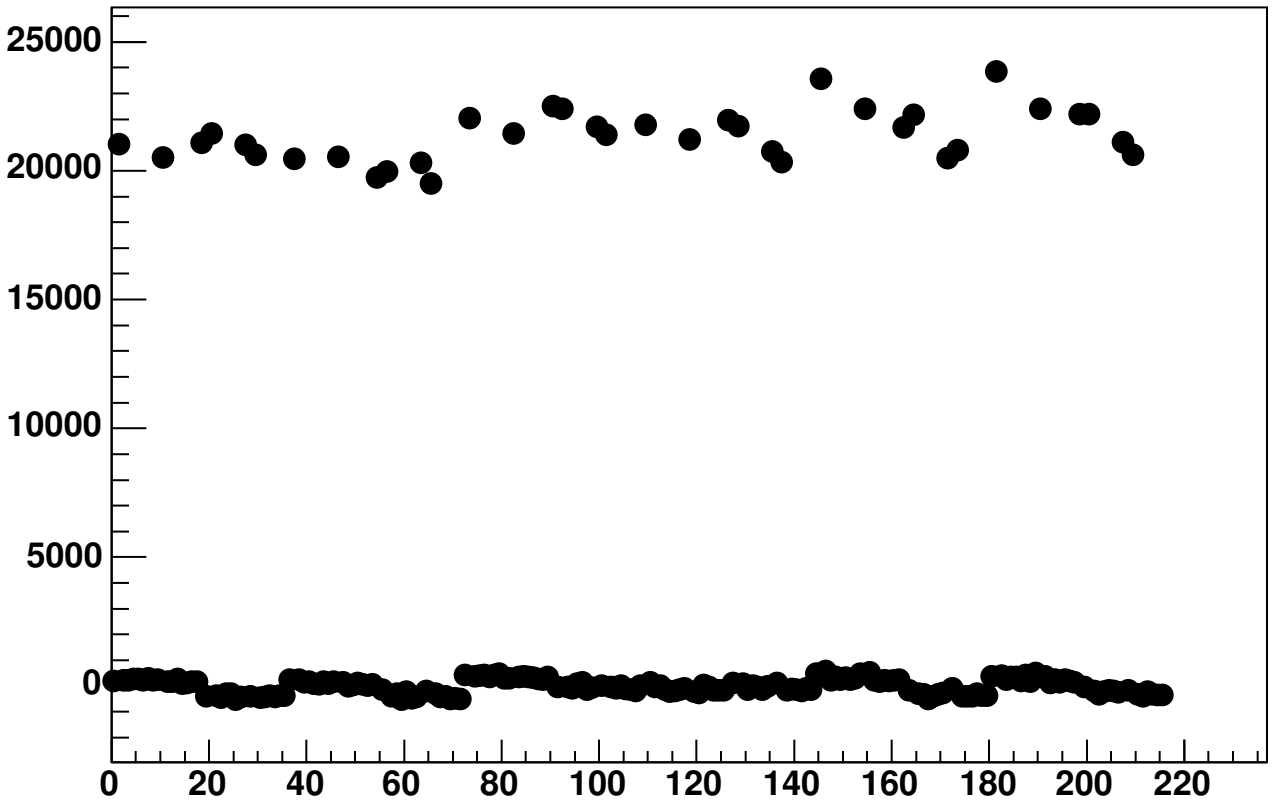
Enable 3, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



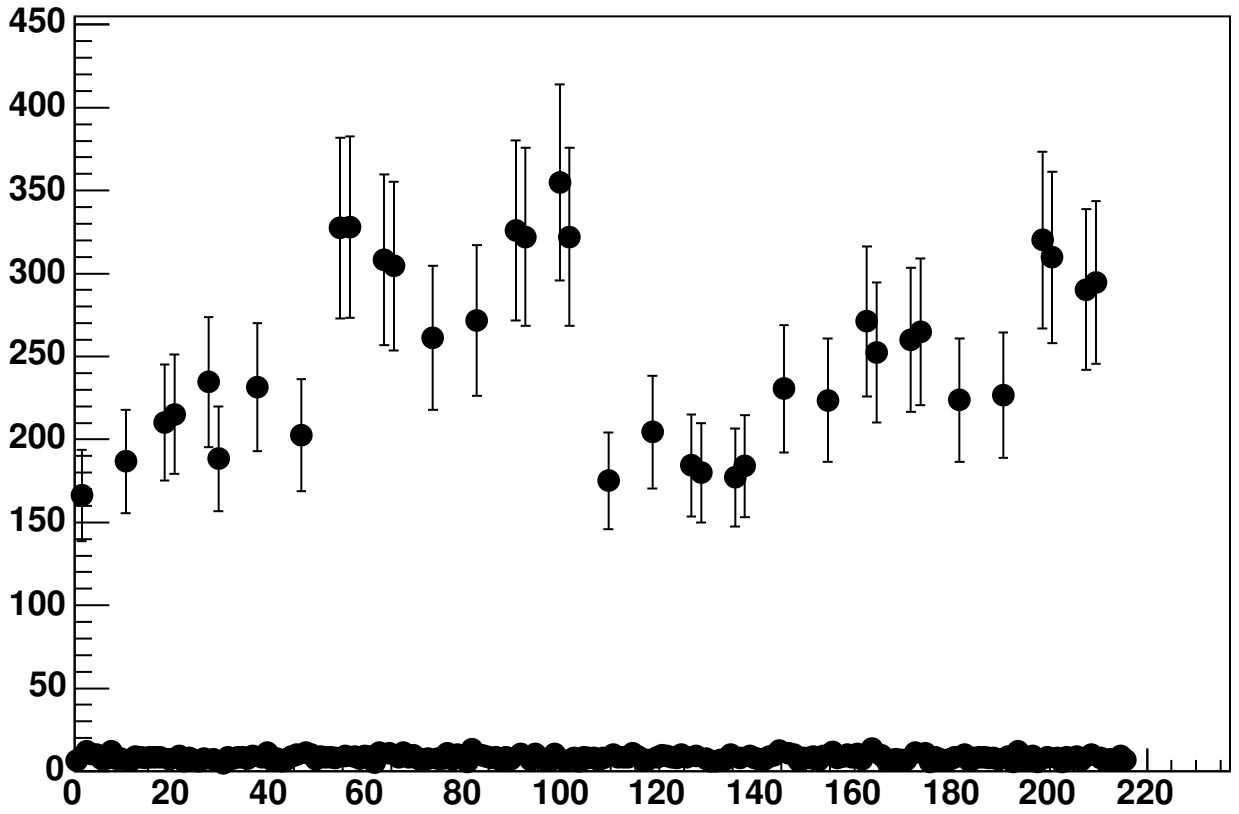
Enable 3, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



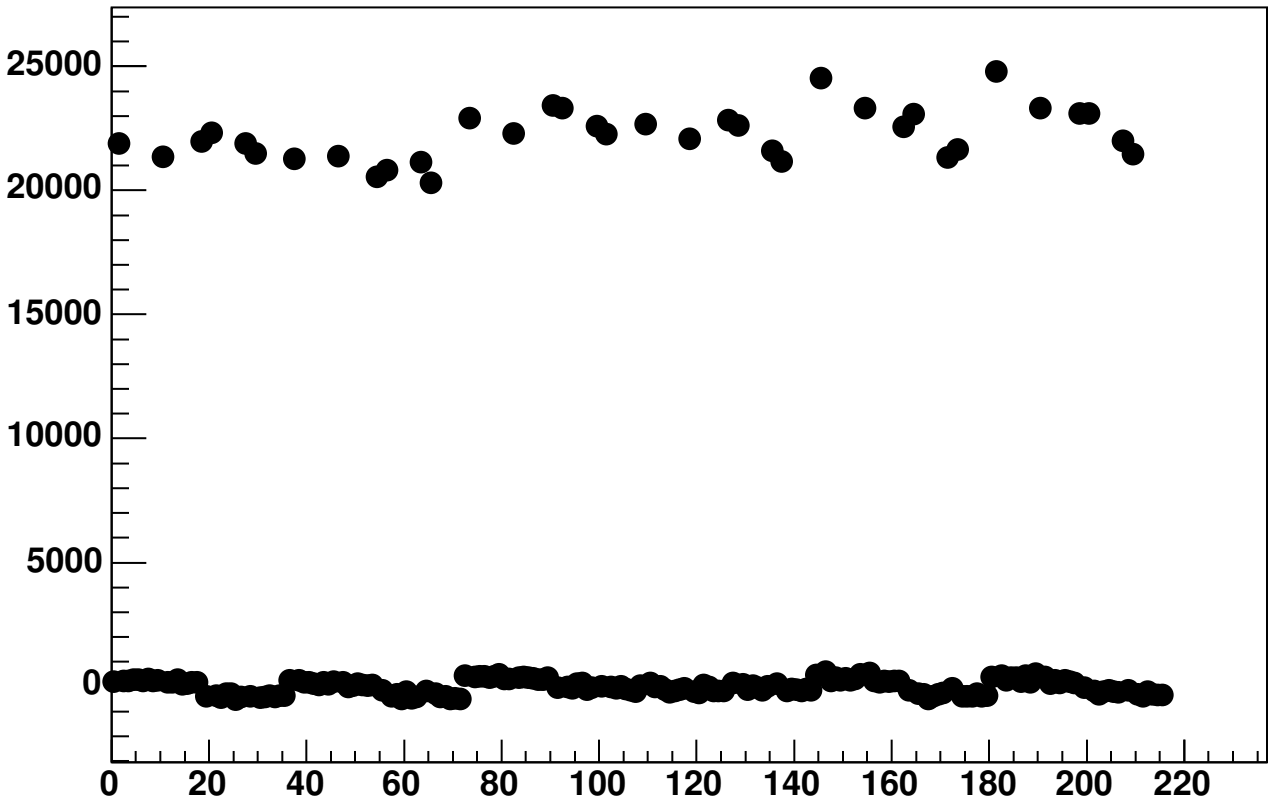
Enable 3, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



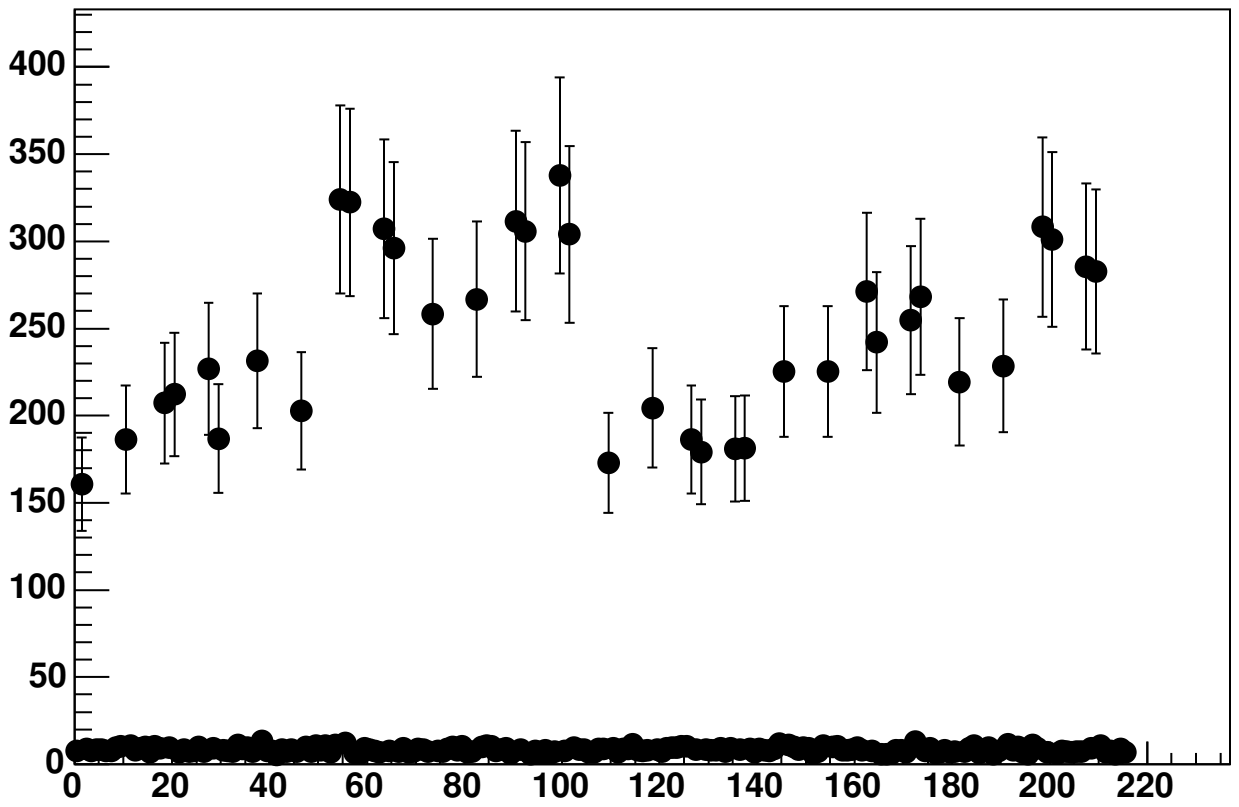
Enable 3, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



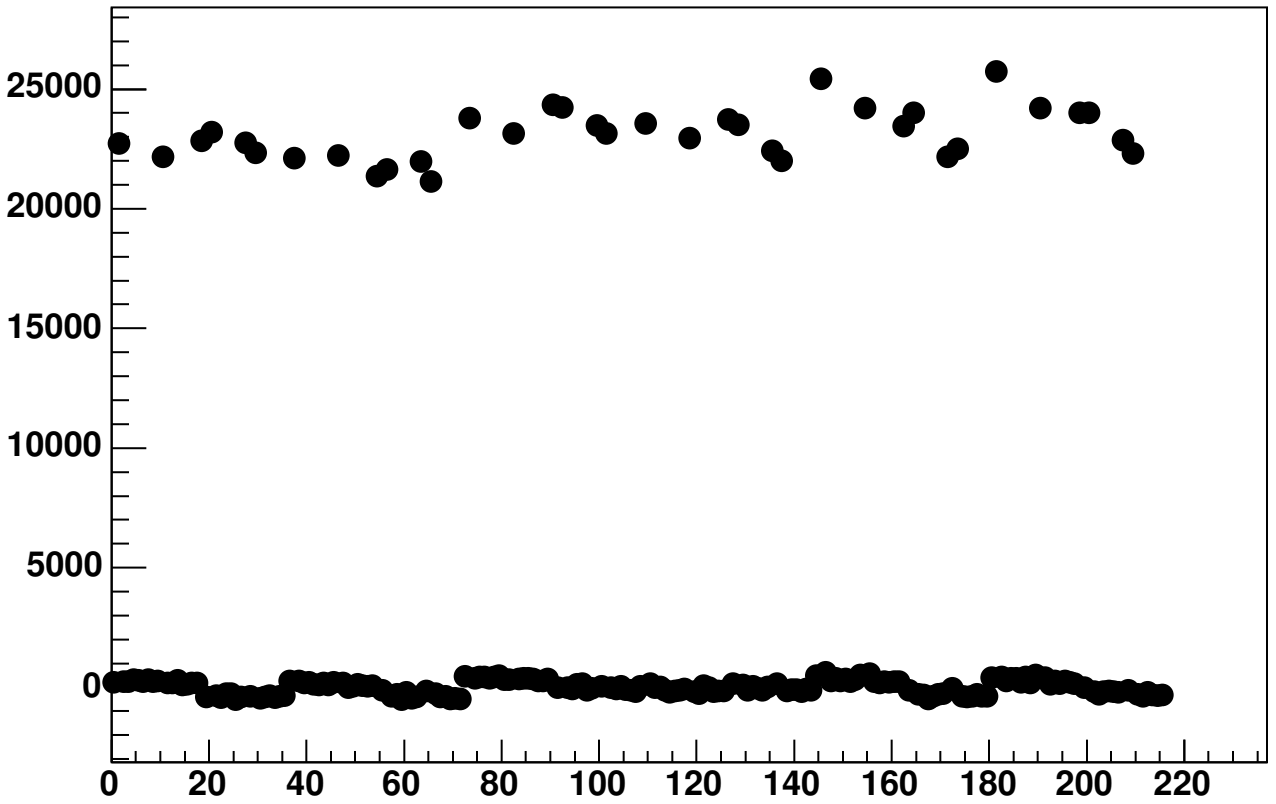
Enable 3, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



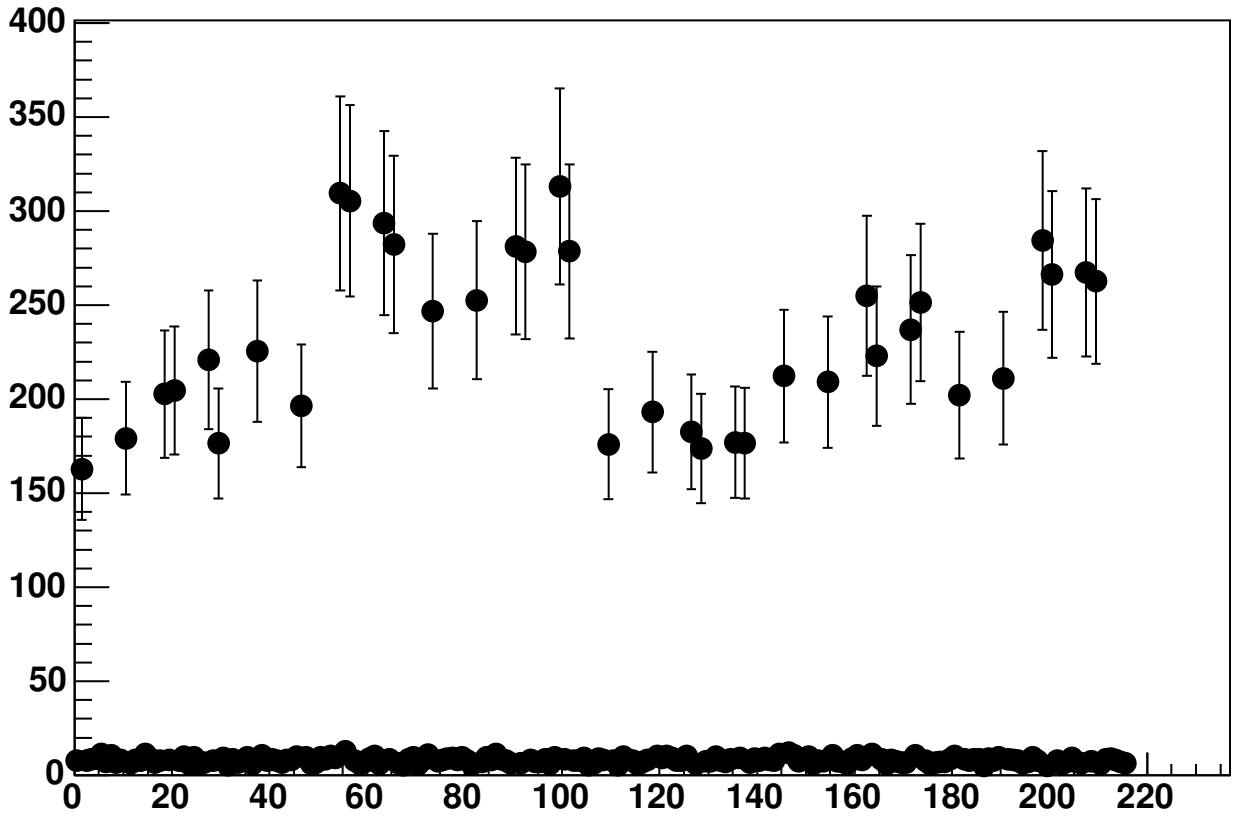
Enable 3, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



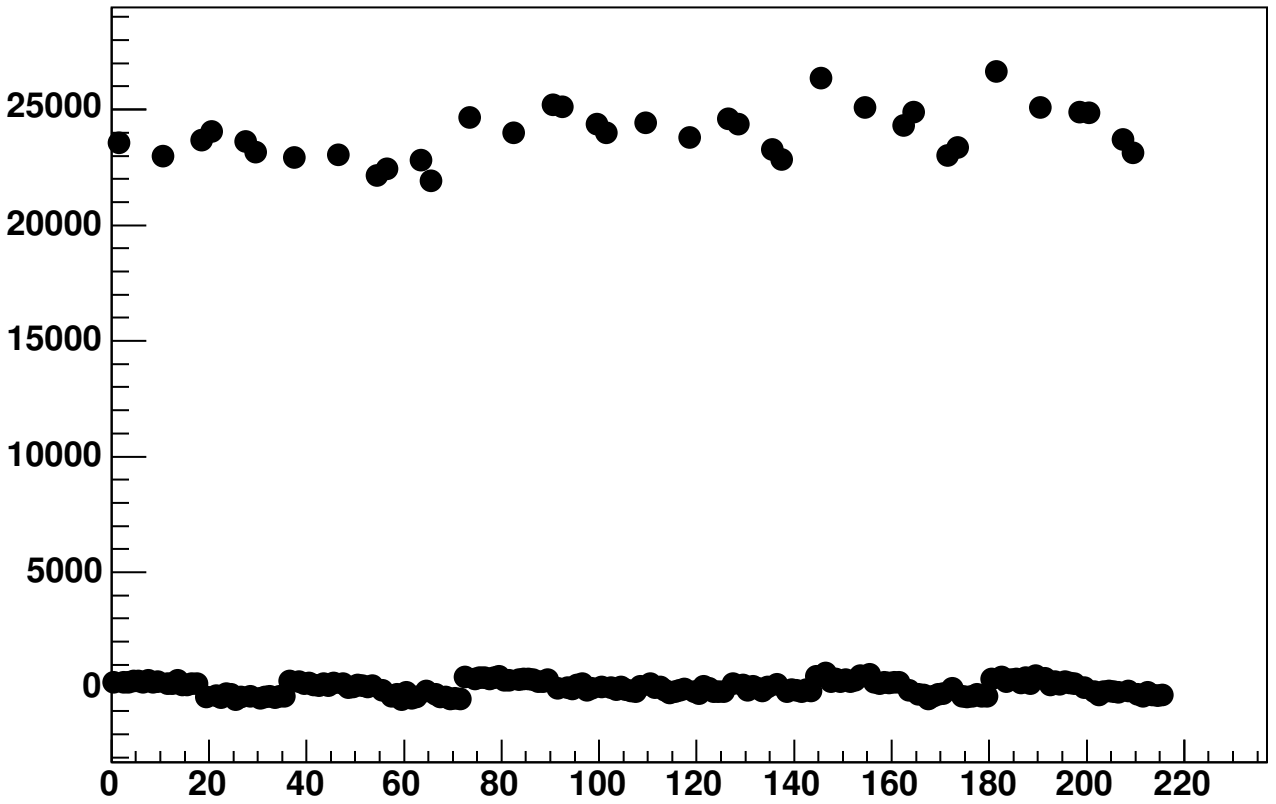
Enable 3, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



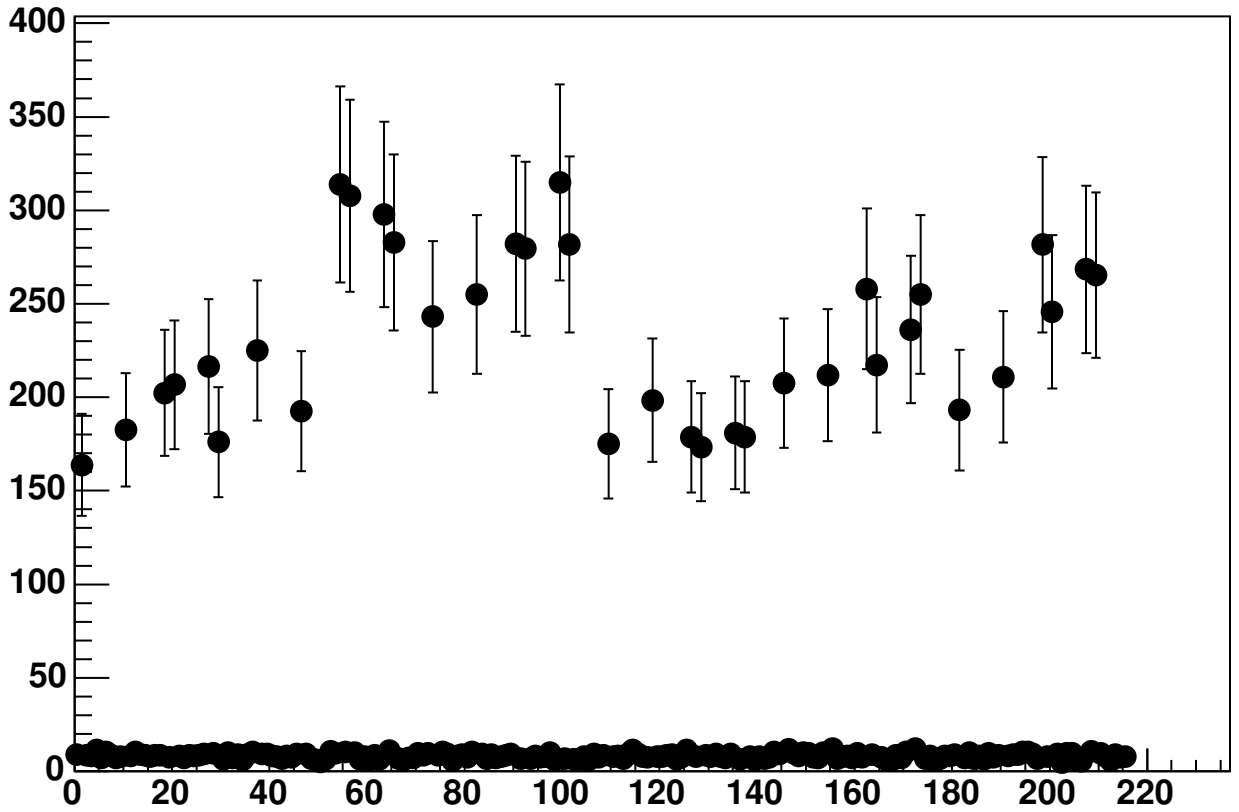
Enable 3, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



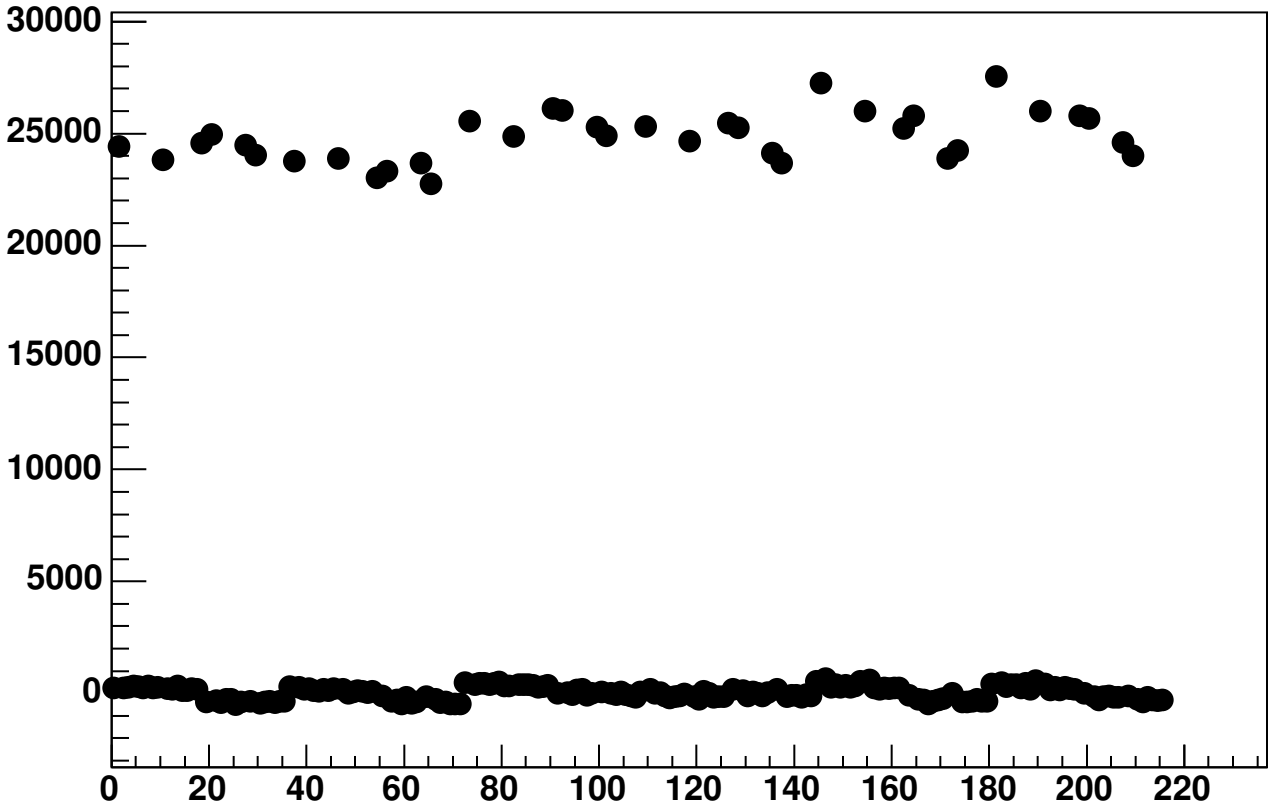
Enable 3, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



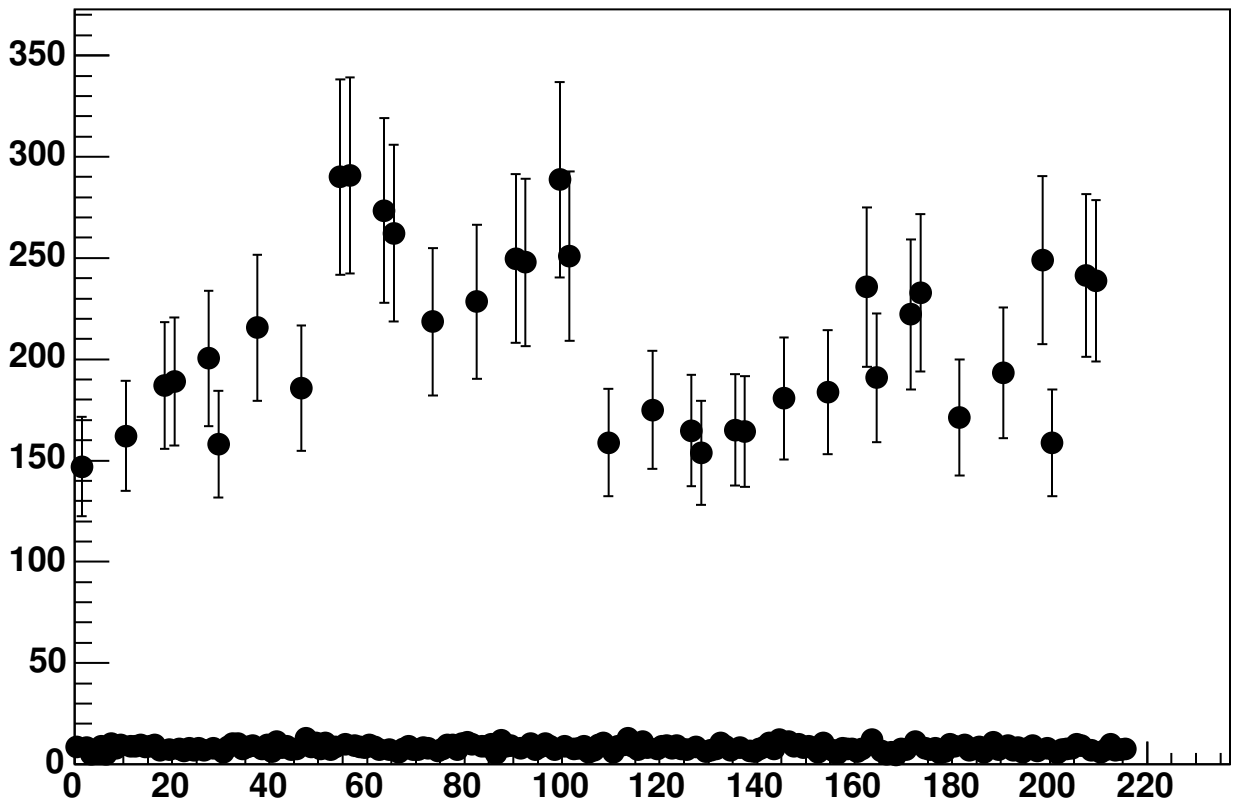
Enable 3, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



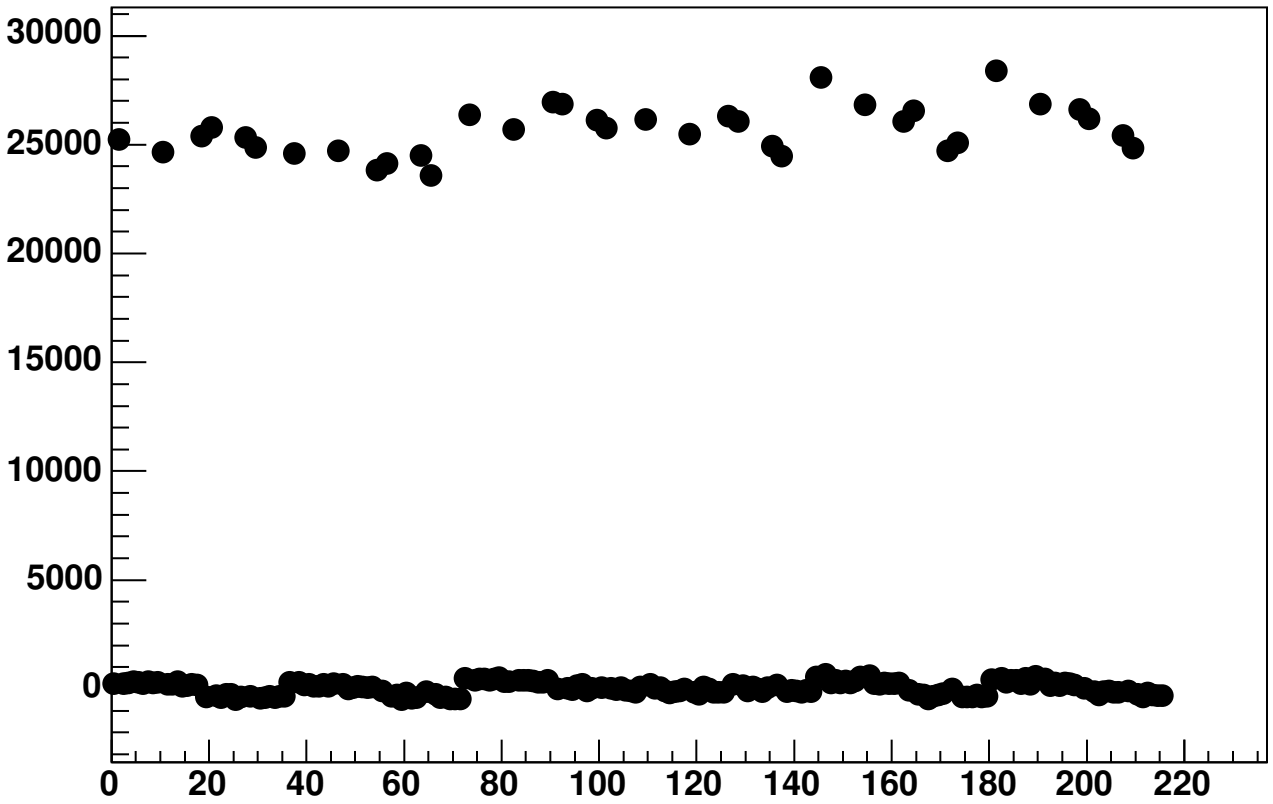
Enable 3, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



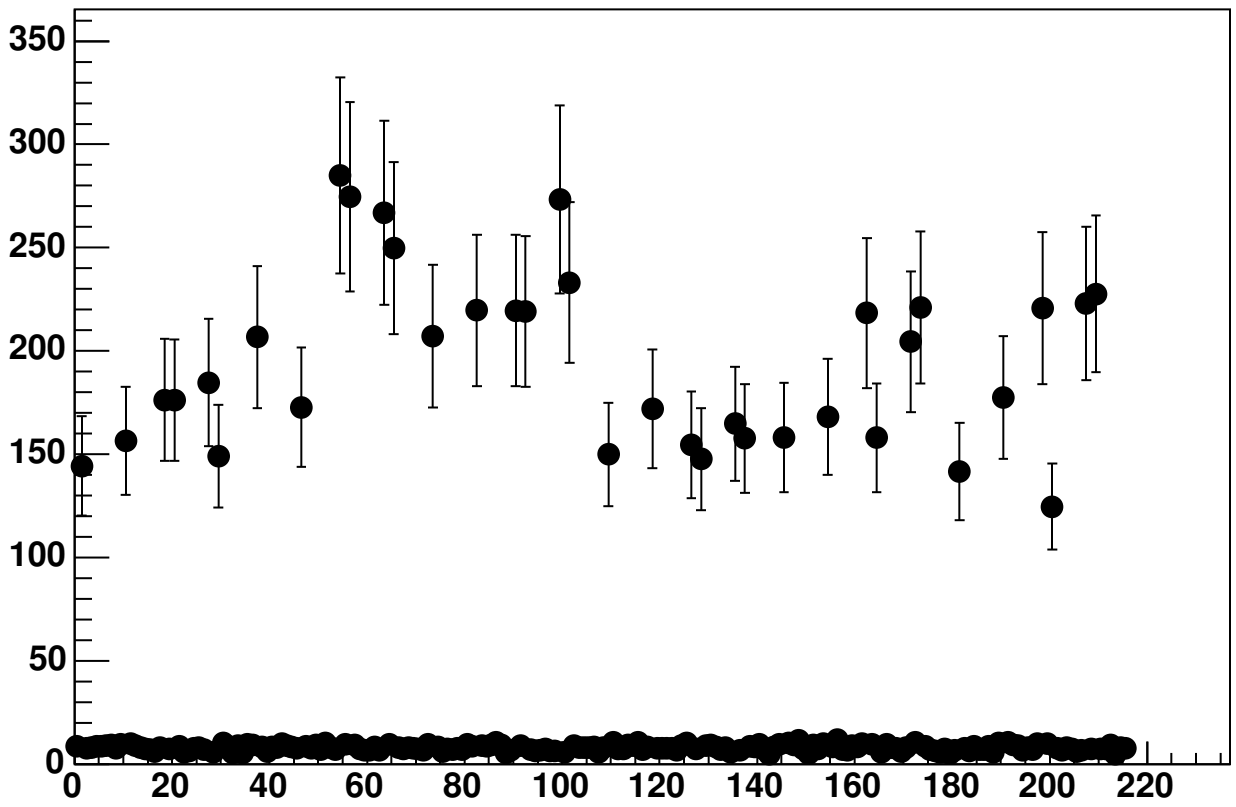
Enable 3, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



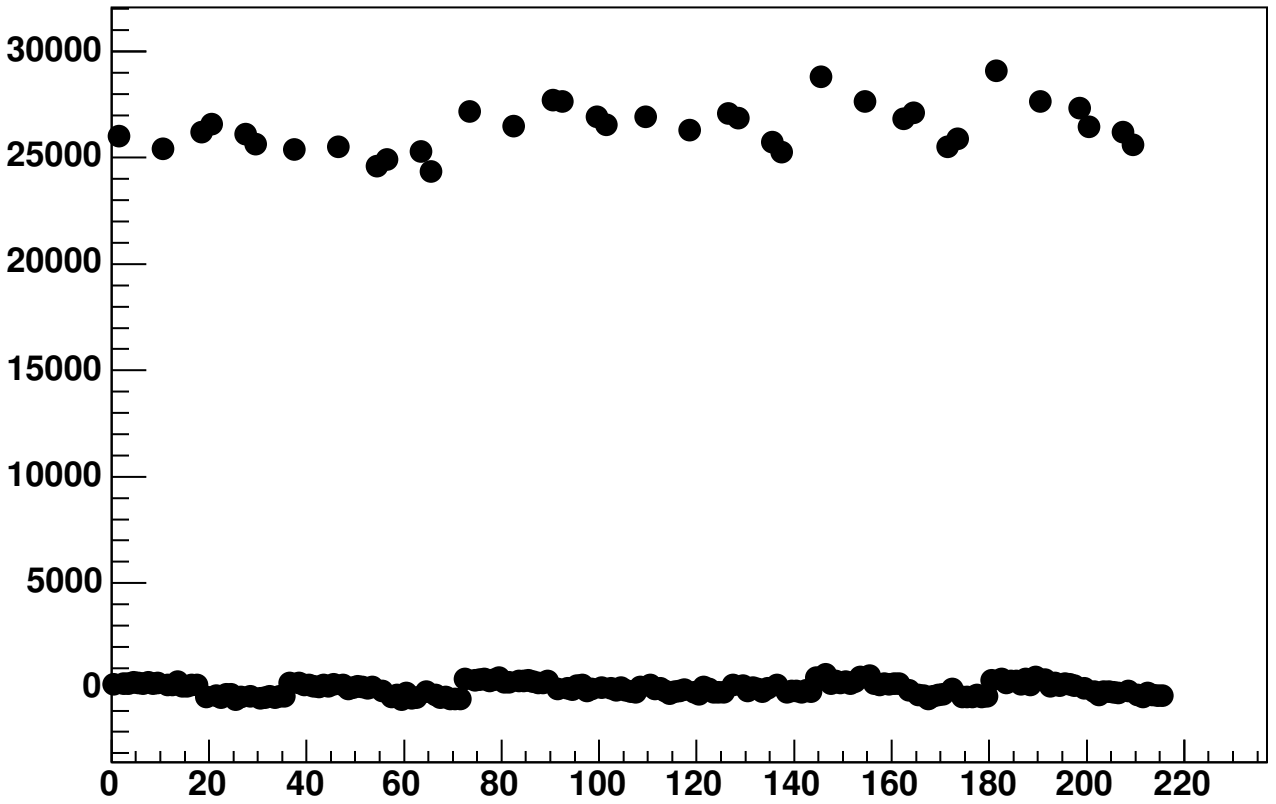
Enable 3, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



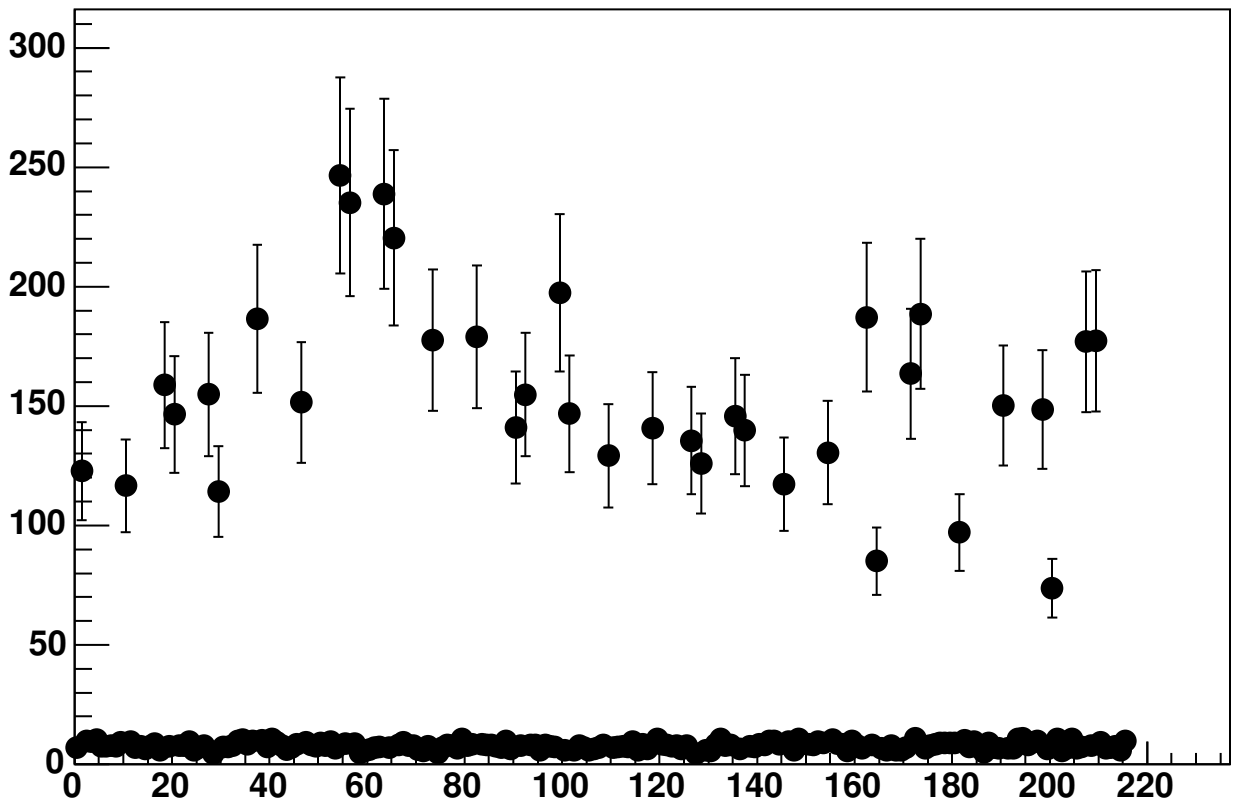
Enable 3, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



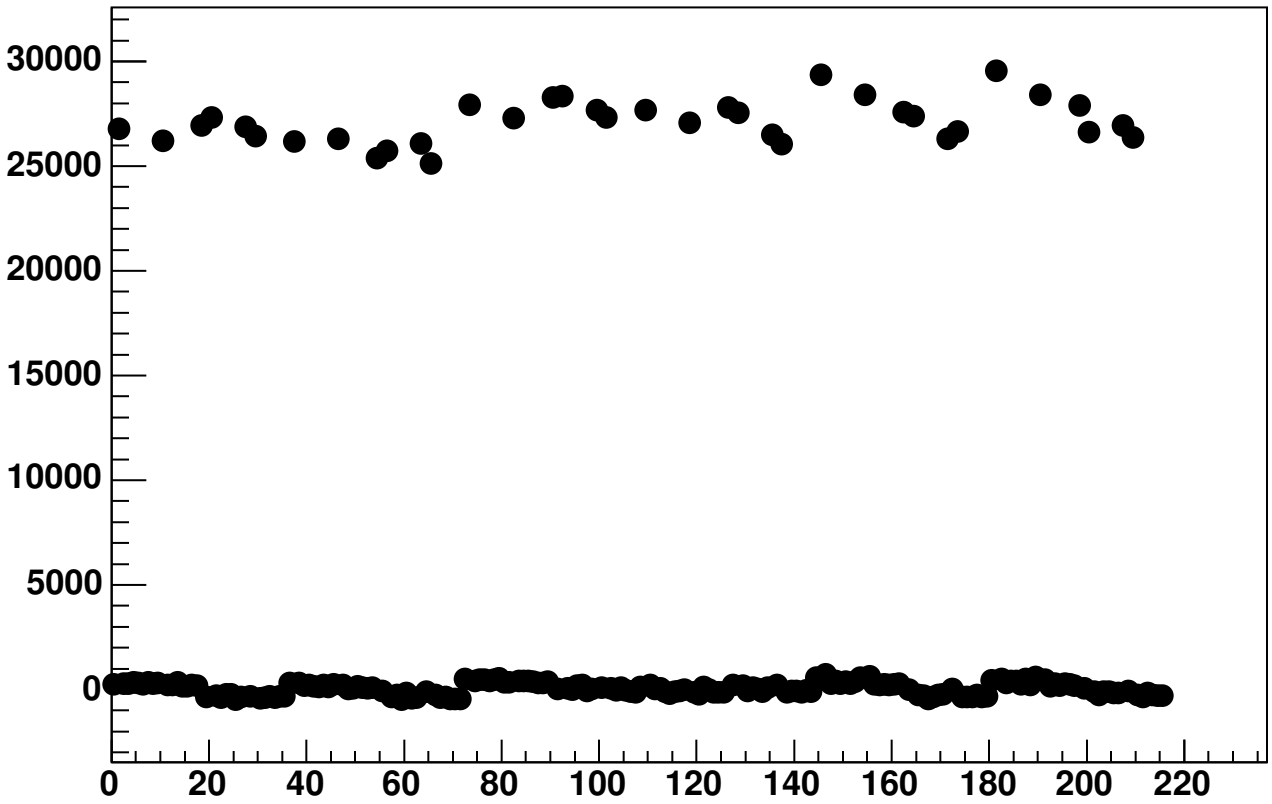
Enable 3, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



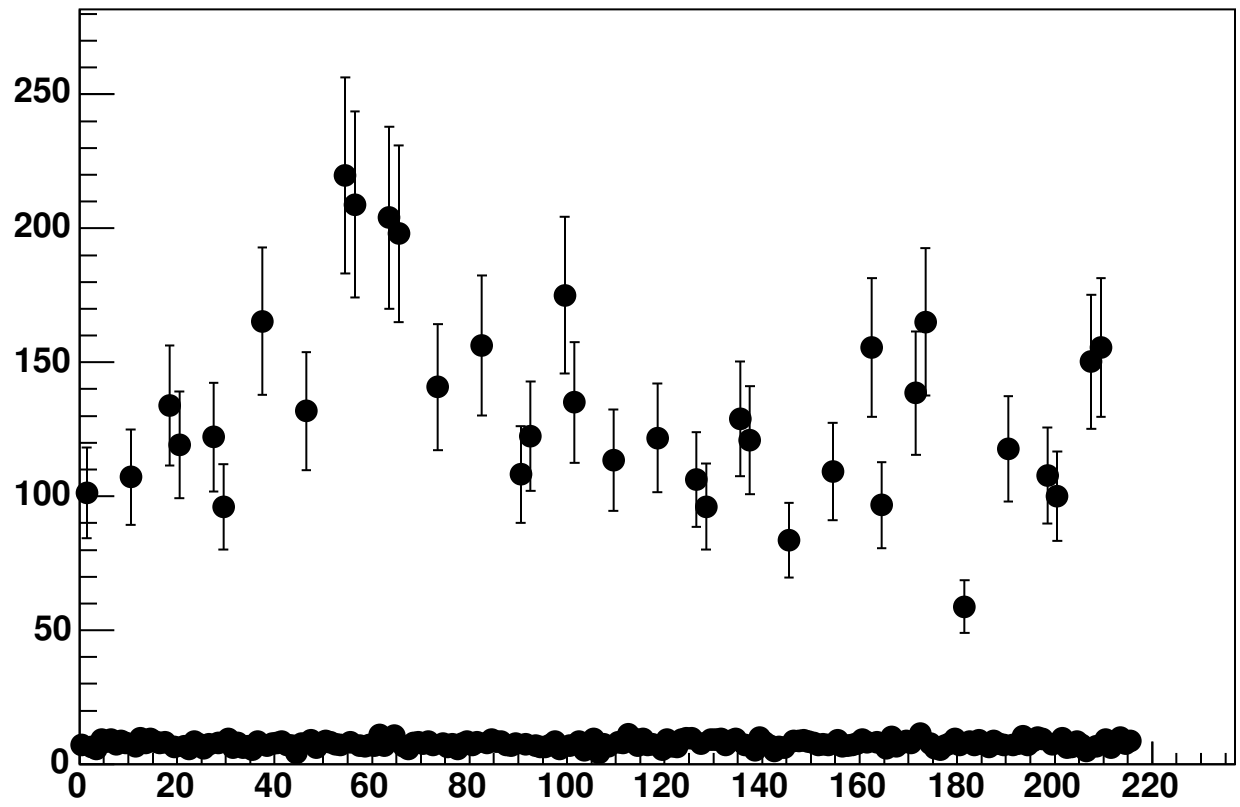
Enable 3, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



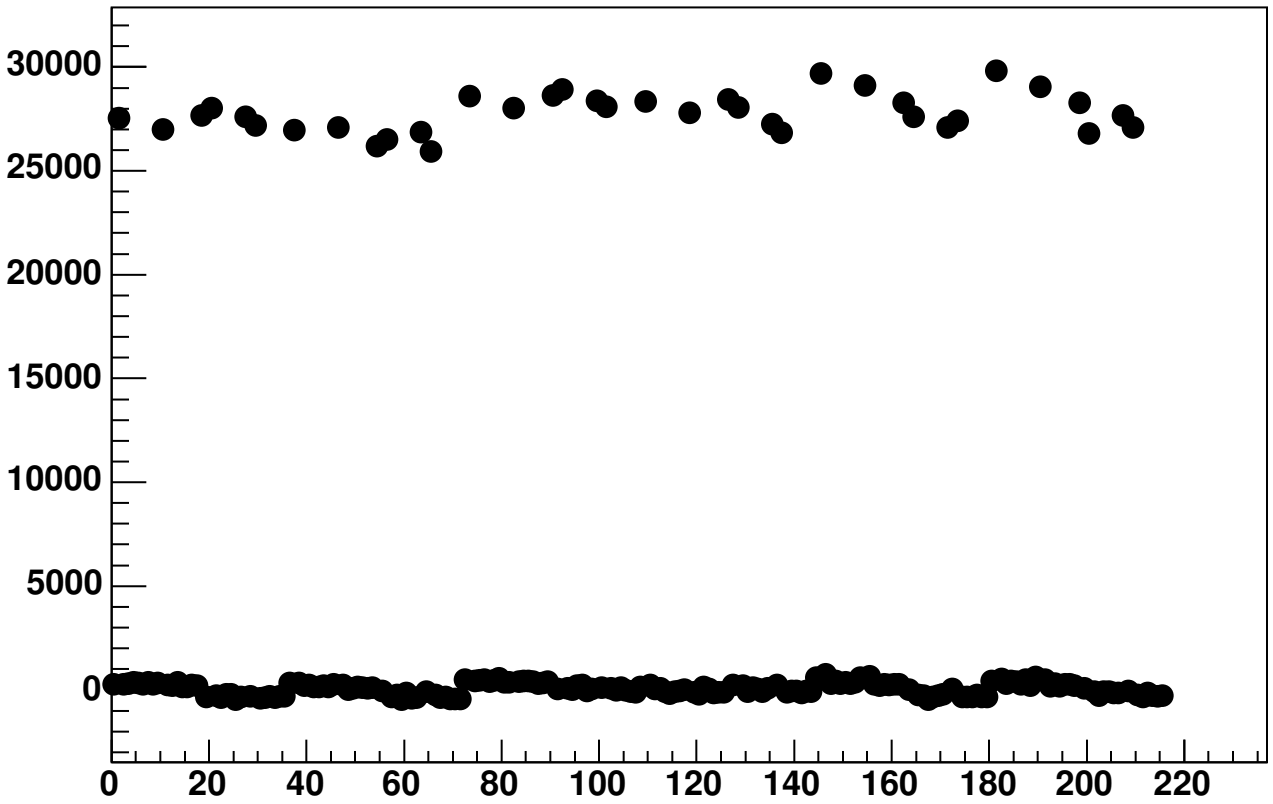
Enable 3, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



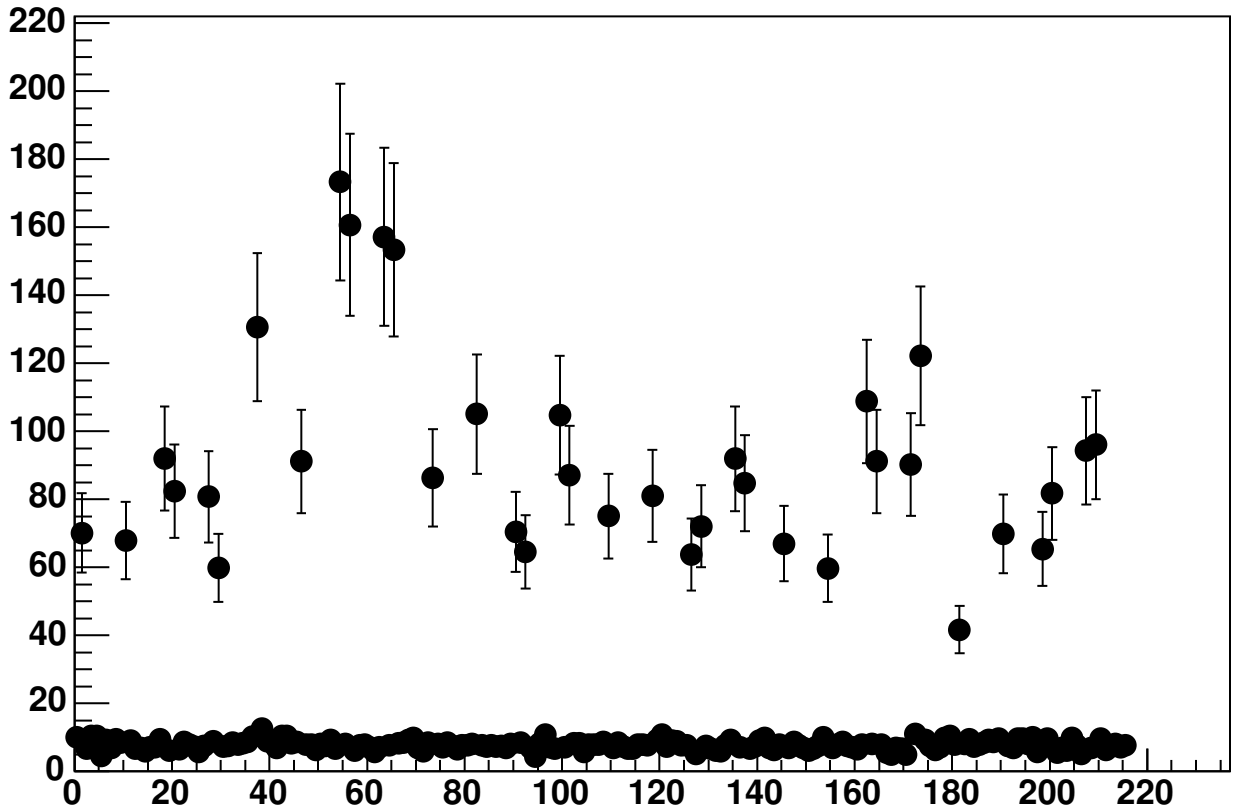
Enable 3, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



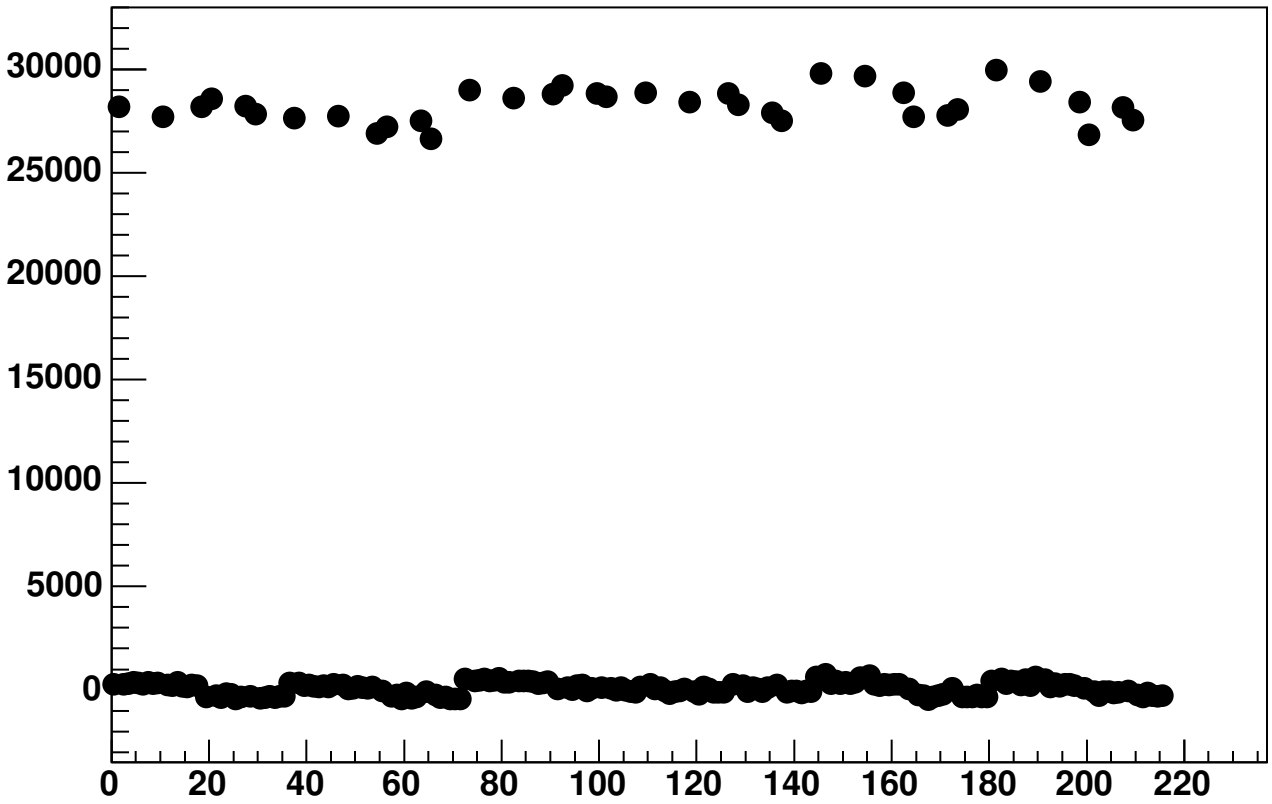
Enable 3, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



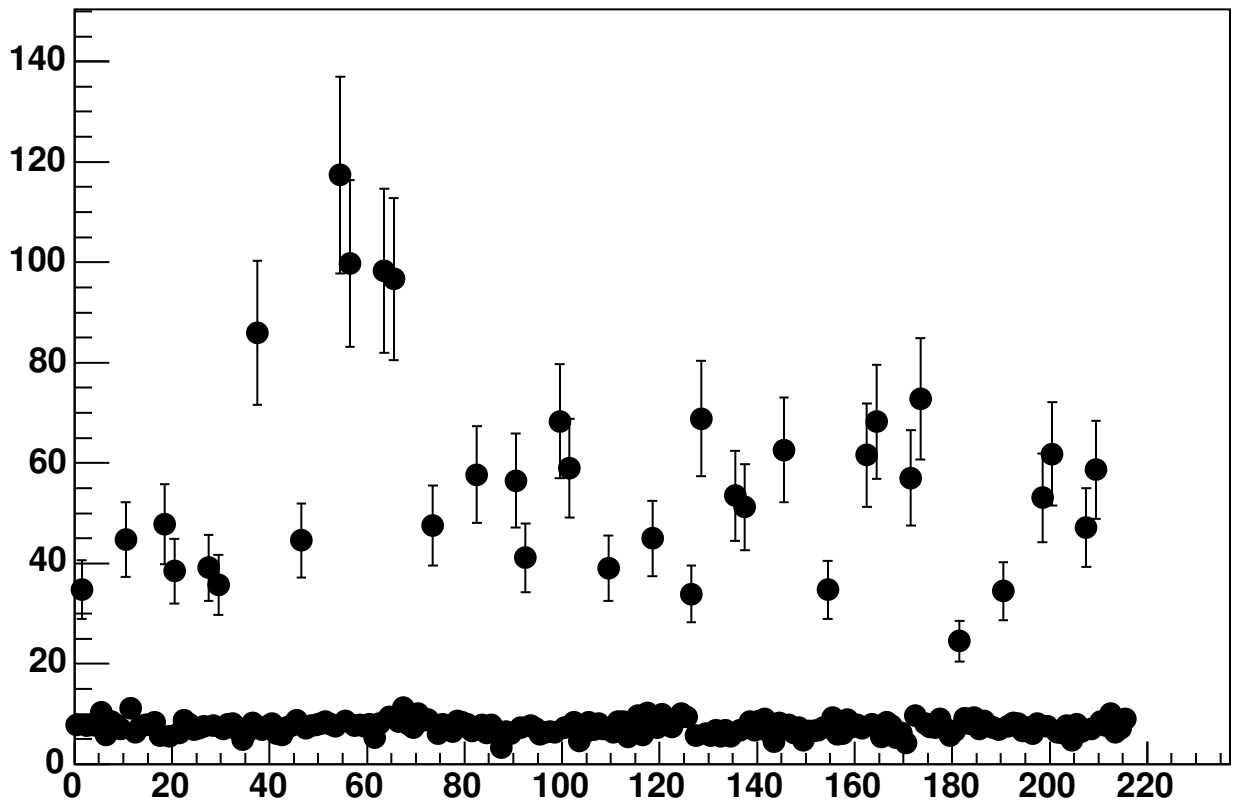
Enable 3, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



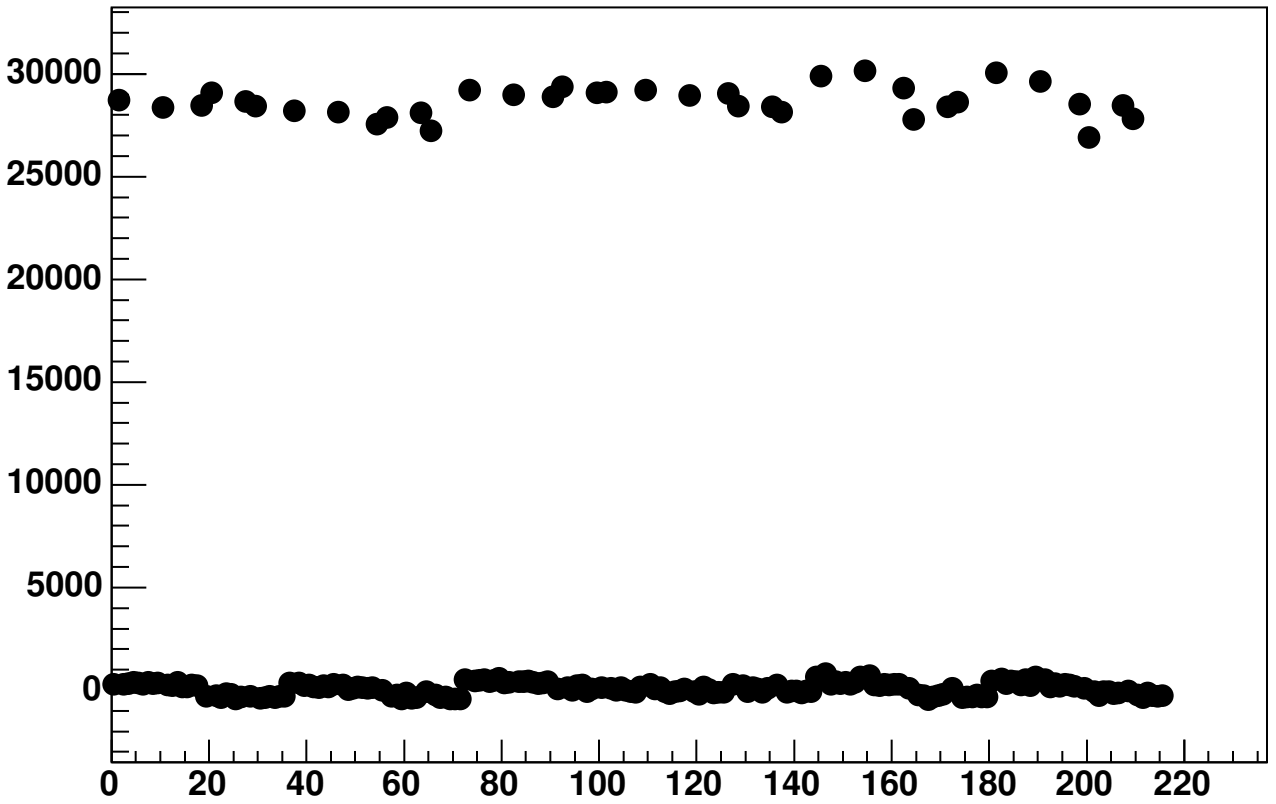
Enable 3, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



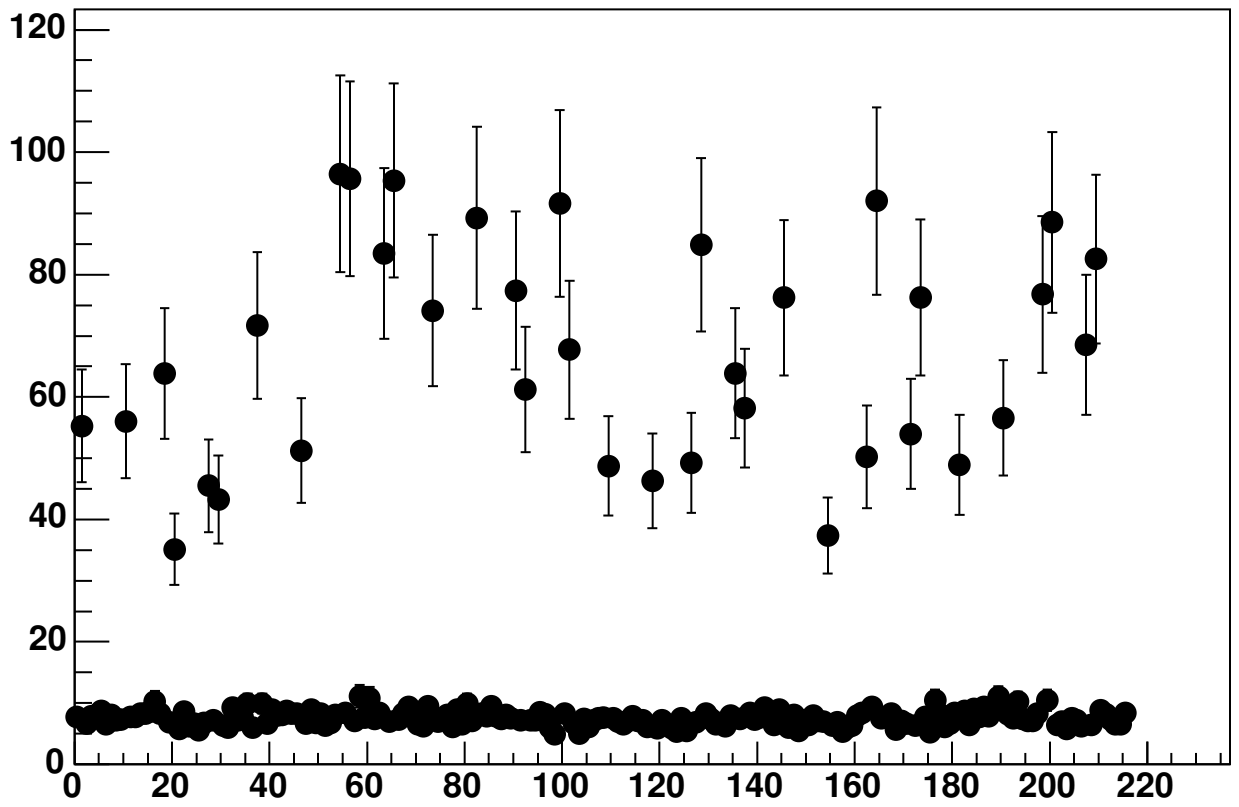
Enable 3, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



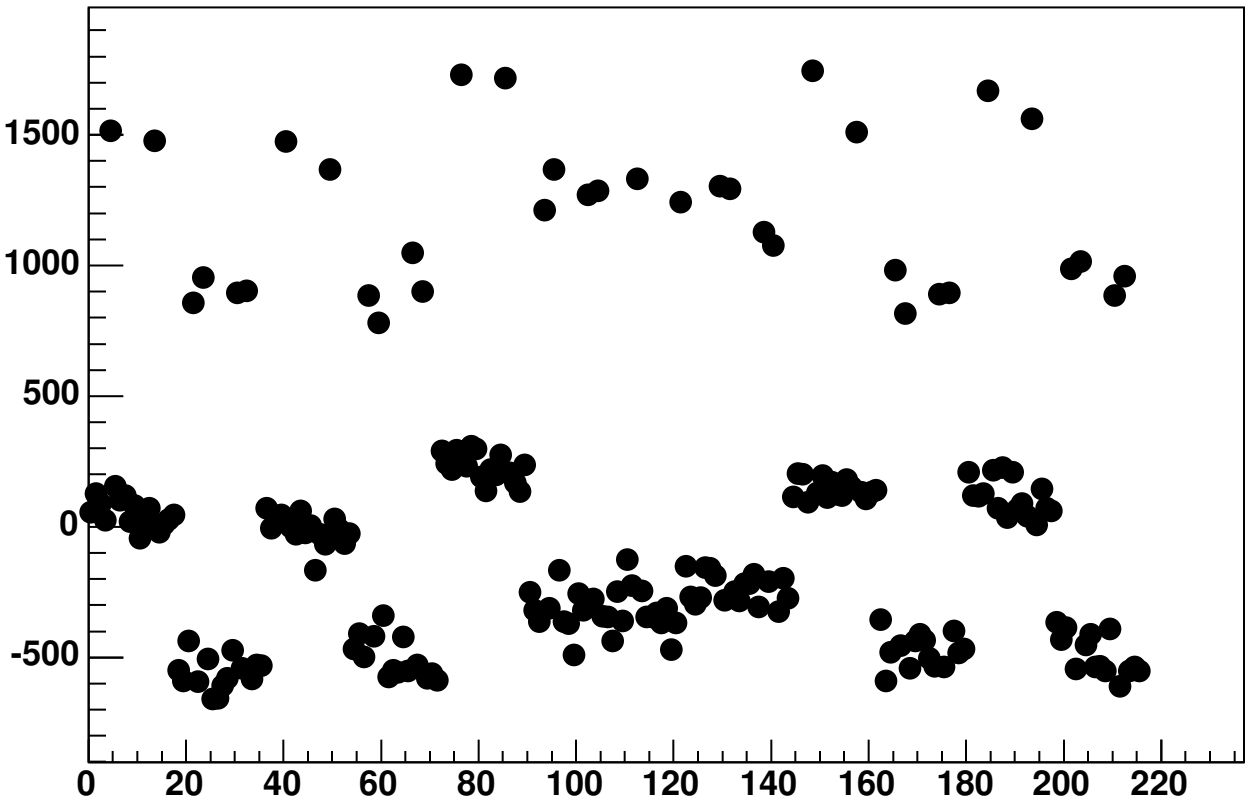
Enable 3, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



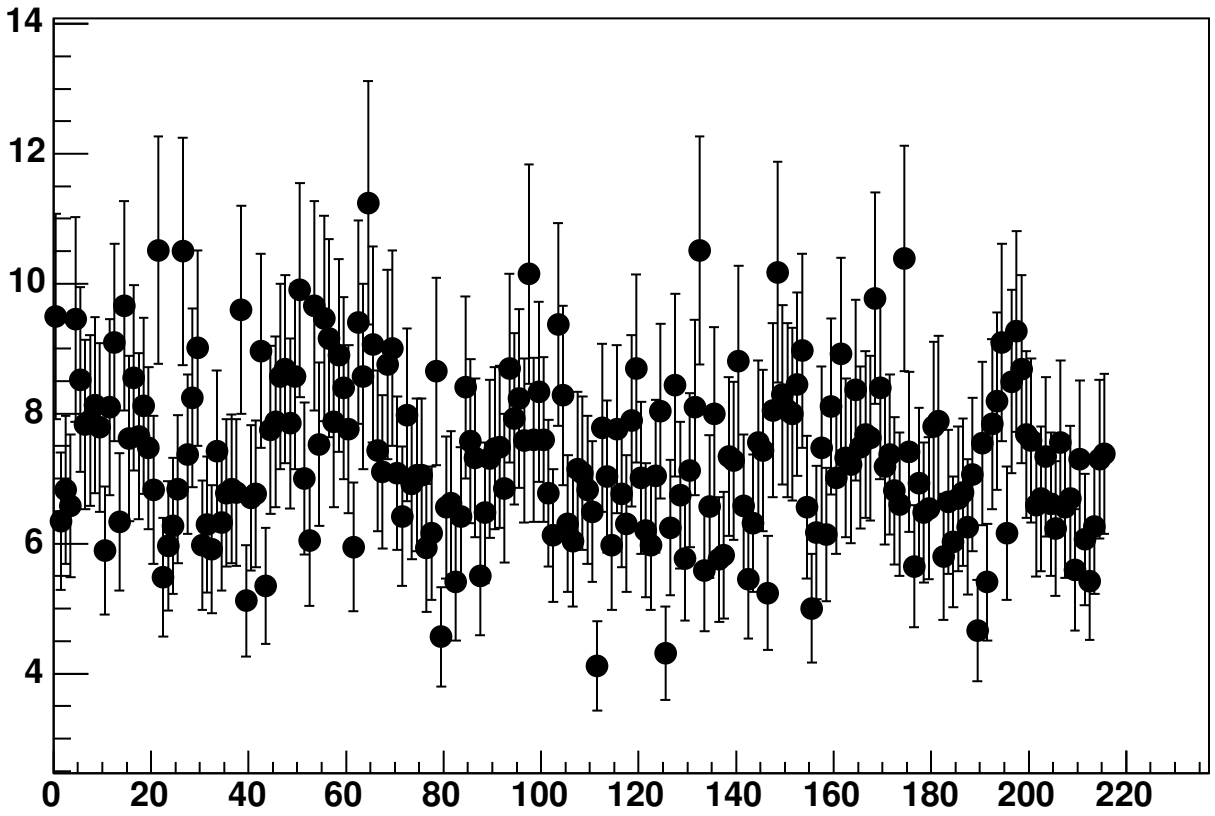
Enable 3, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



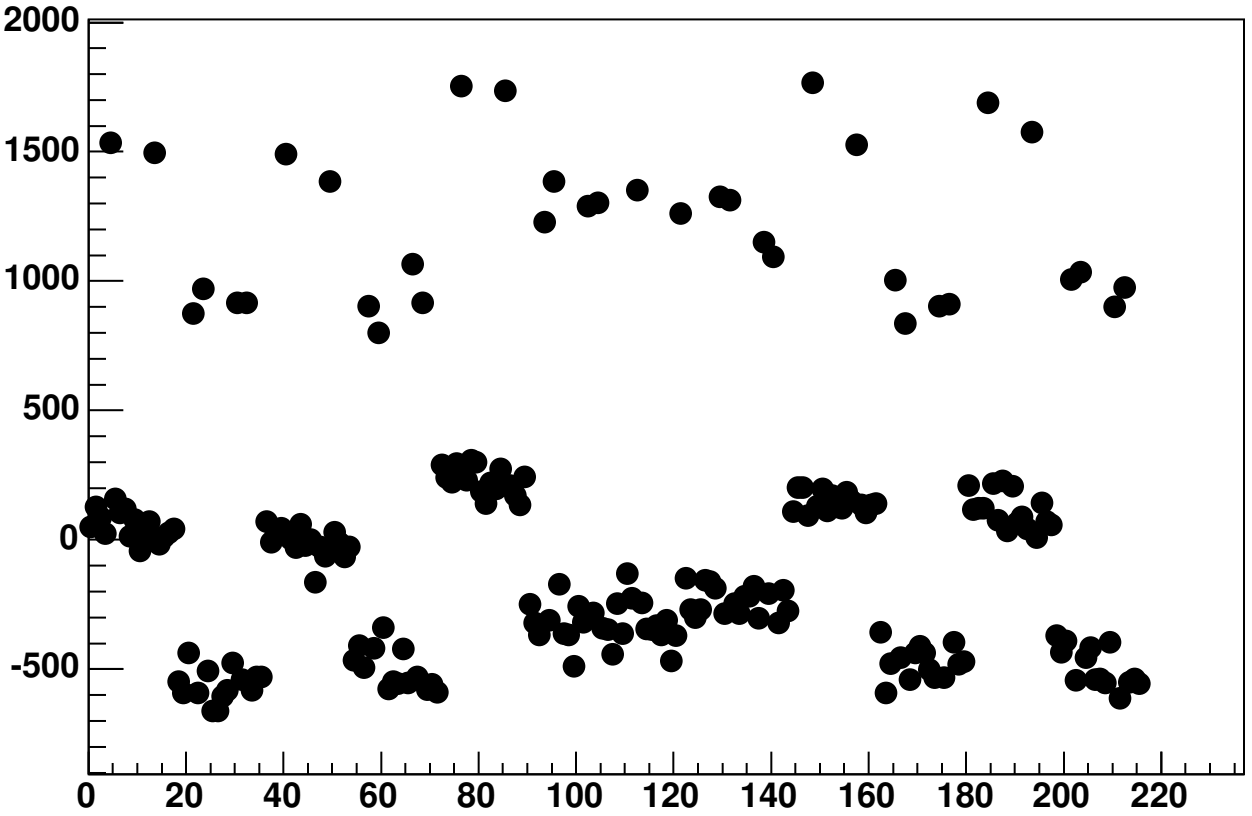
Enable 4, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



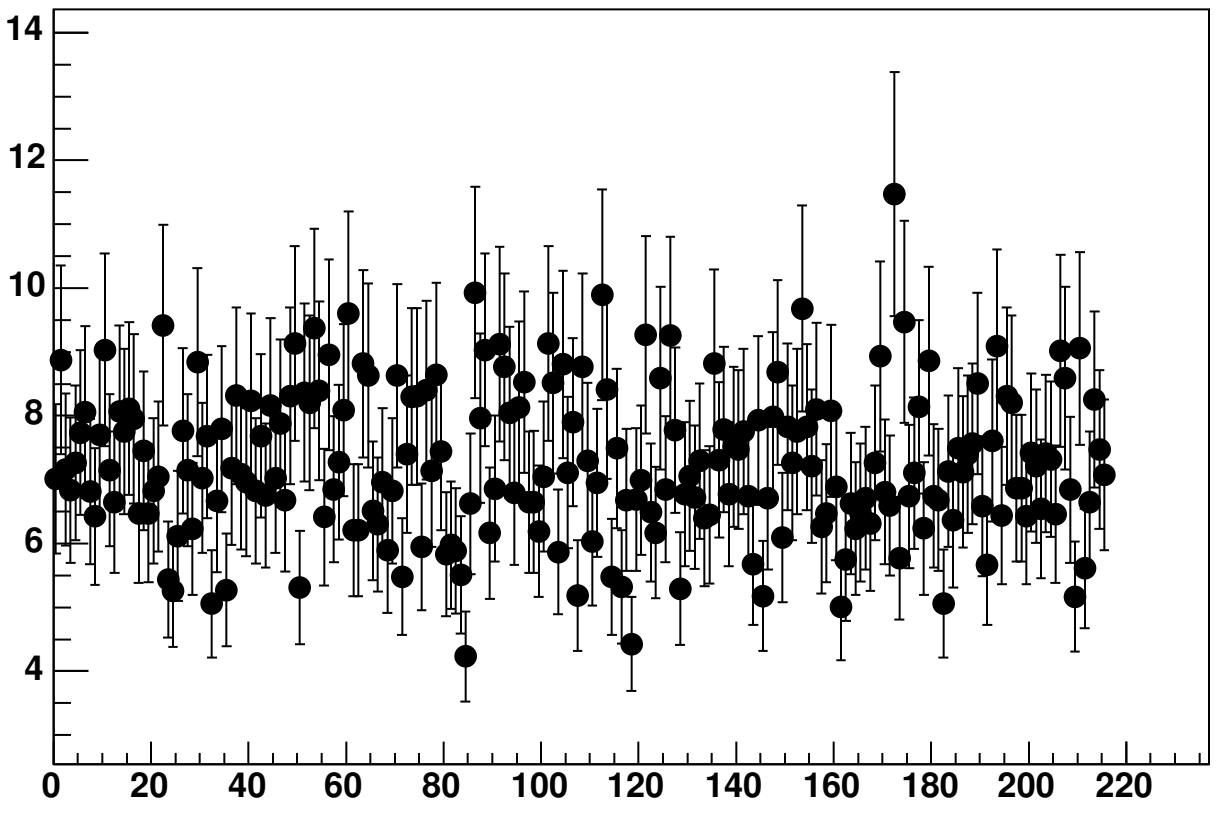
Enable 4, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



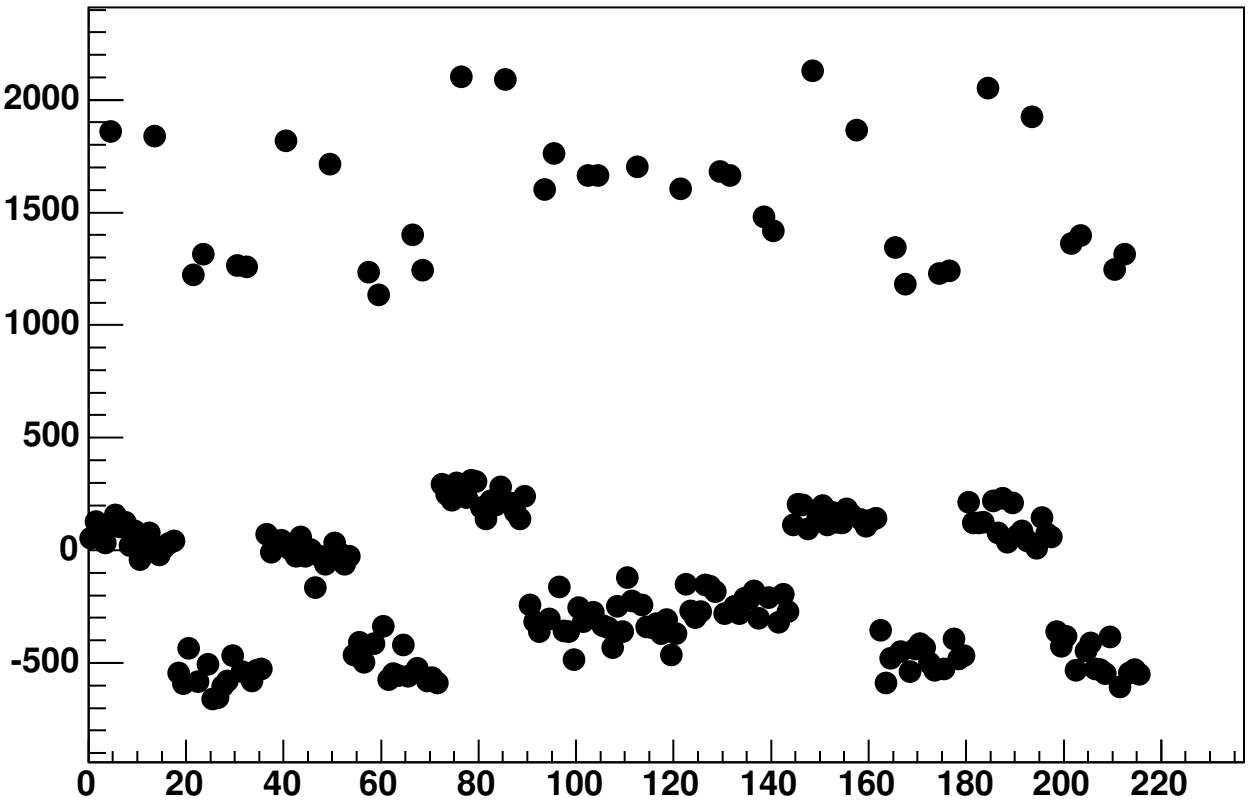
Enable 4, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



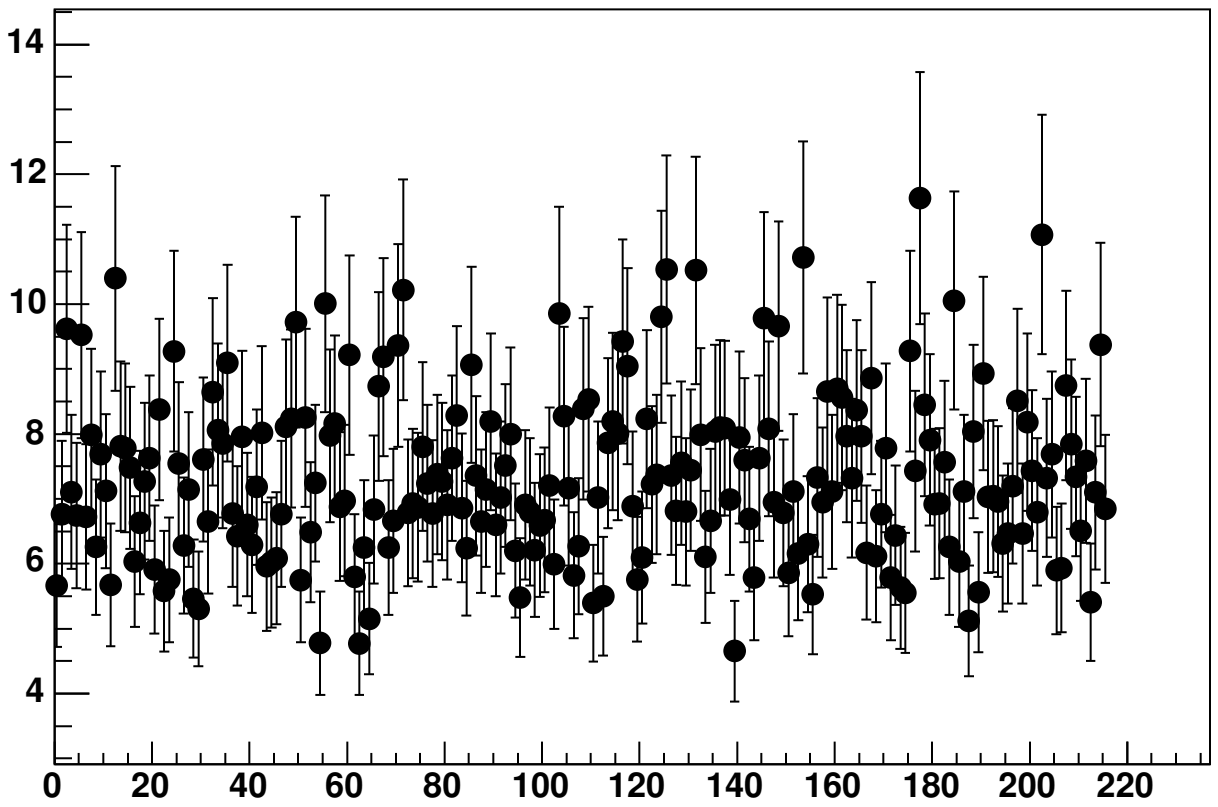
Enable 4, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



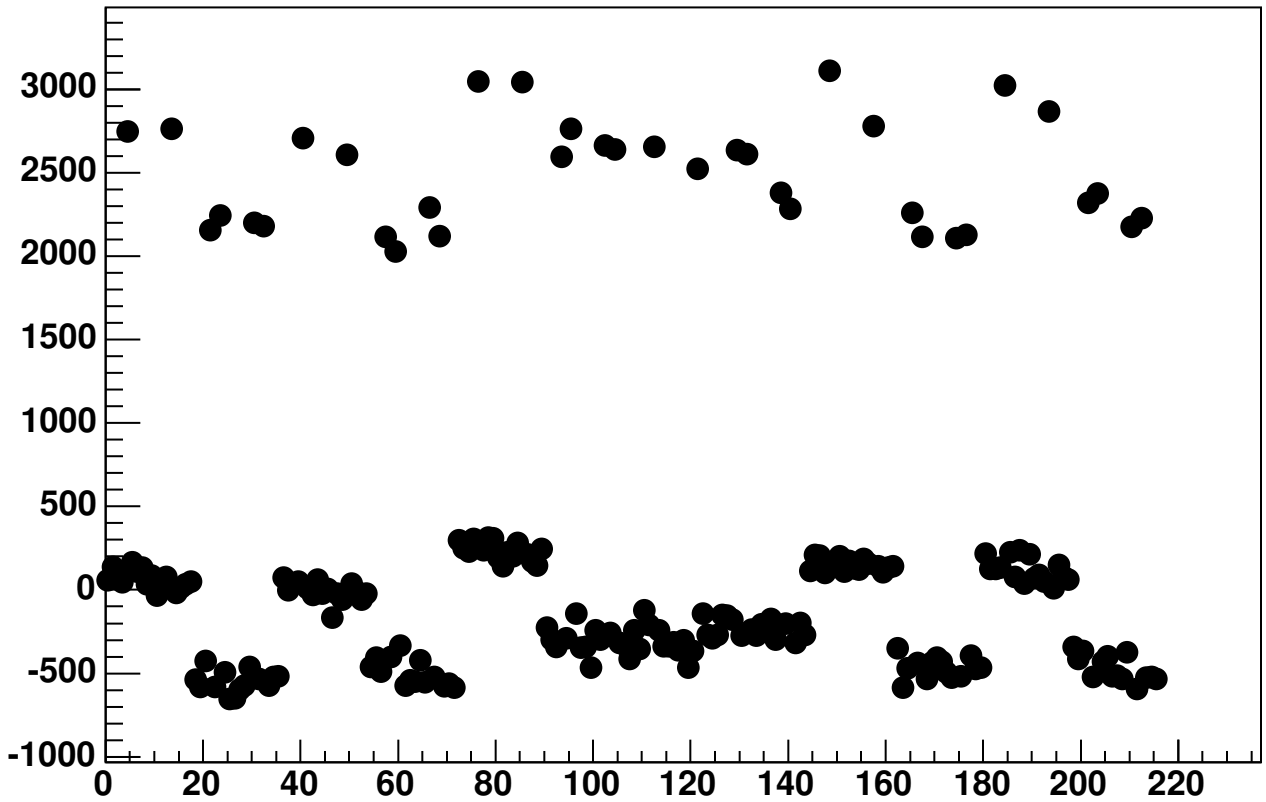
Enable 4, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



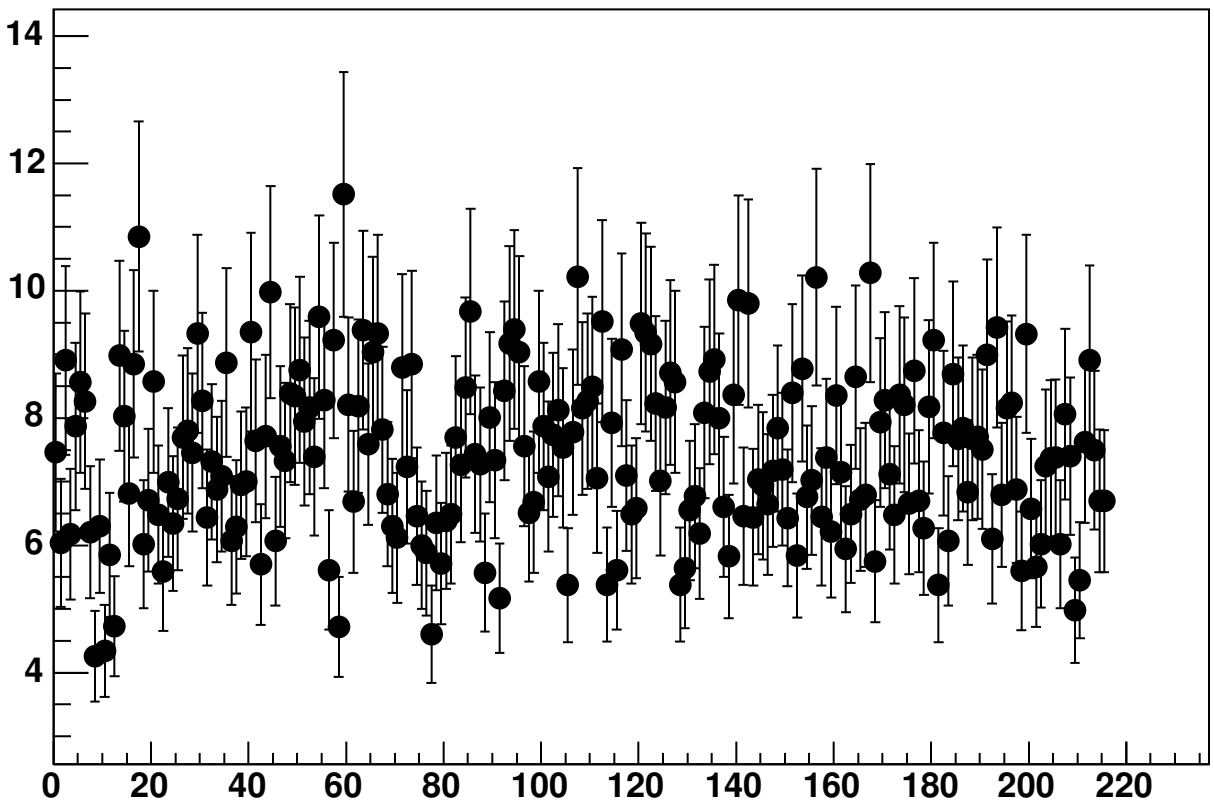
Enable 4, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



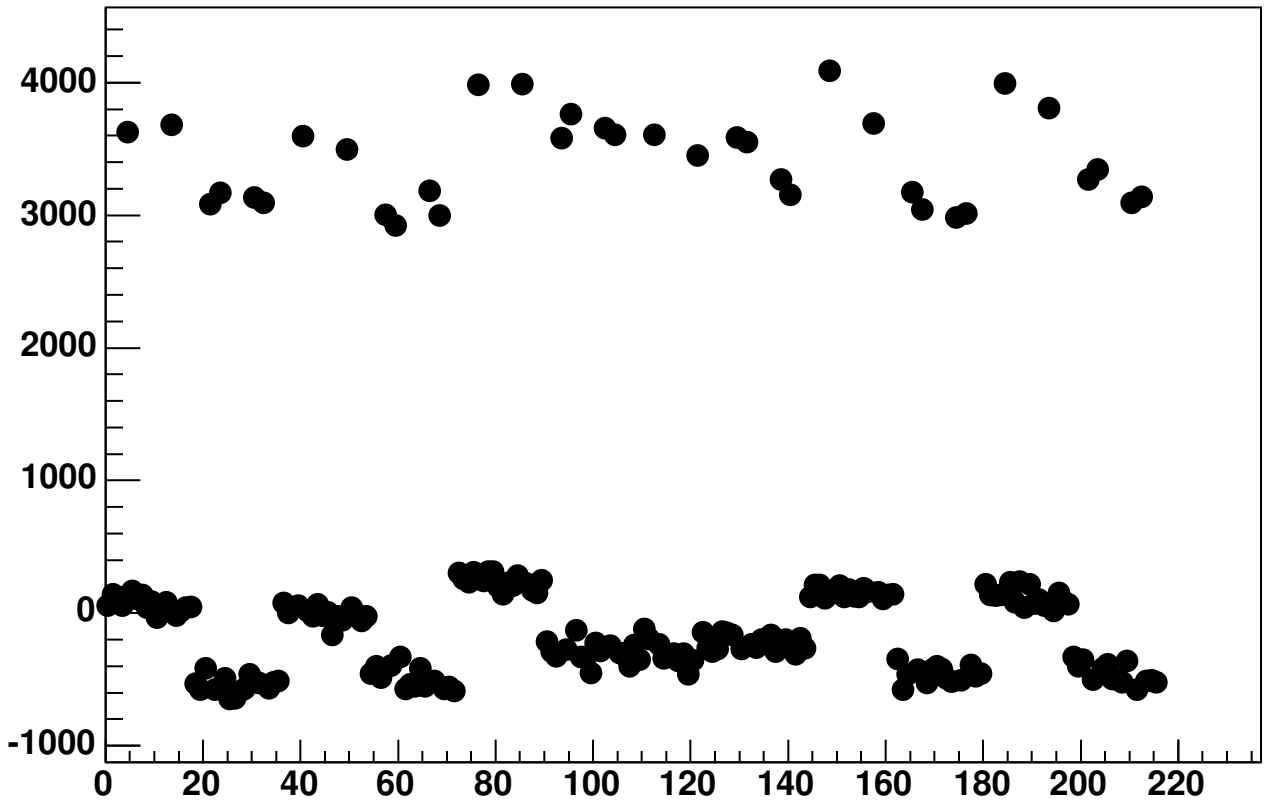
Enable 4, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



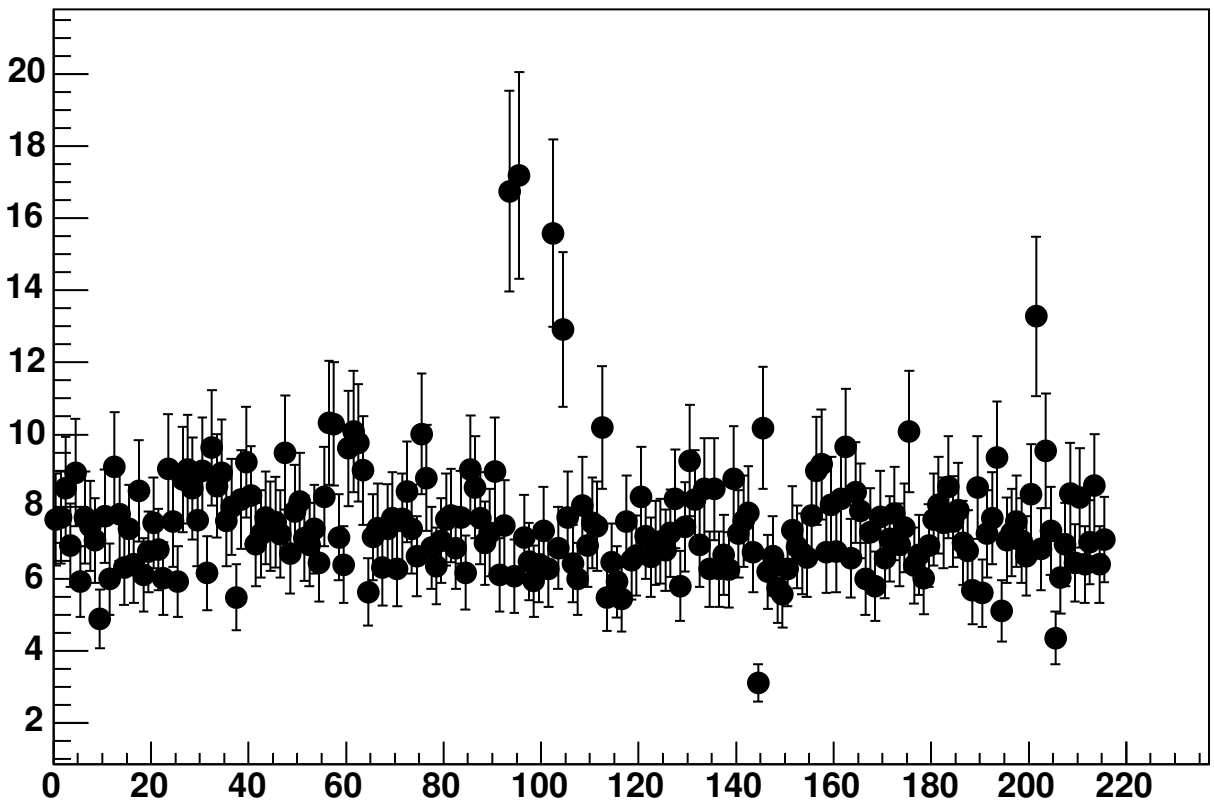
Enable 4, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



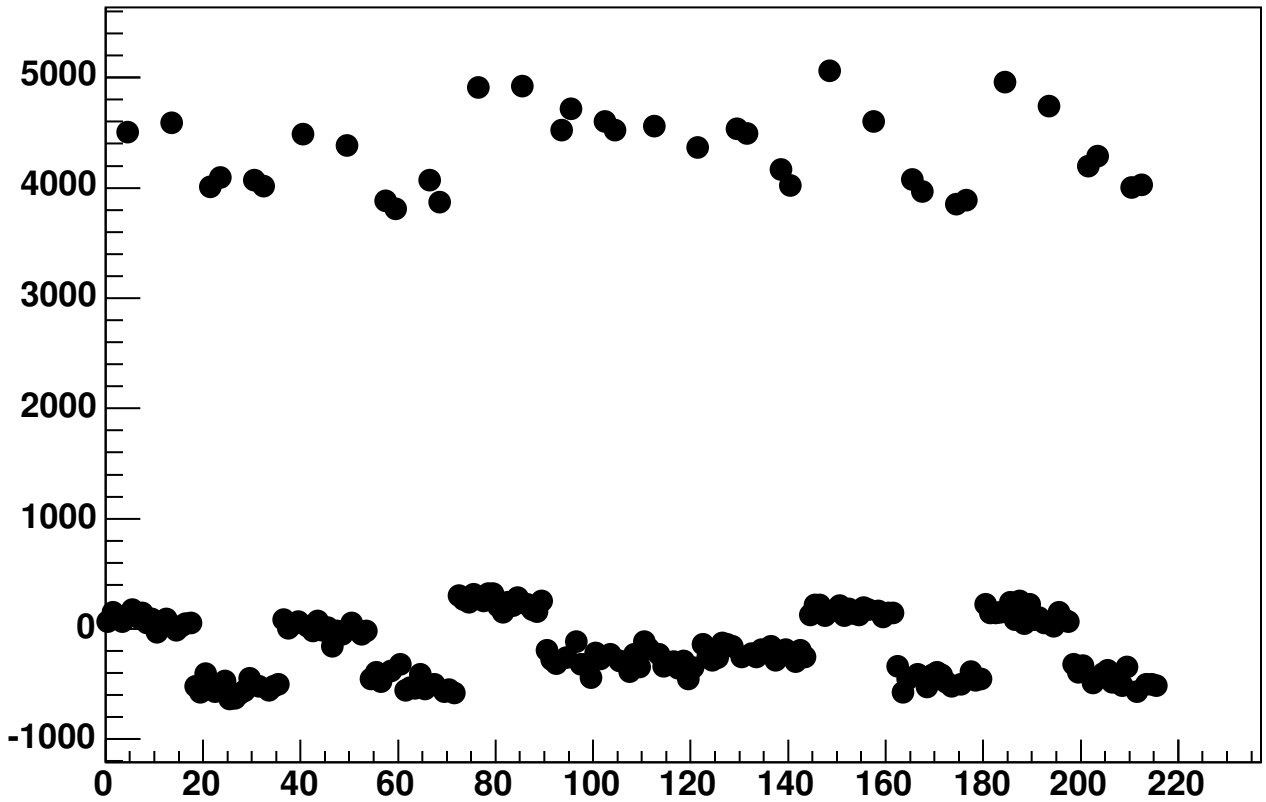
Enable 4, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



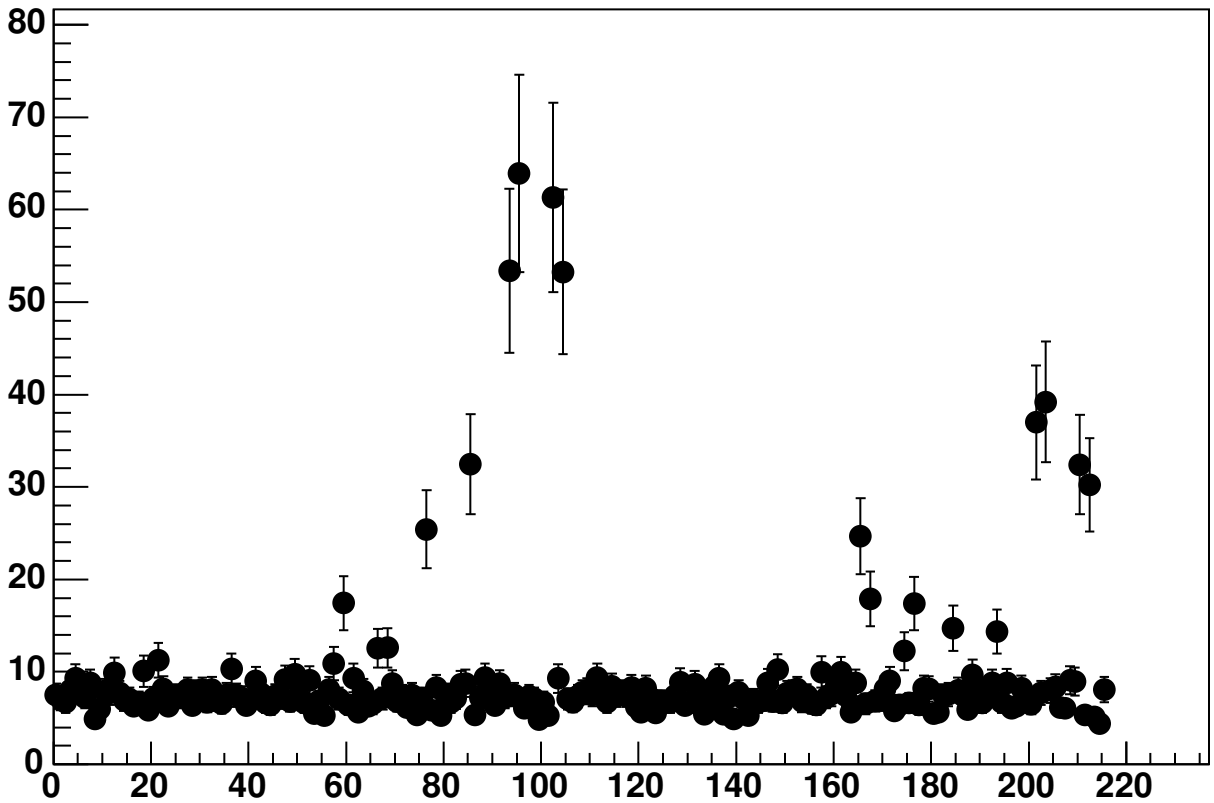
Enable 4, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



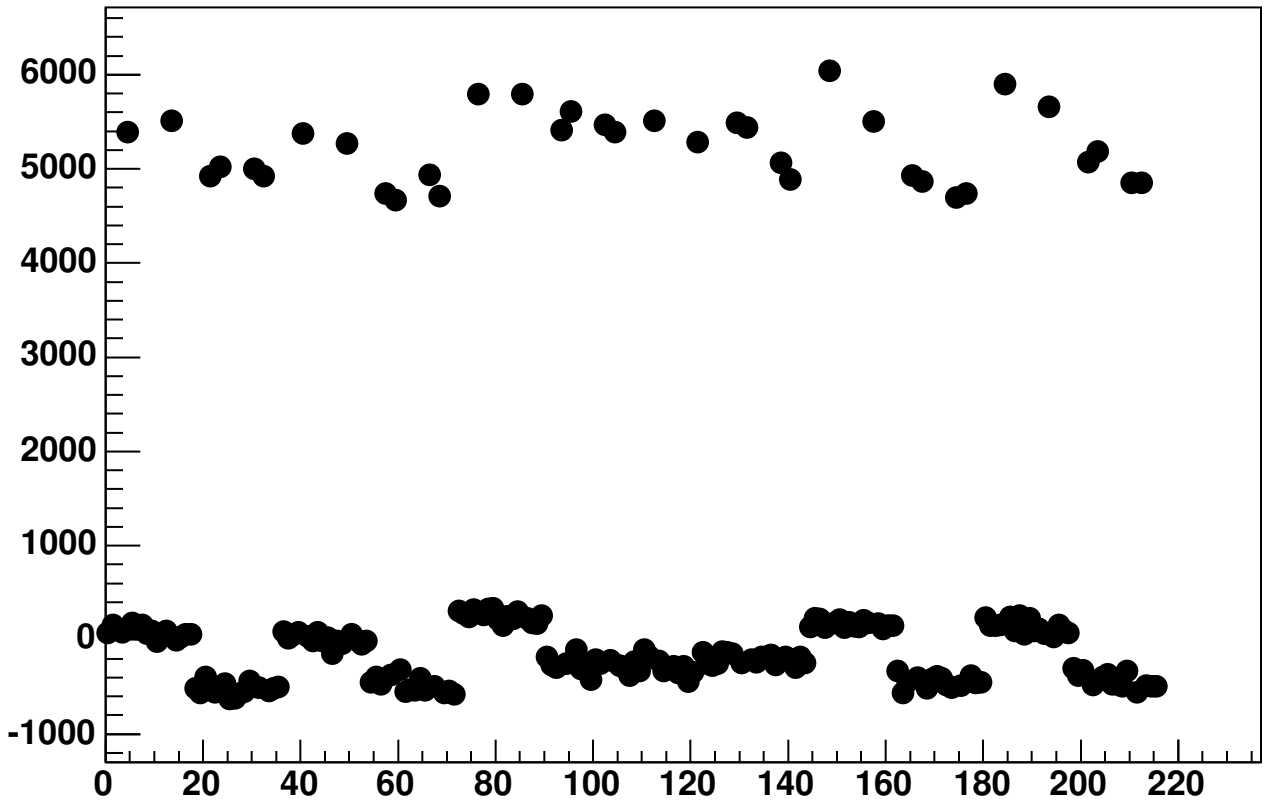
Enable 4, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



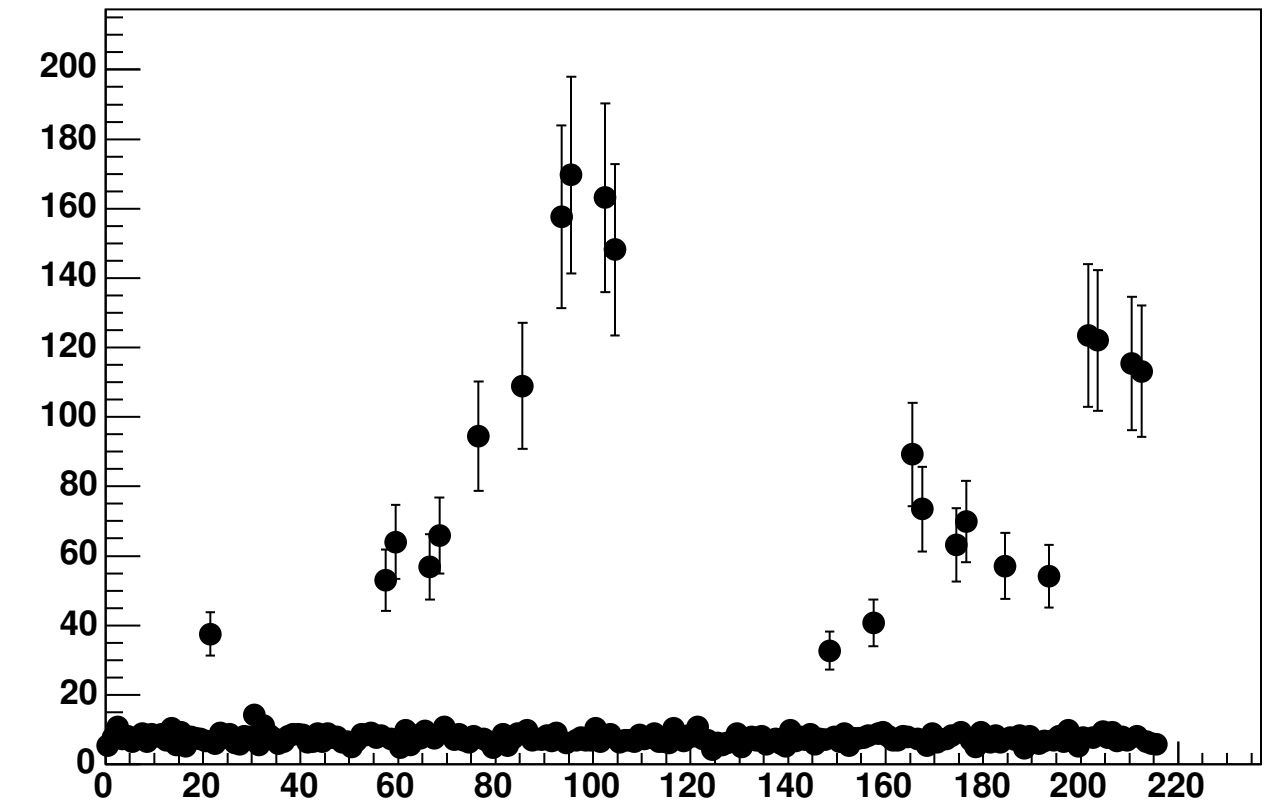
Enable 4, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



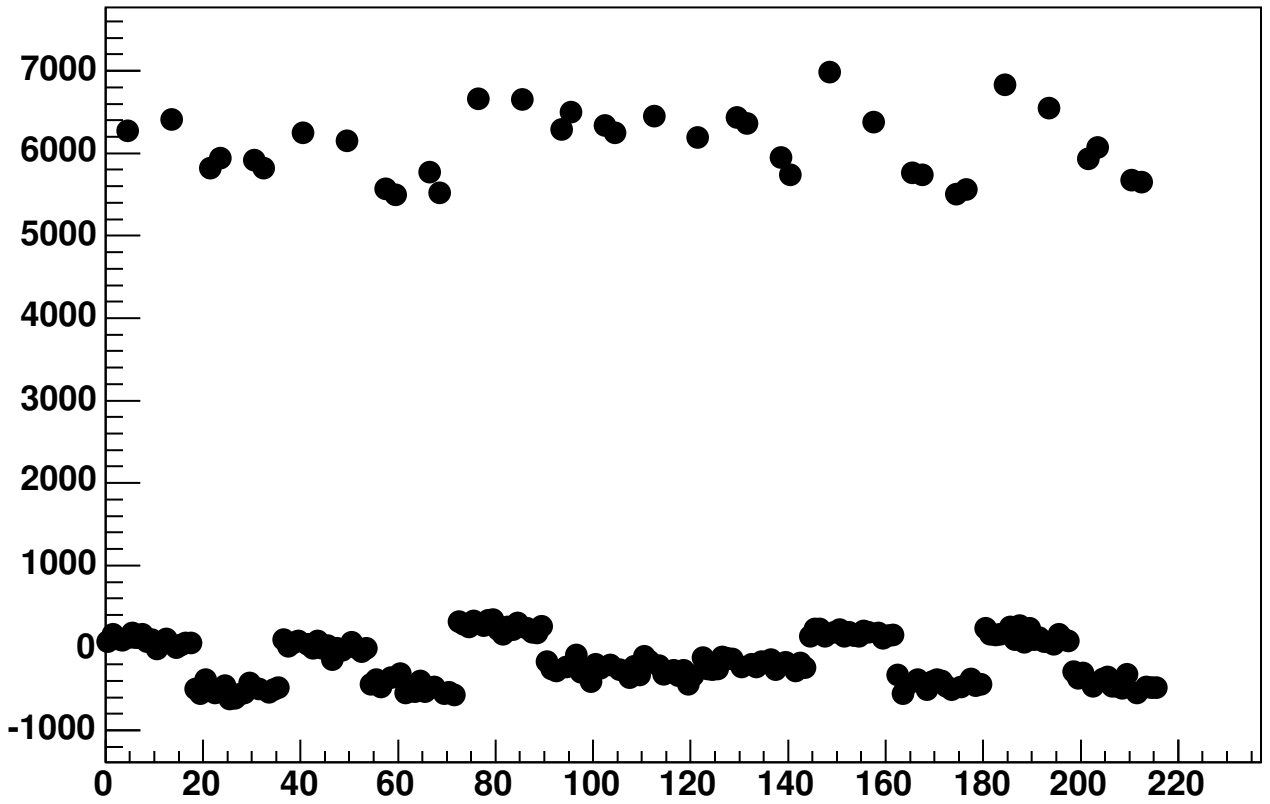
Enable 4, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



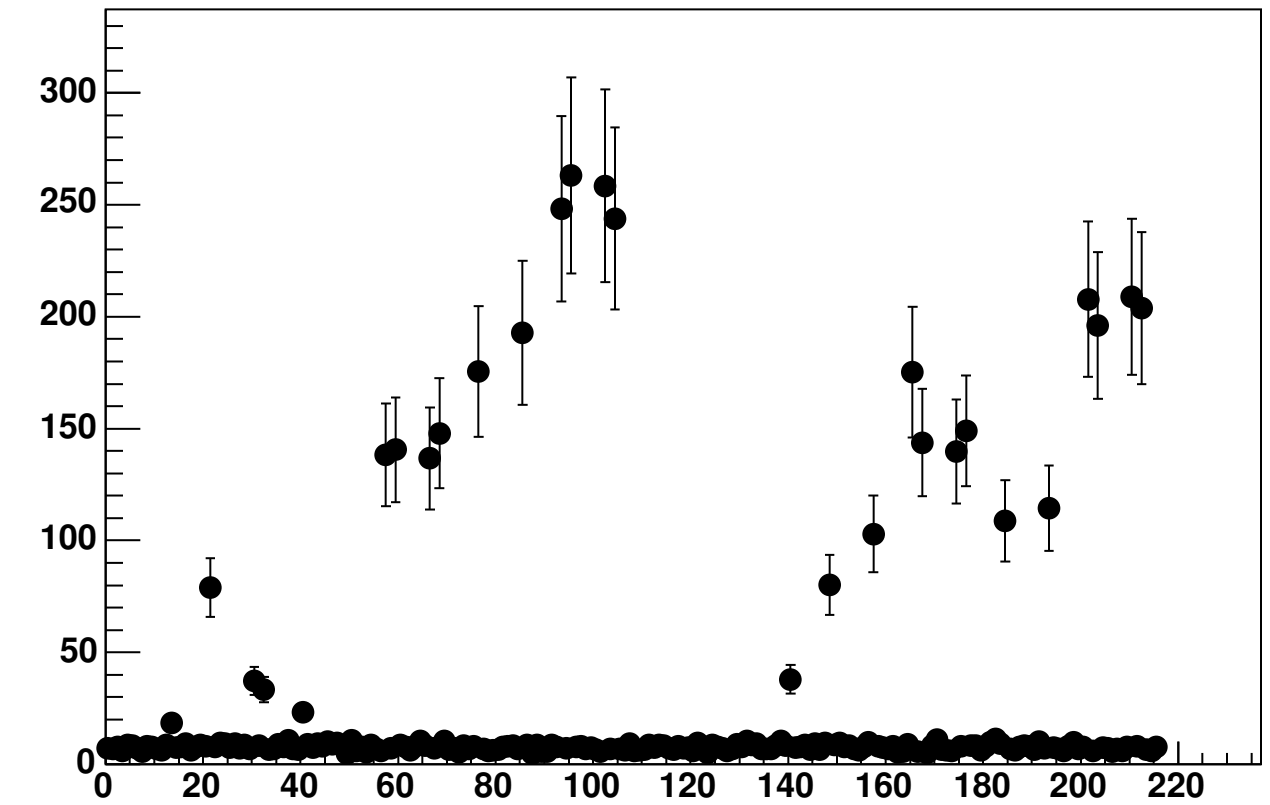
Enable 4, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



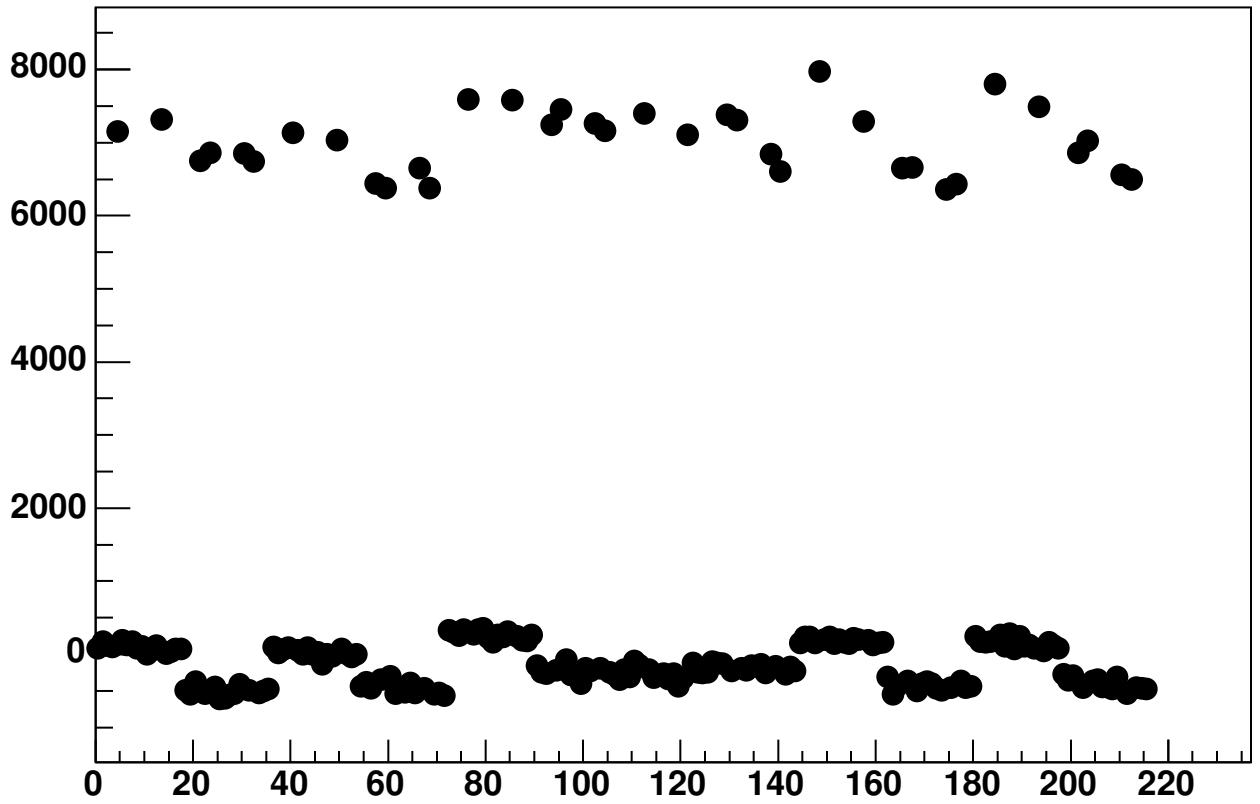
Enable 4, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



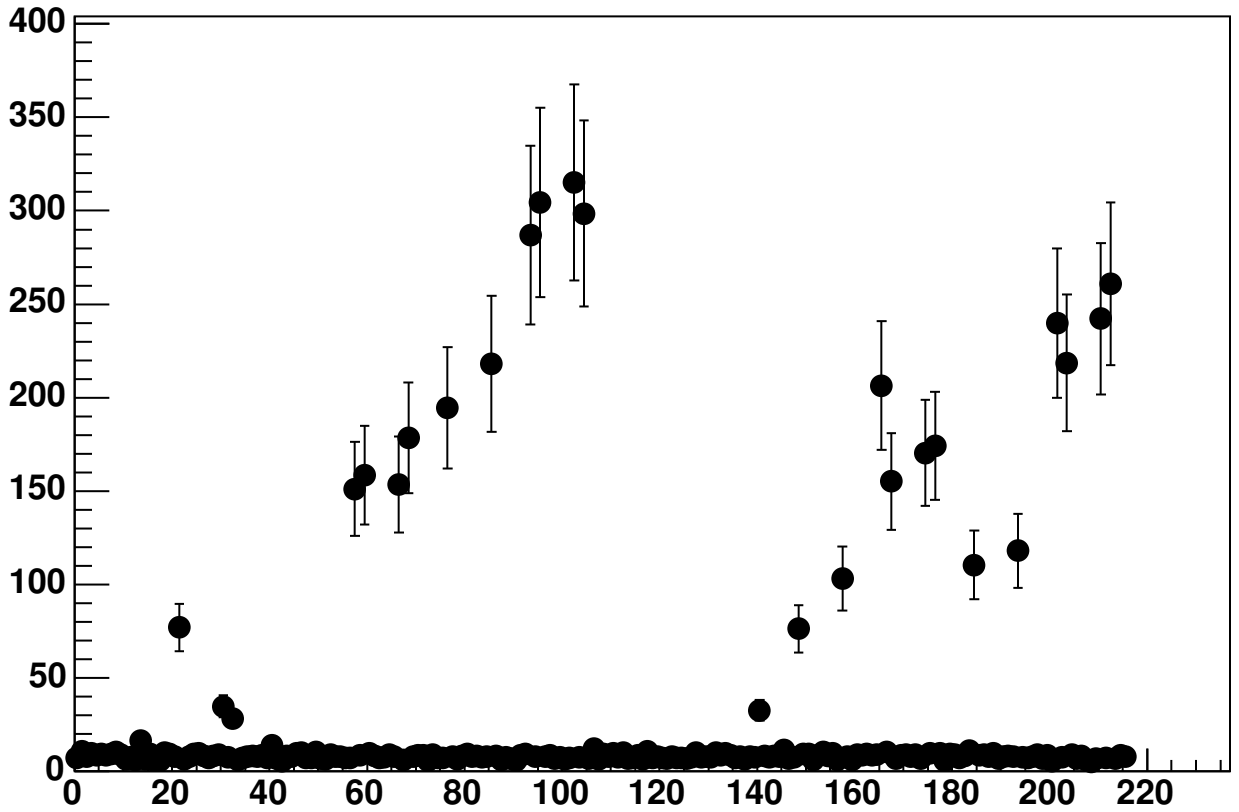
Enable 4, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



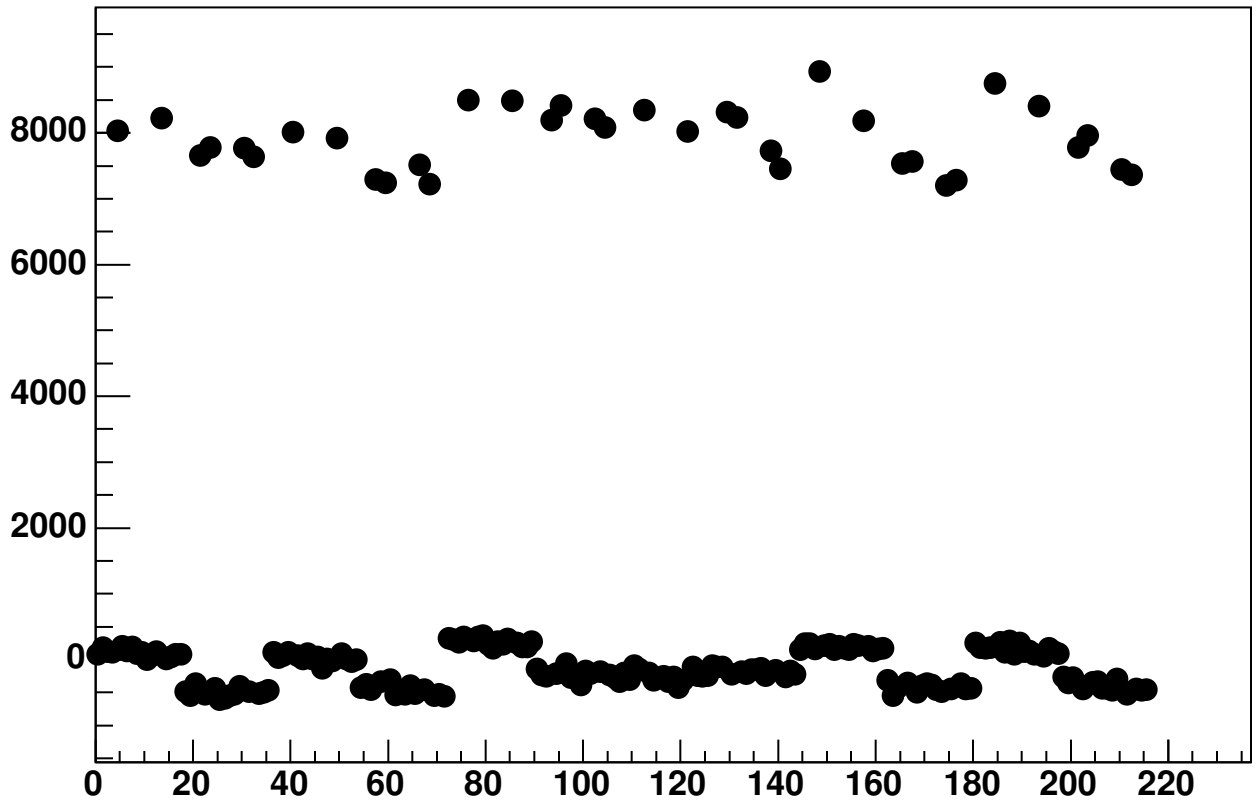
Enable 4, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



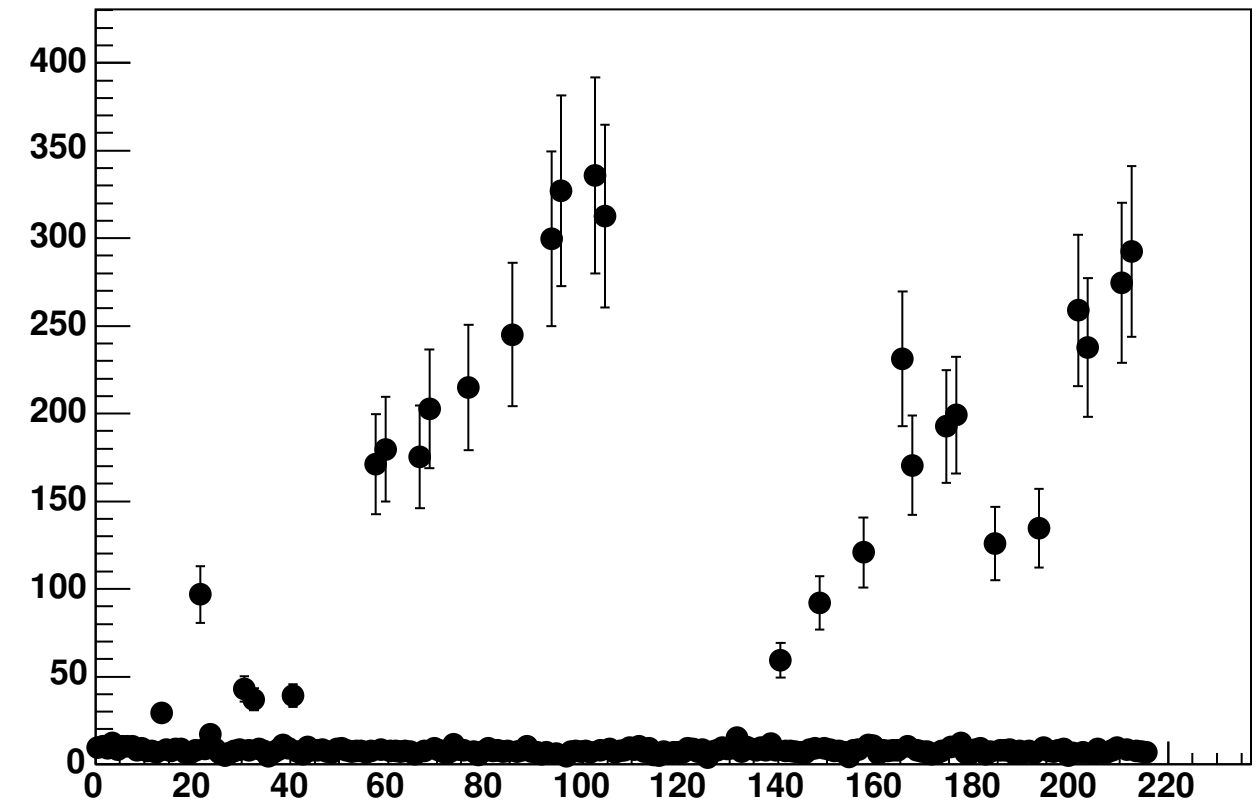
Enable 4, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



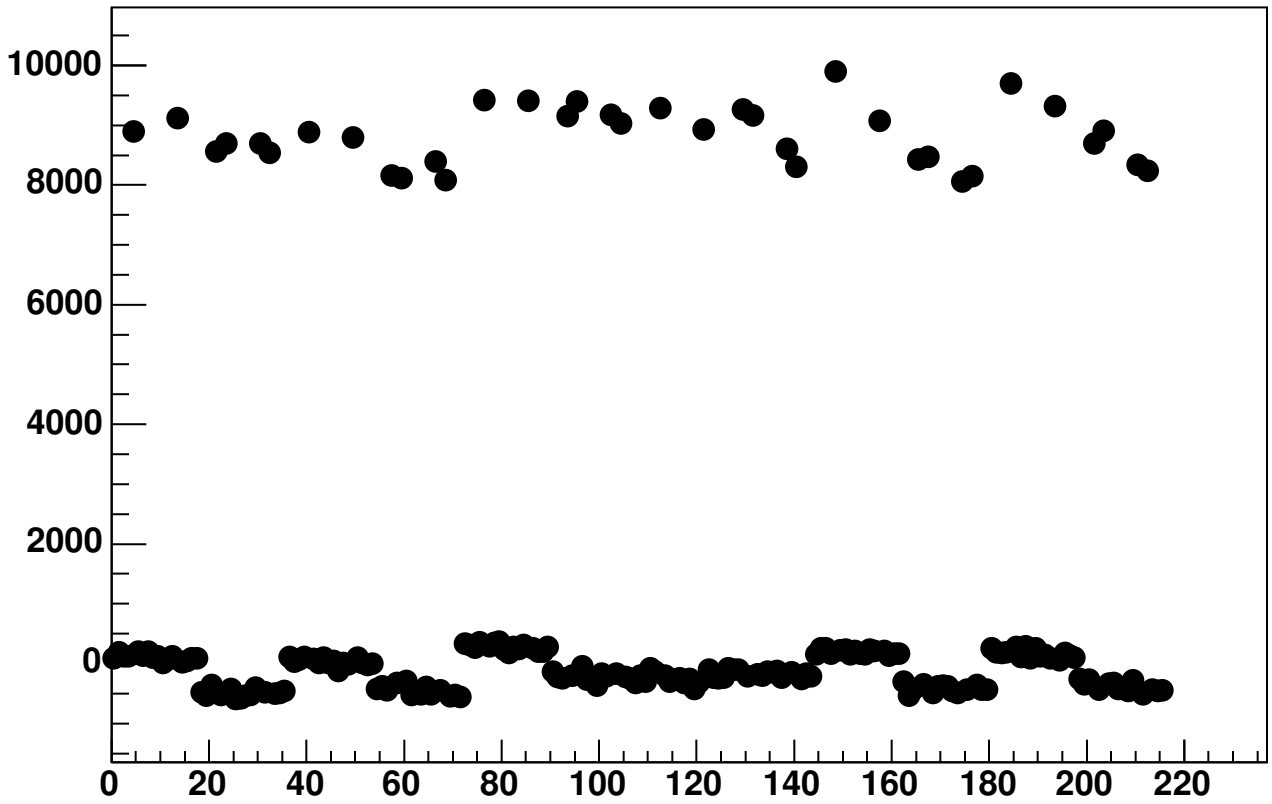
Enable 4, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



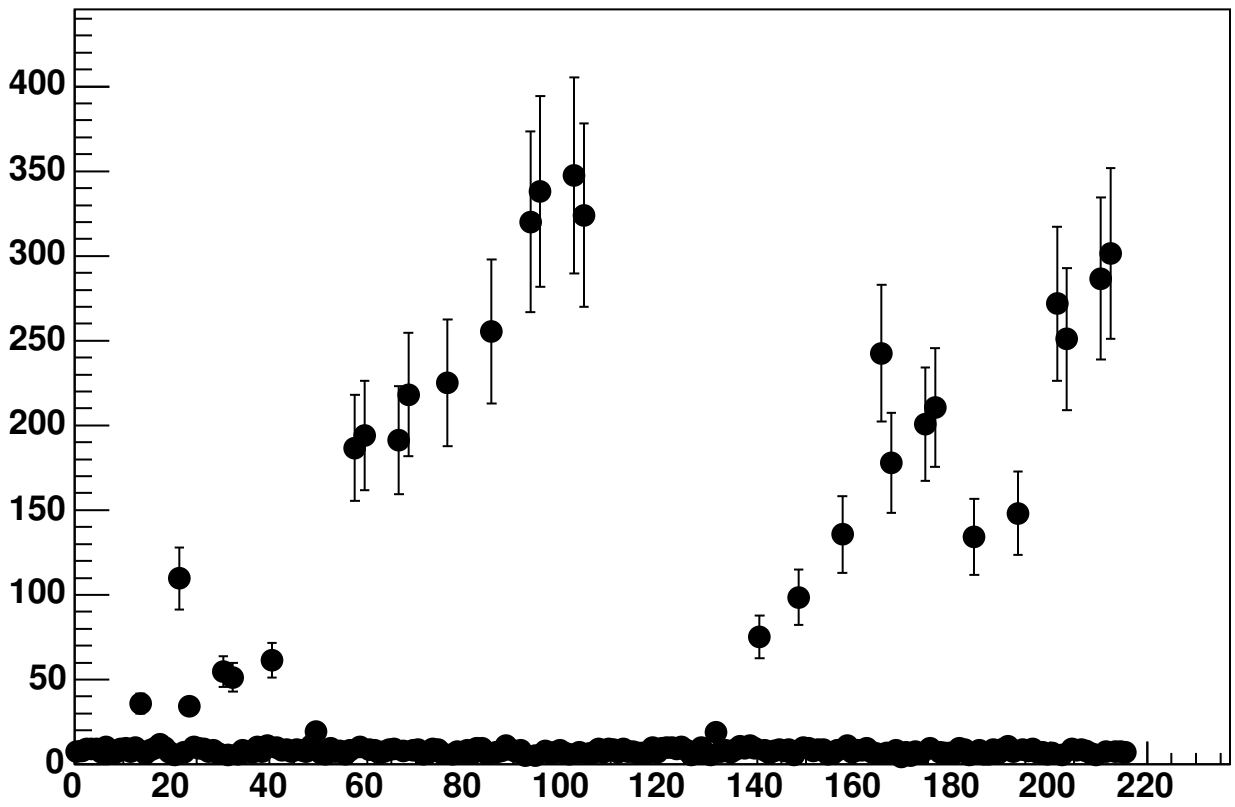
Enable 4, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



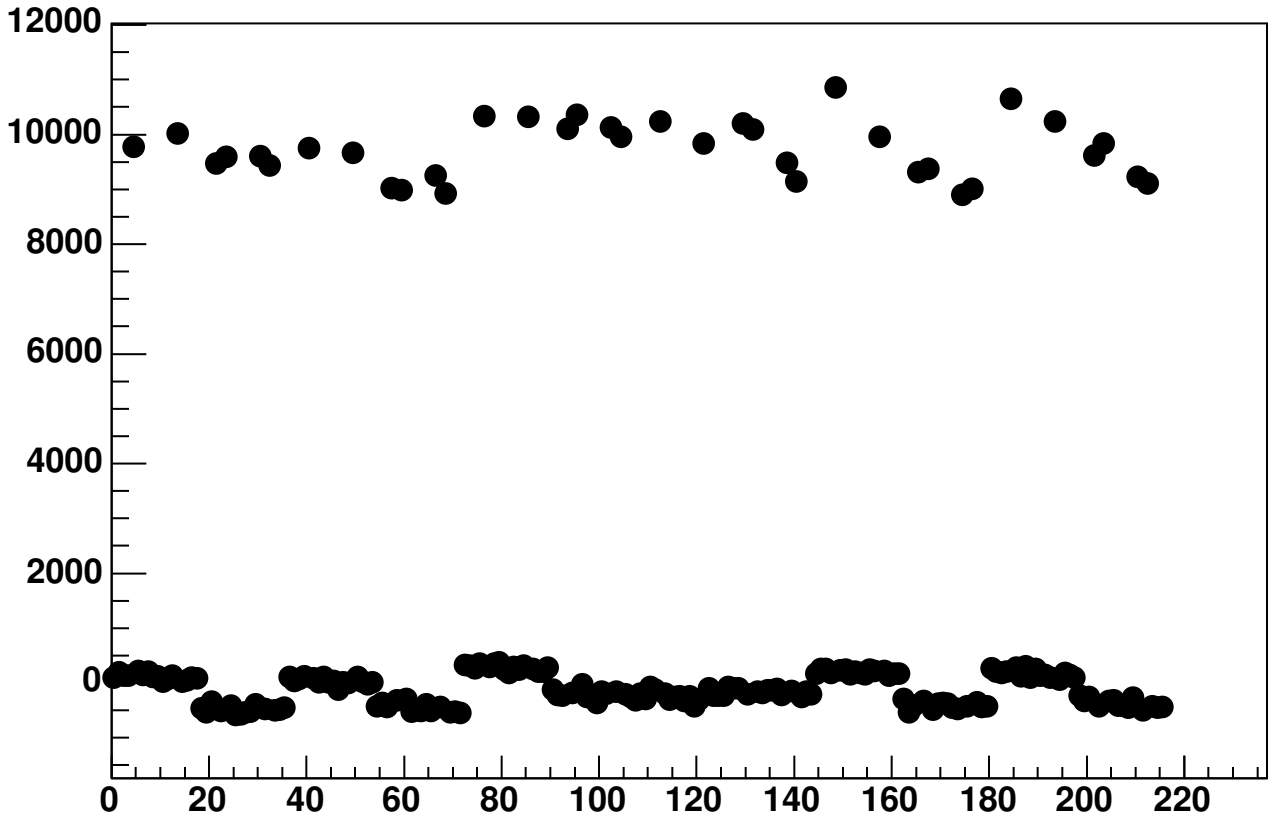
Enable 4, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



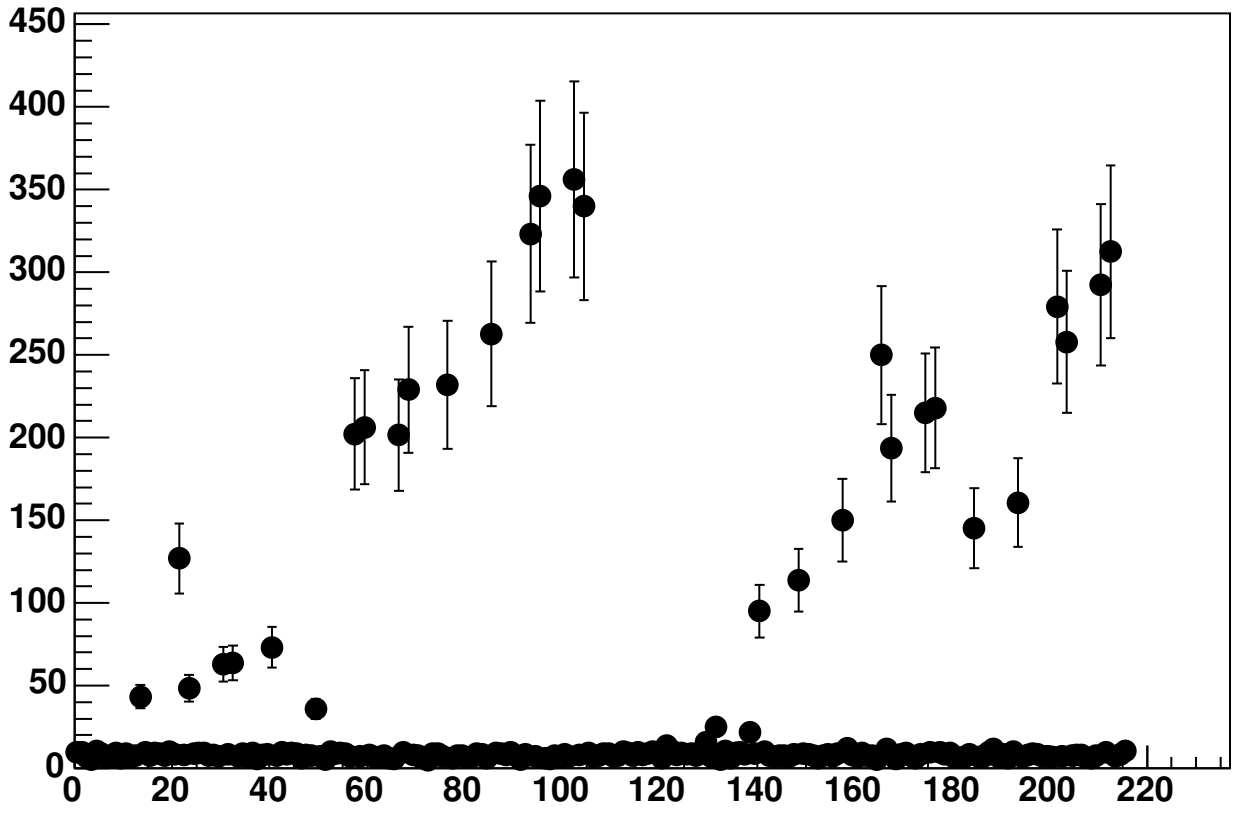
Enable 4, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



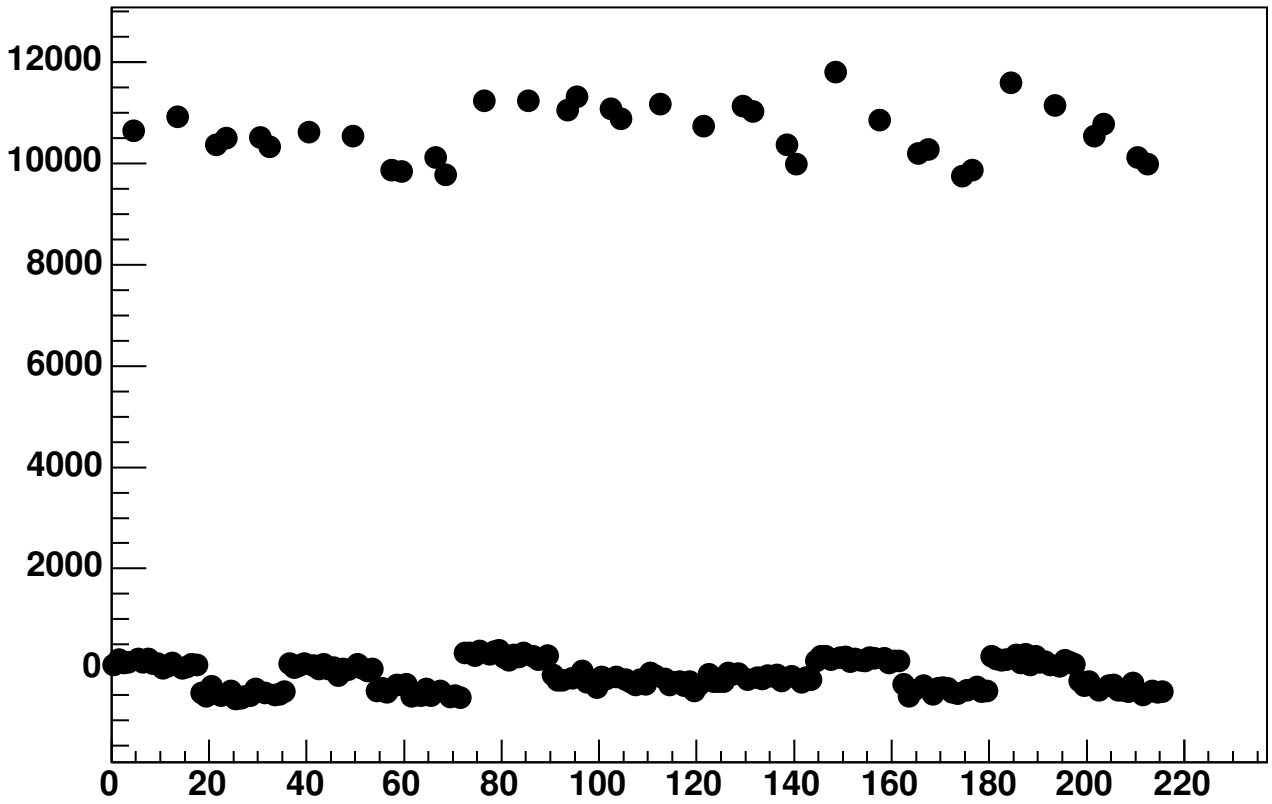
Enable 4, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



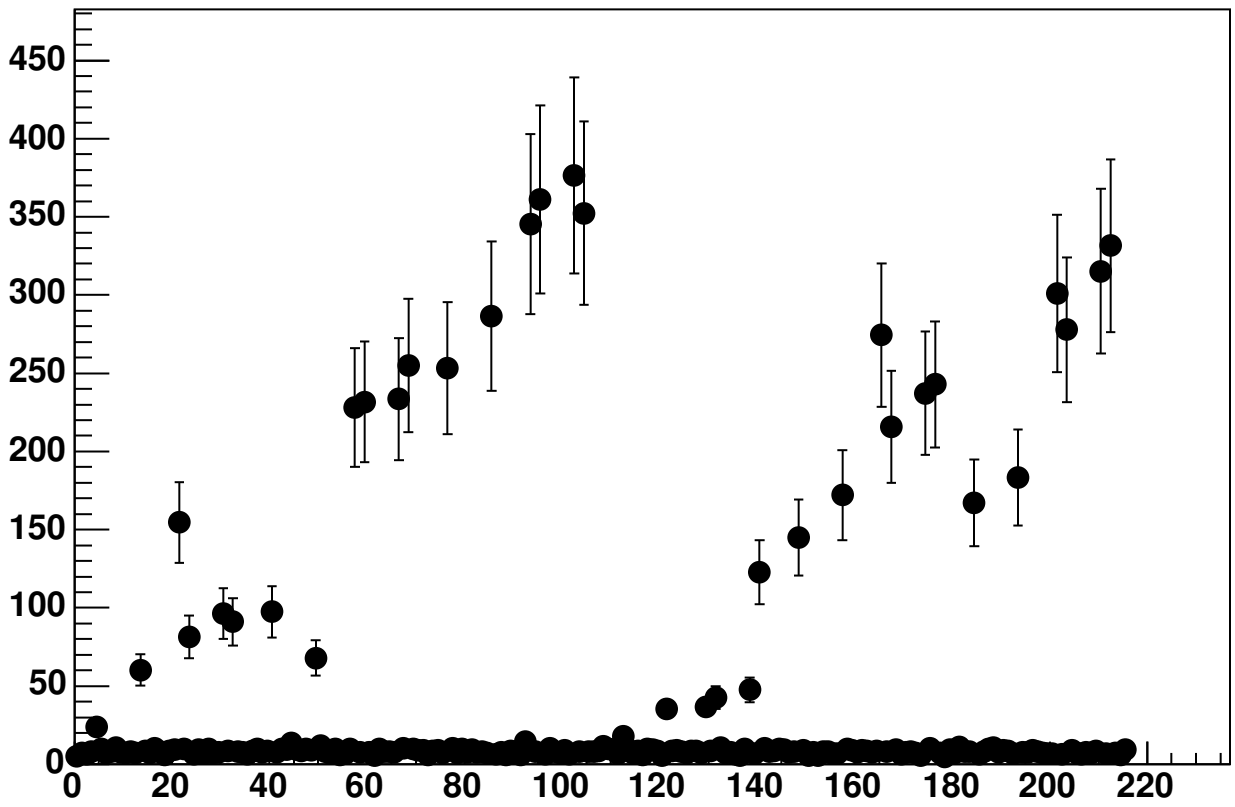
Enable 4, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



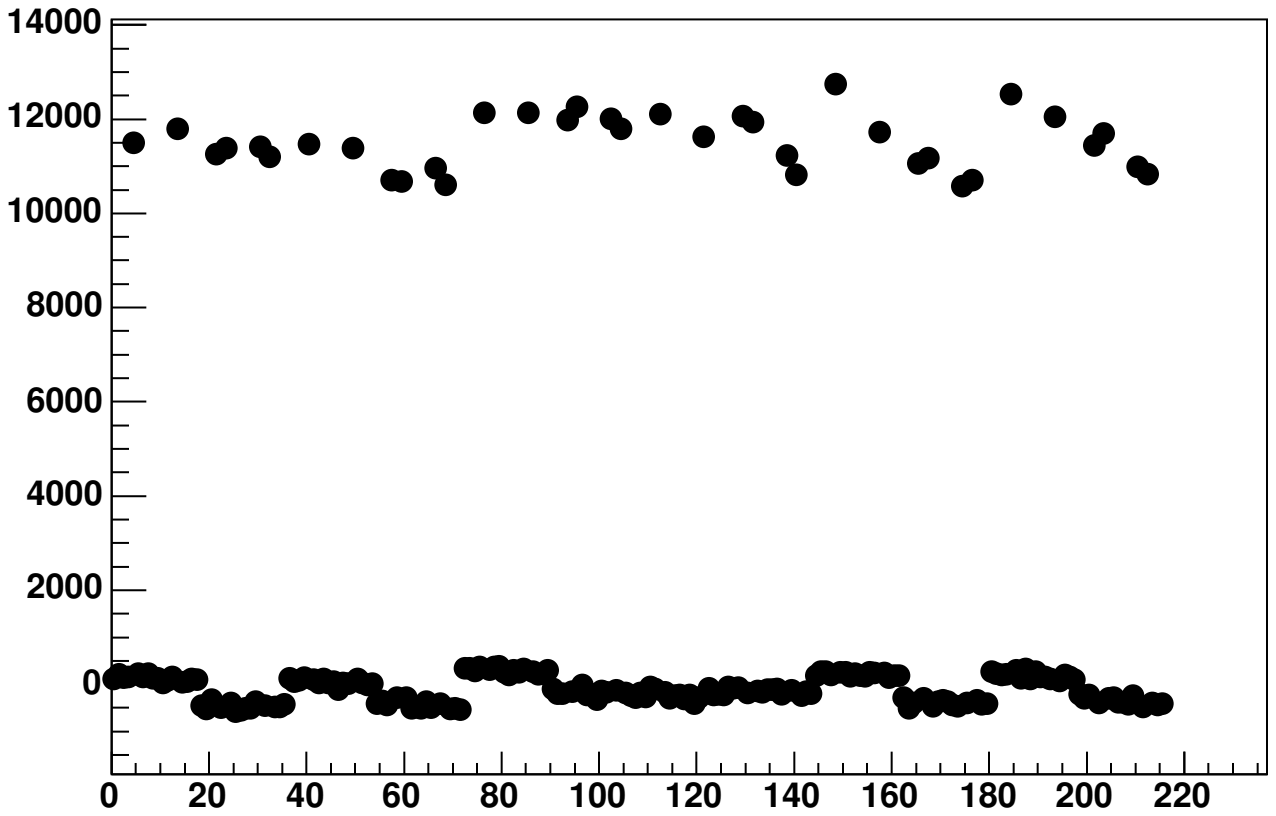
Enable 4, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



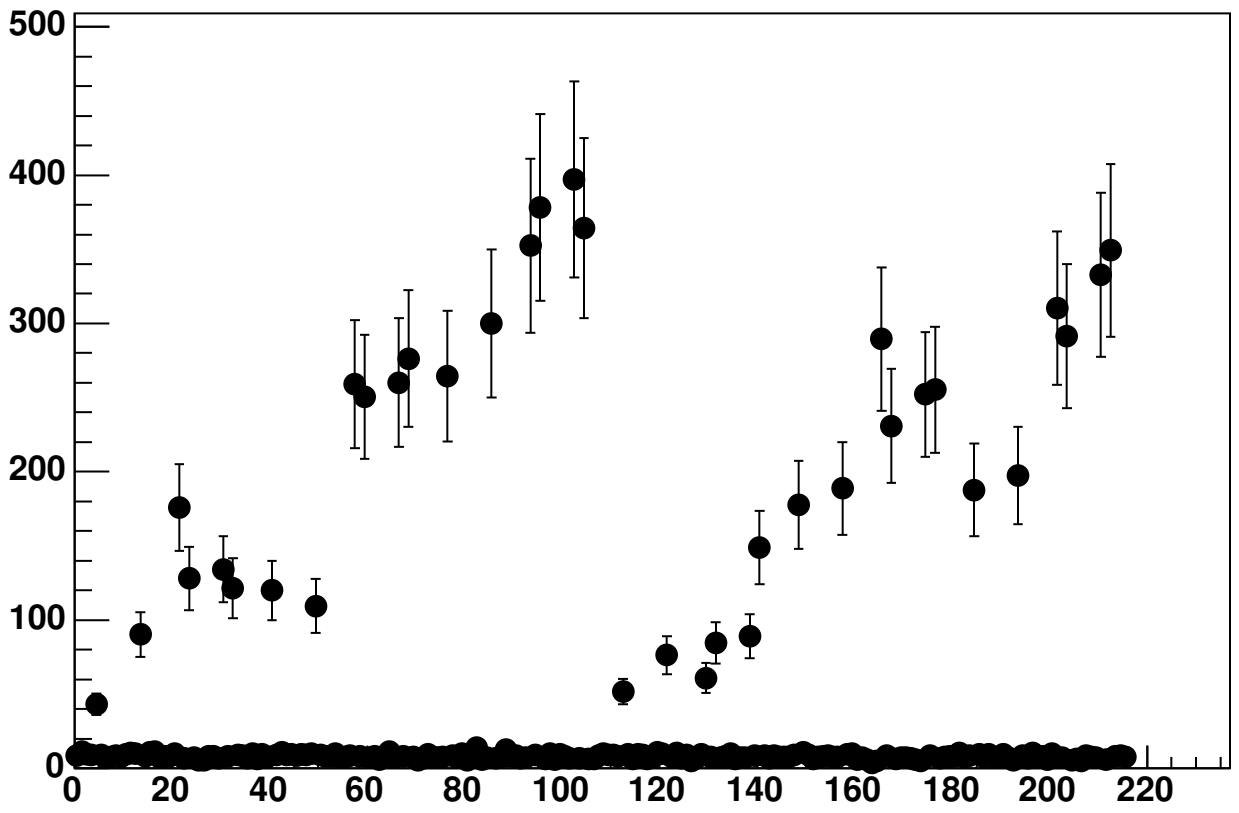
Enable 4, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



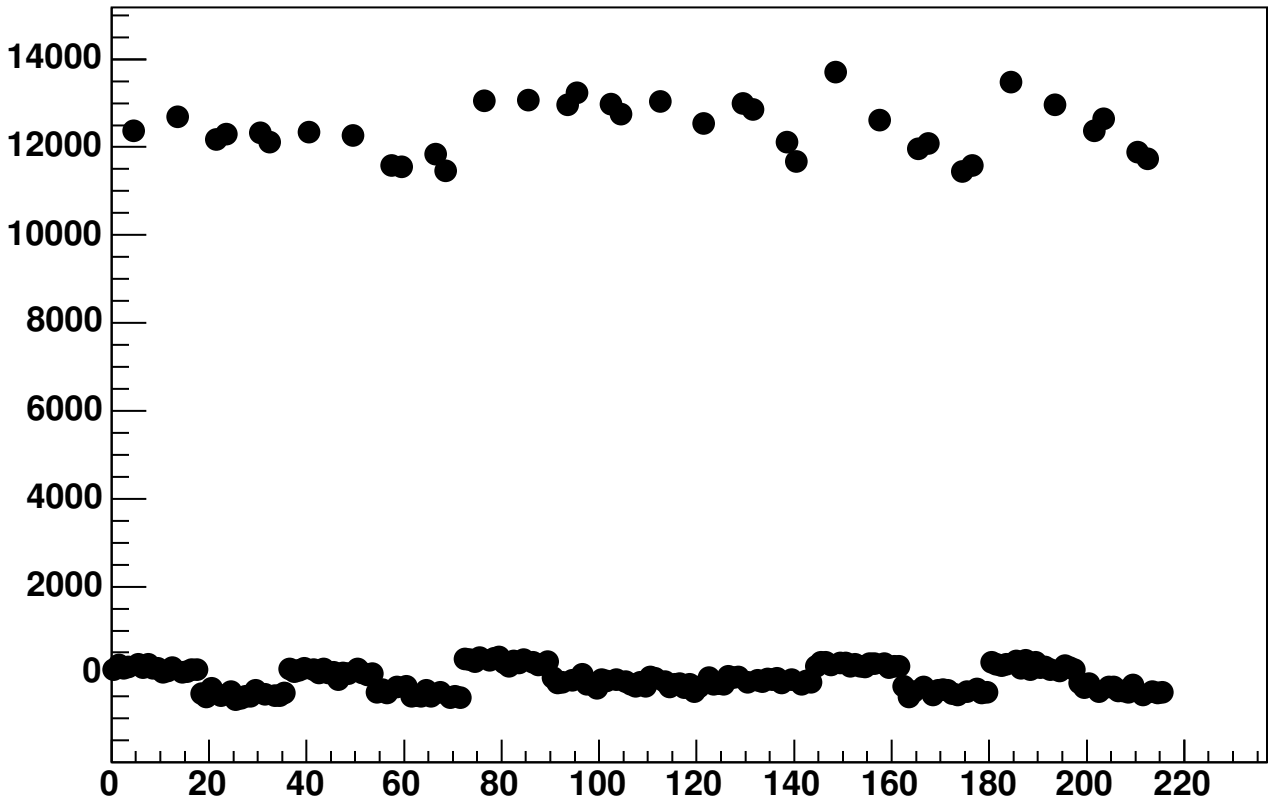
Enable 4, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



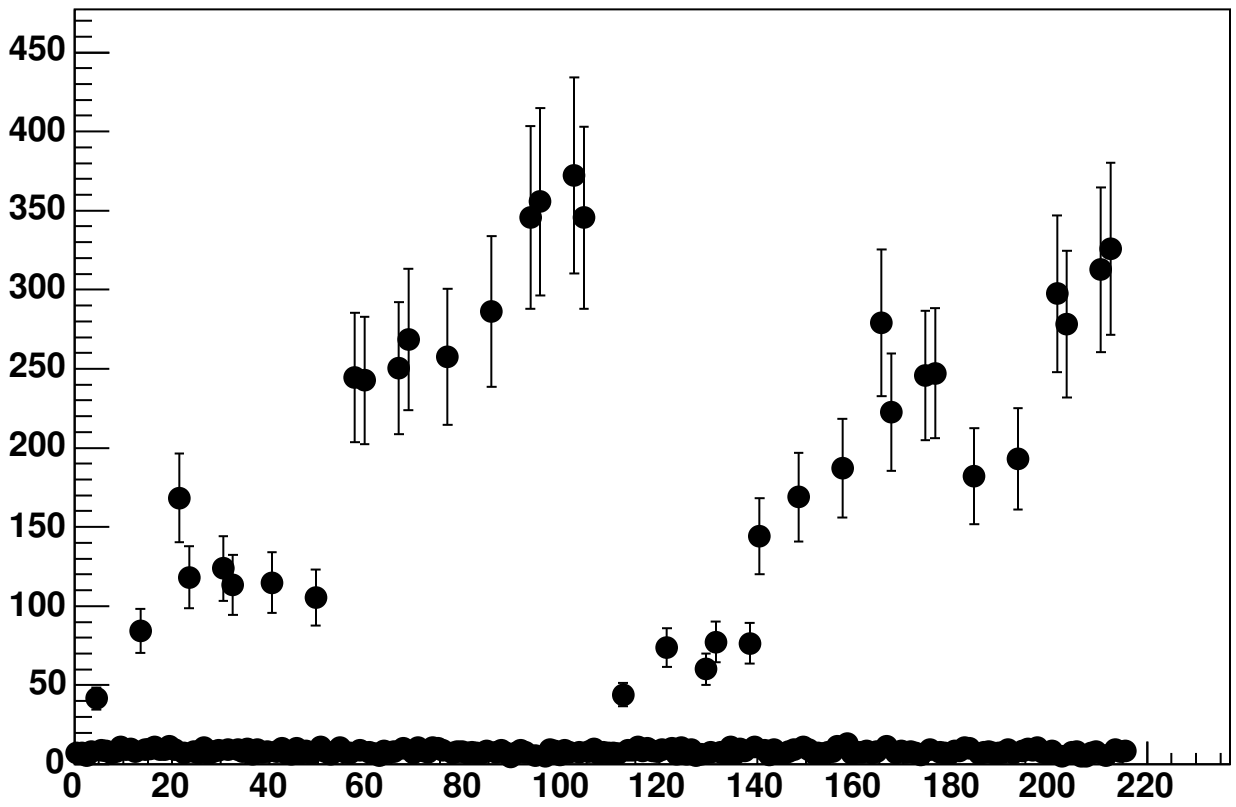
Enable 4, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



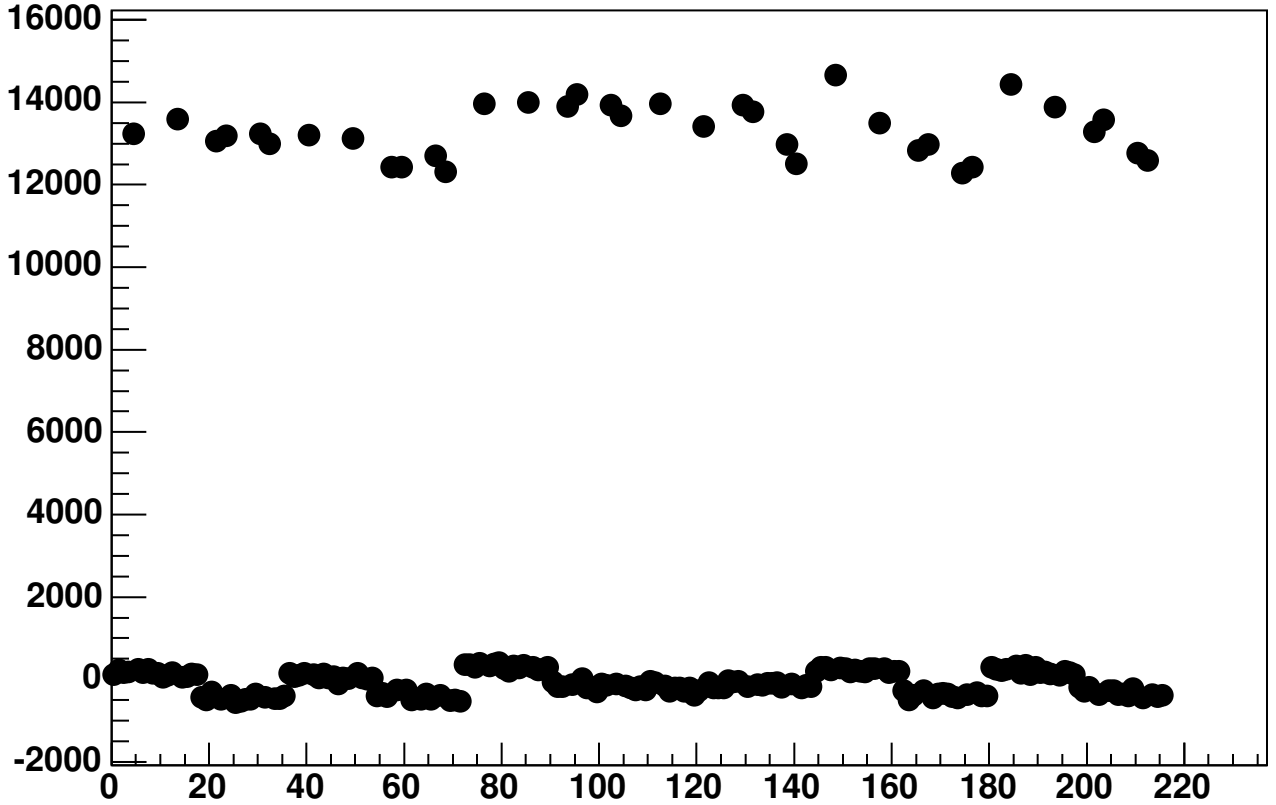
Enable 4, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



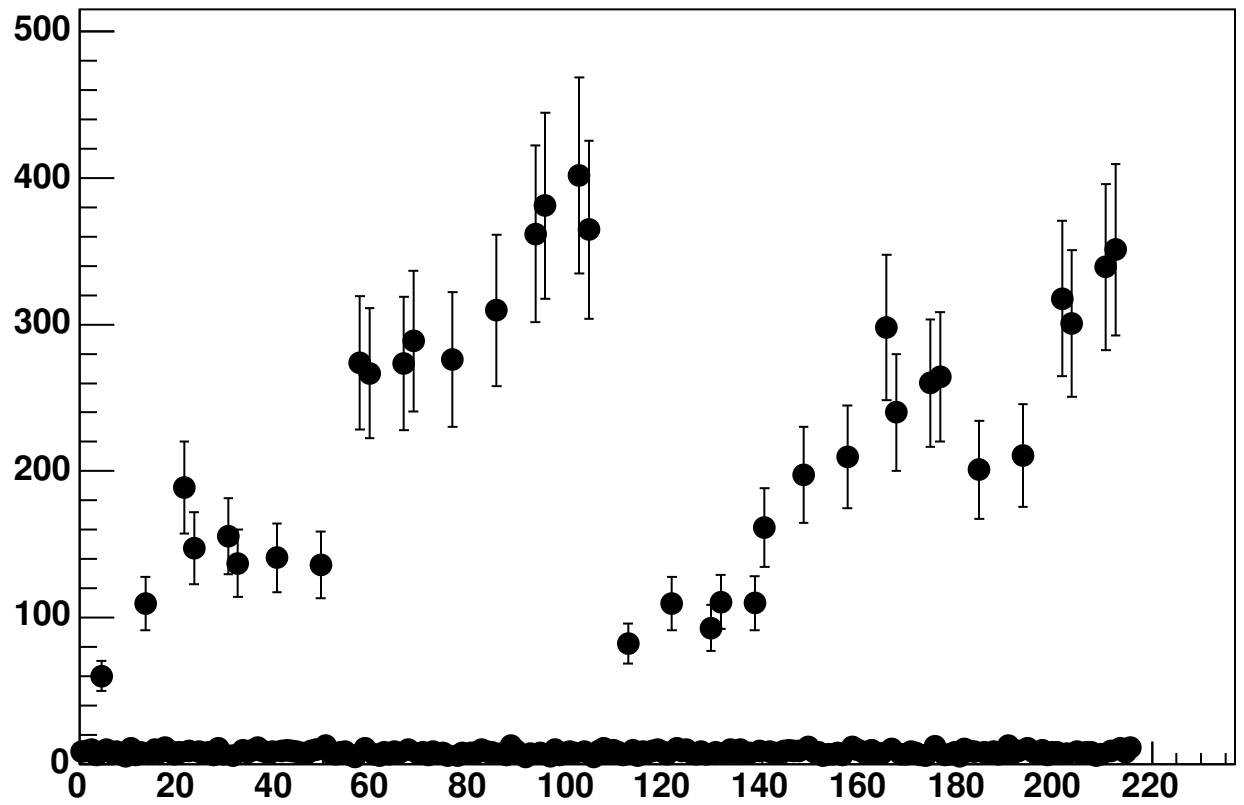
Enable 4, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



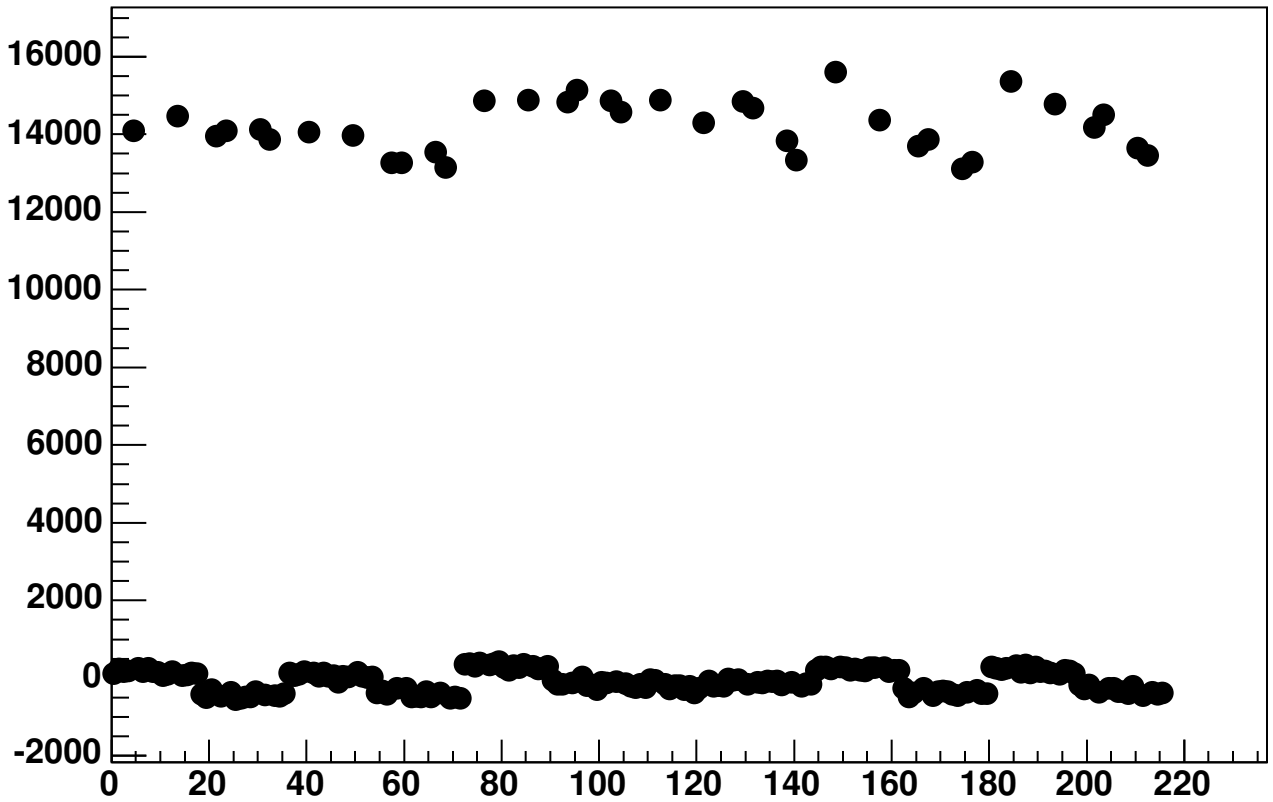
Enable 4, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



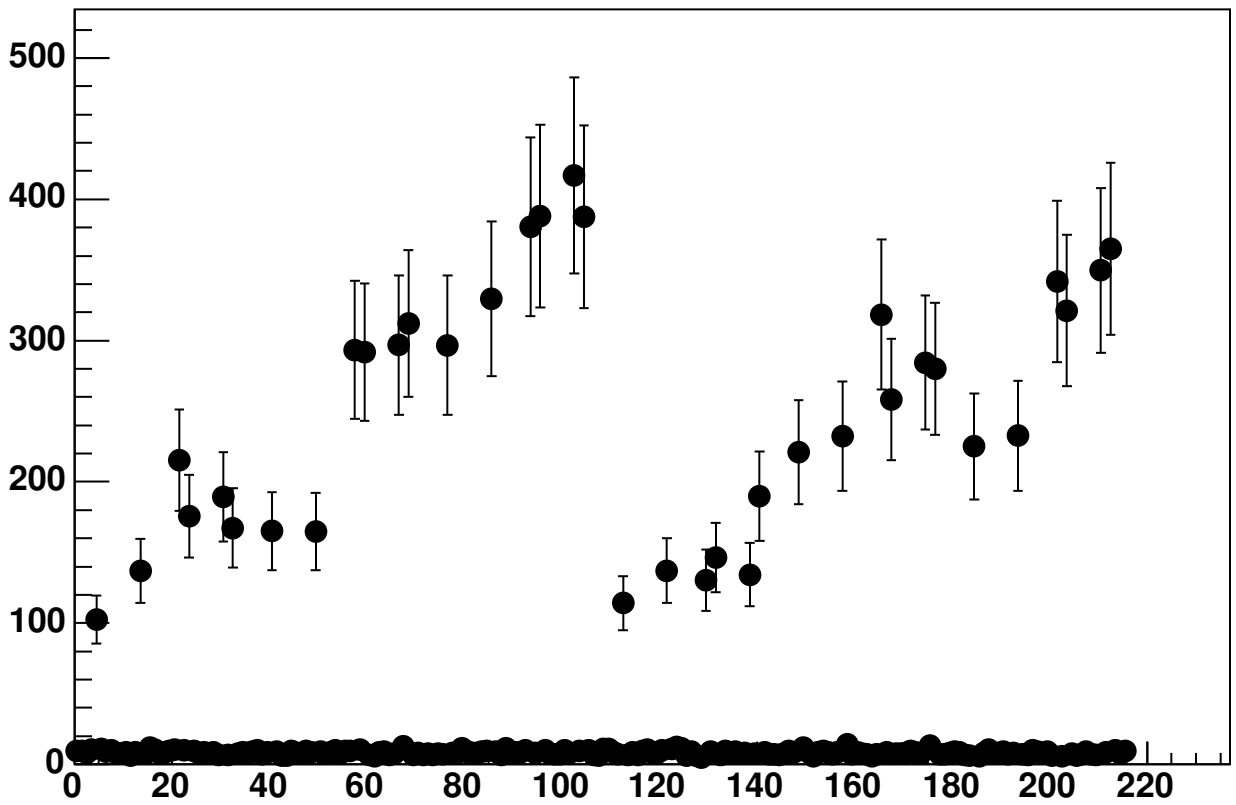
Enable 4, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



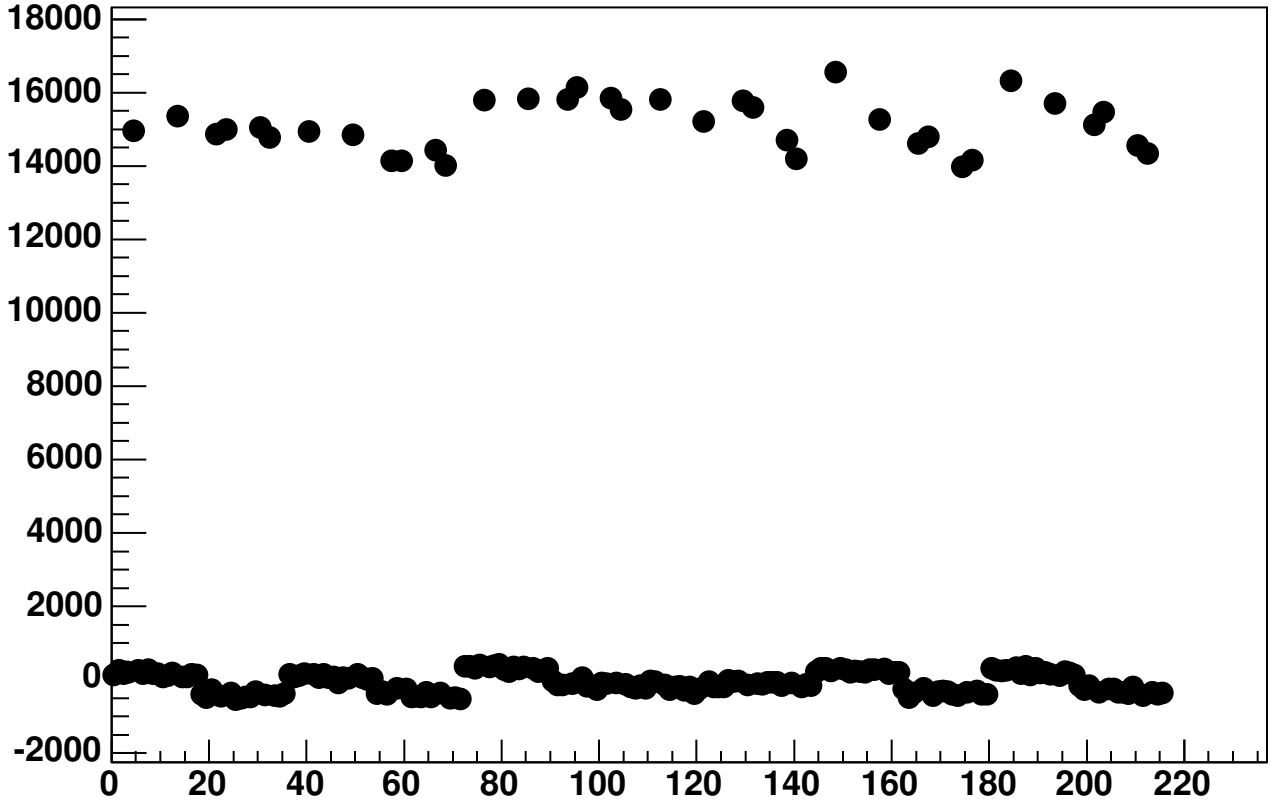
Enable 4, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



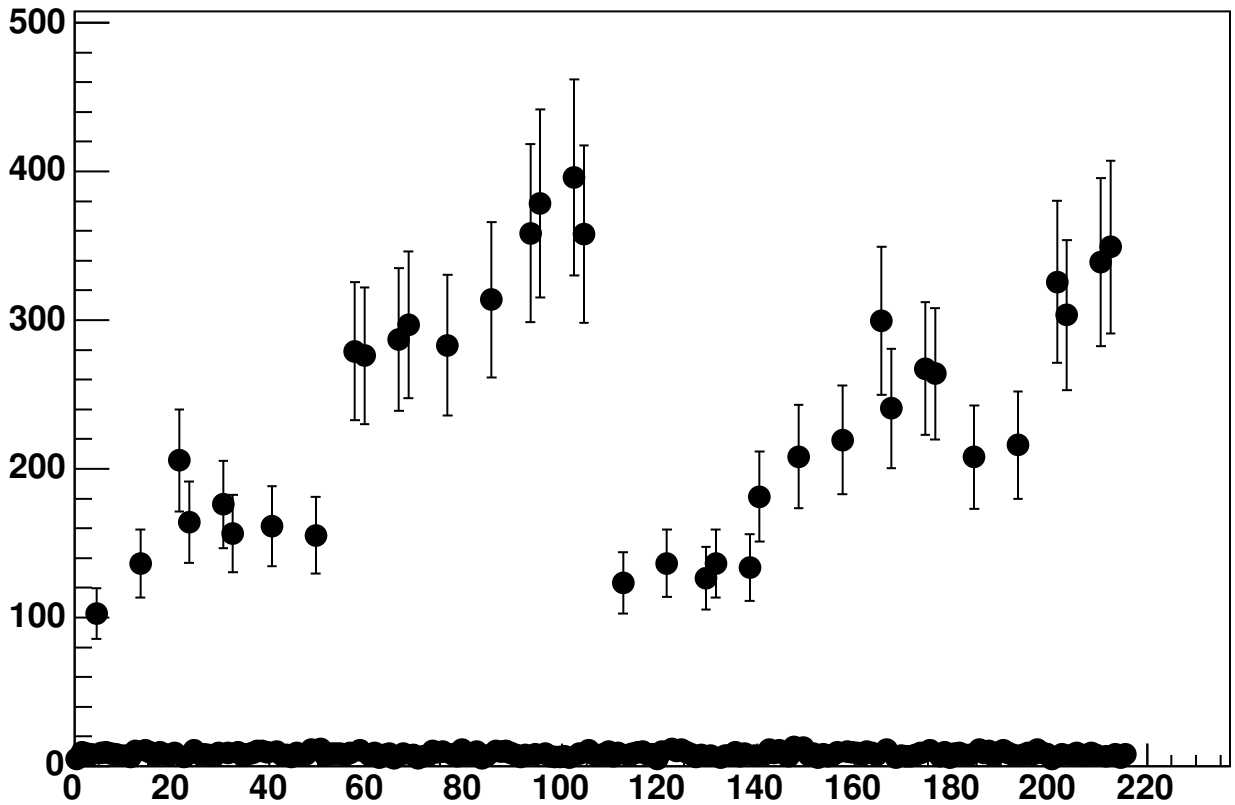
Enable 4, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



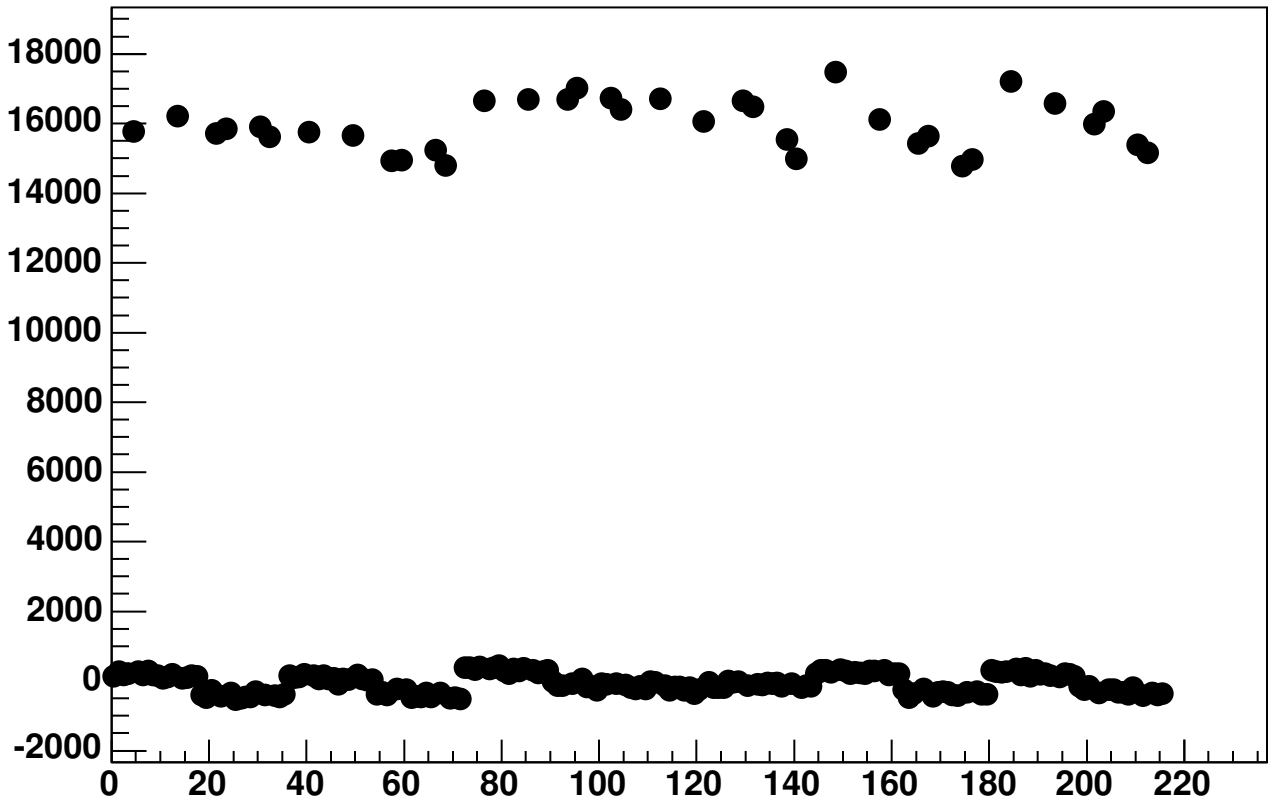
Enable 4, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



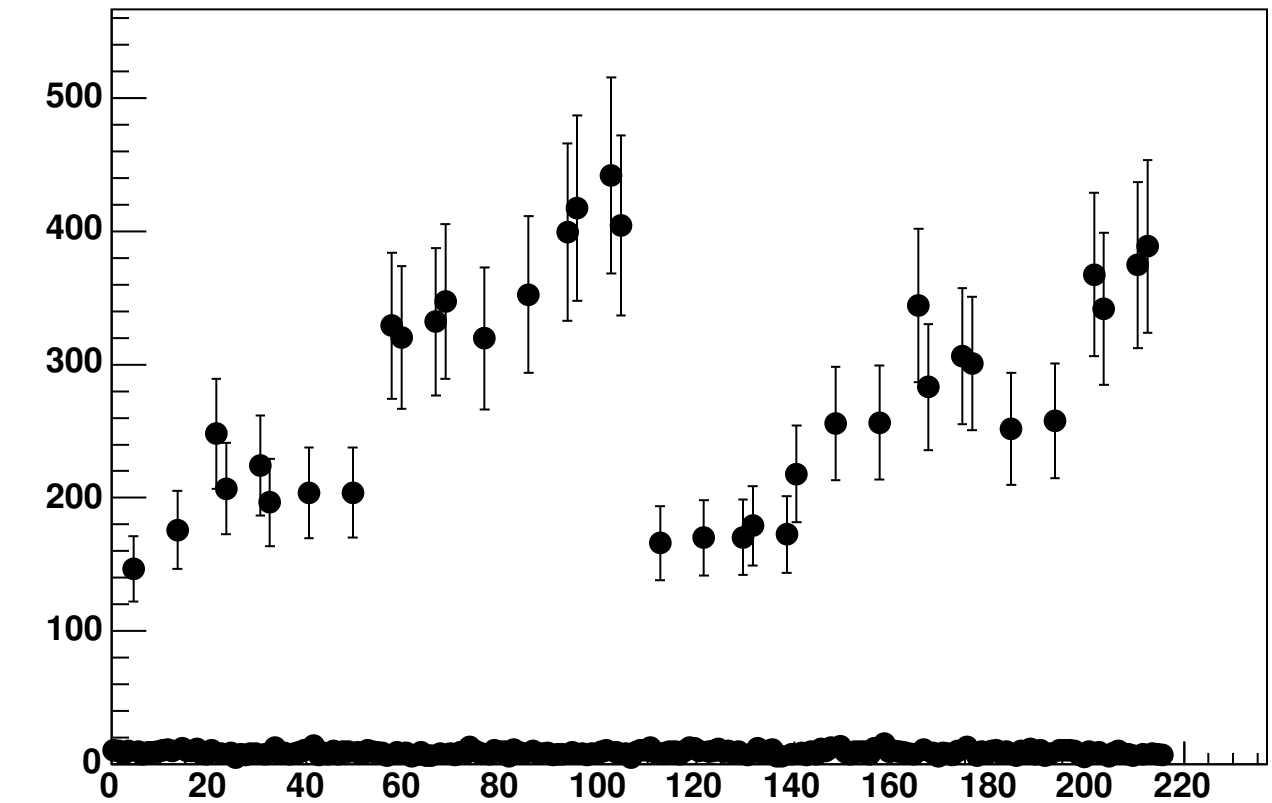
Enable 4, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



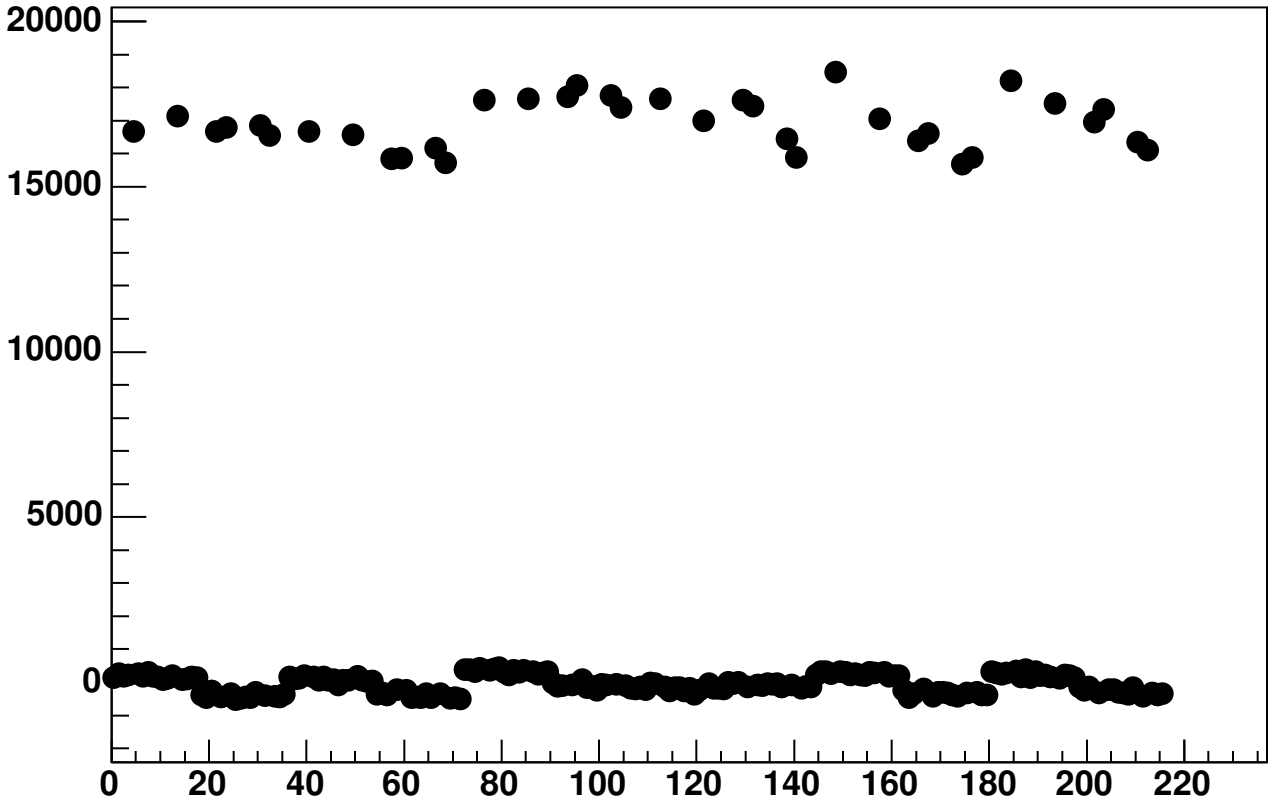
Enable 4, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



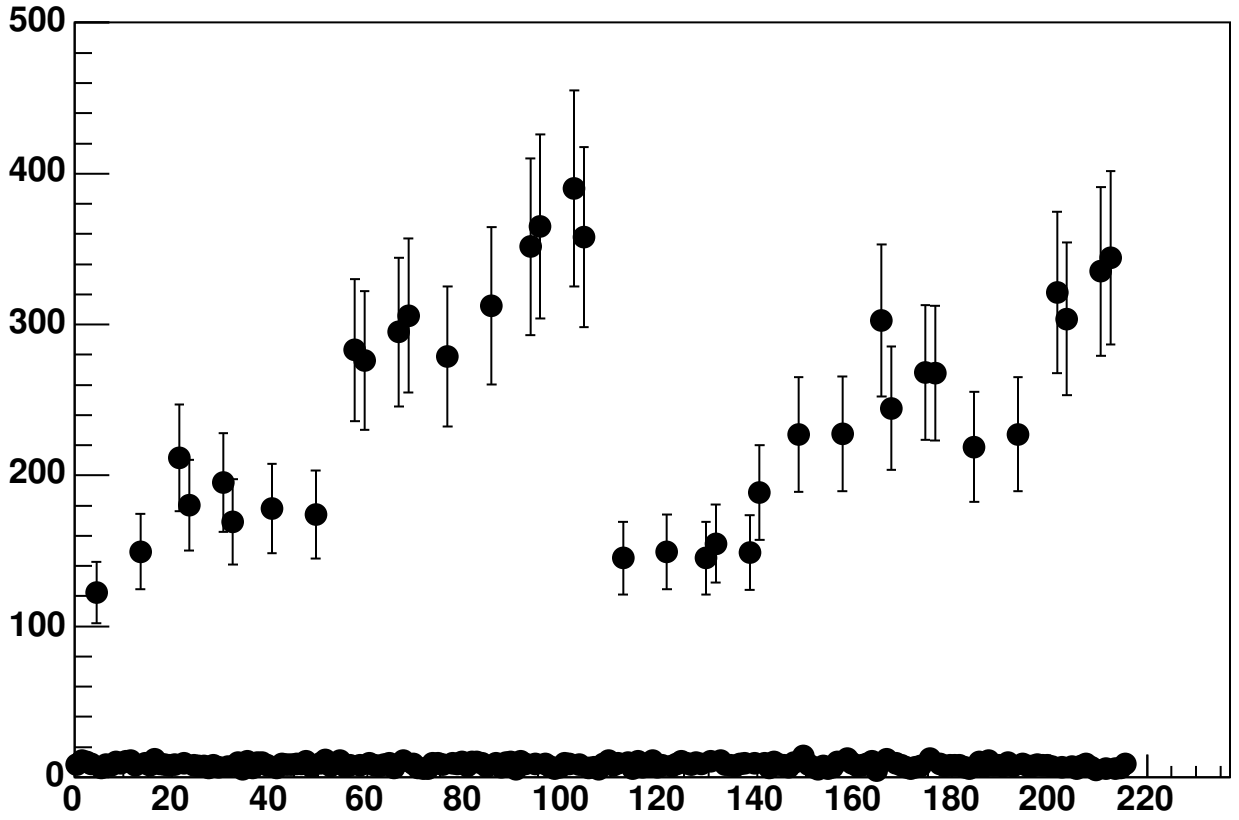
Enable 4, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



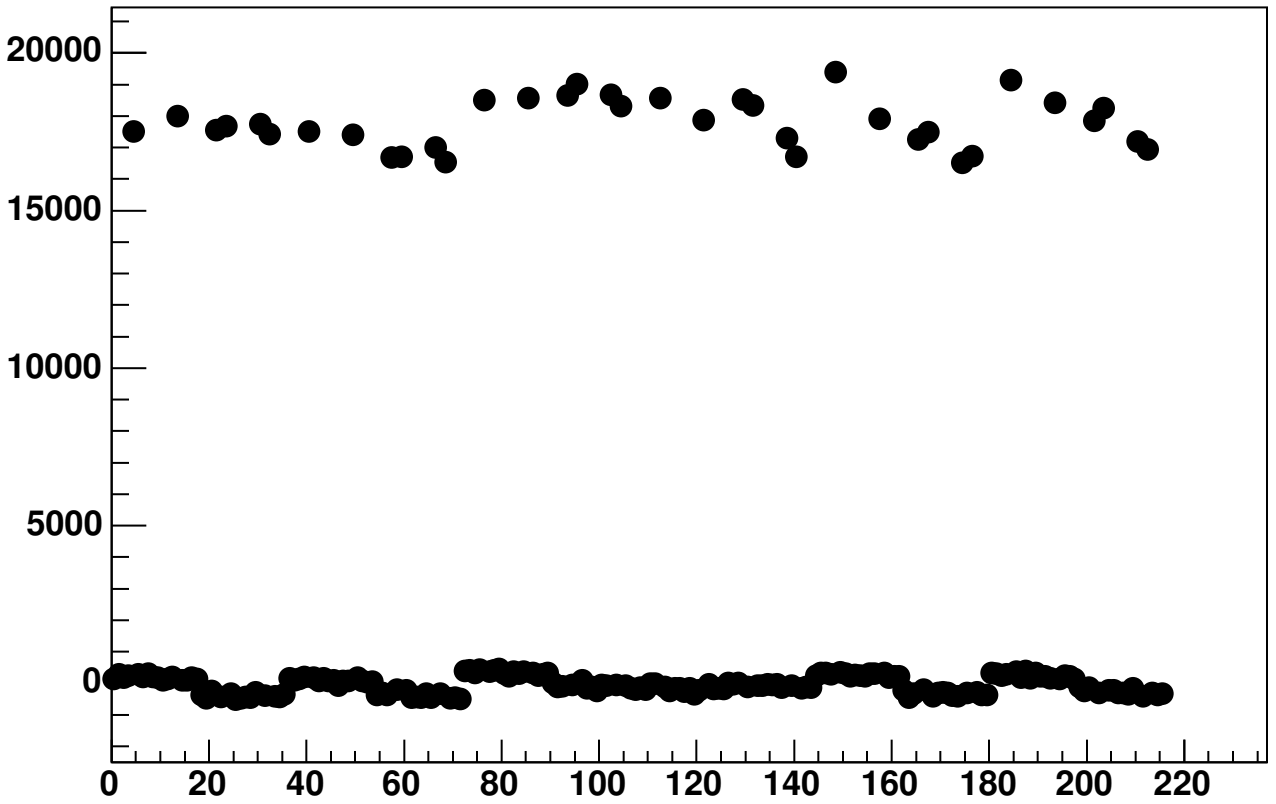
Enable 4, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



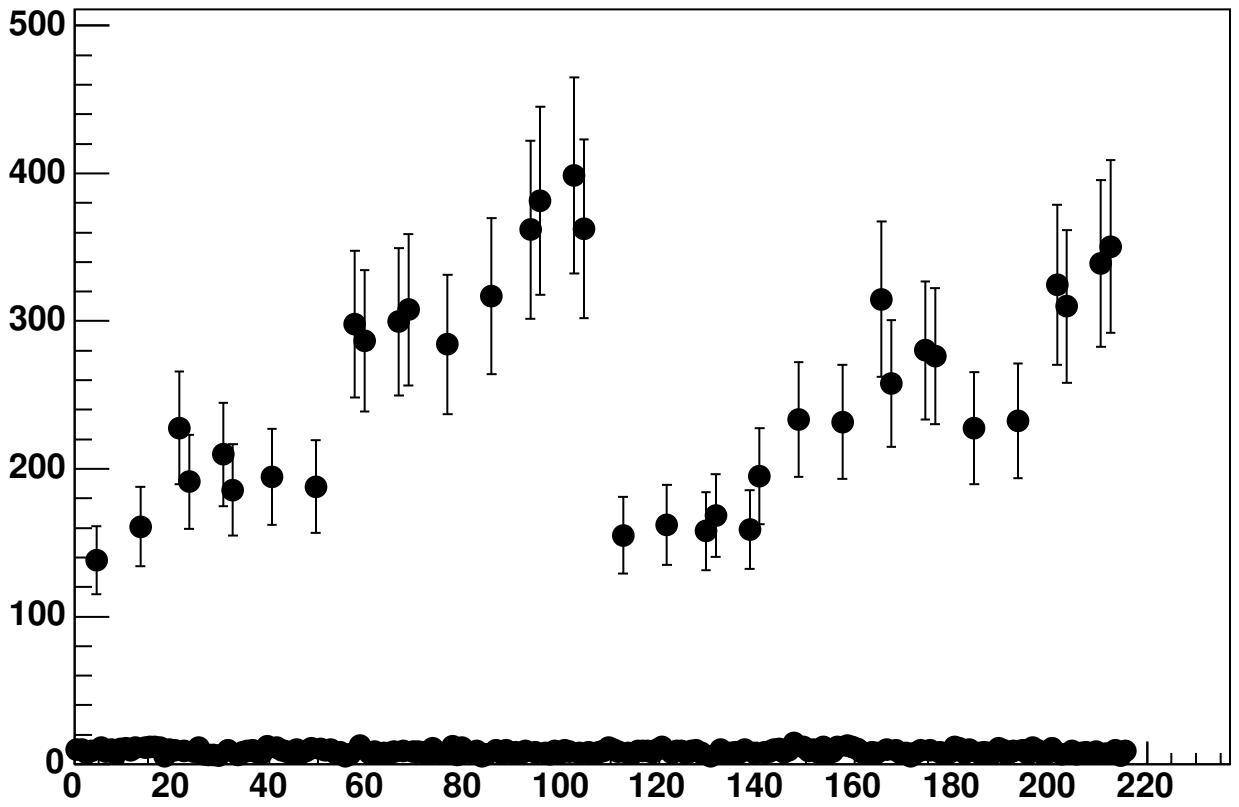
Enable 4, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



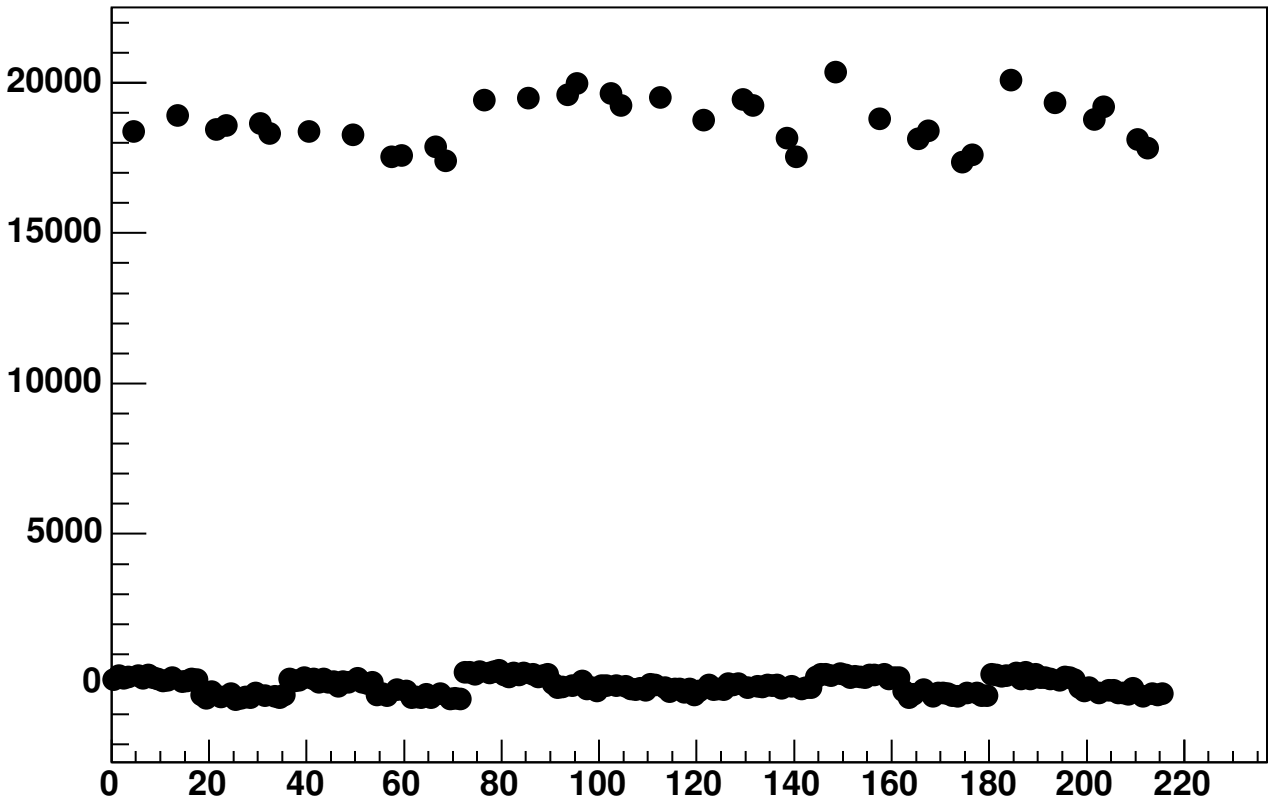
Enable 4, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



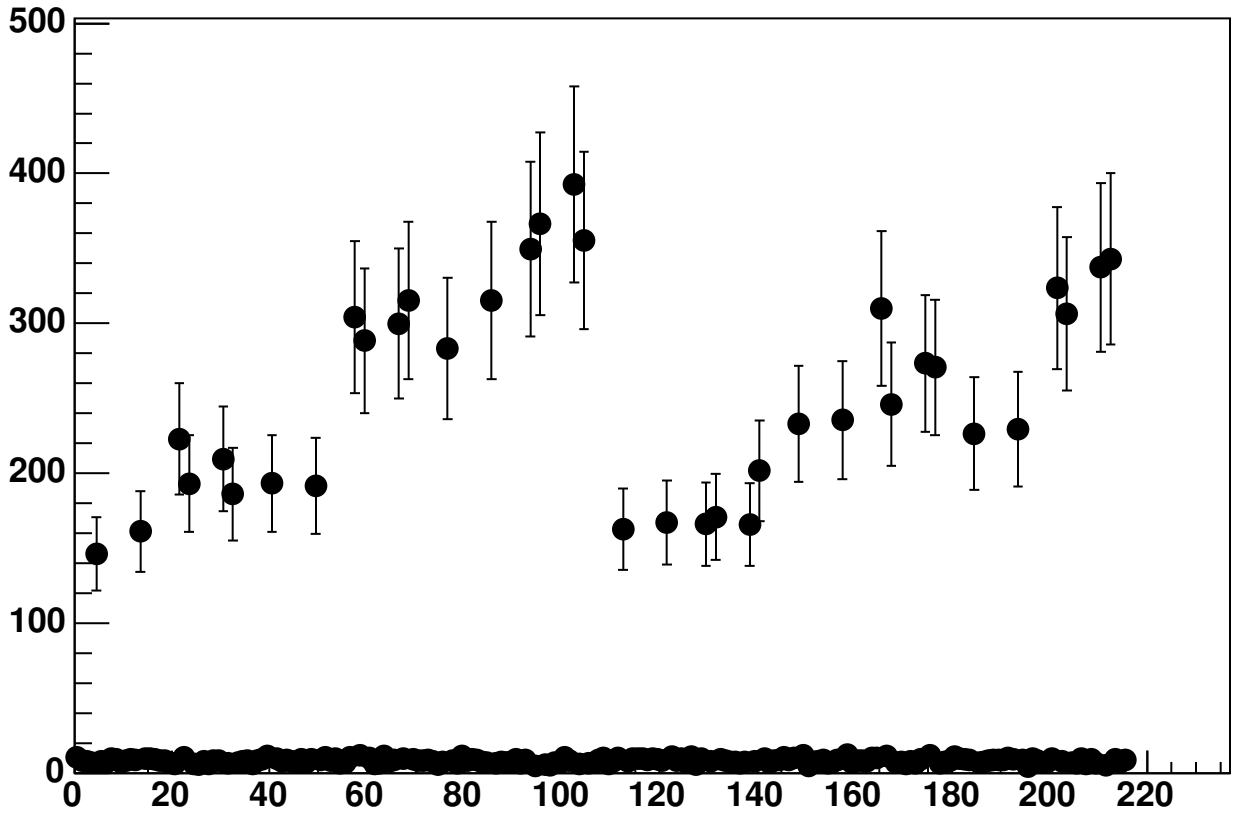
Enable 4, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



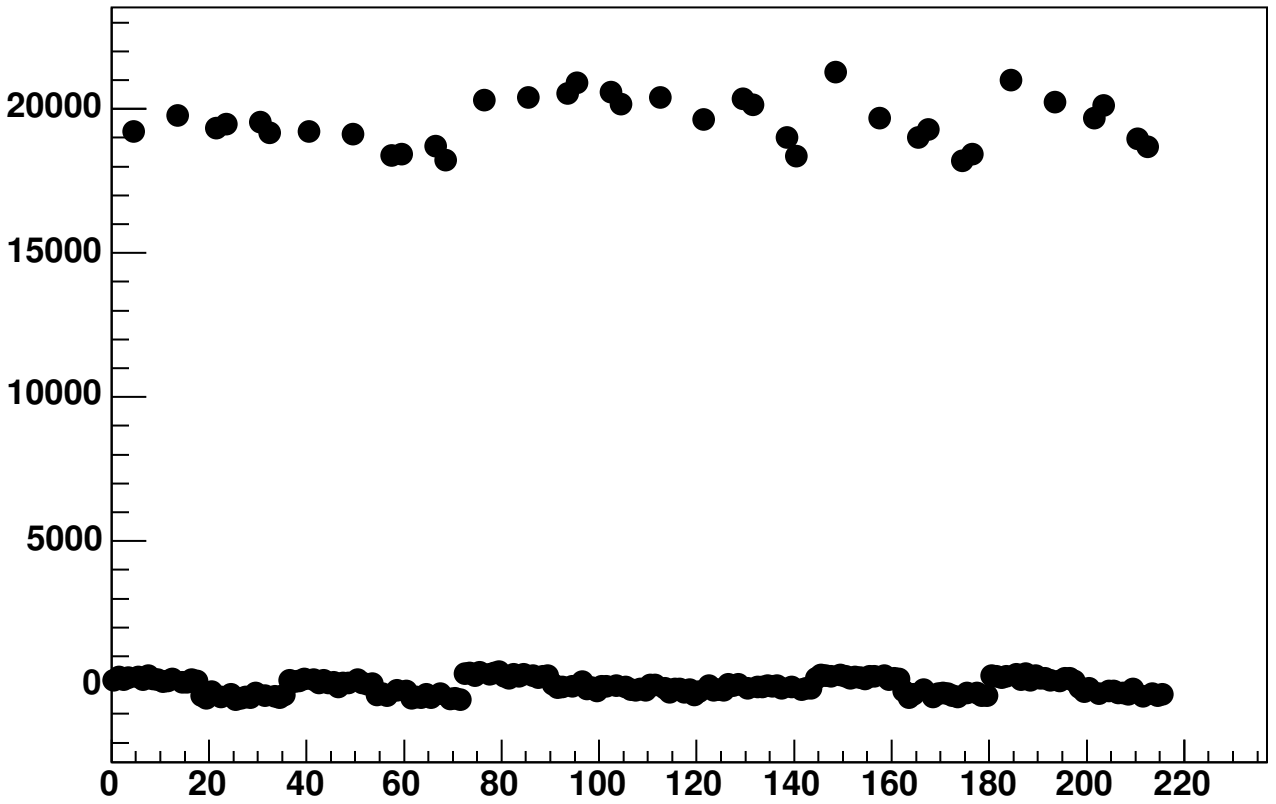
Enable 4, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



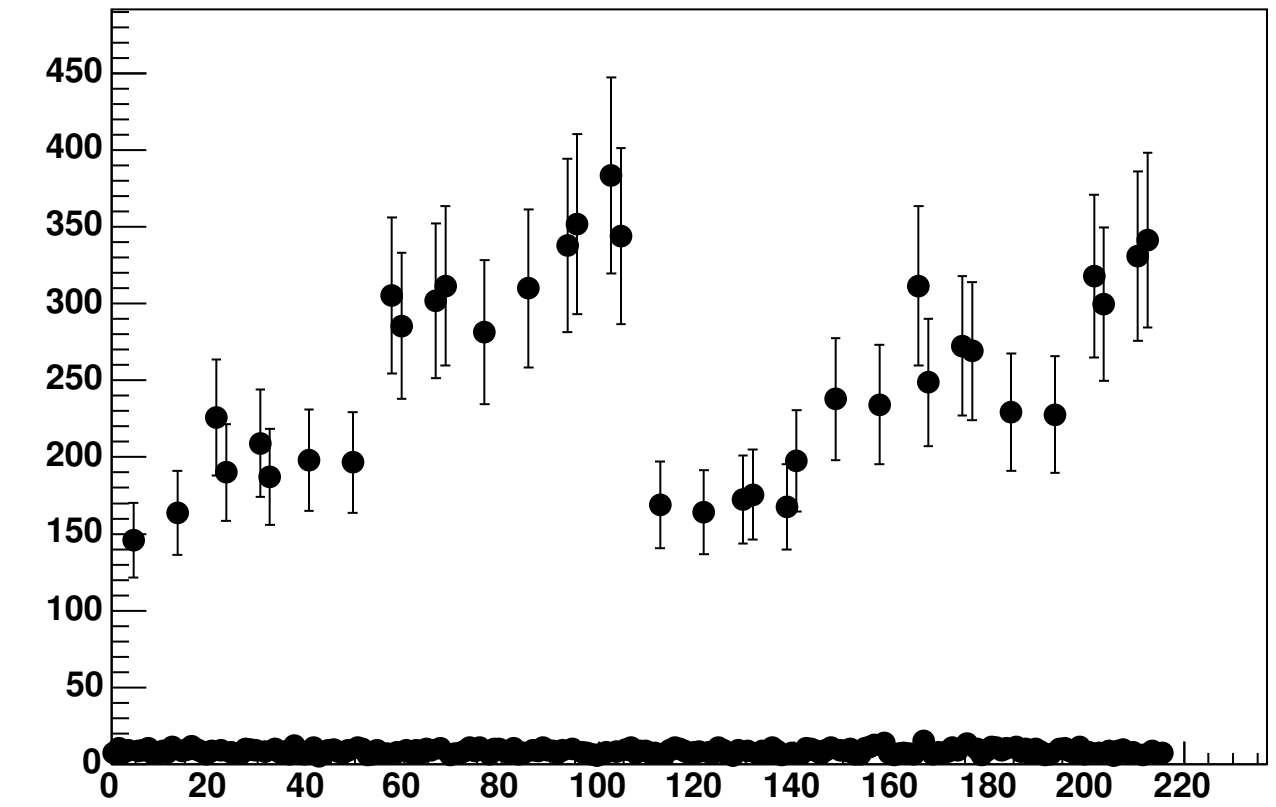
Enable 4, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



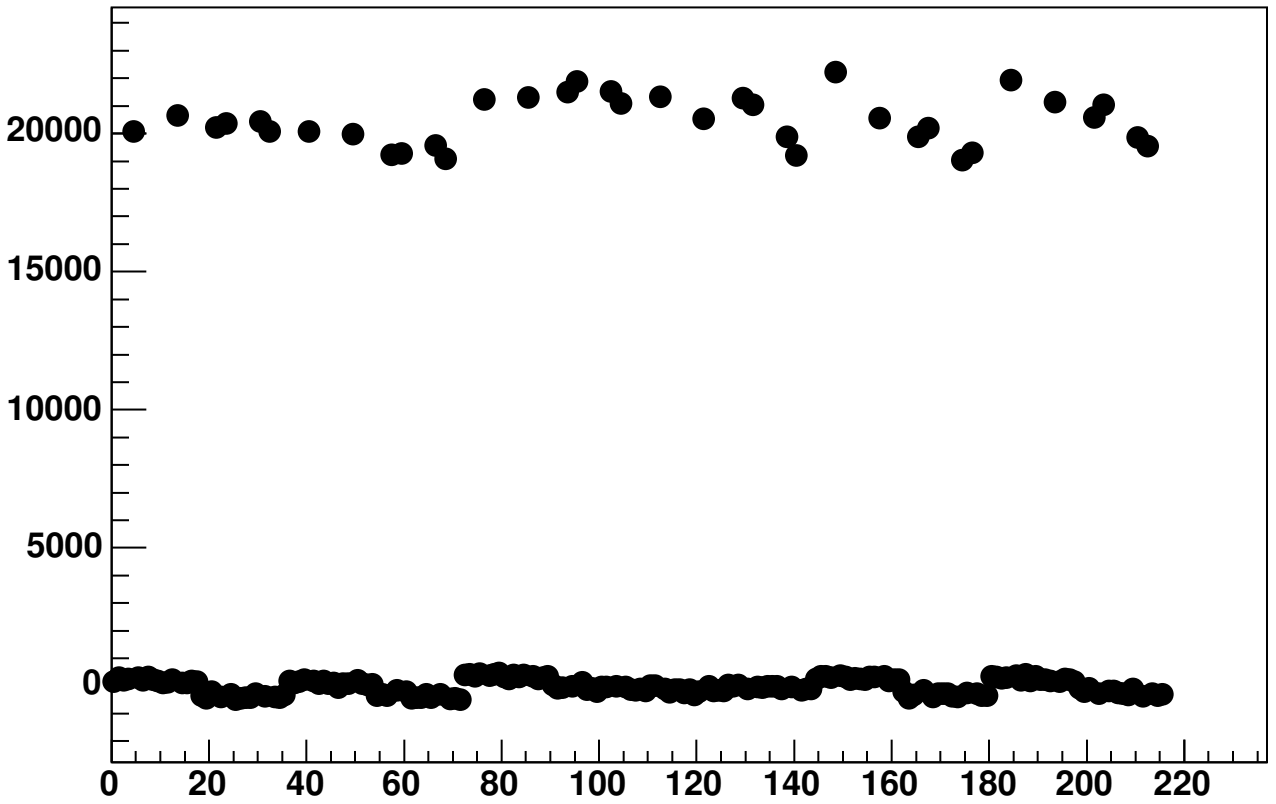
Enable 4, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



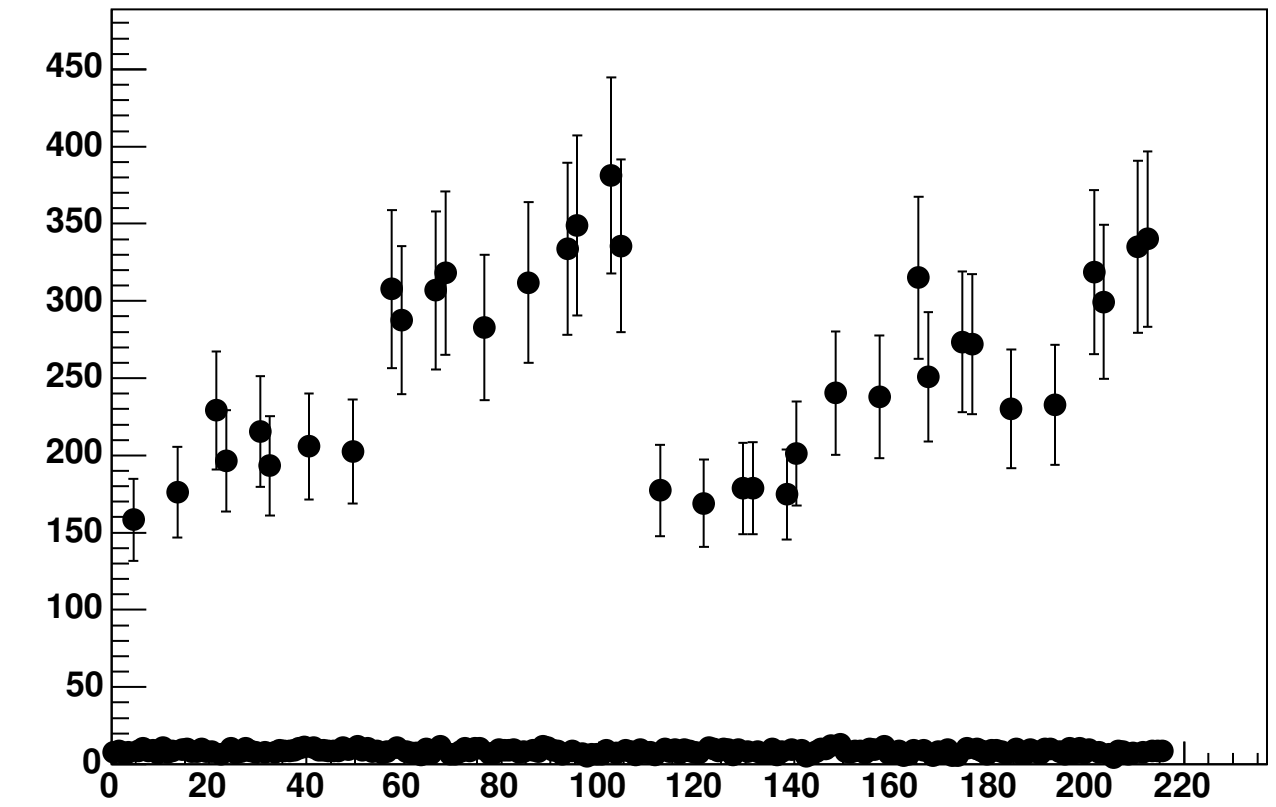
Enable 4, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



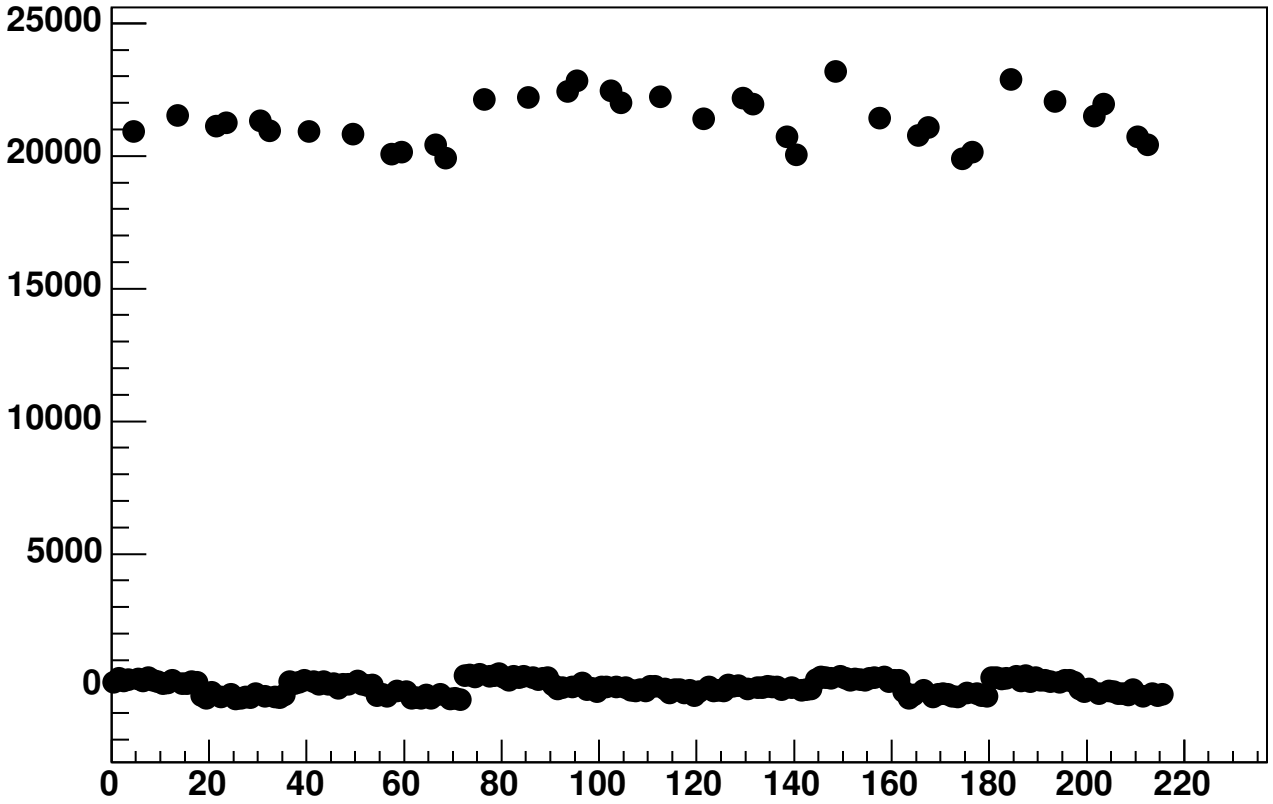
Enable 4, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



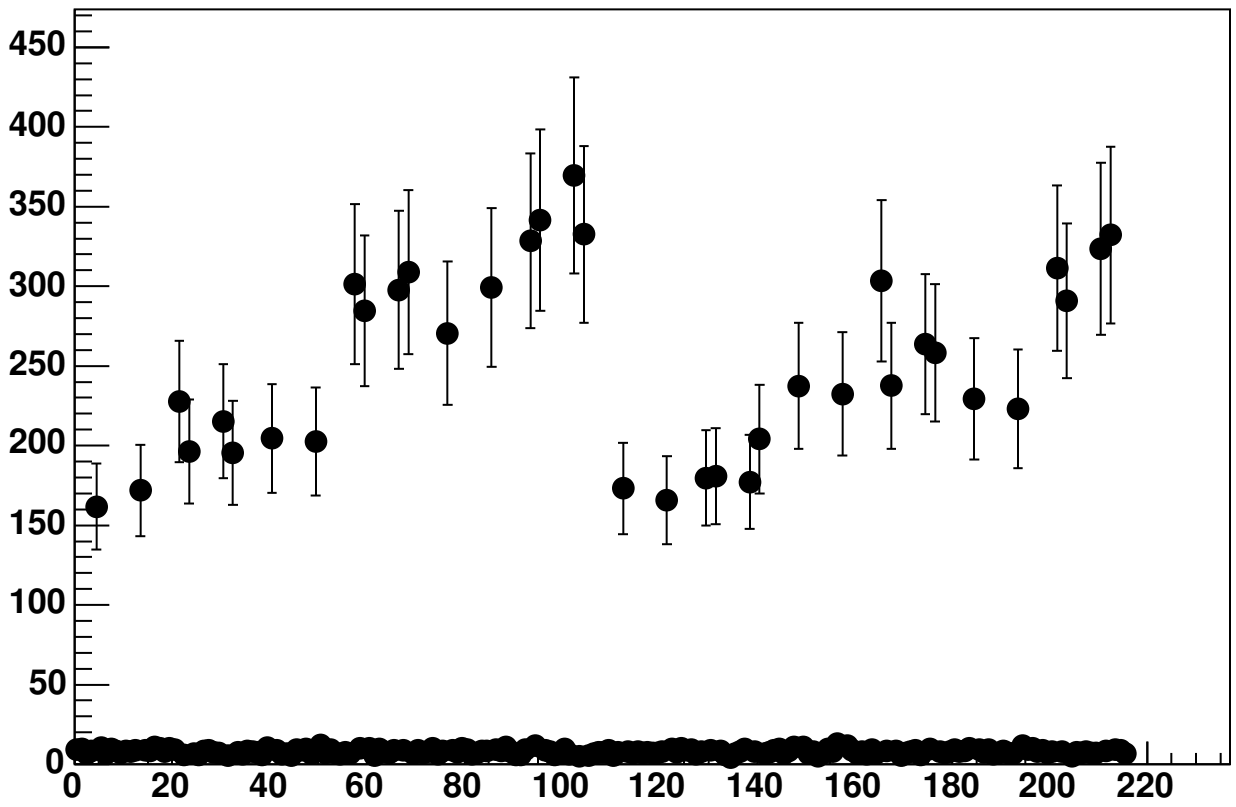
Enable 4, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



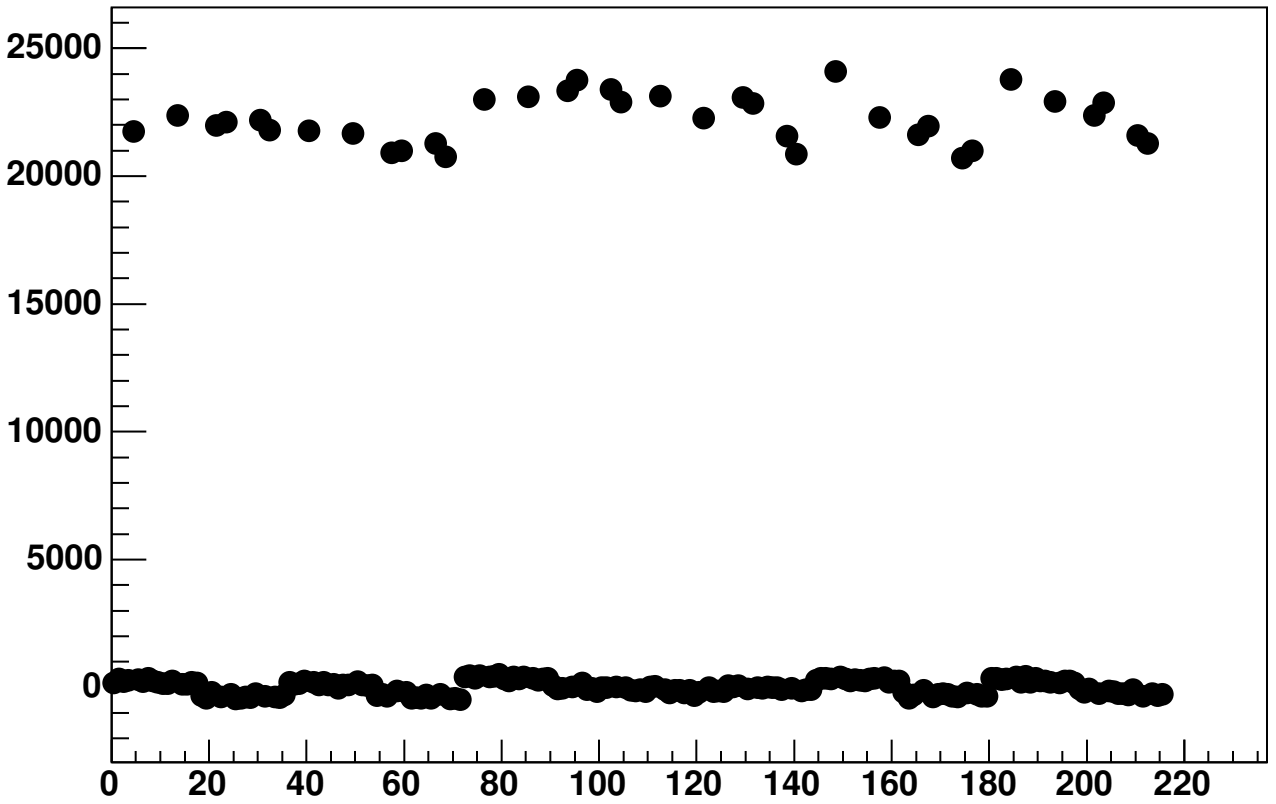
Enable 4, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



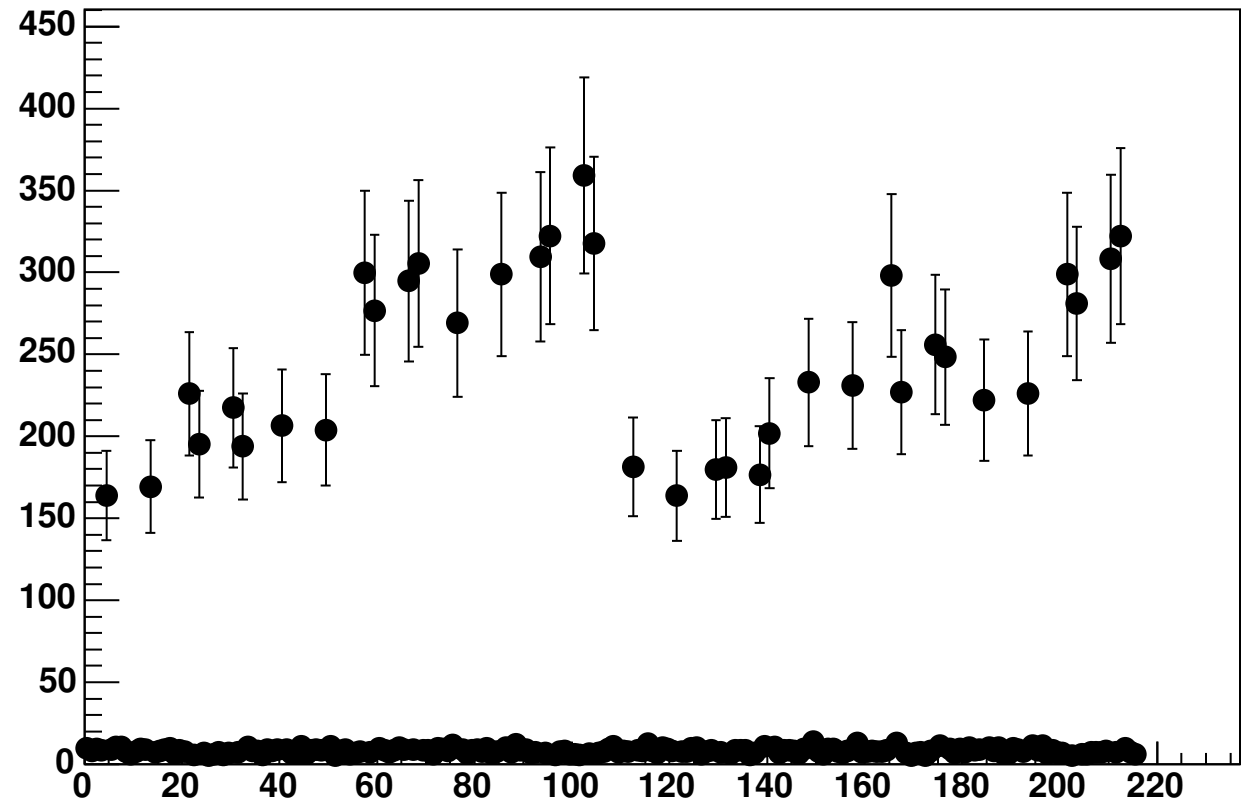
Enable 4, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



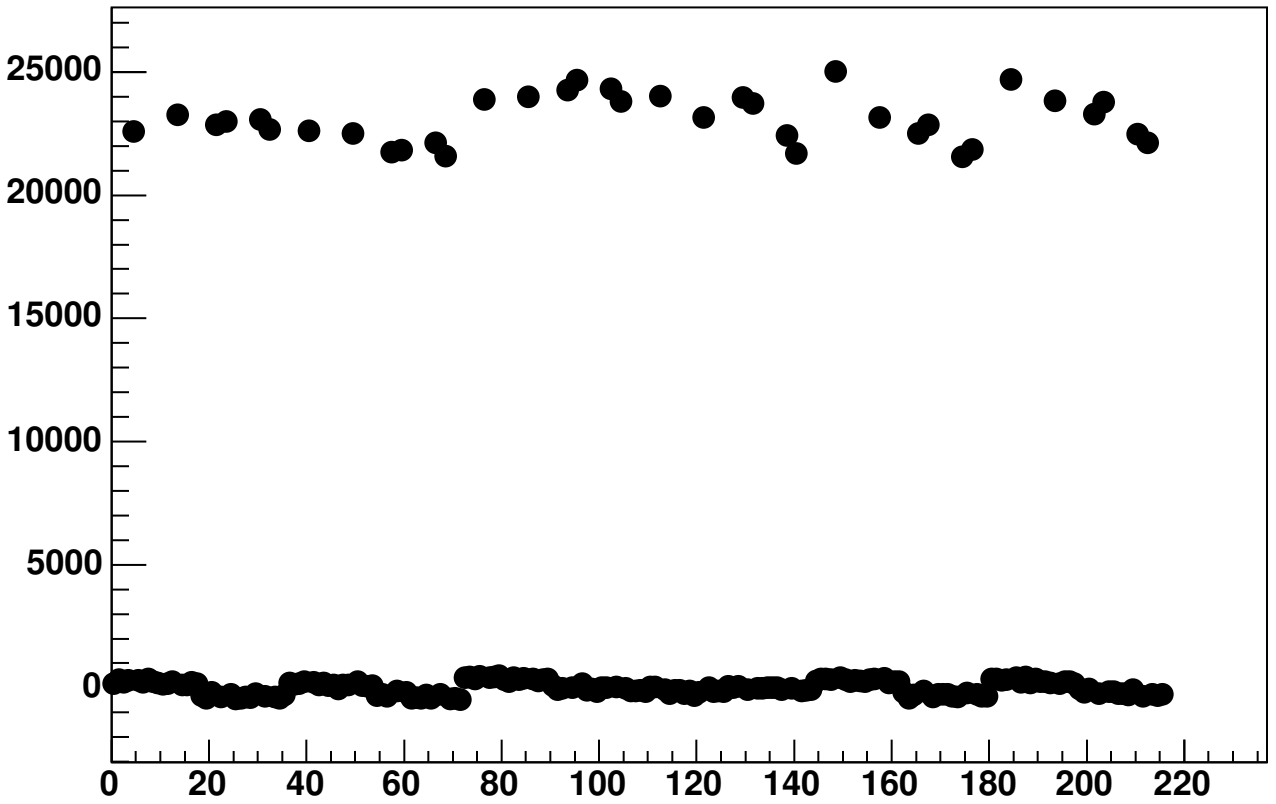
Enable 4, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



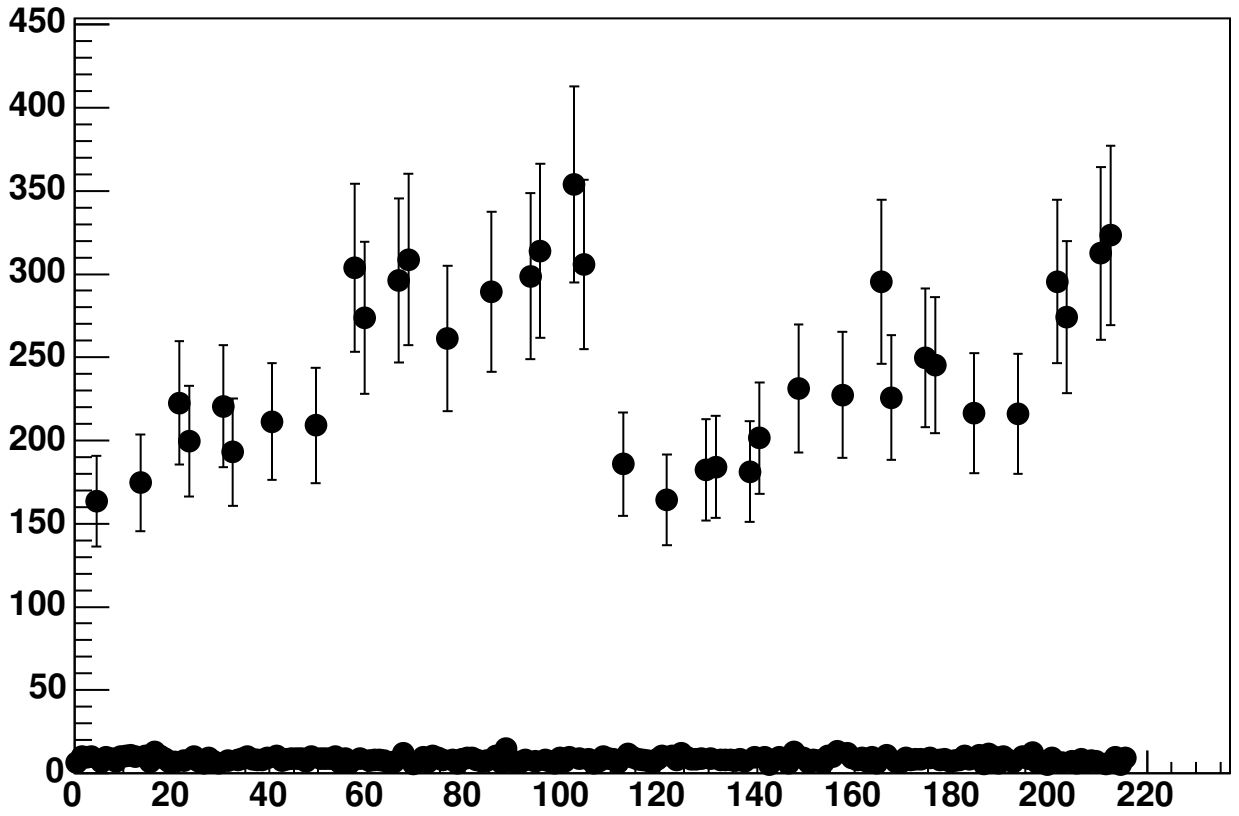
Enable 4, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



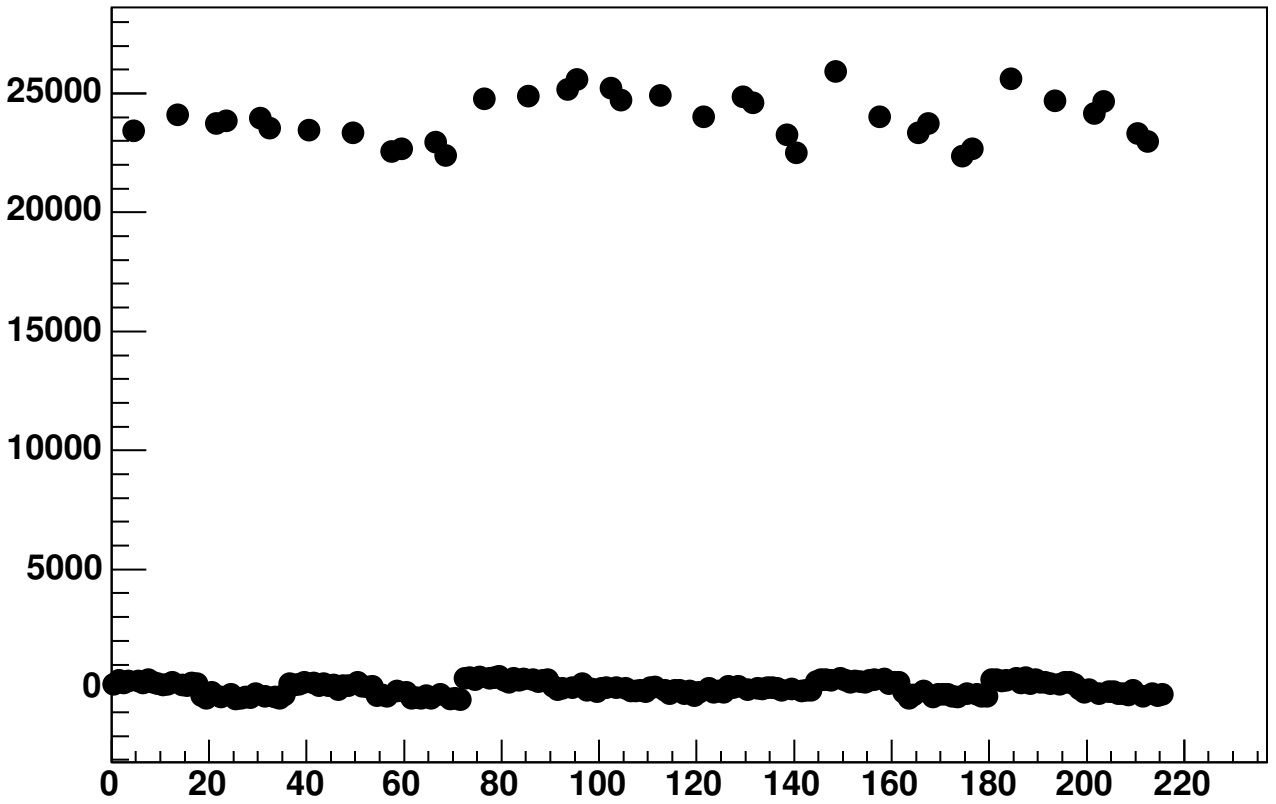
Enable 4, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



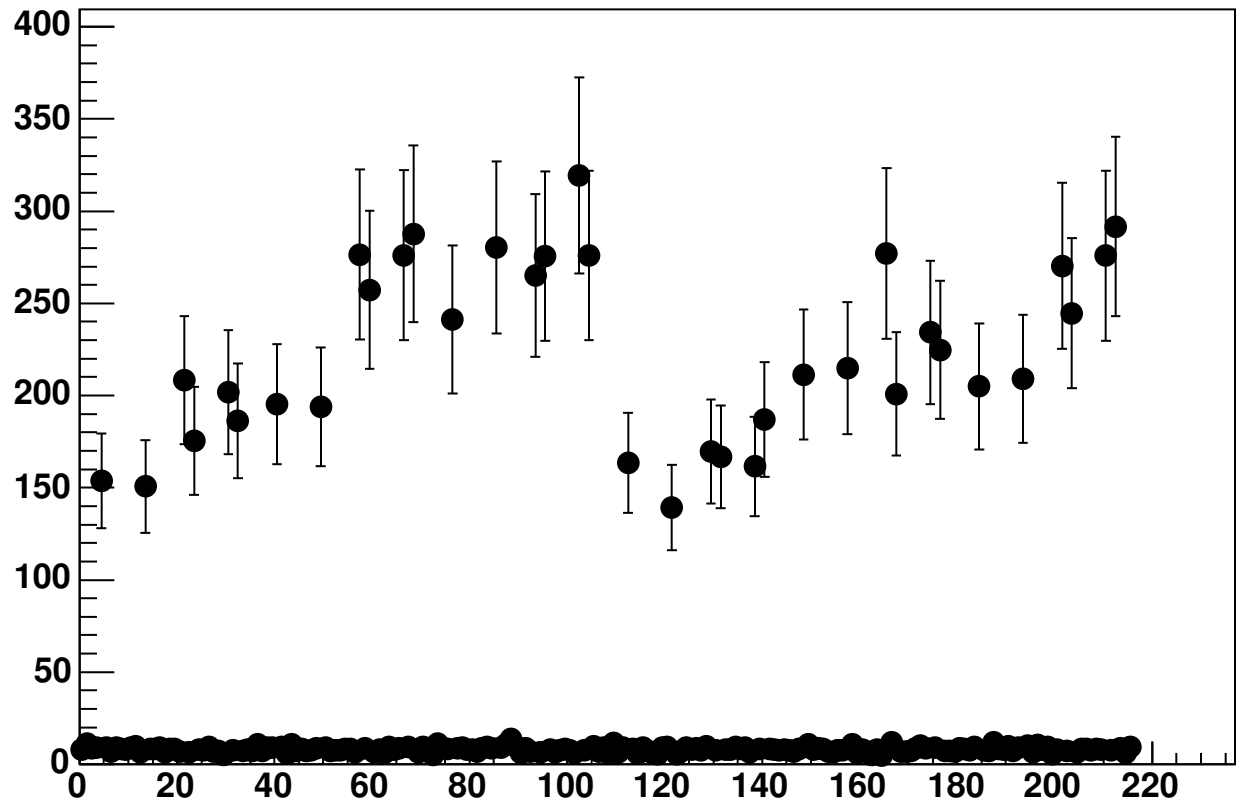
Enable 4, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



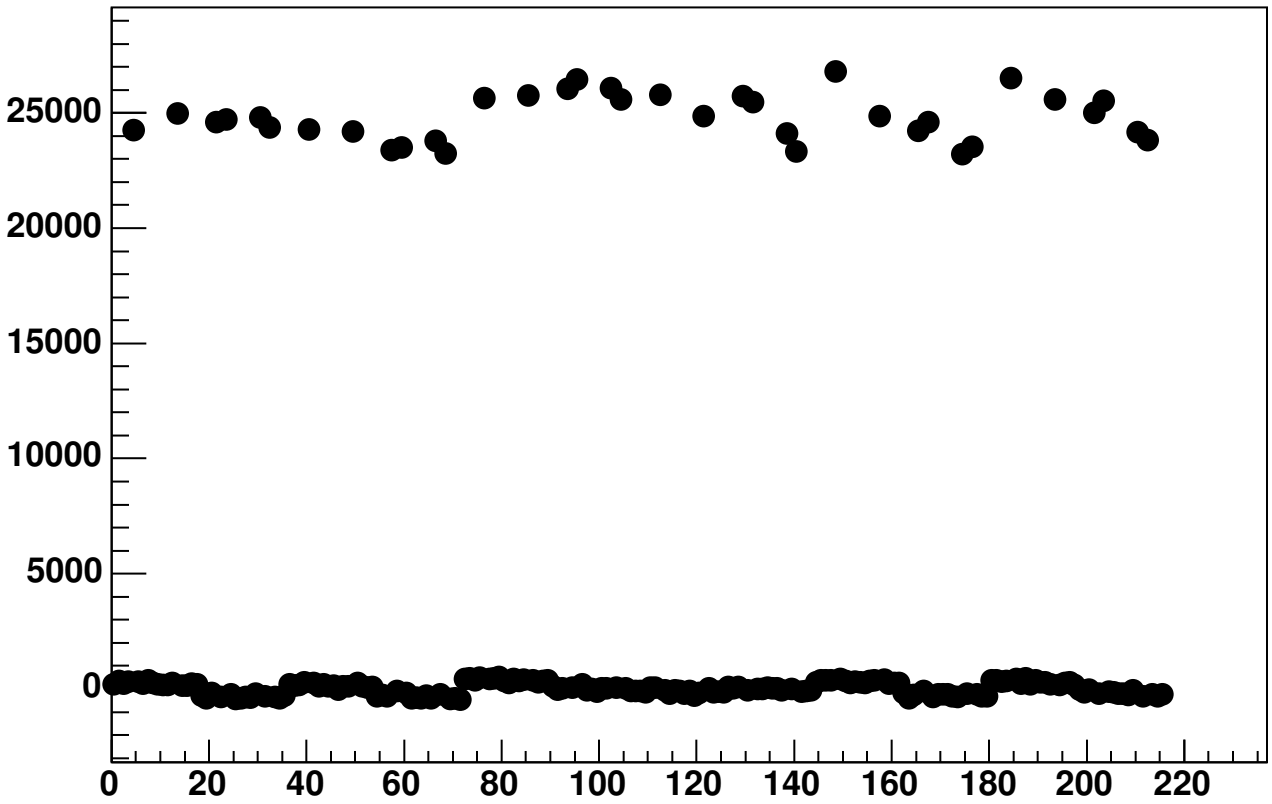
Enable 4, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



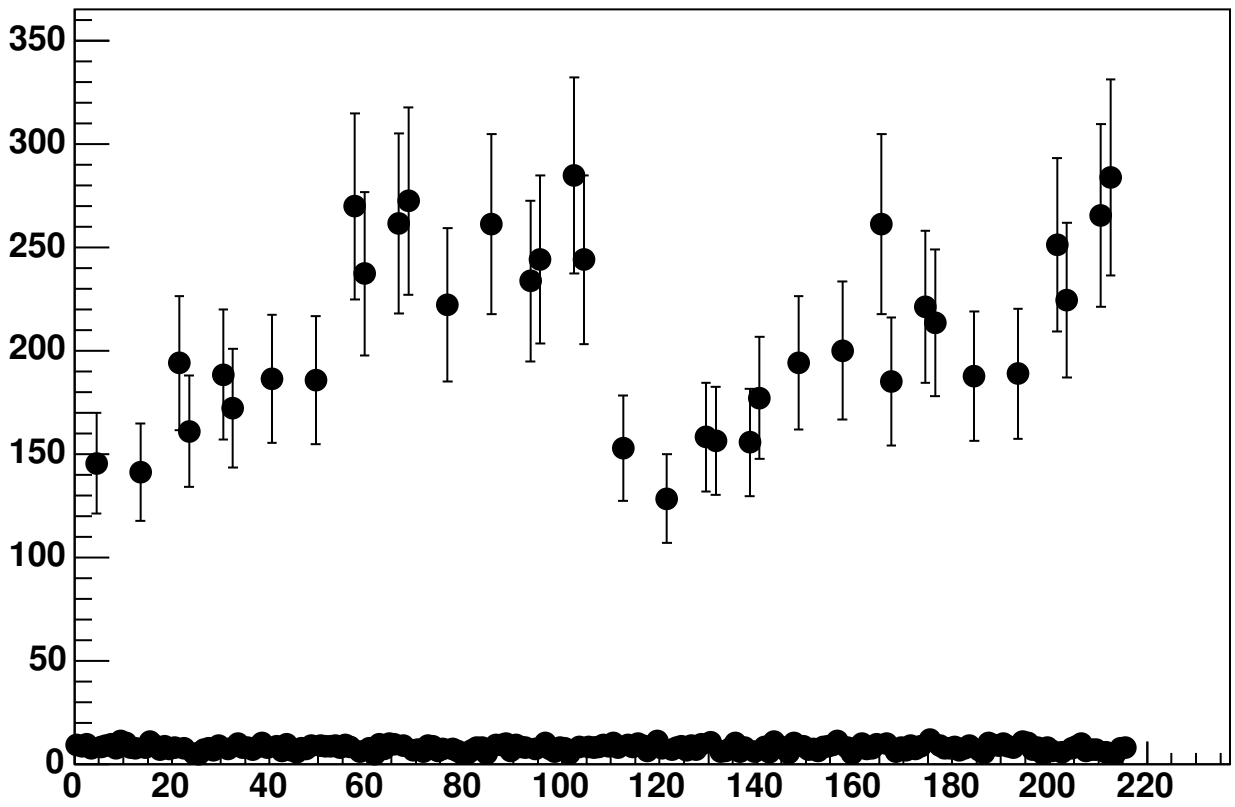
Enable 4, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



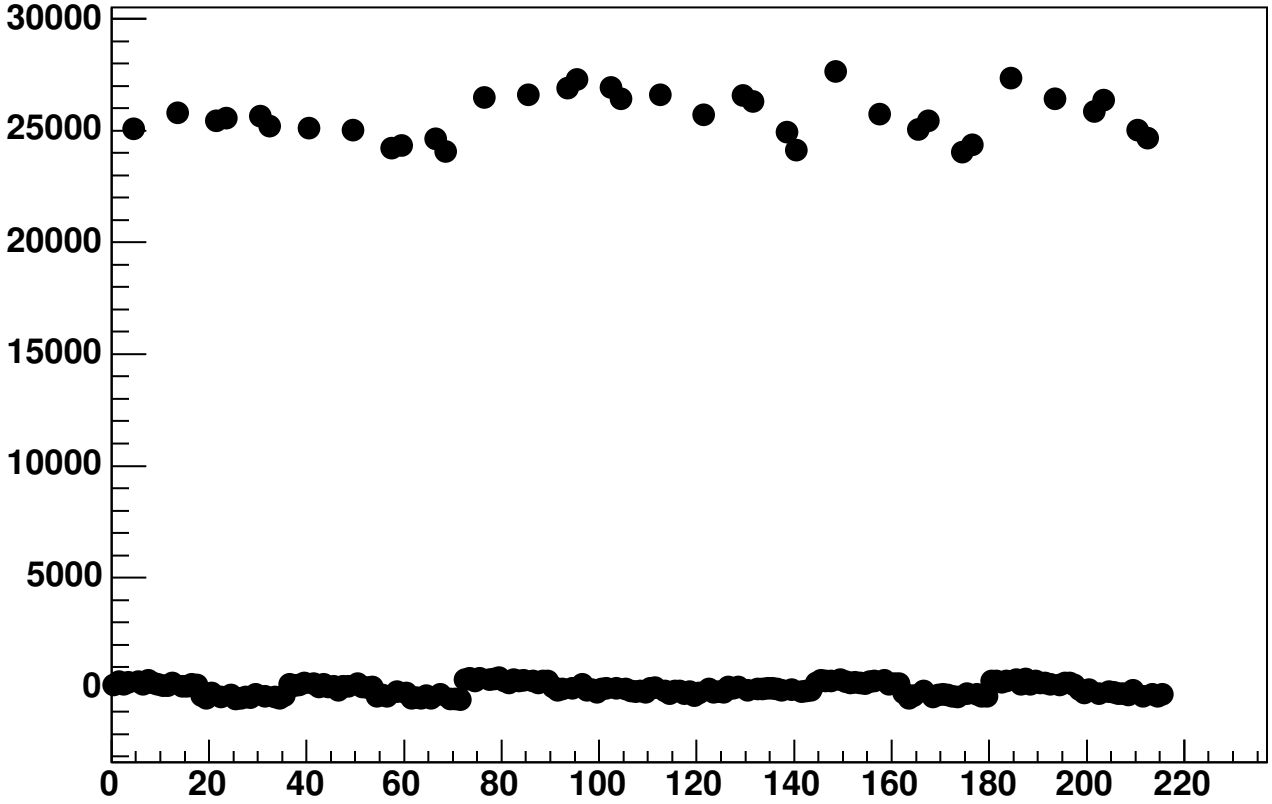
Enable 4, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



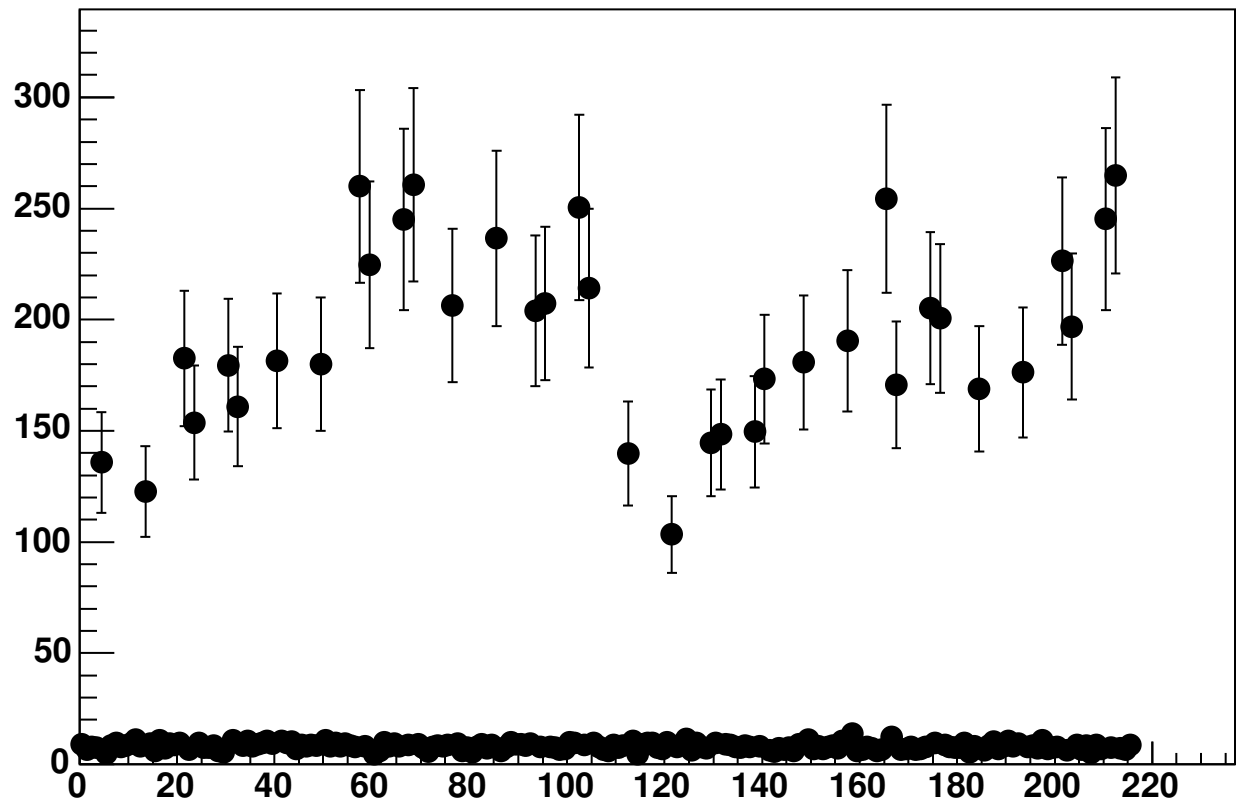
Enable 4, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



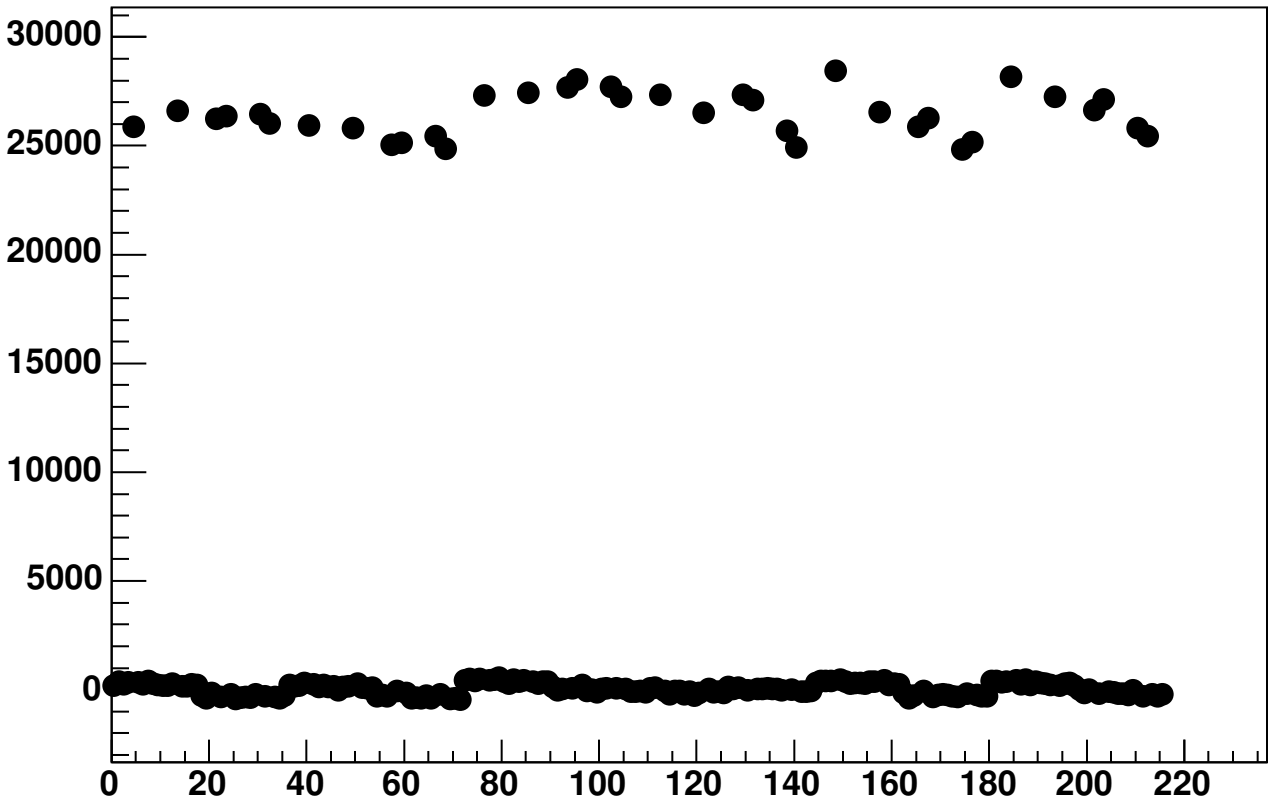
Enable 4, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



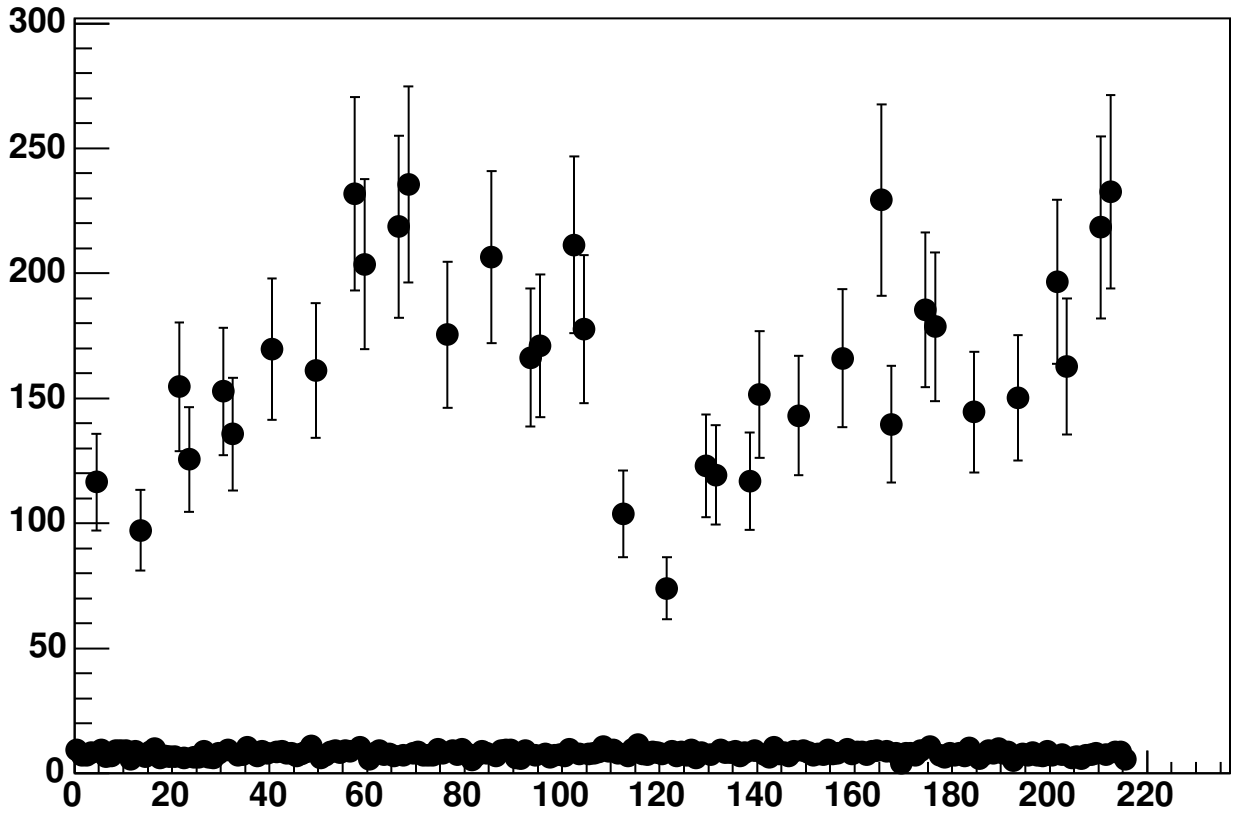
Enable 4, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



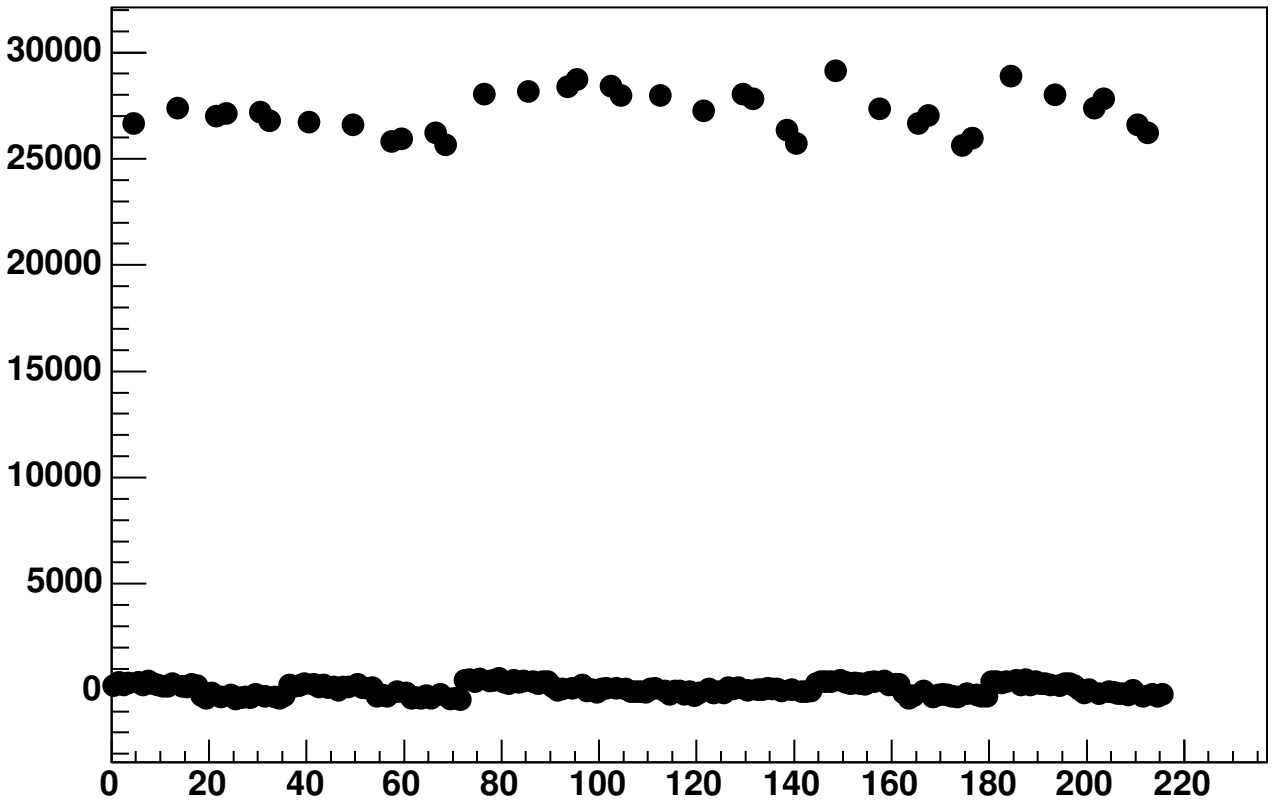
Enable 4, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



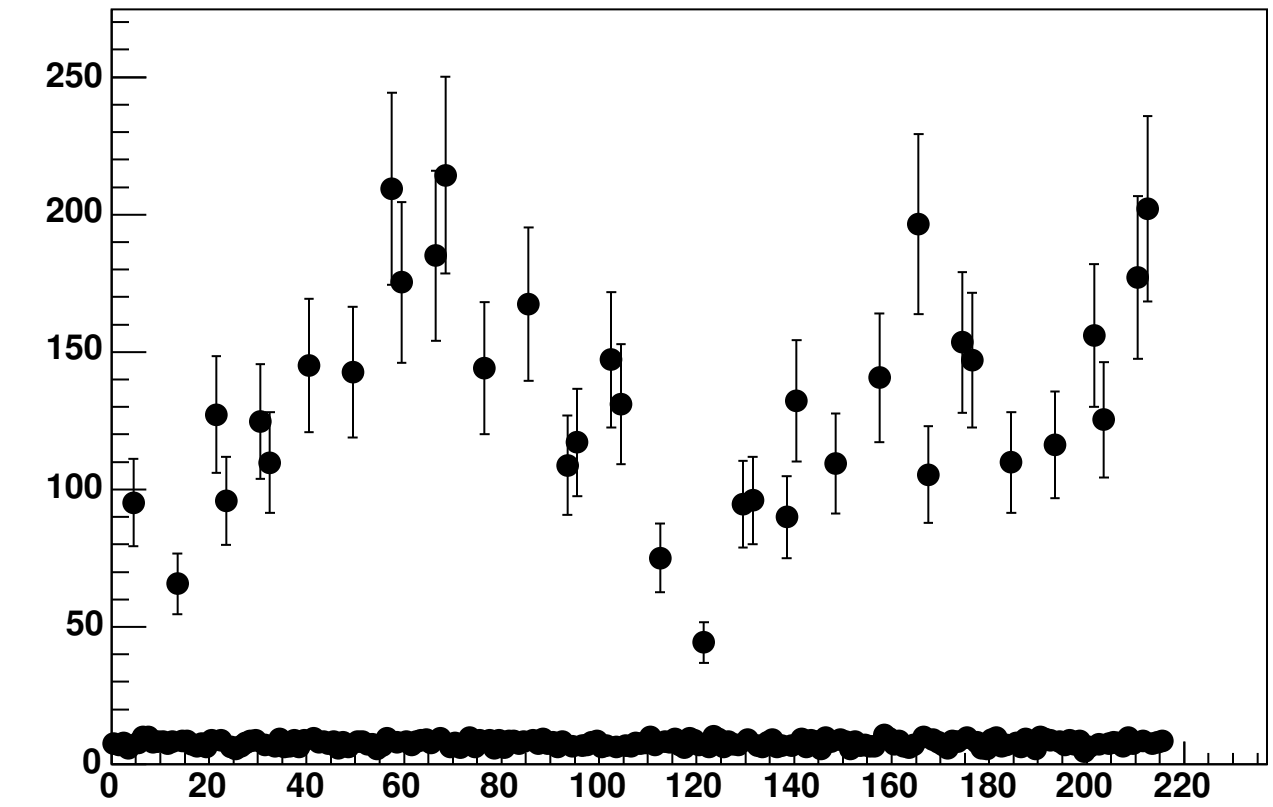
Enable 4, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



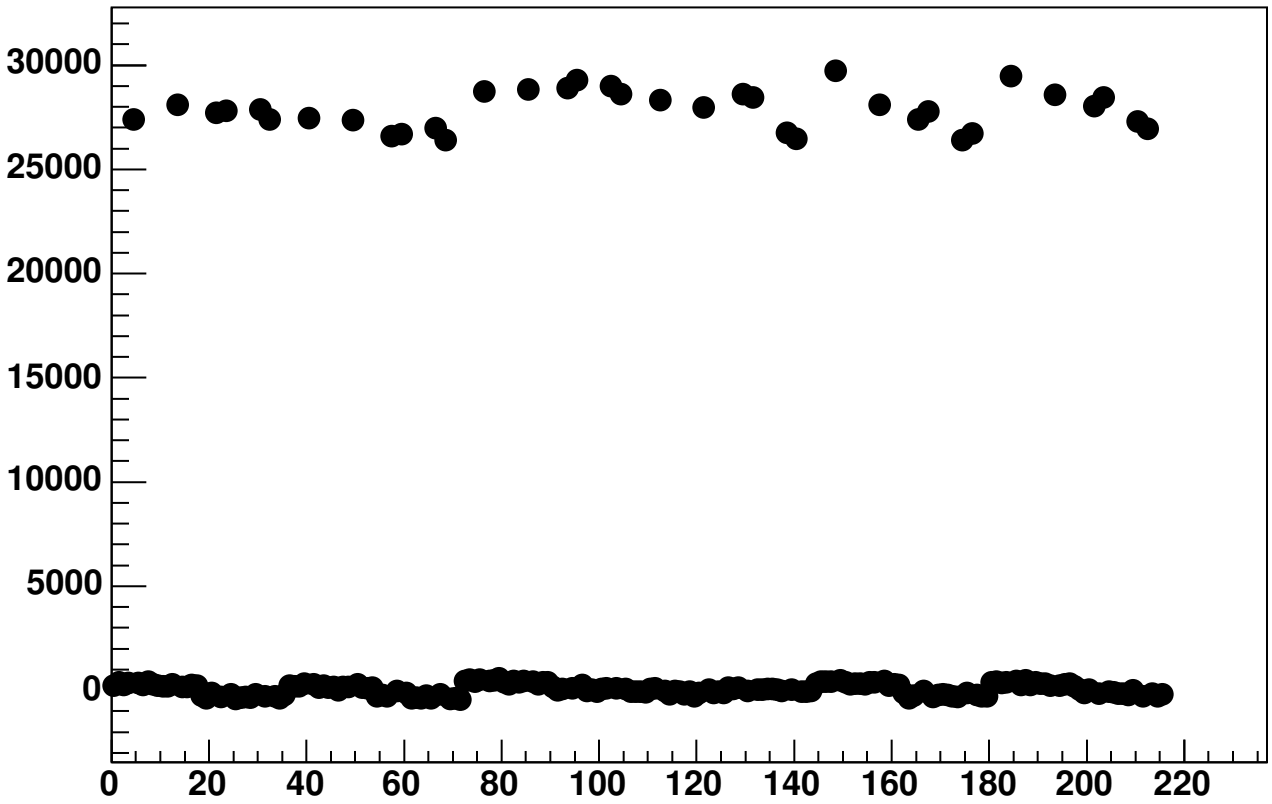
Enable 4, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



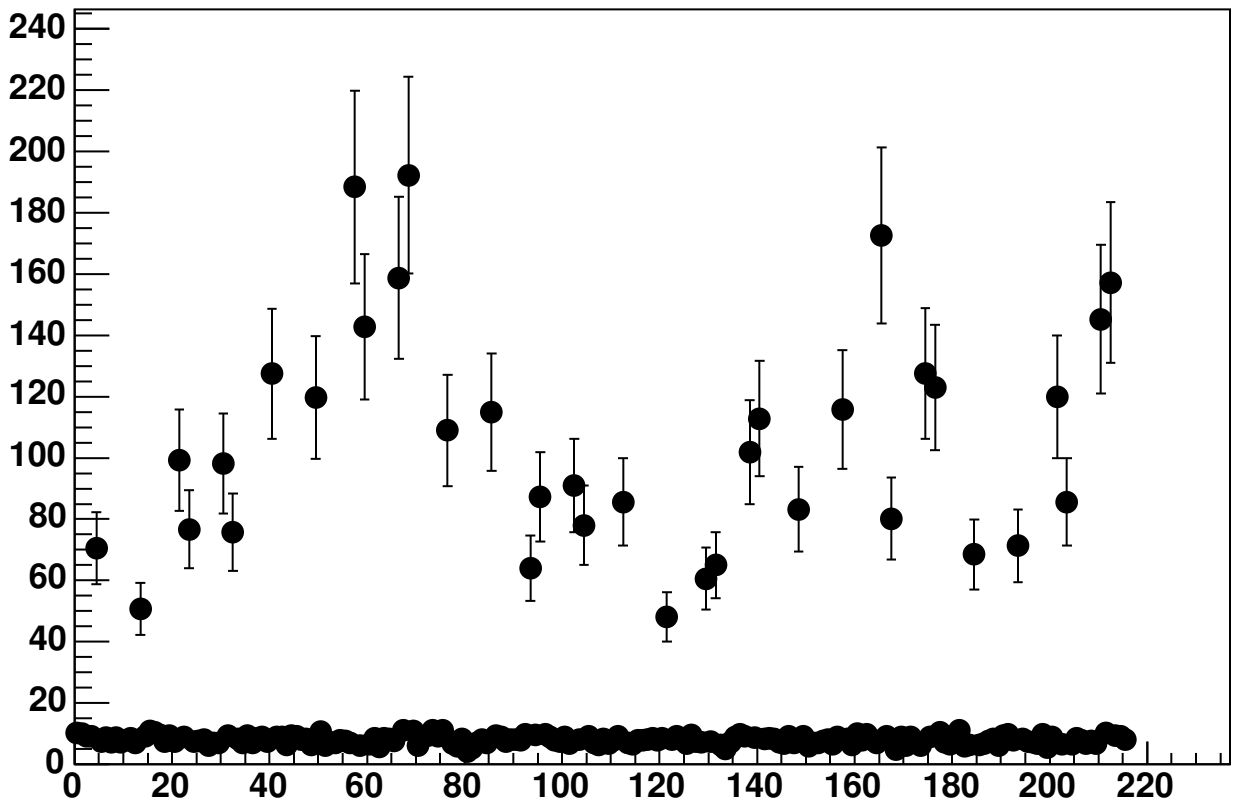
Enable 4, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



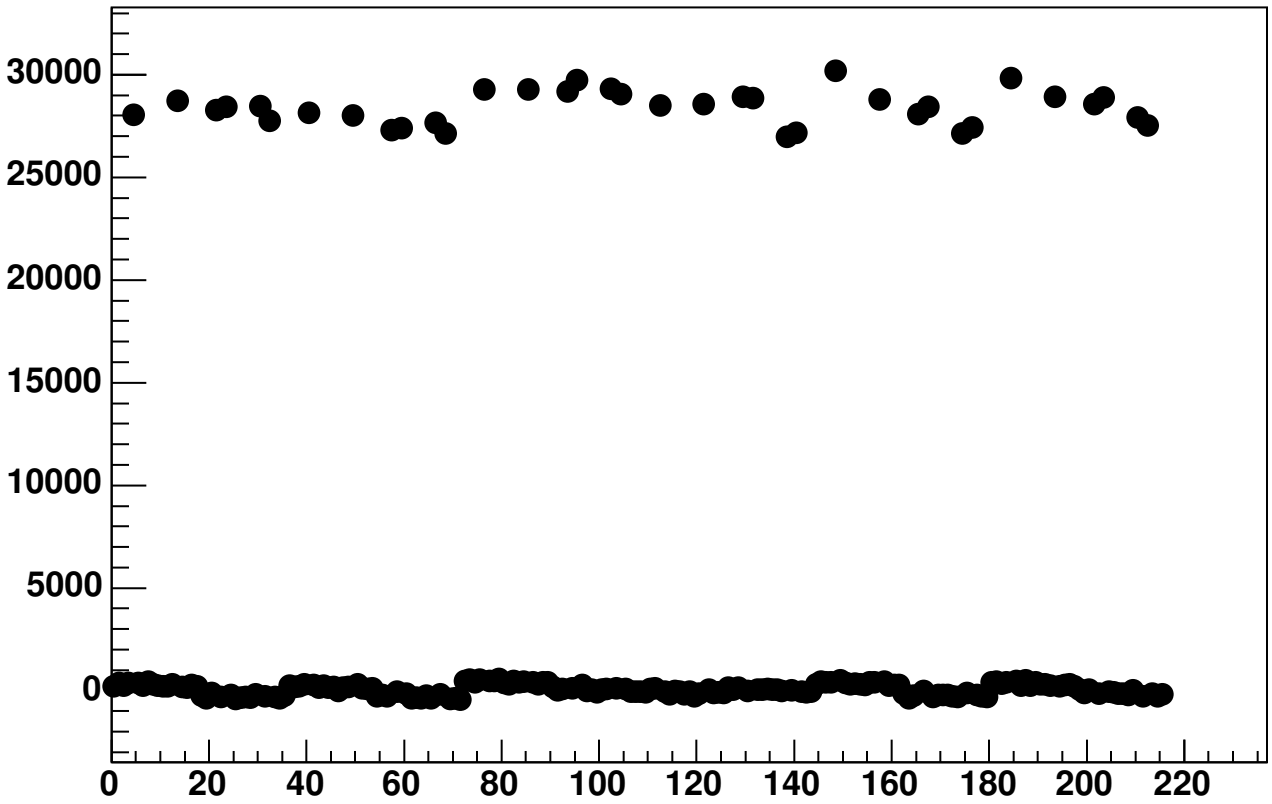
Enable 4, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



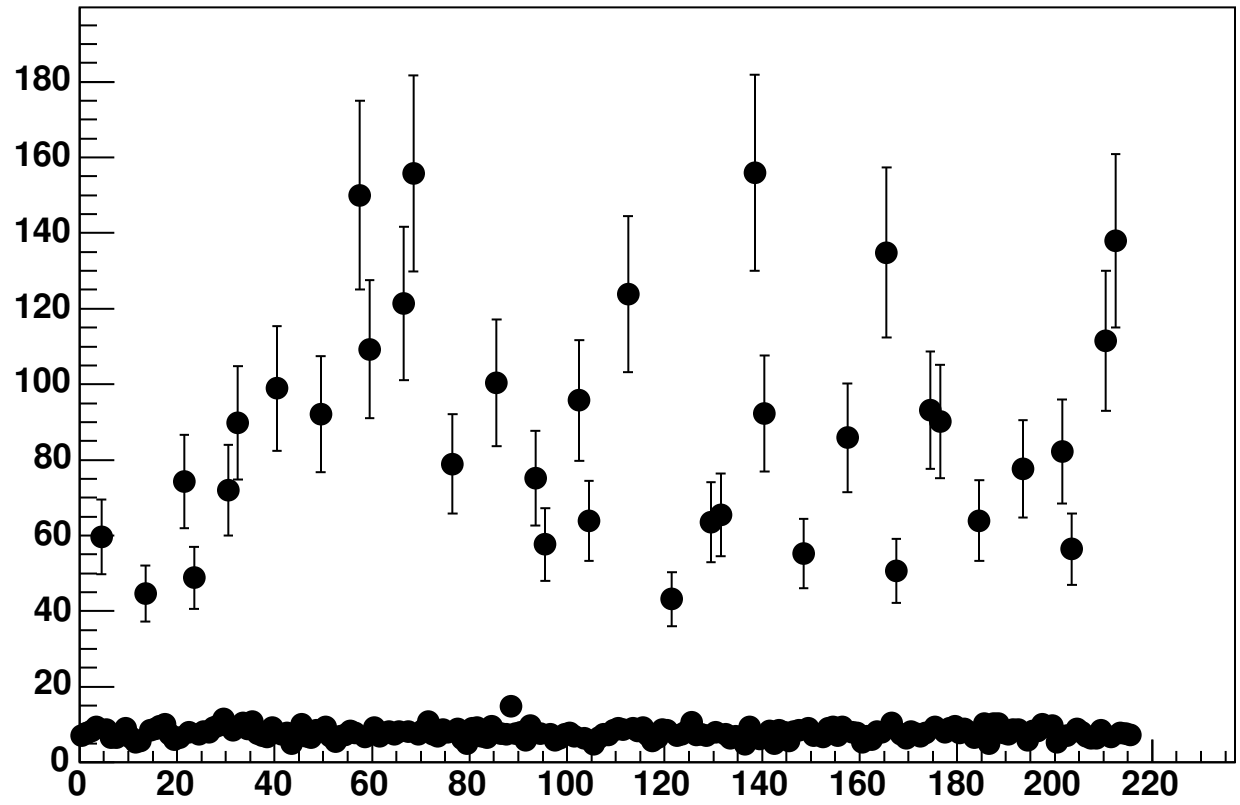
Enable 4, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



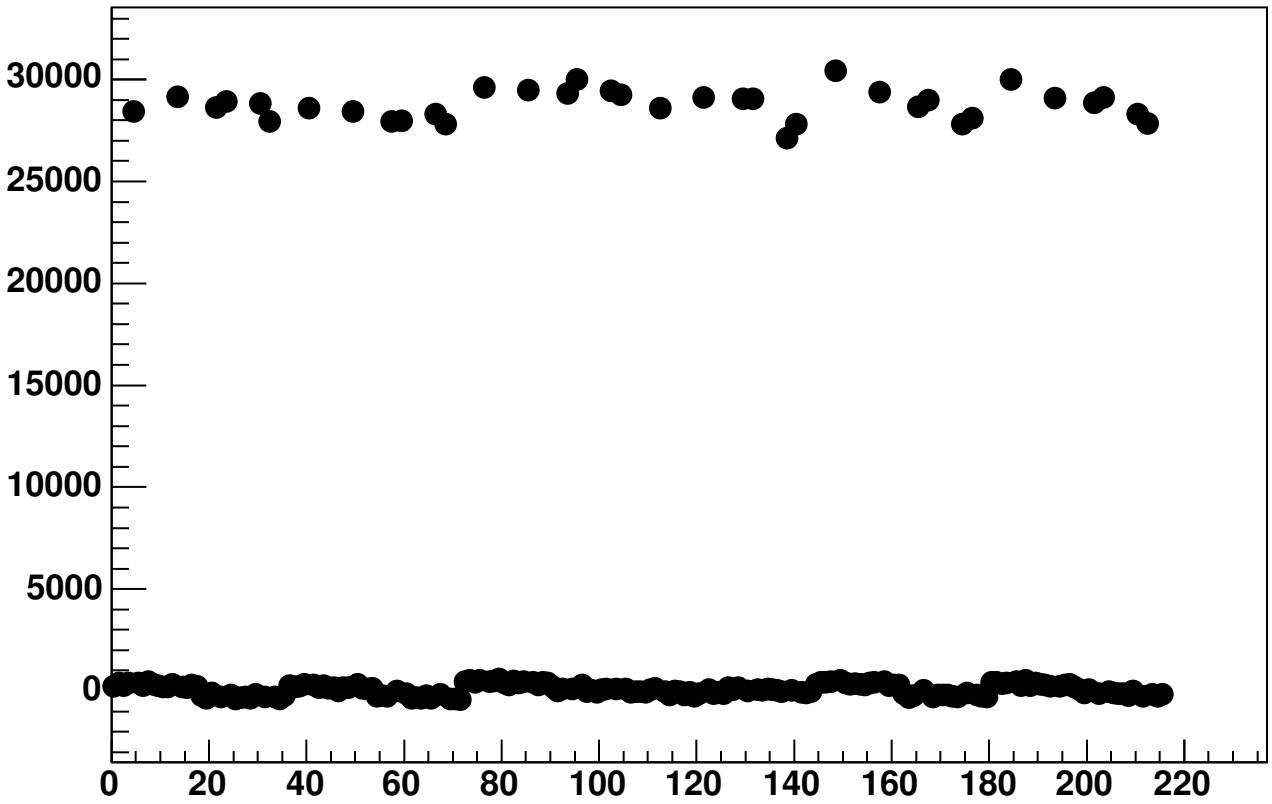
Enable 4, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



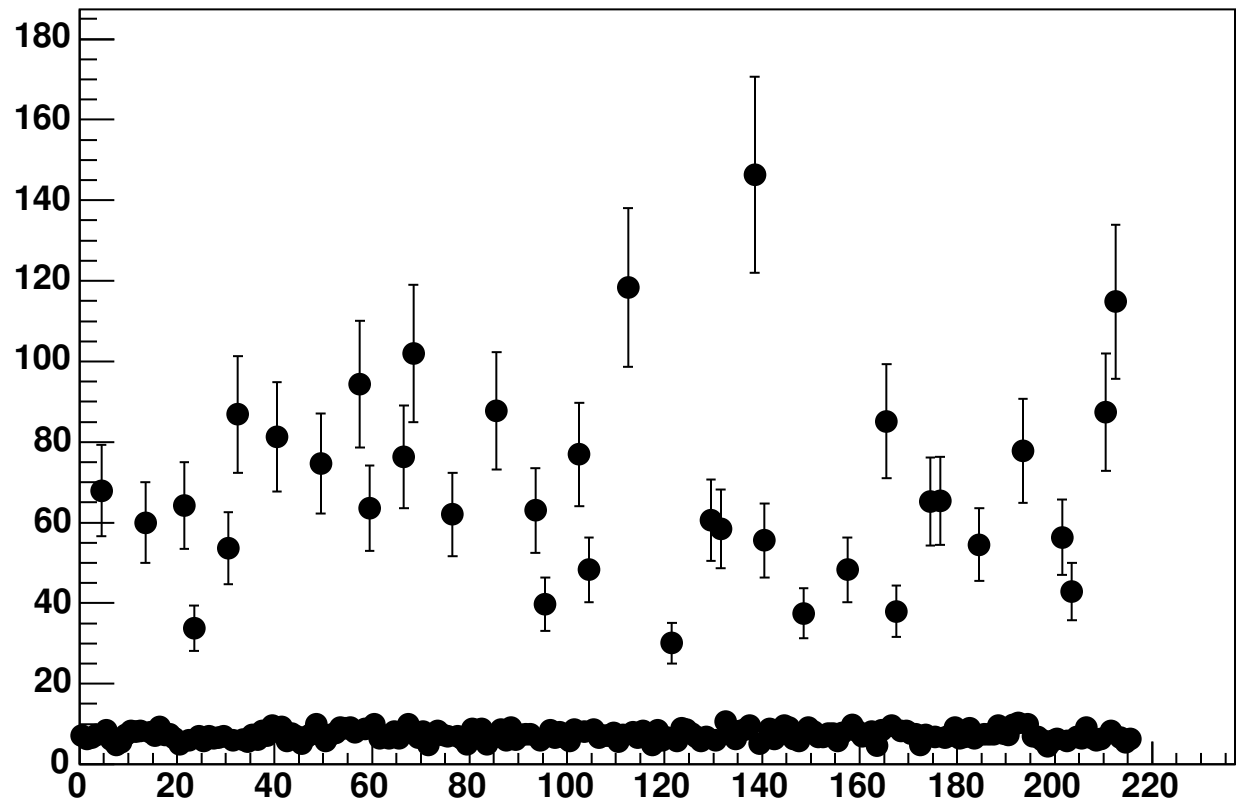
Enable 4, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



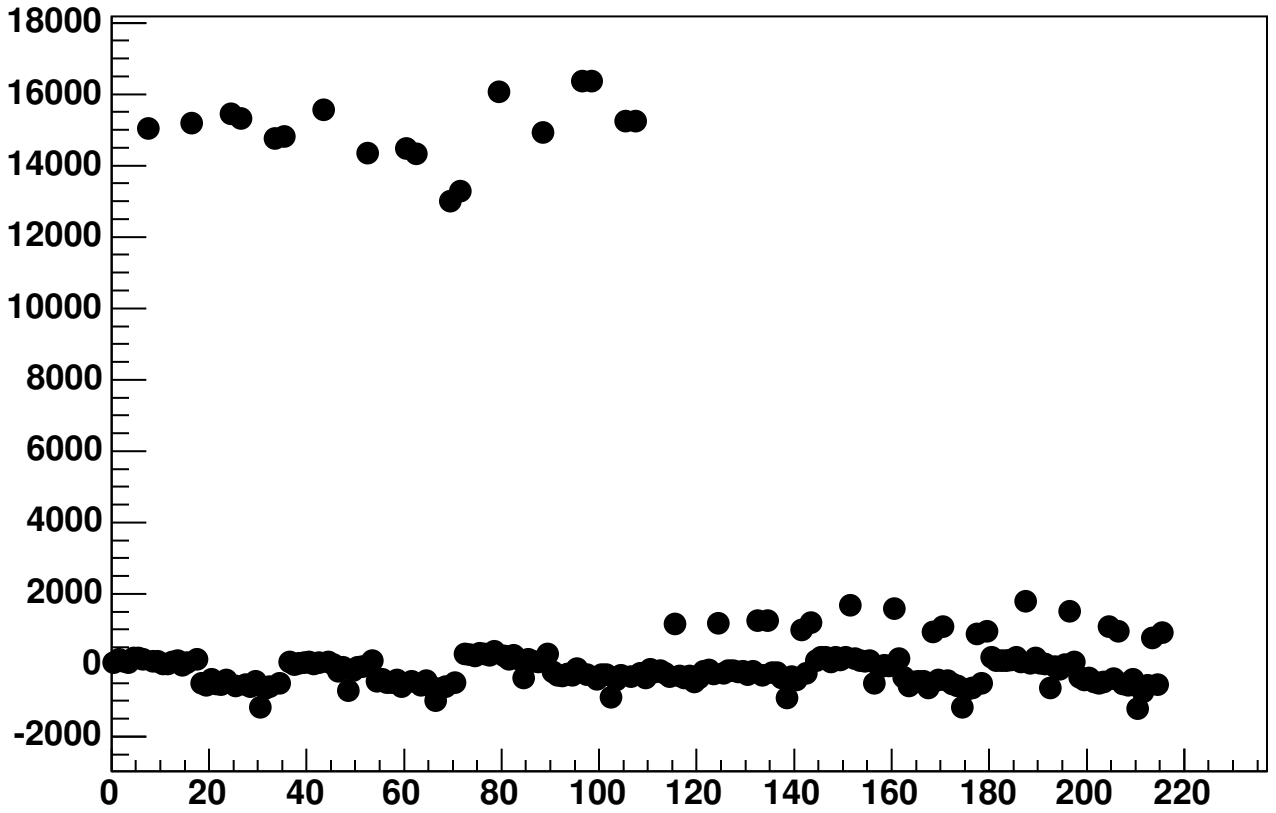
Enable 4, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



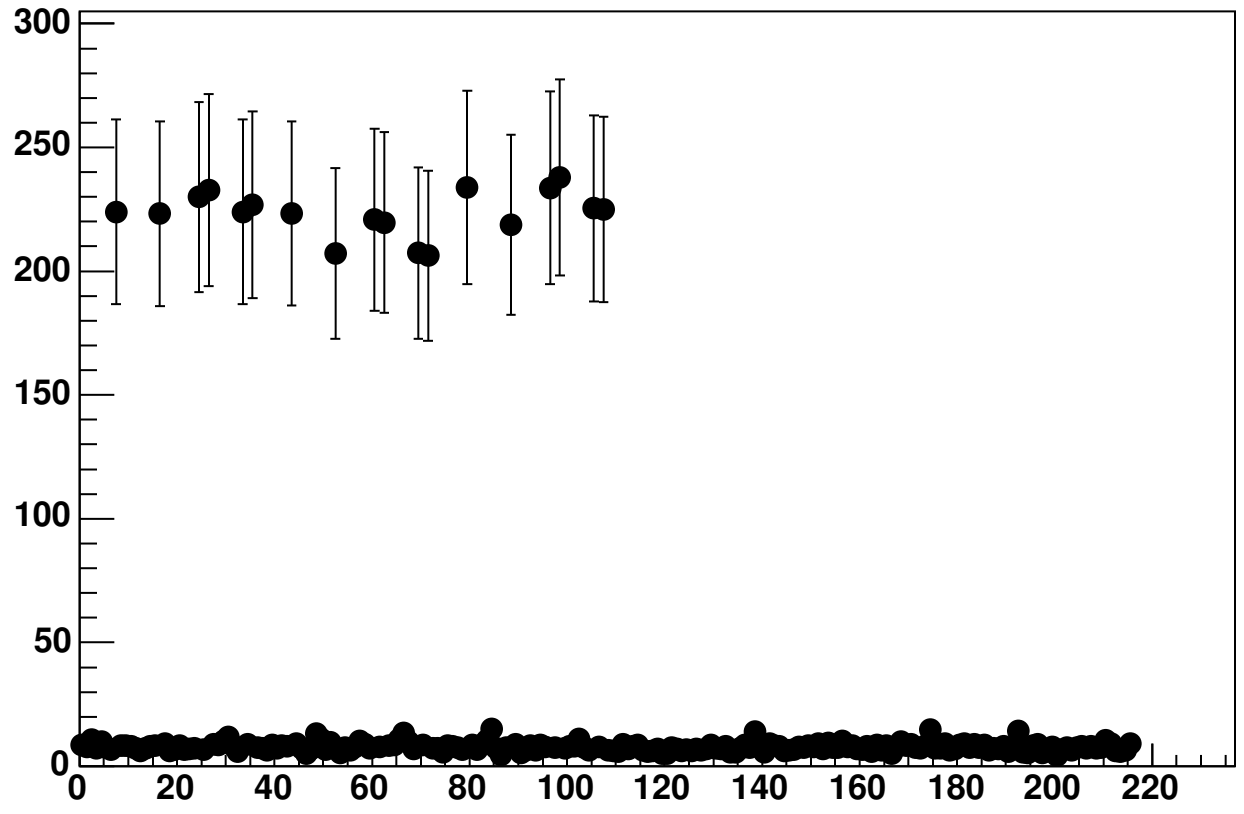
Enable 4, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



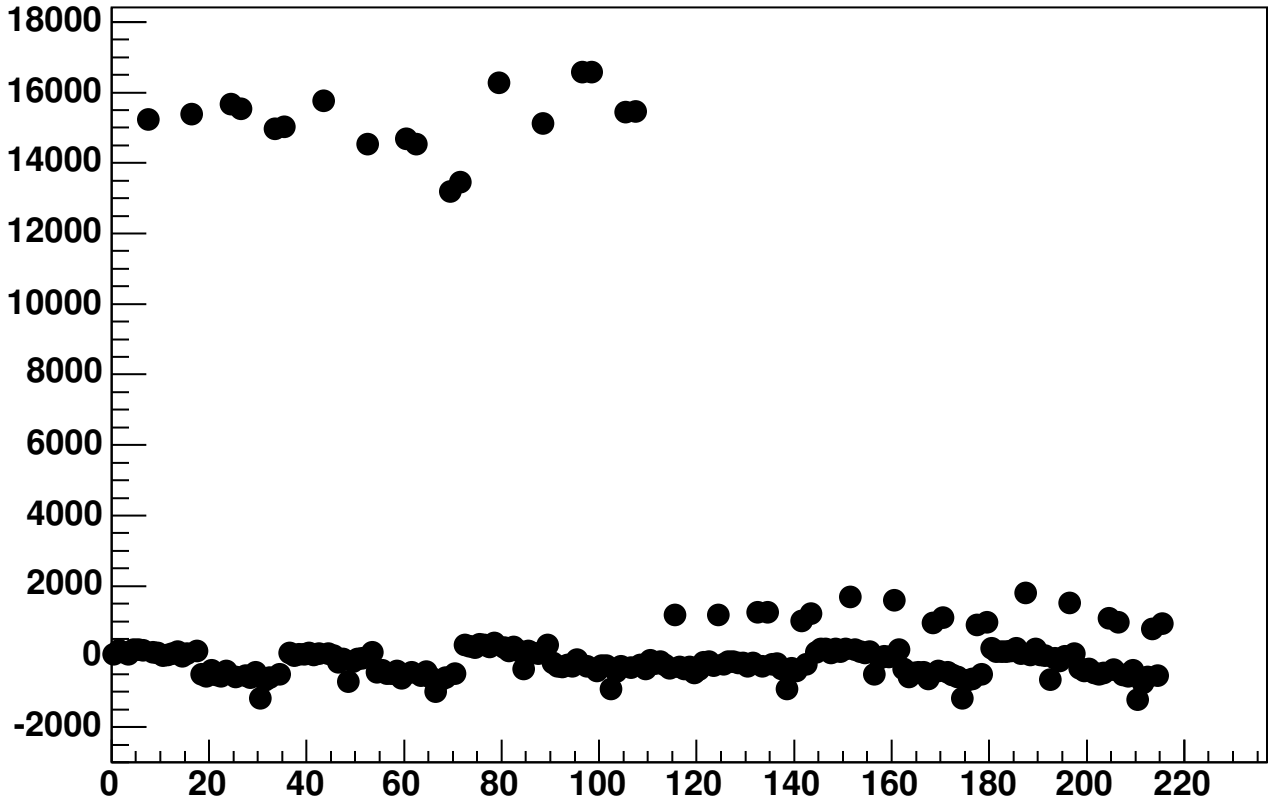
Enable 5, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



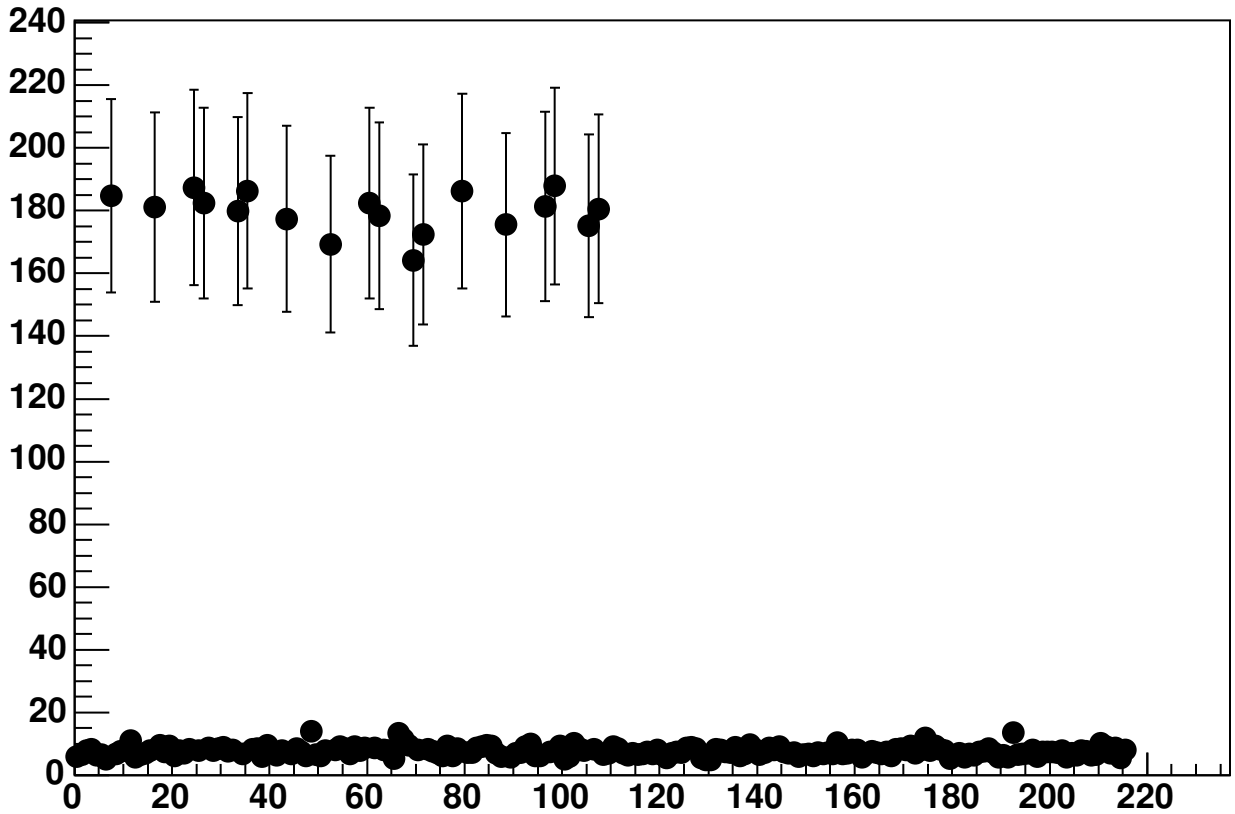
Enable 5, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



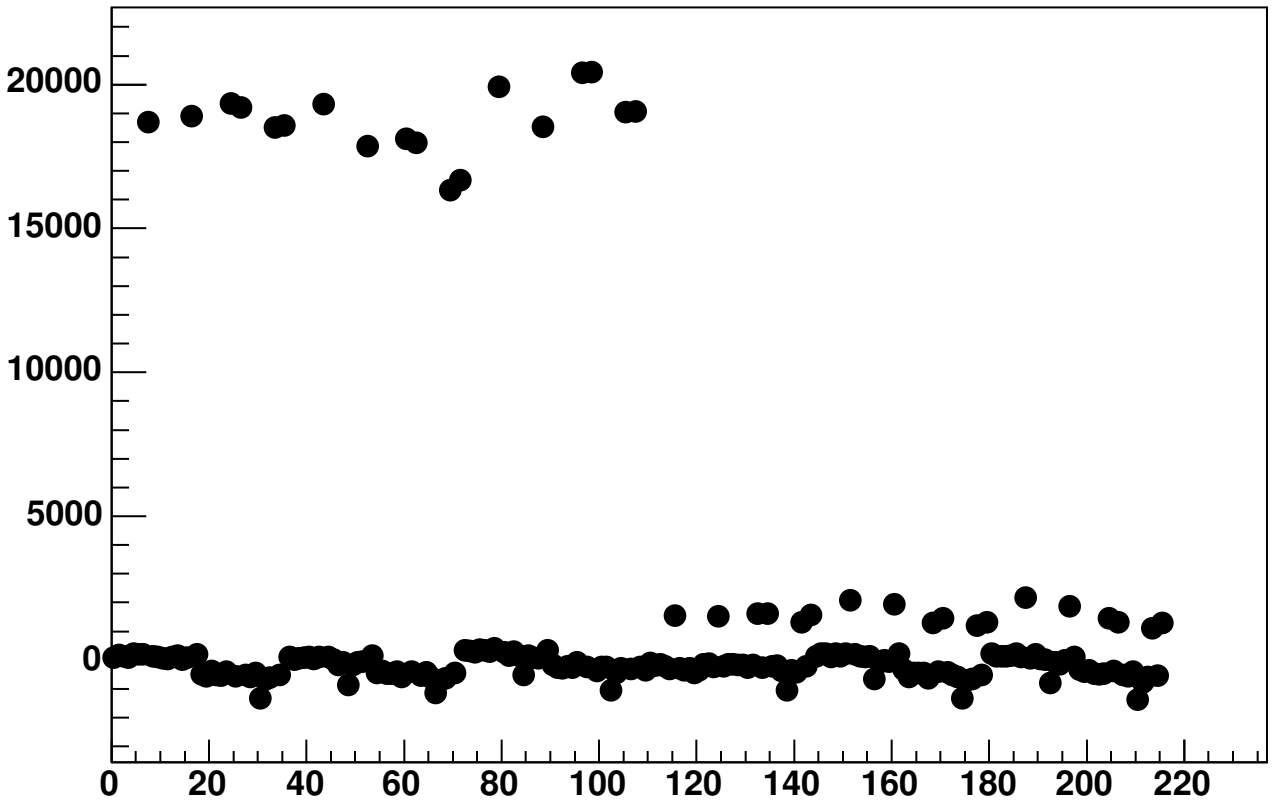
Enable 5, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



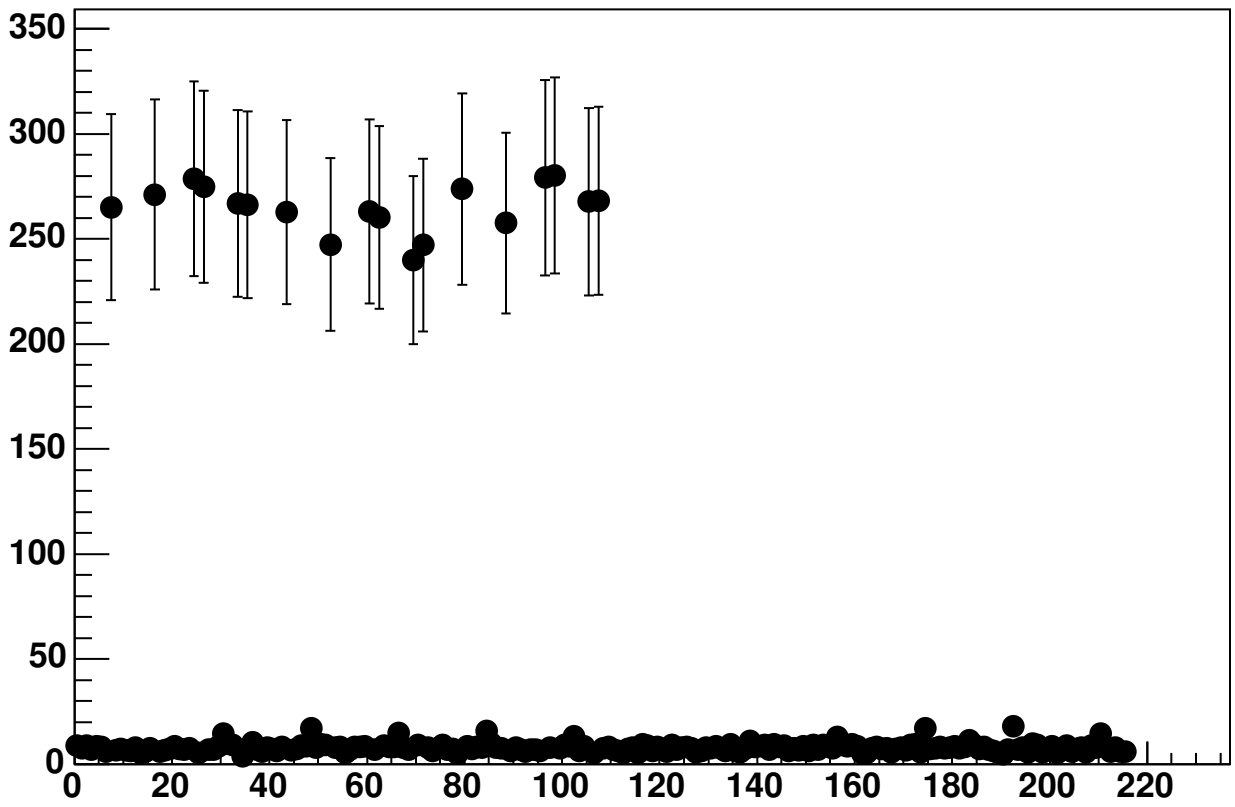
Enable 5, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



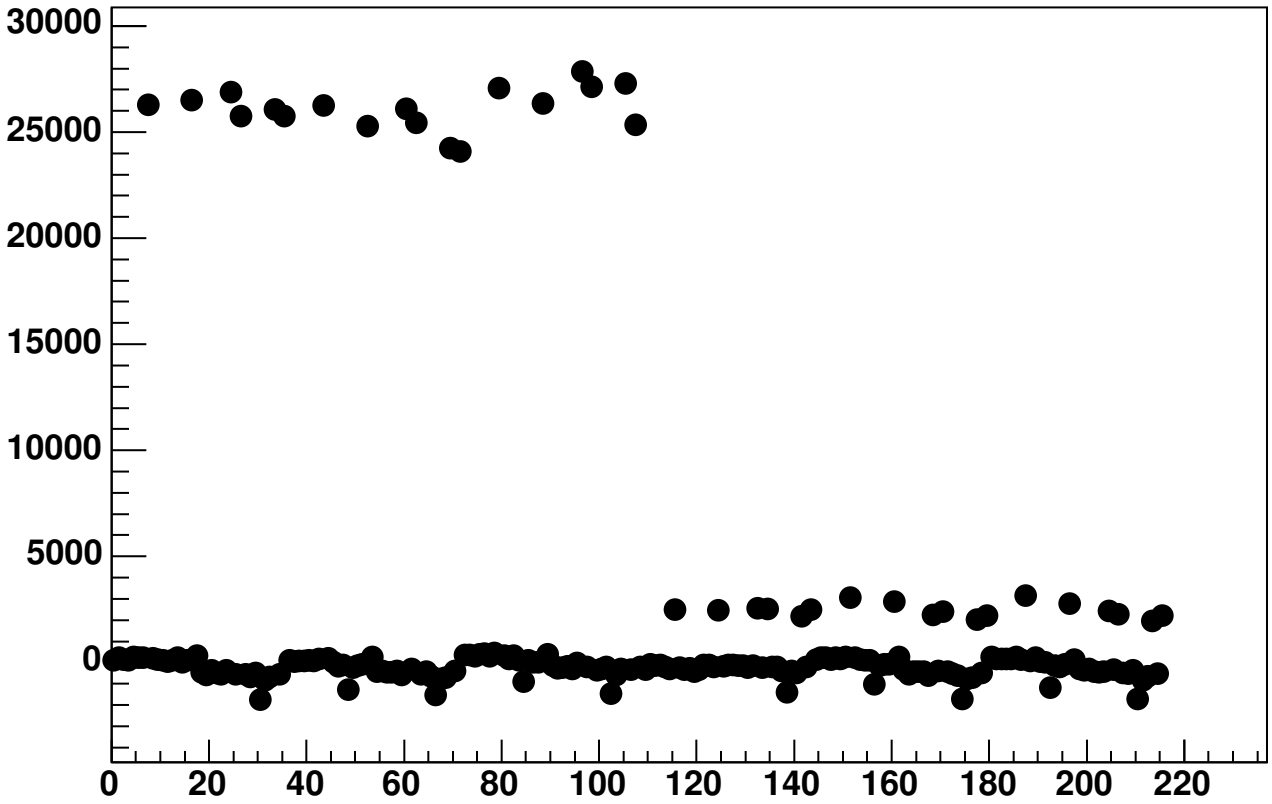
Enable 5, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



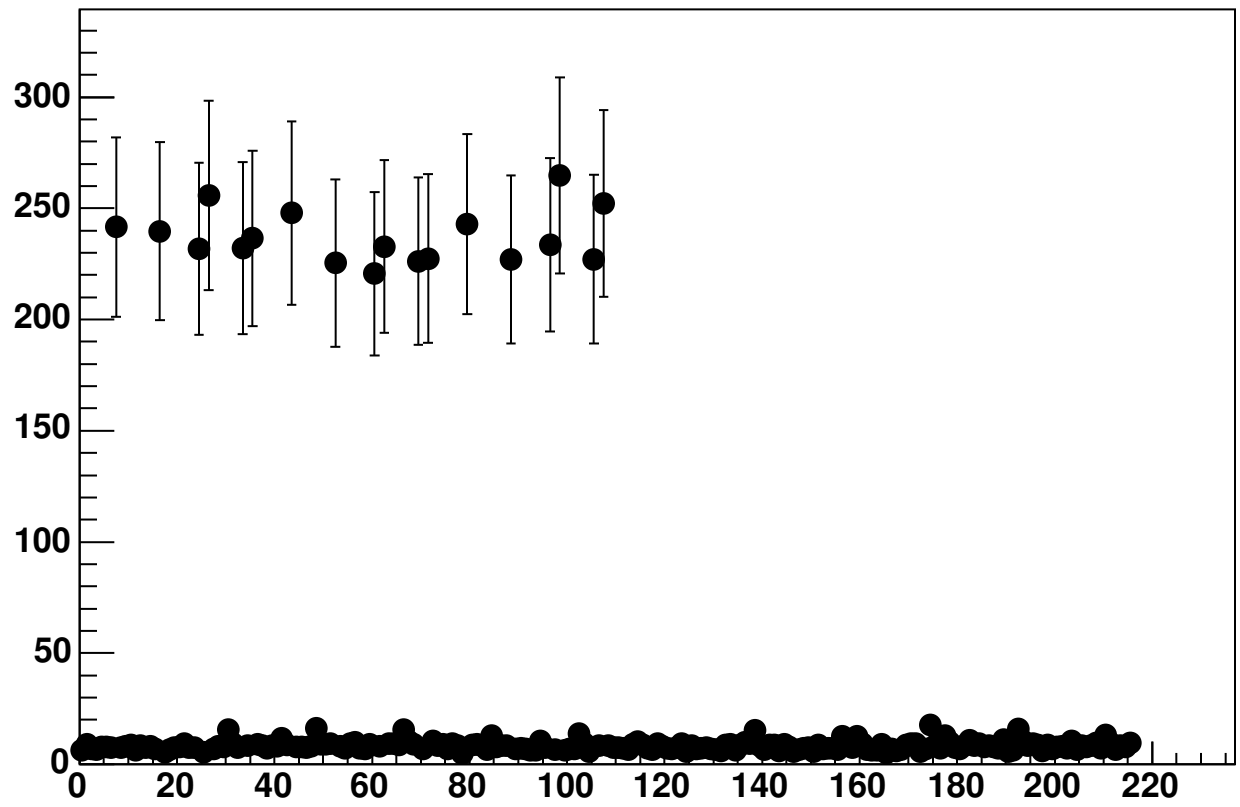
Enable 5, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



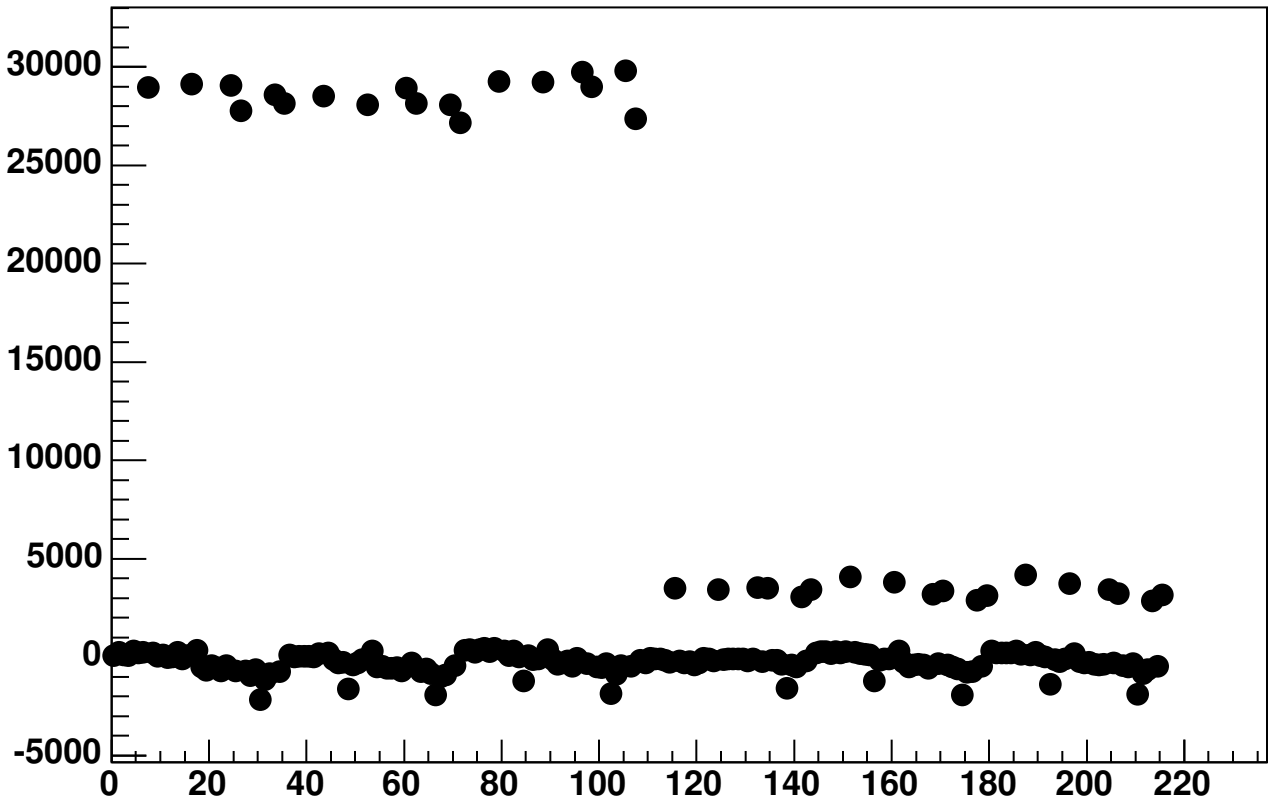
Enable 5, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



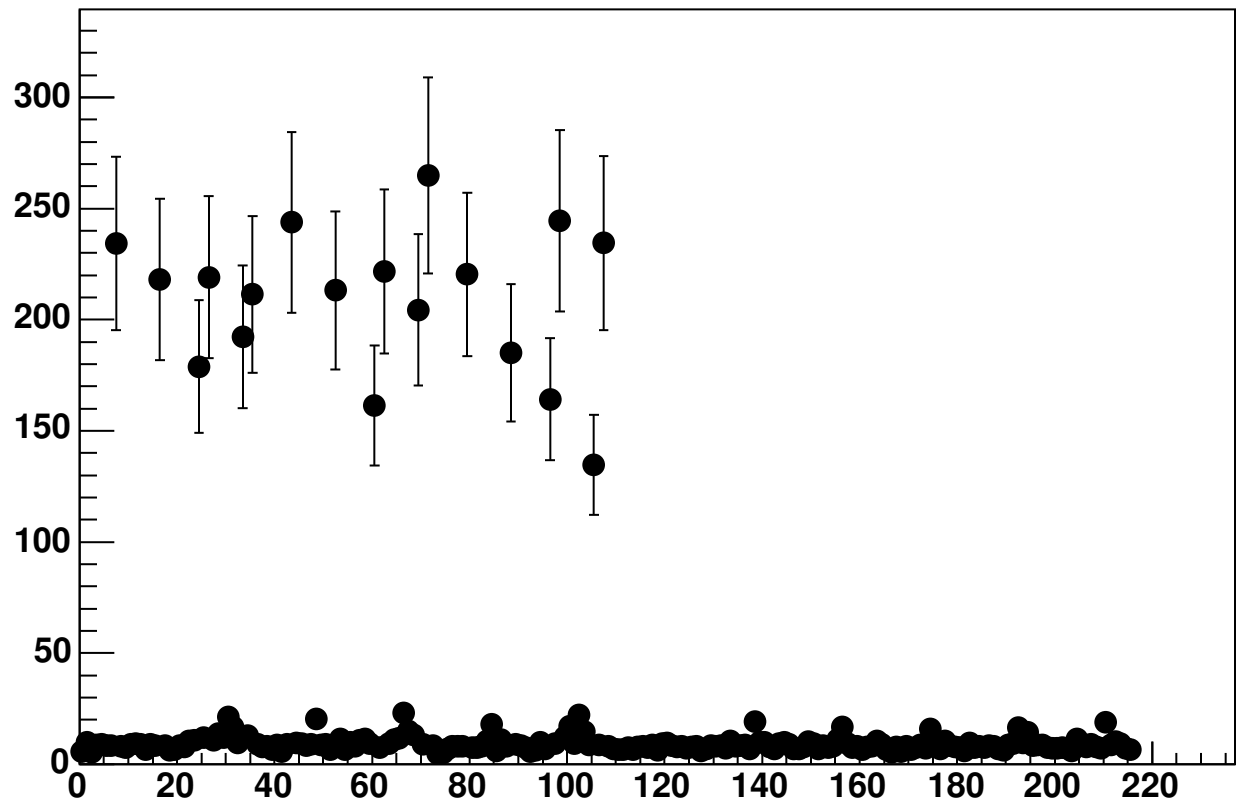
Enable 5, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



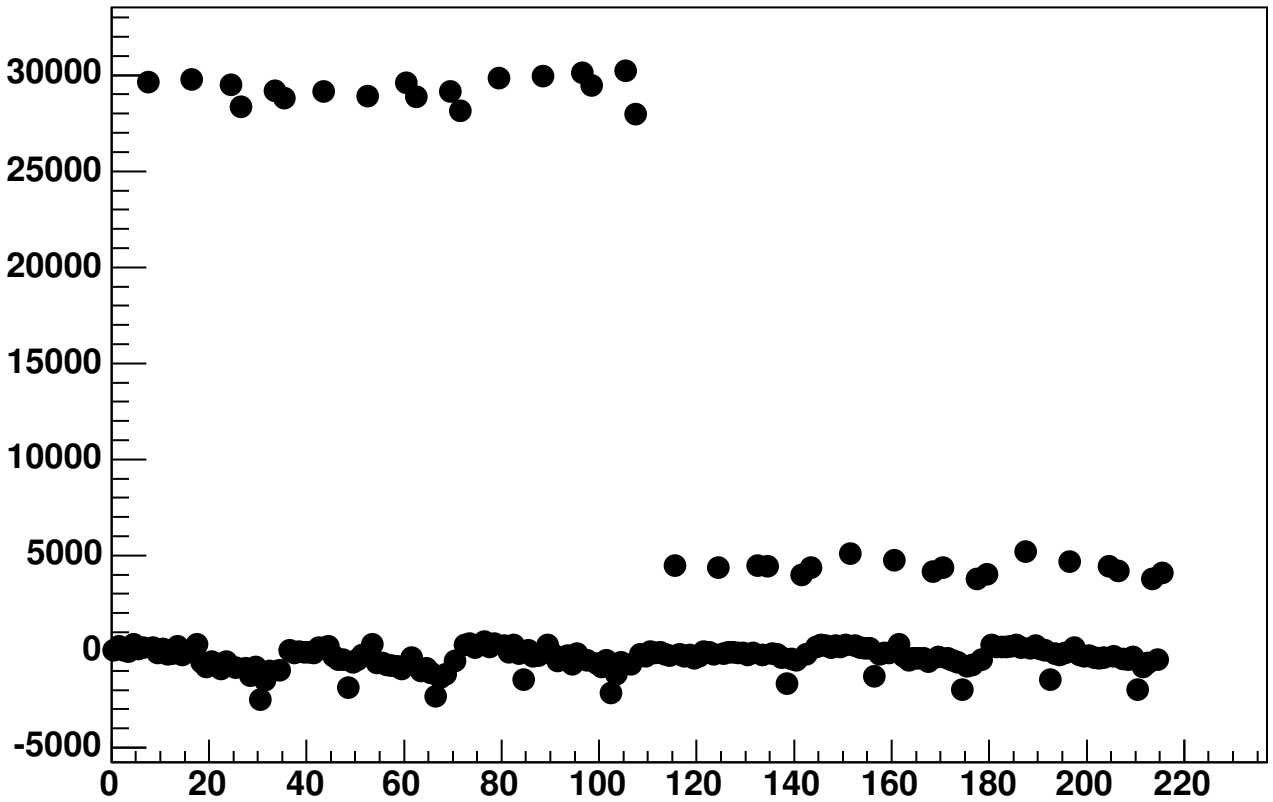
Enable 5, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



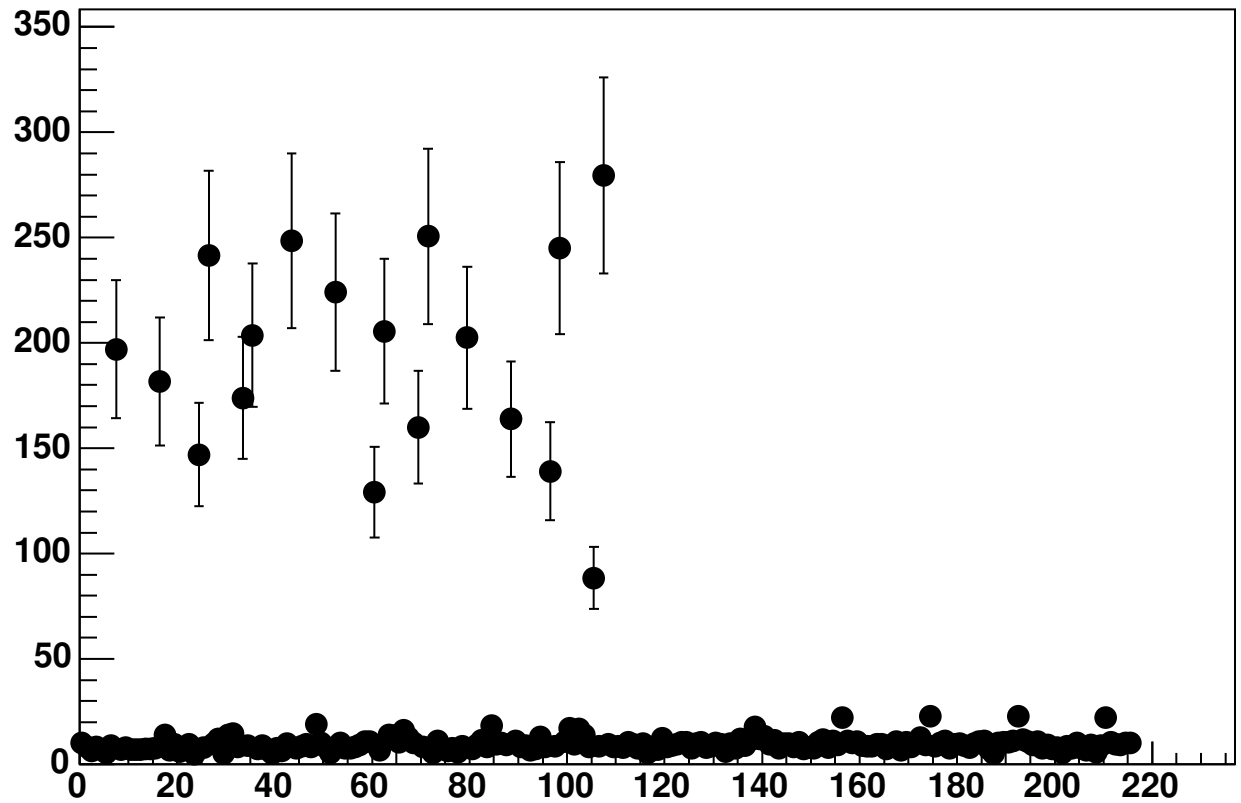
Enable 5, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



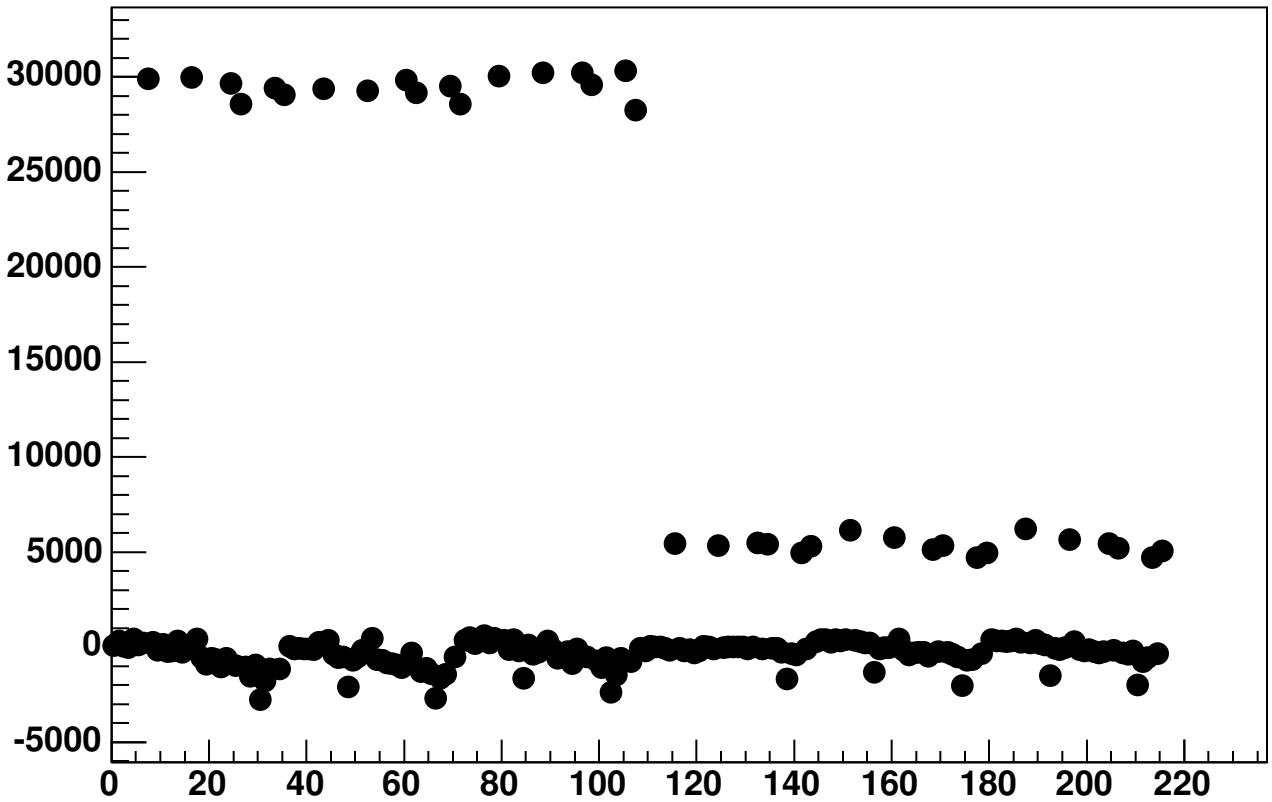
Enable 5, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



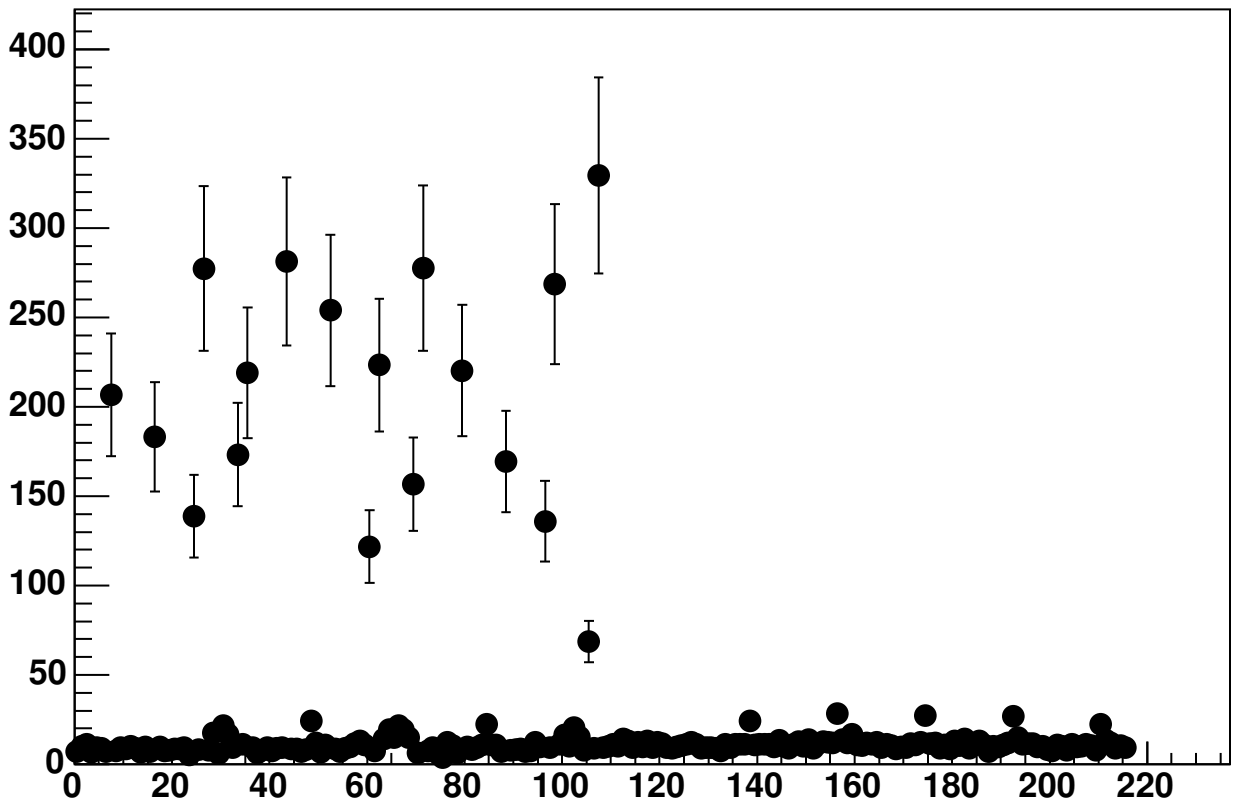
Enable 5, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



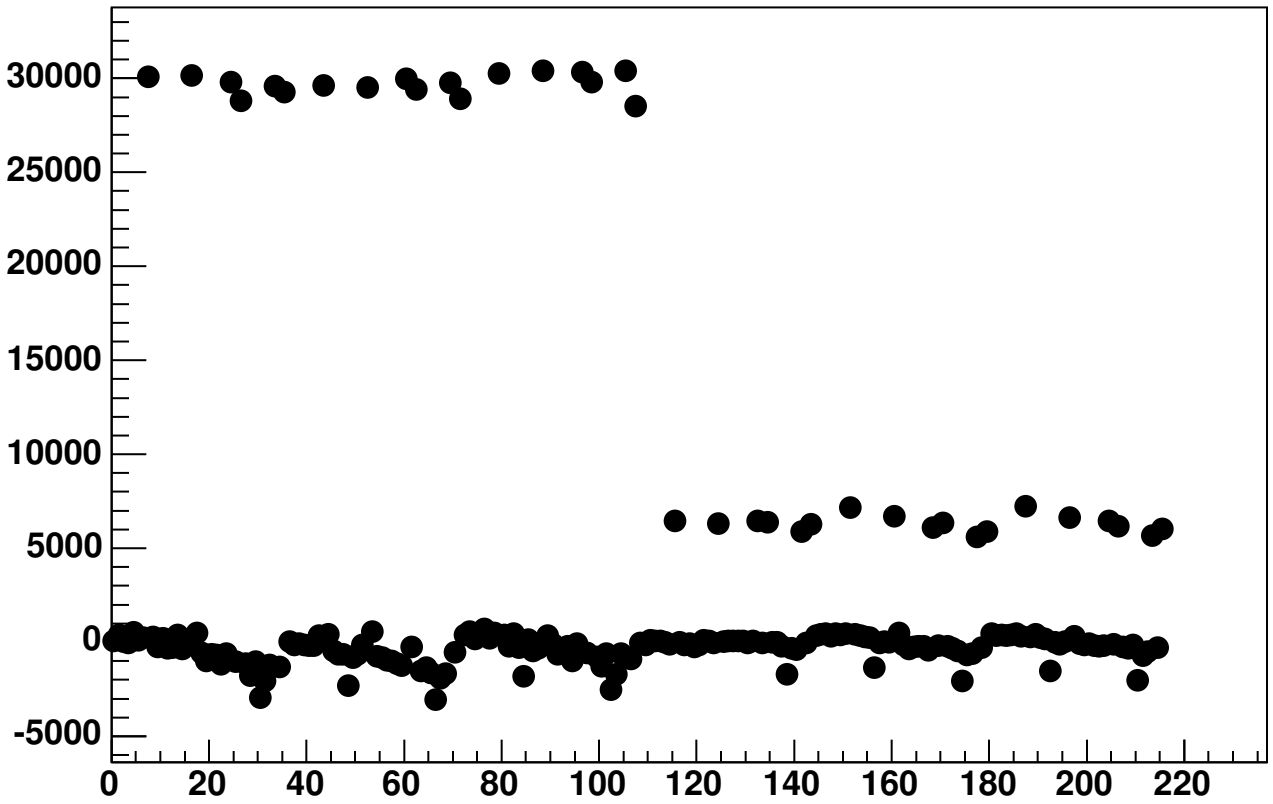
Enable 5, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



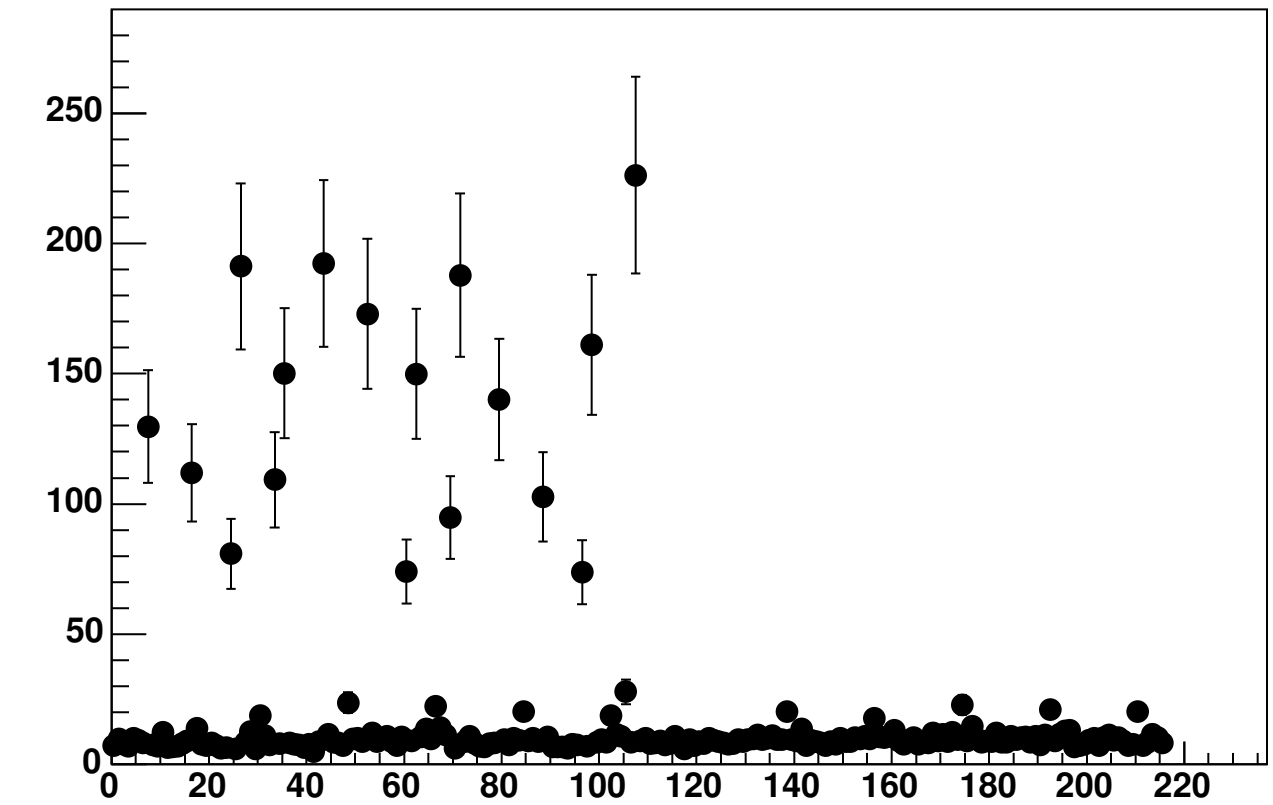
Enable 5, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



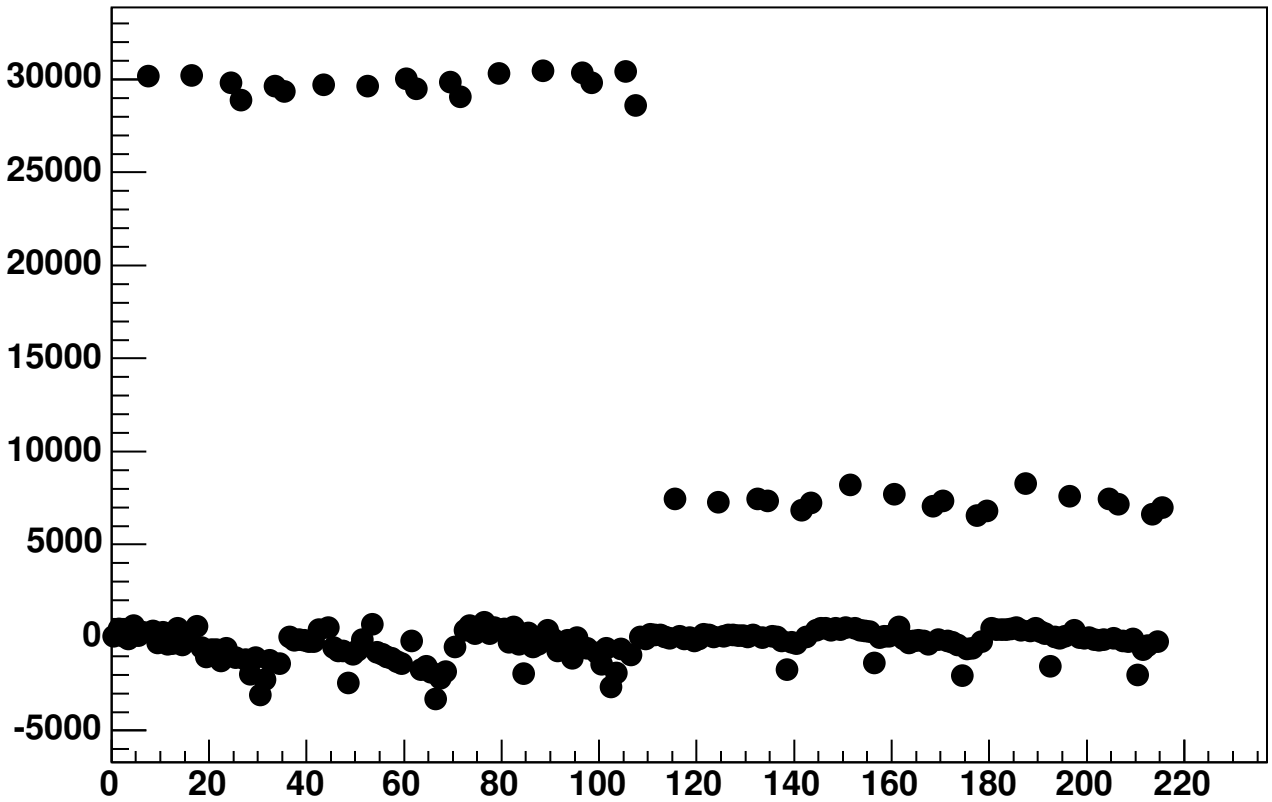
Enable 5, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



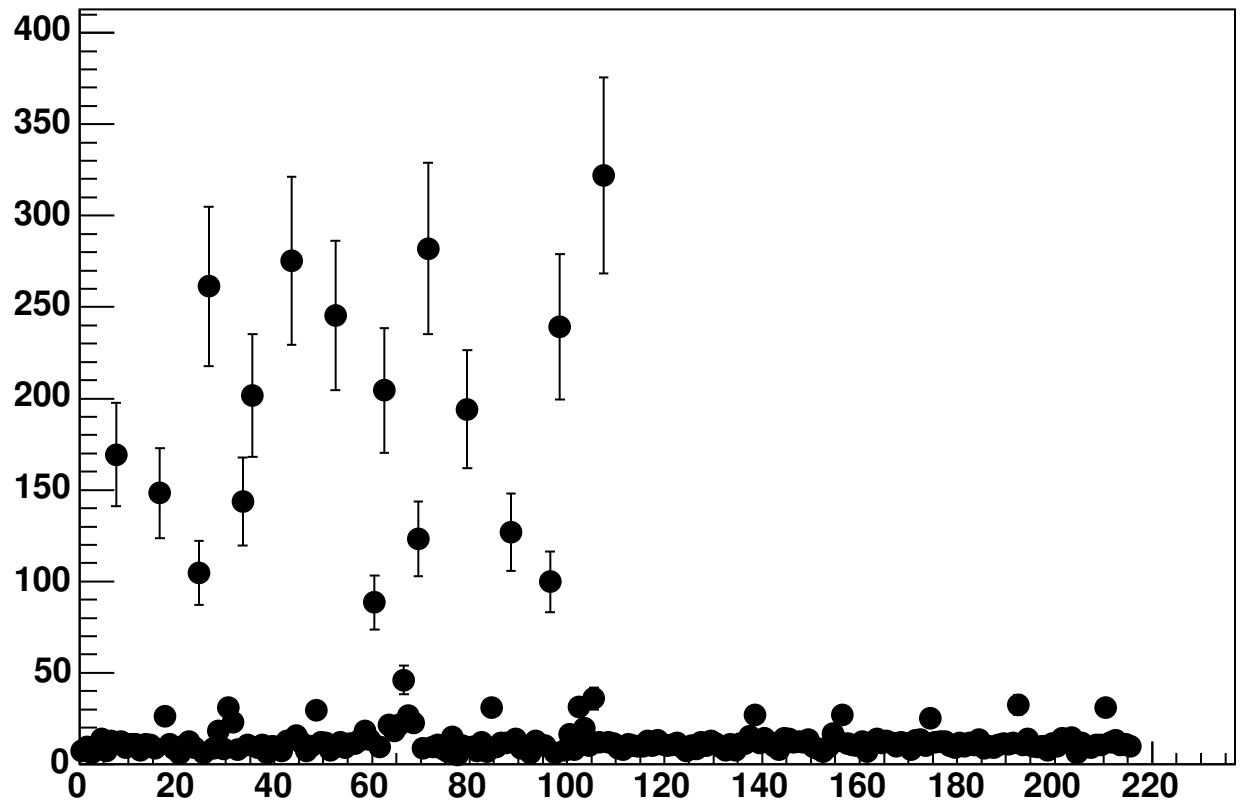
Enable 5, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



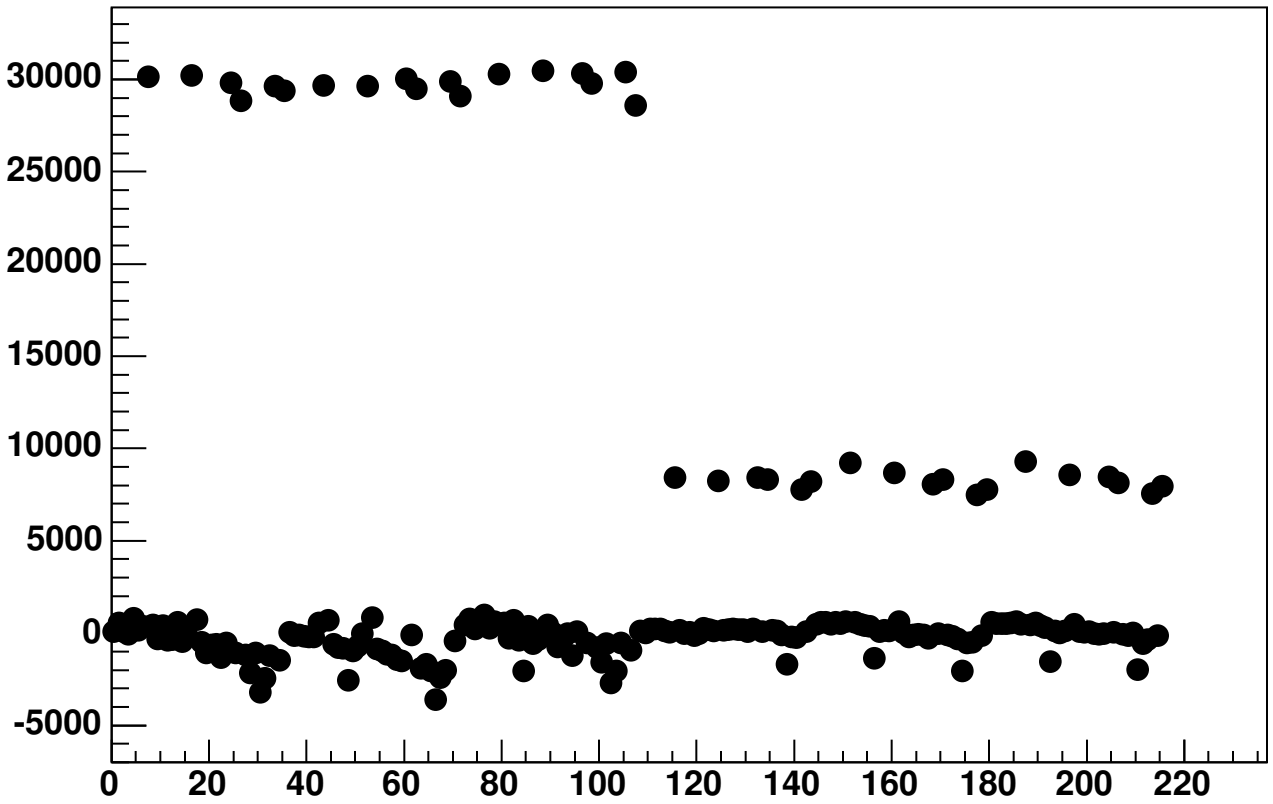
Enable 5, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



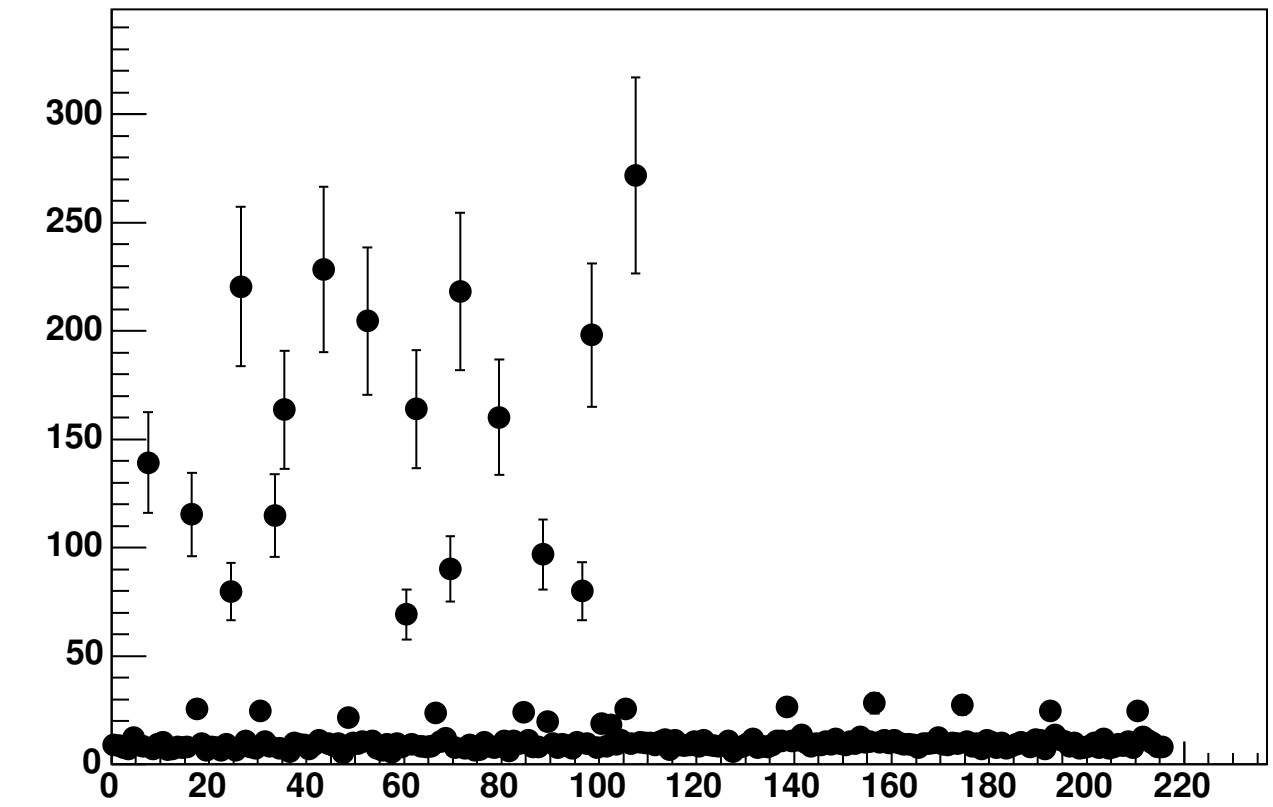
Enable 5, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



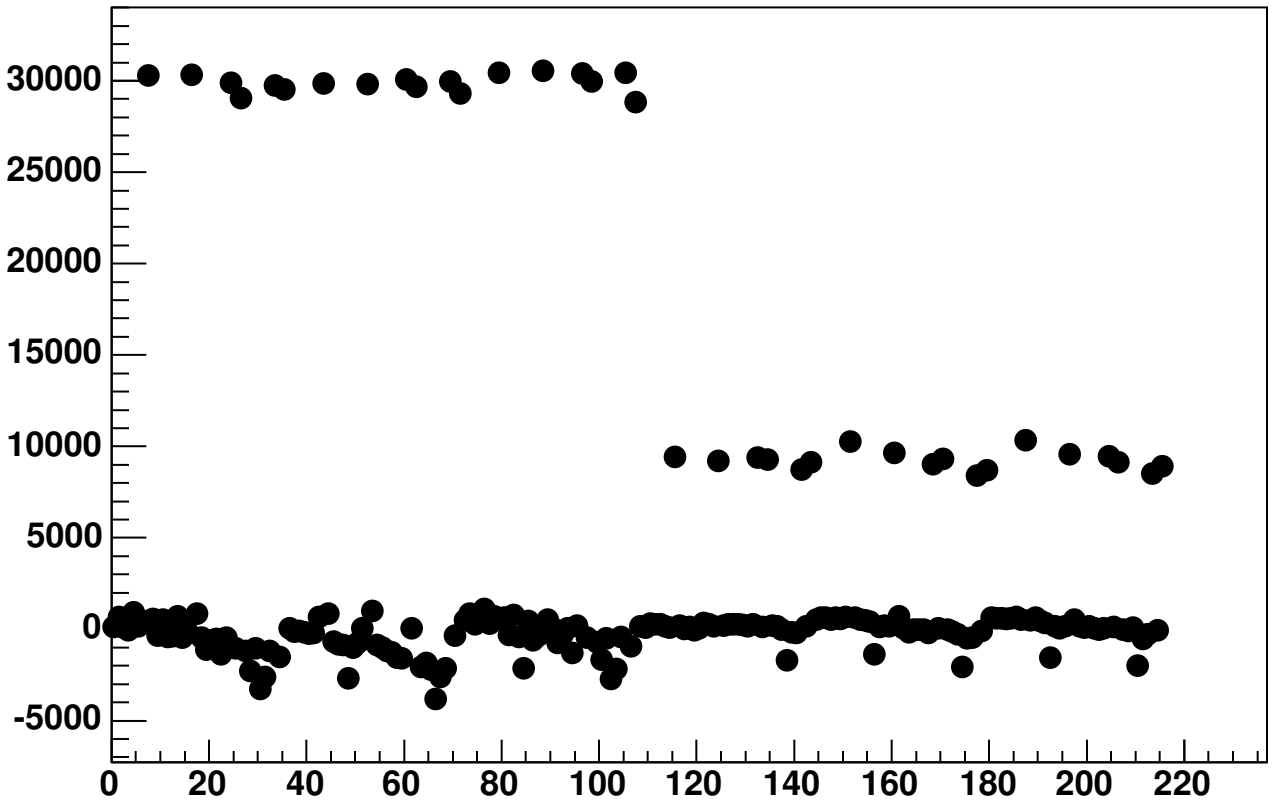
Enable 5, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



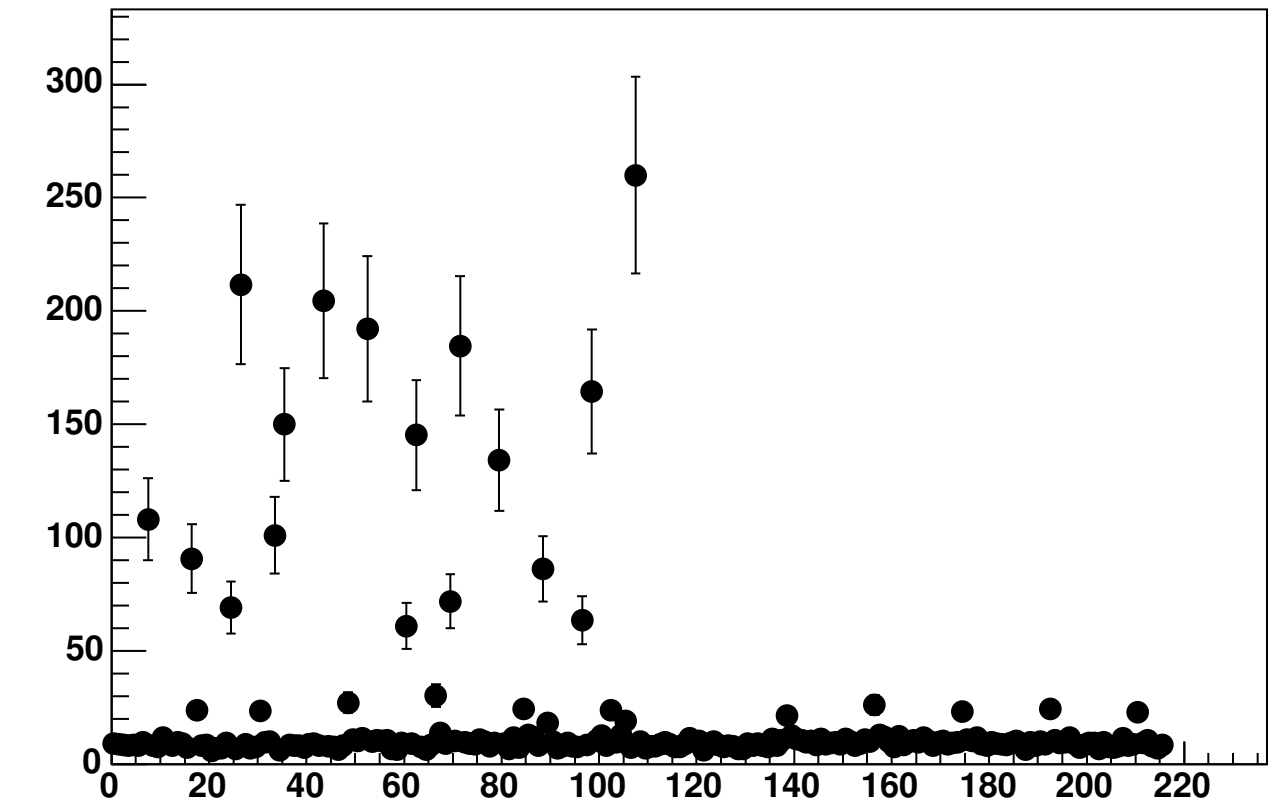
Enable 5, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



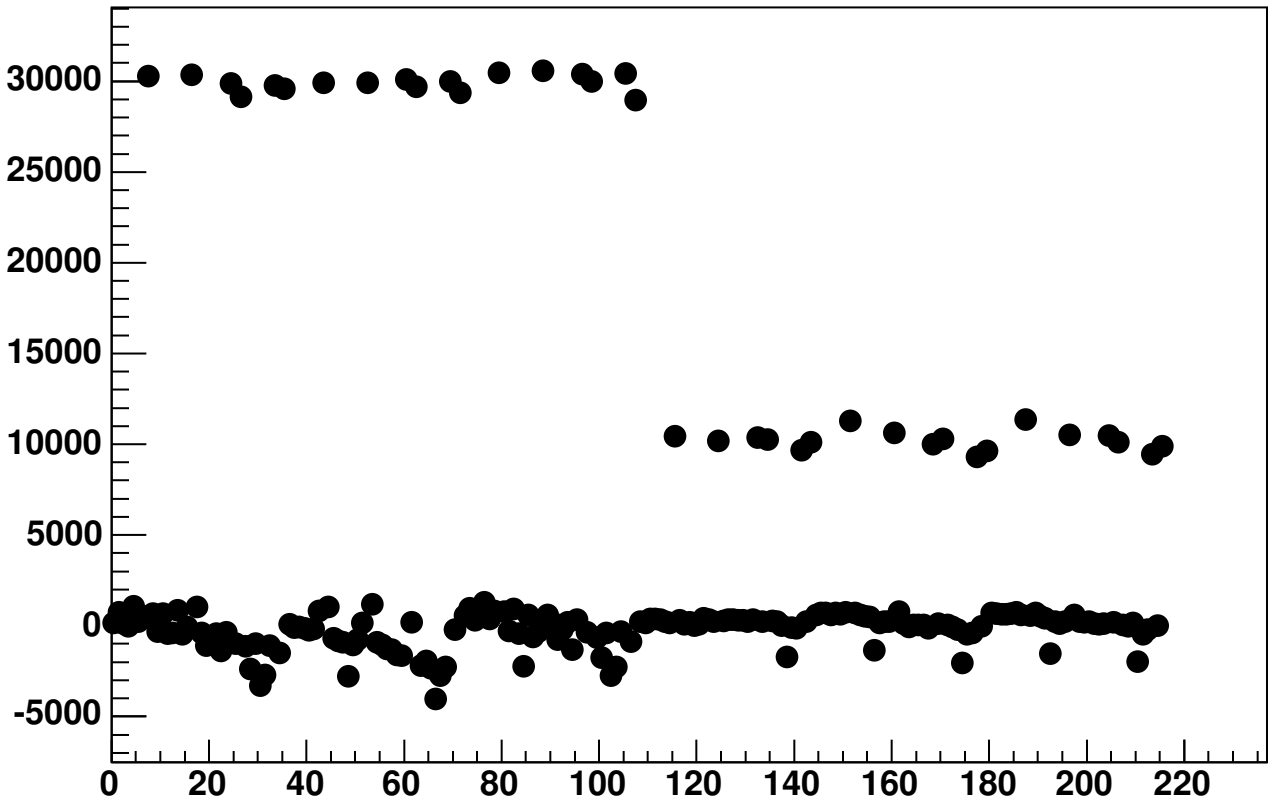
Enable 5, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



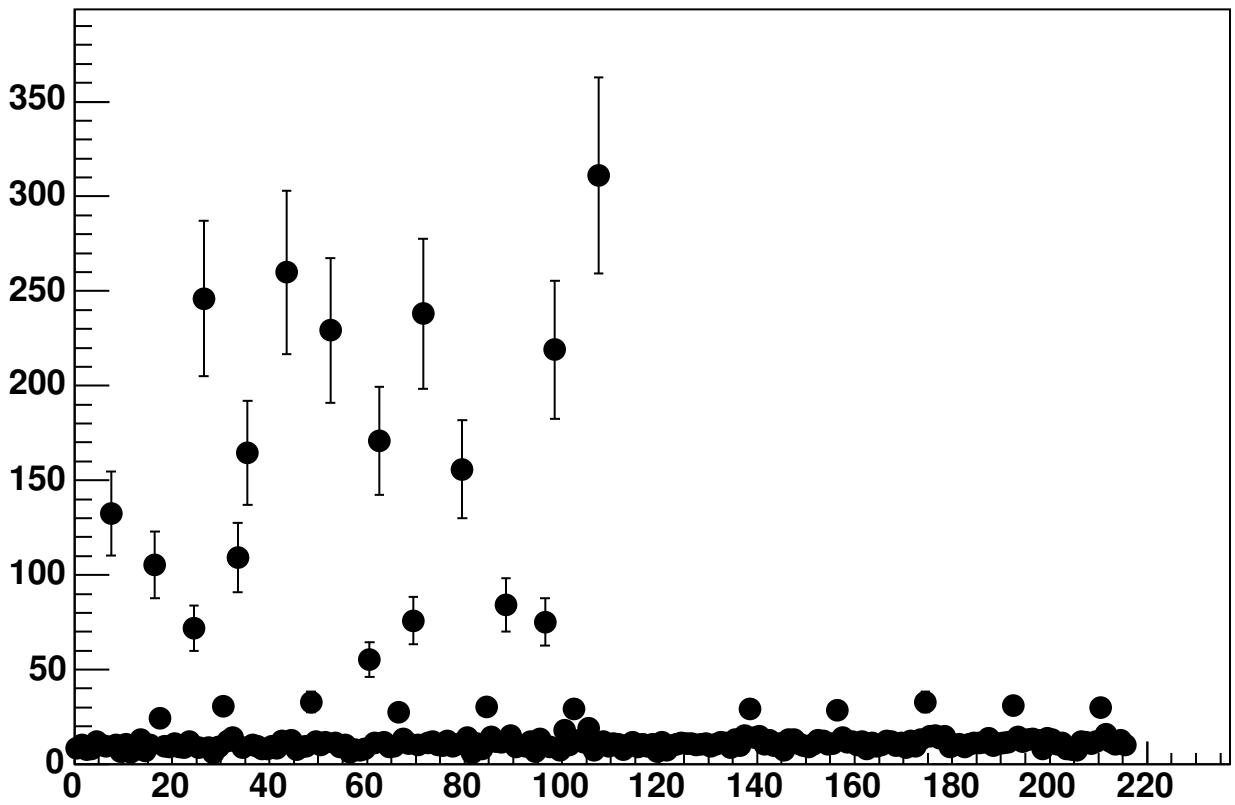
Enable 5, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



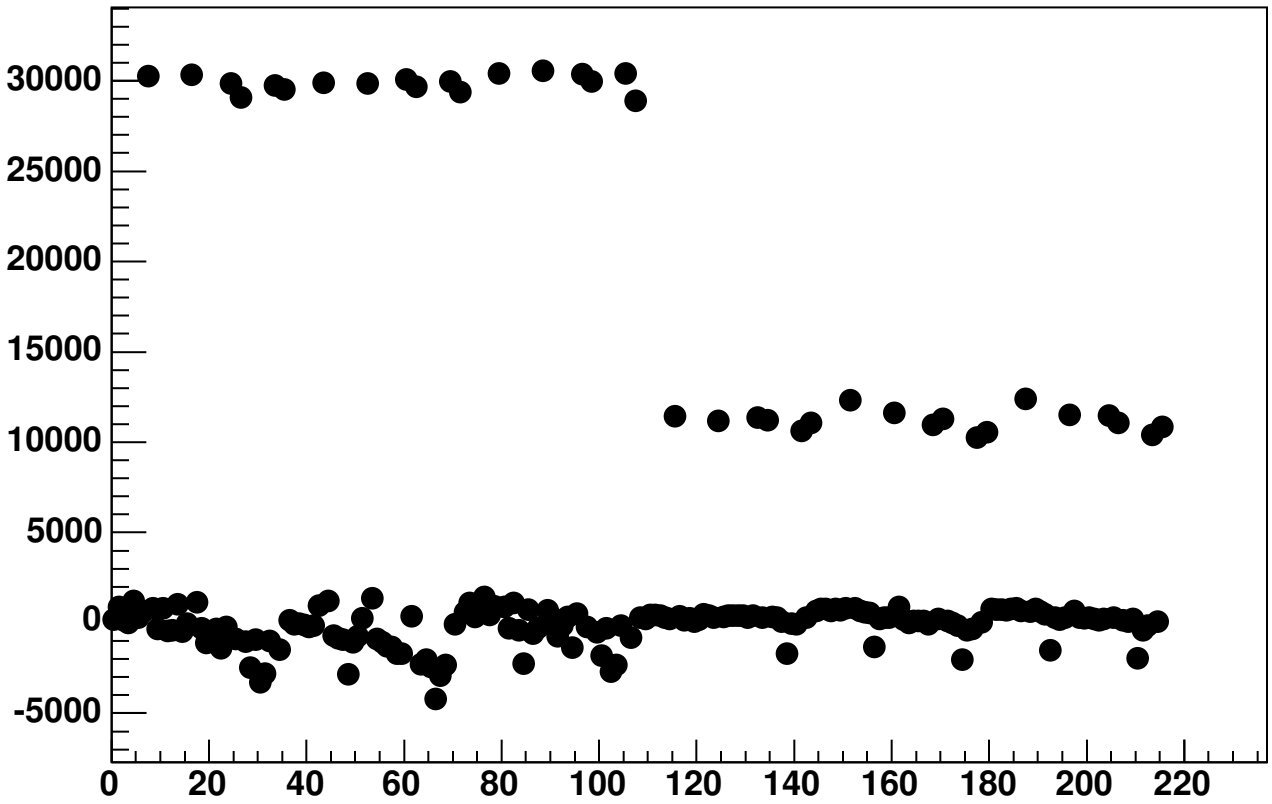
Enable 5, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



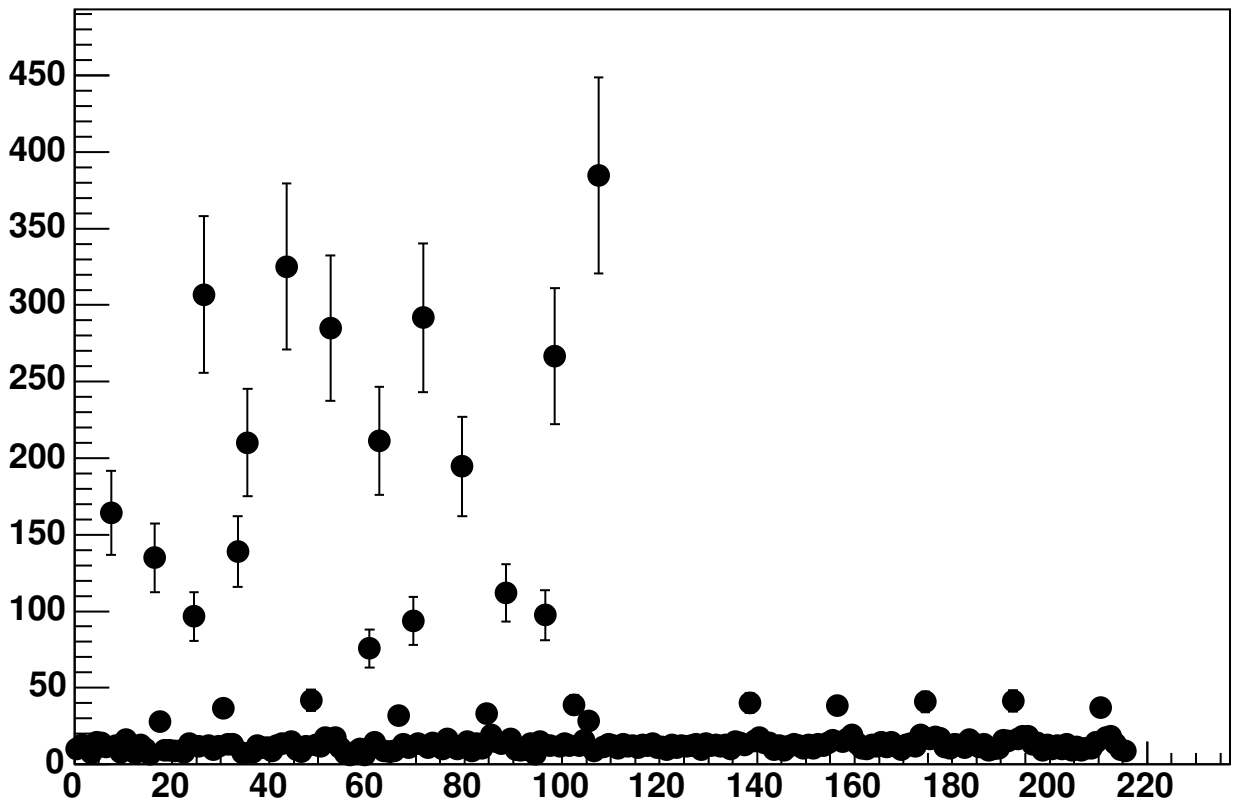
Enable 5, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



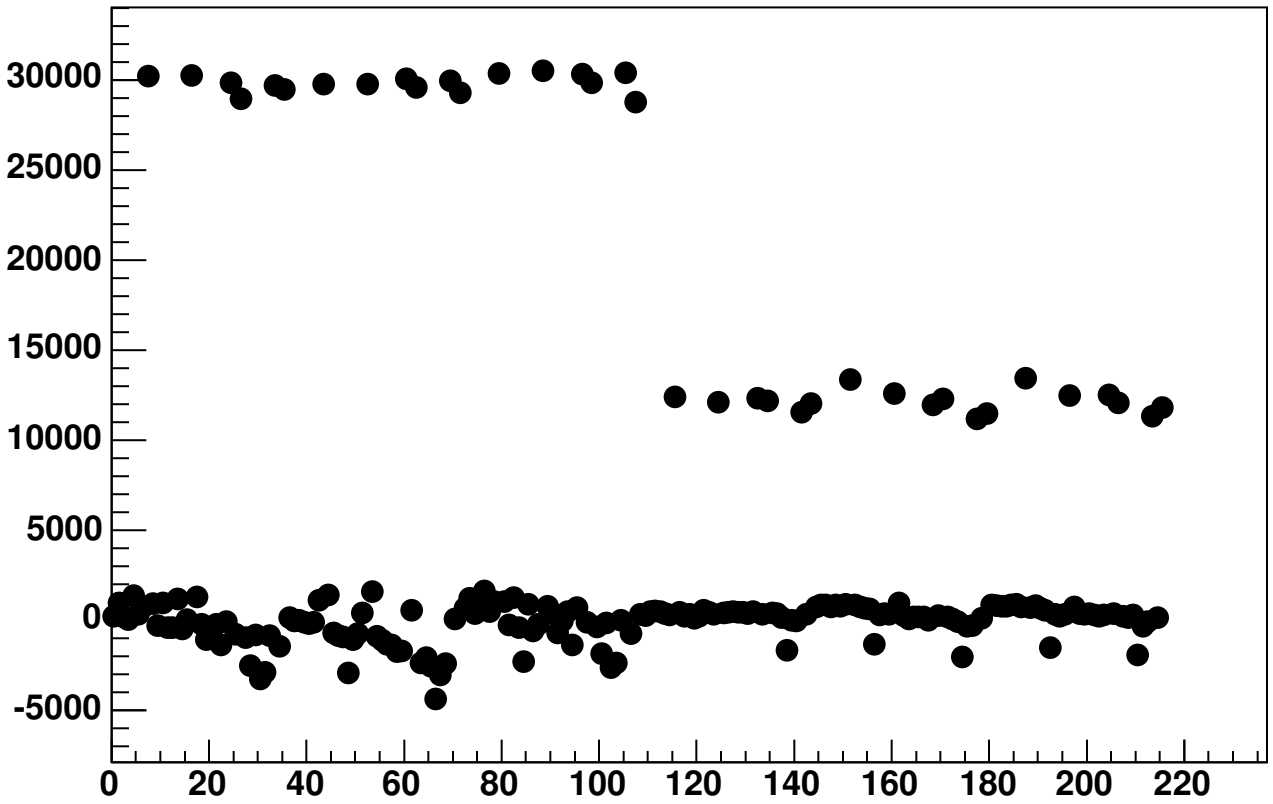
Enable 5, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



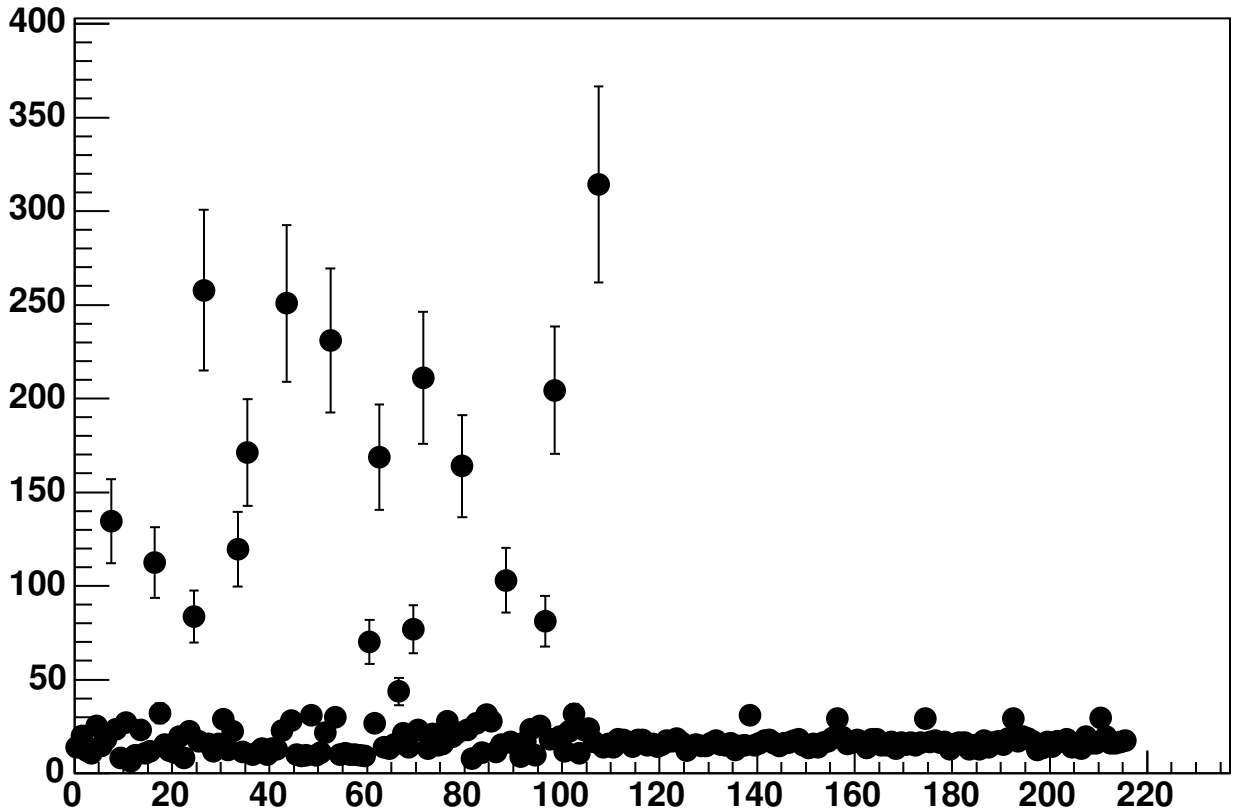
Enable 5, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



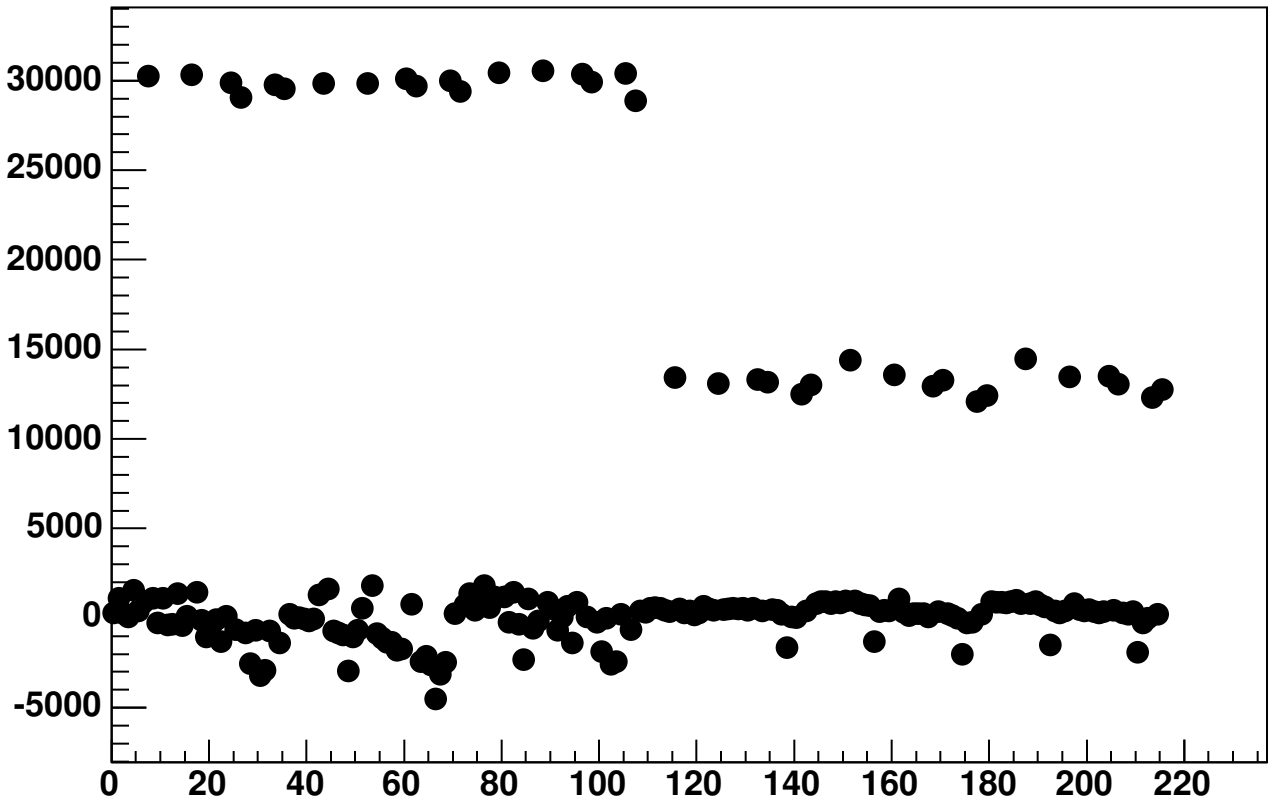
Enable 5, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



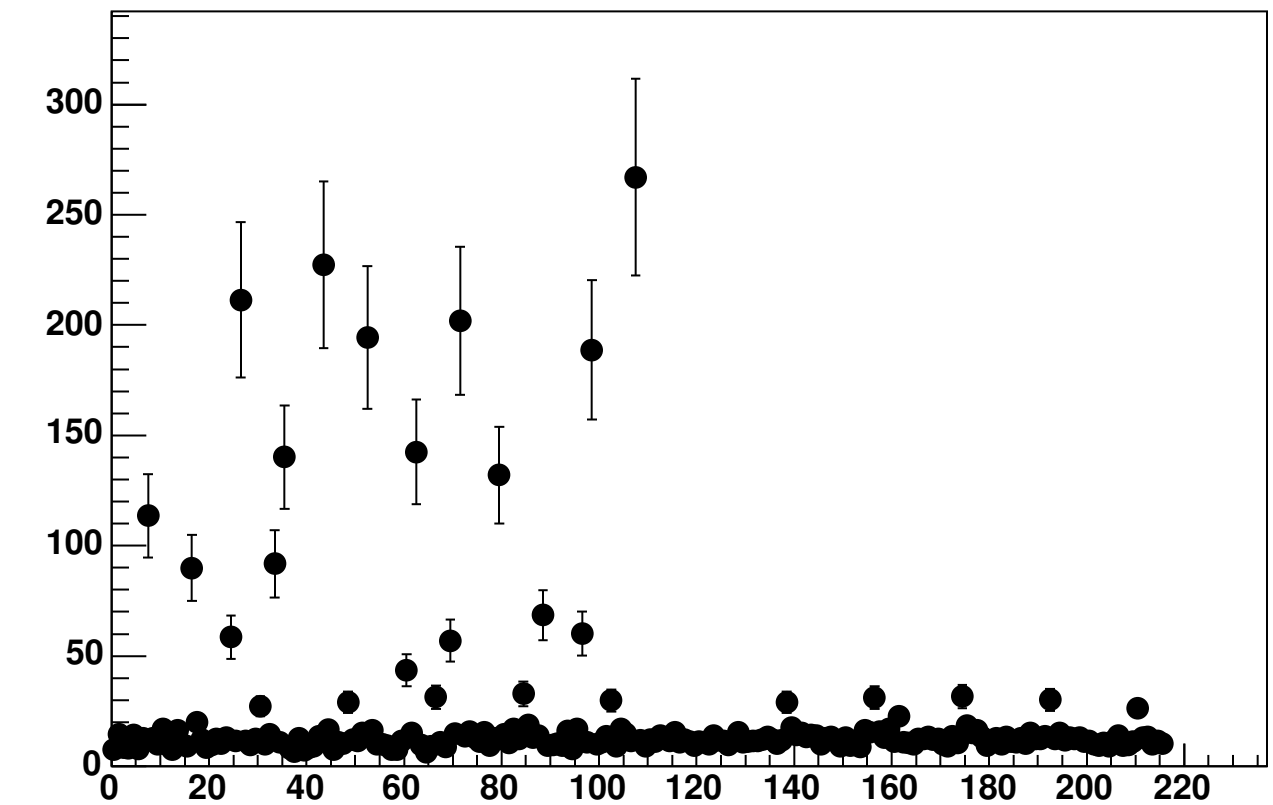
Enable 5, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



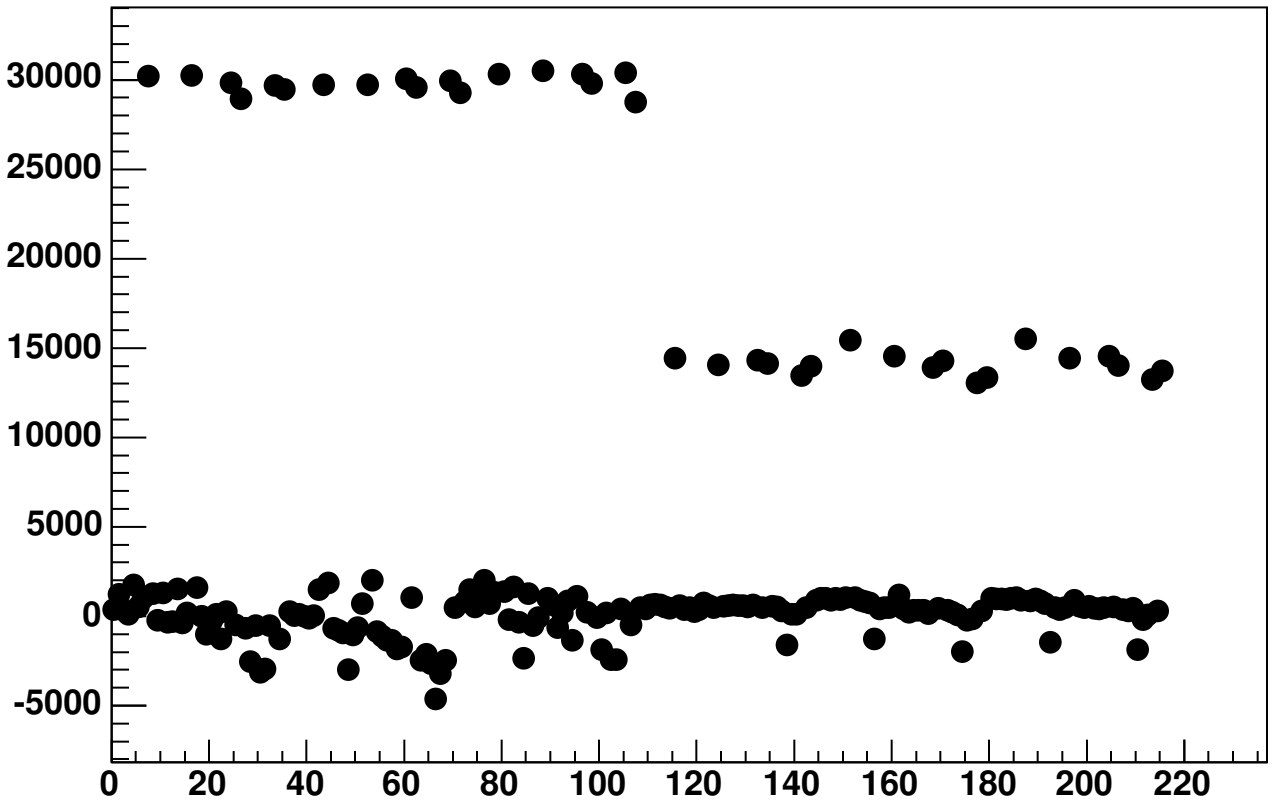
Enable 5, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



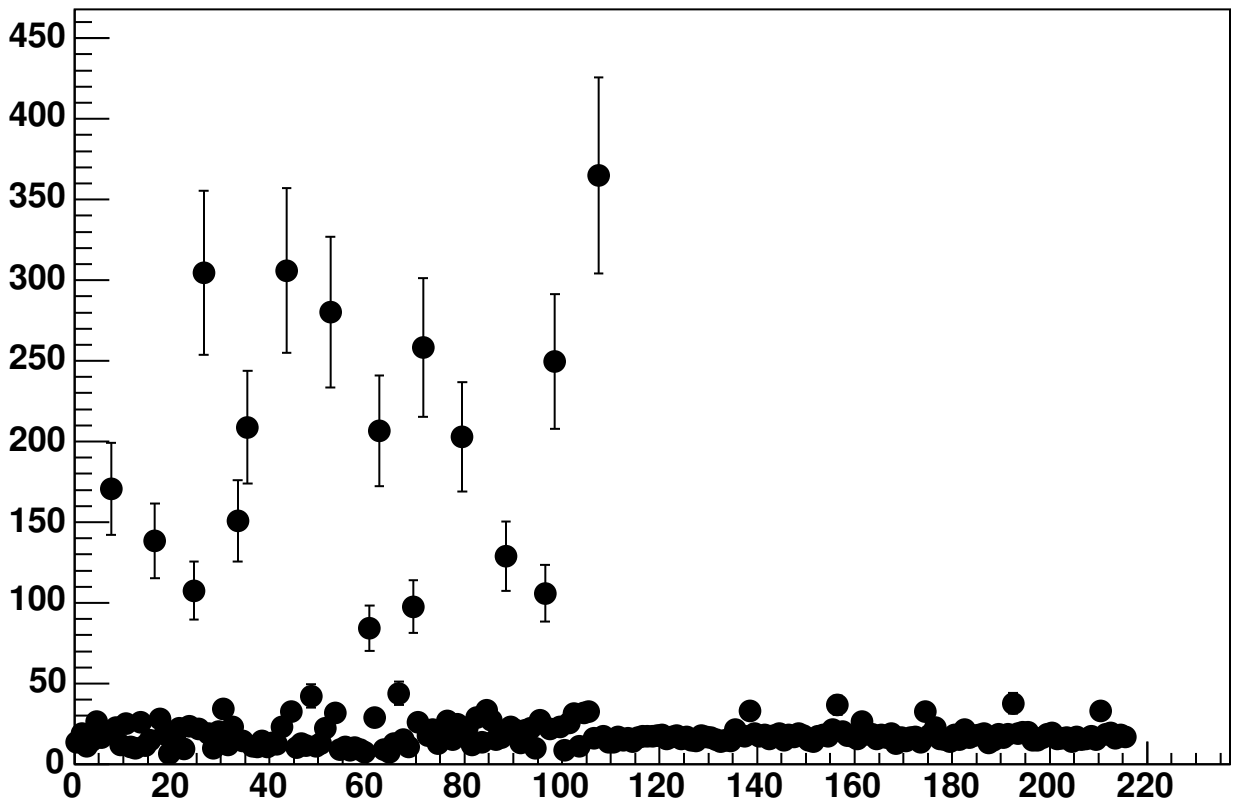
Enable 5, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



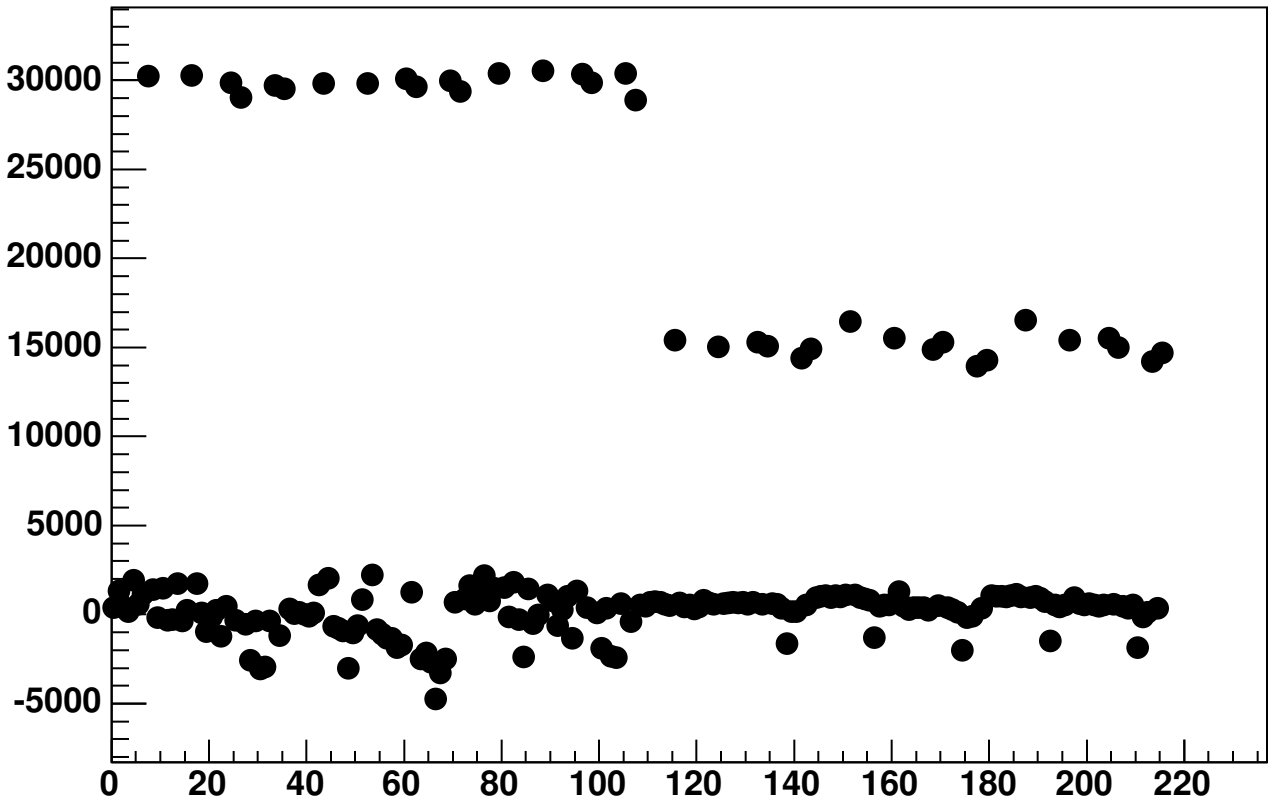
Enable 5, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



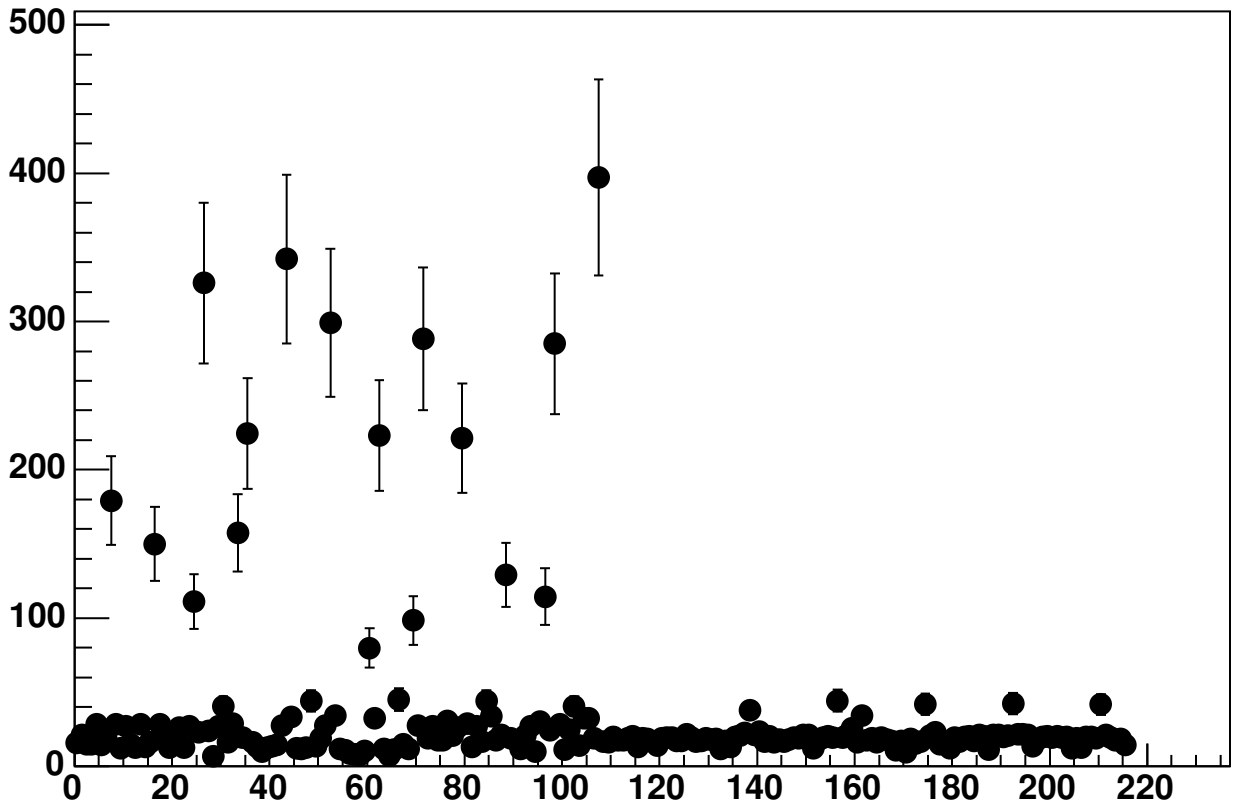
Enable 5, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



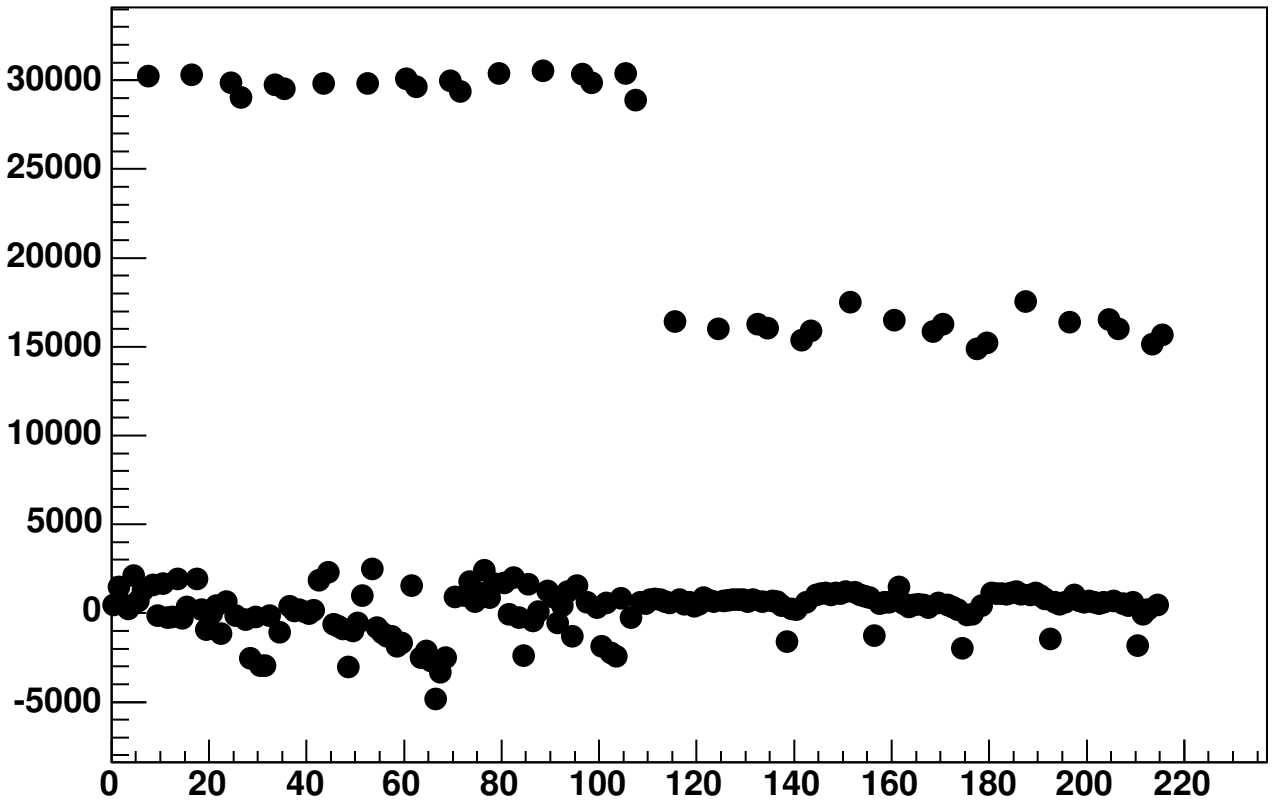
Enable 5, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



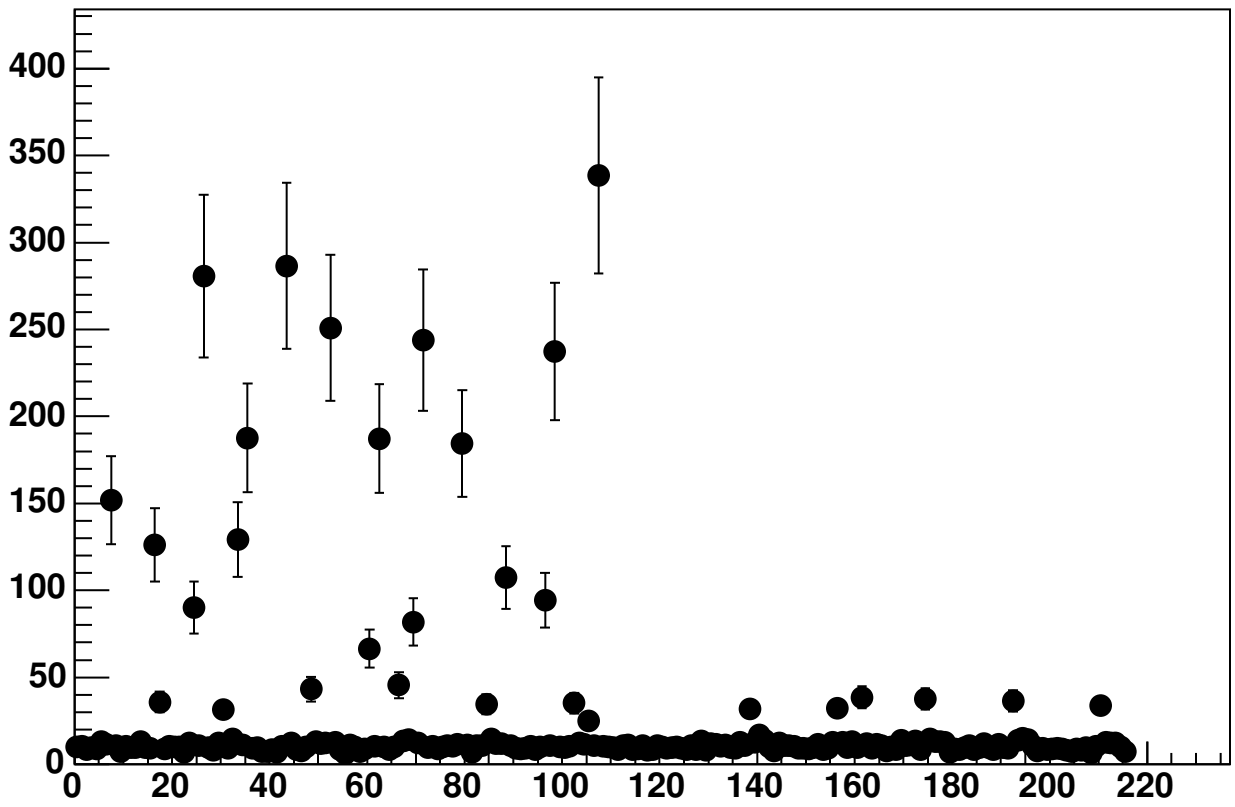
Enable 5, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



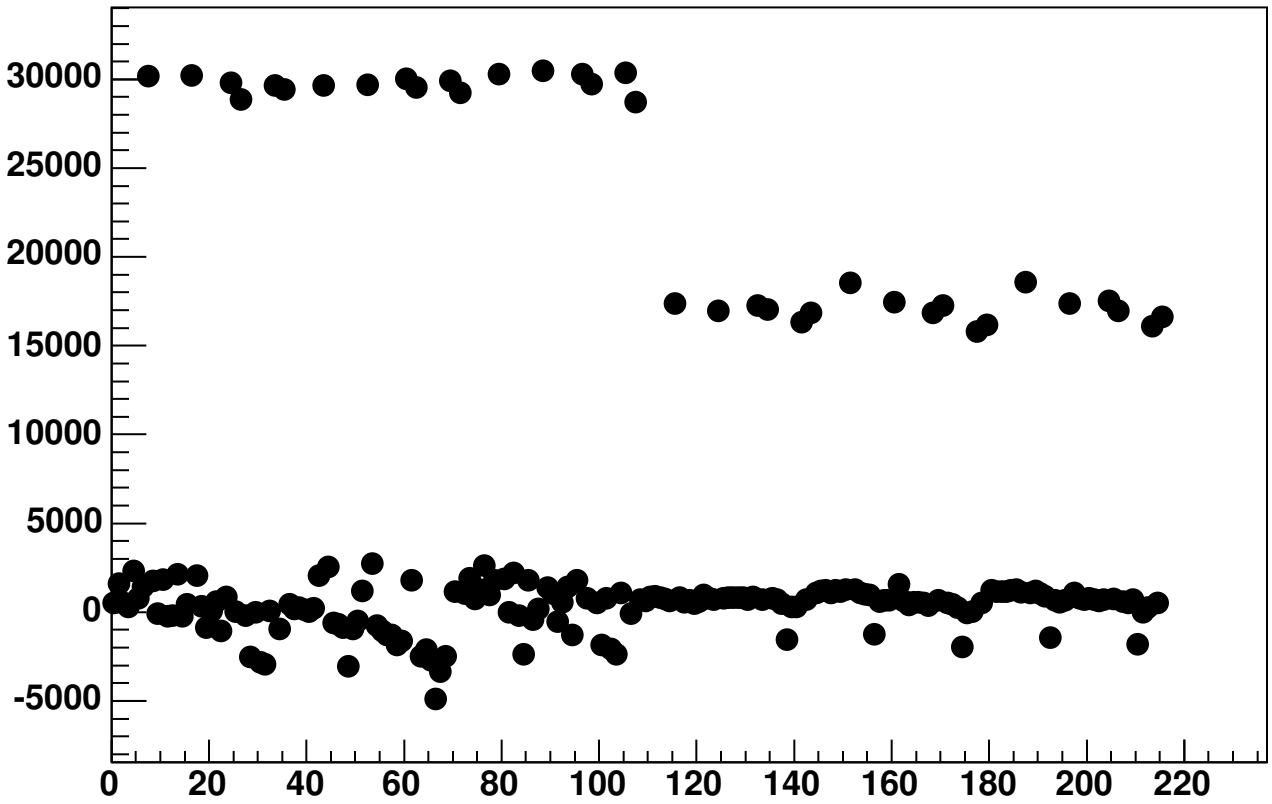
Enable 5, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



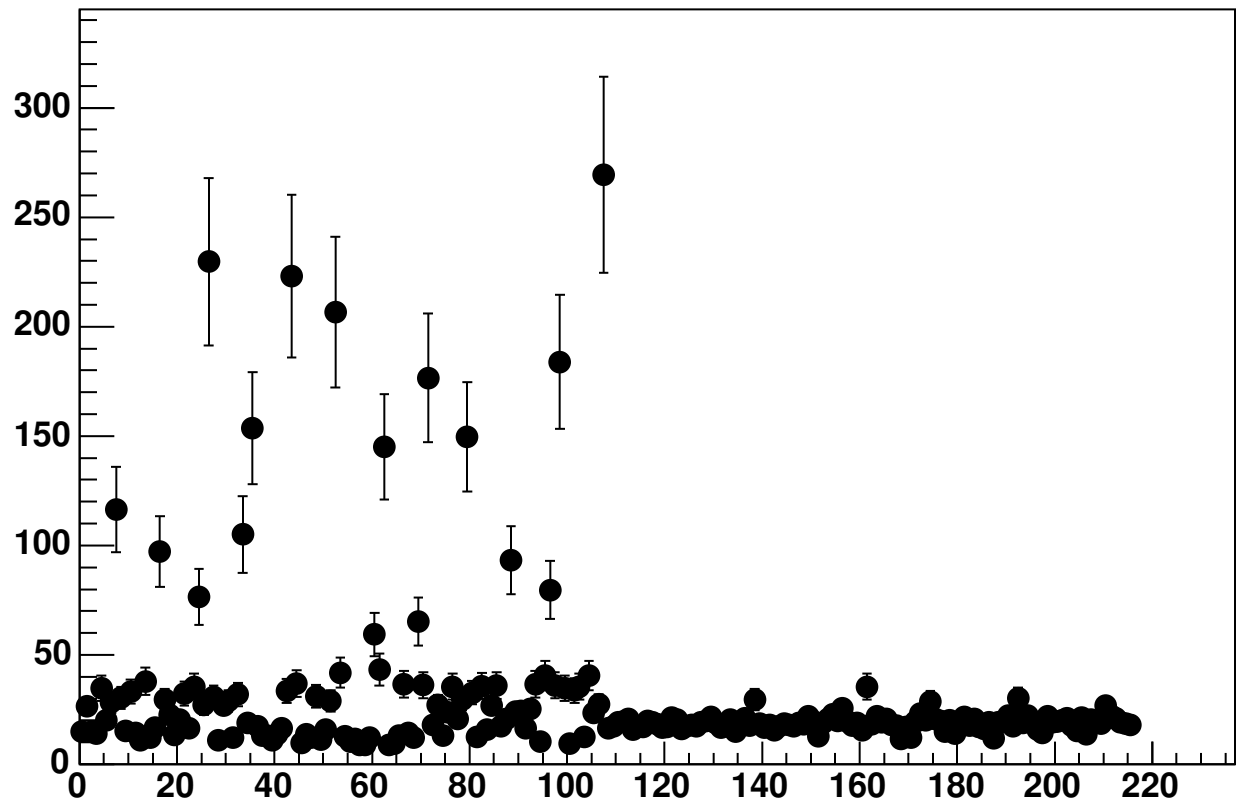
Enable 5, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



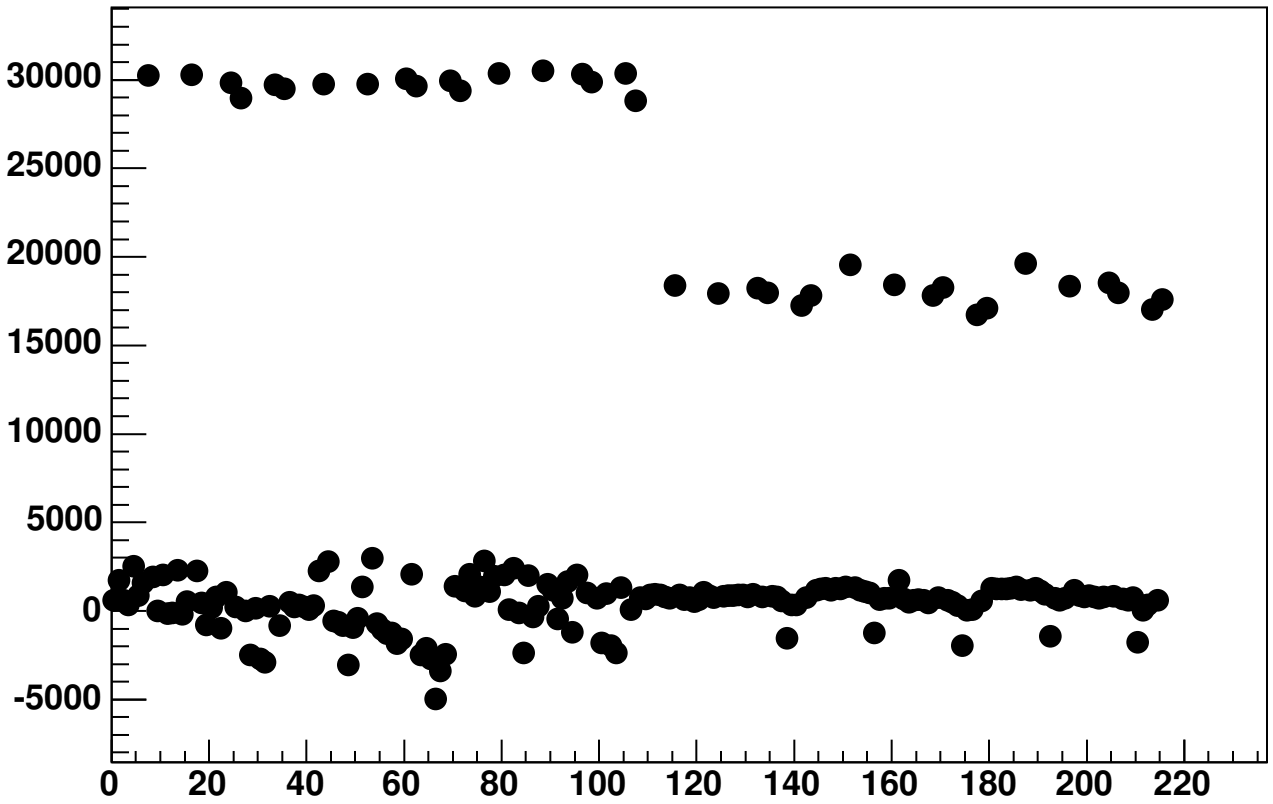
Enable 5, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



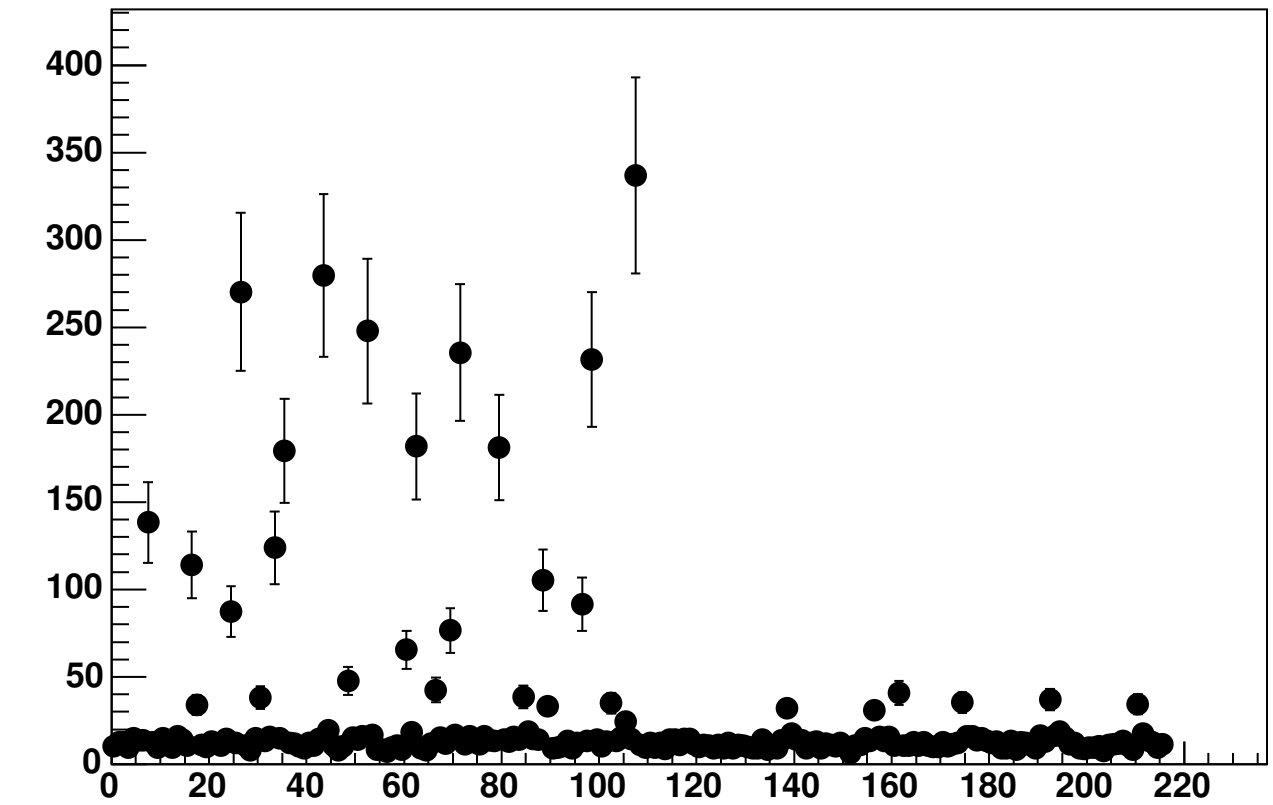
Enable 5, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



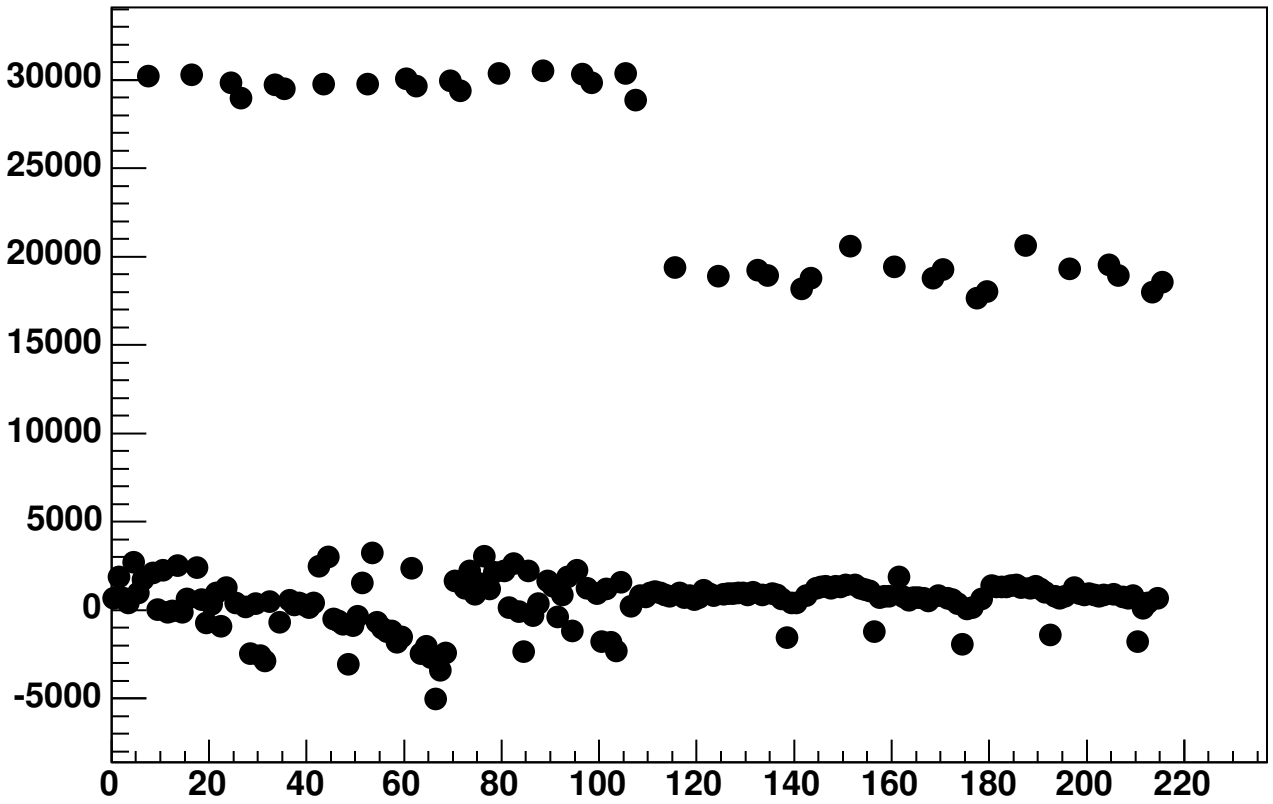
Enable 5, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



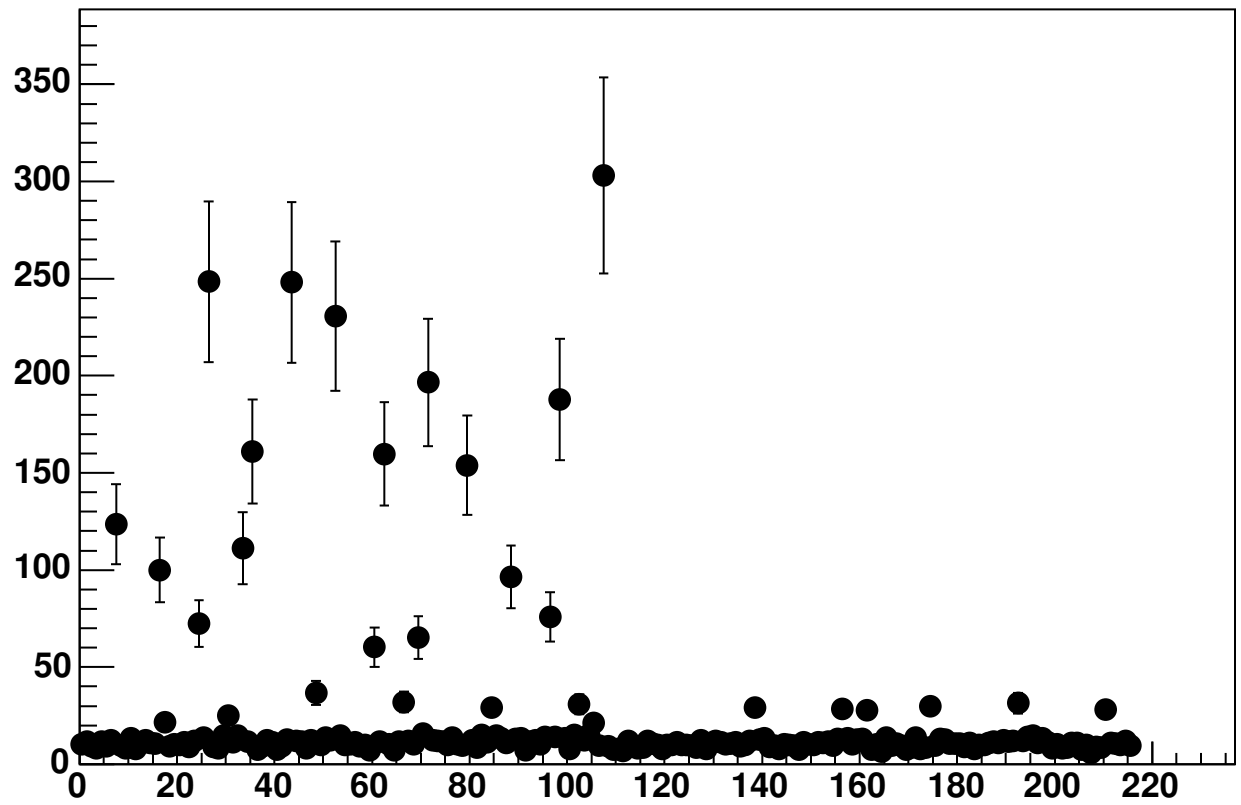
Enable 5, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



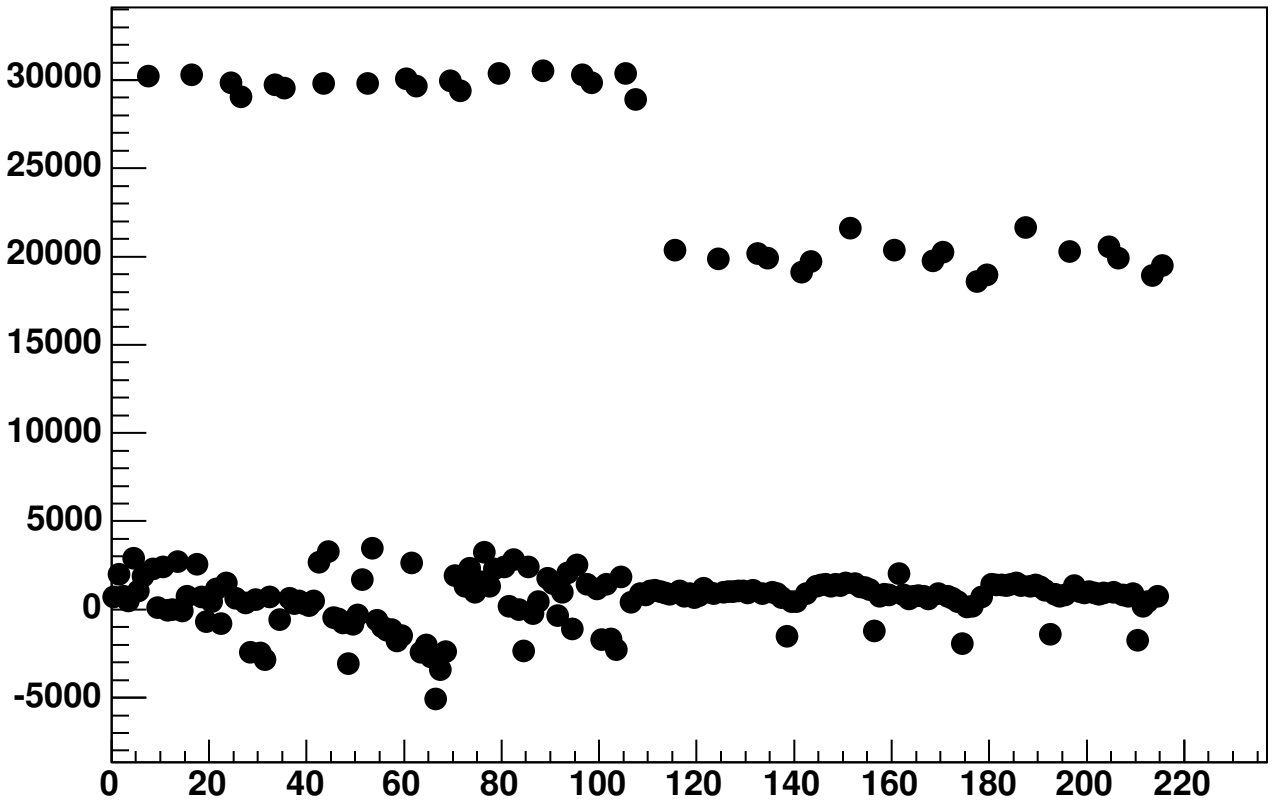
Enable 5, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



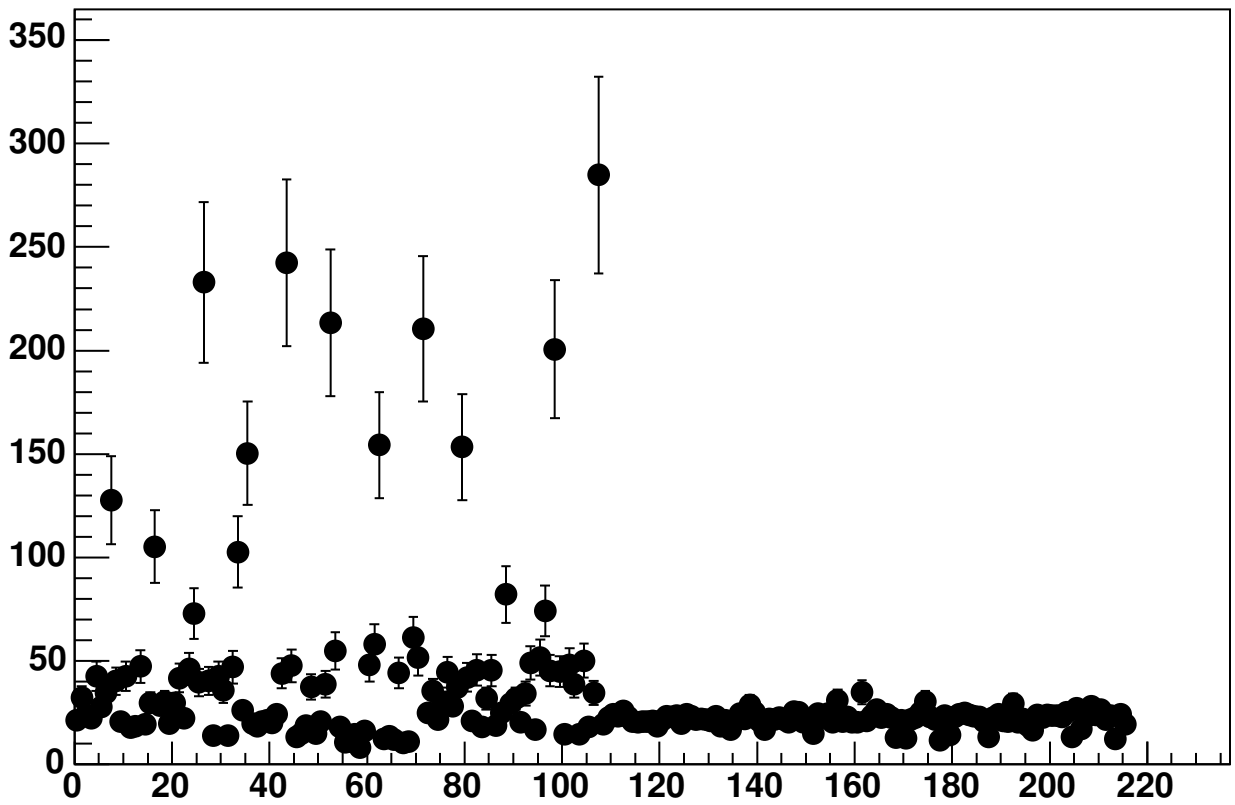
Enable 5, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



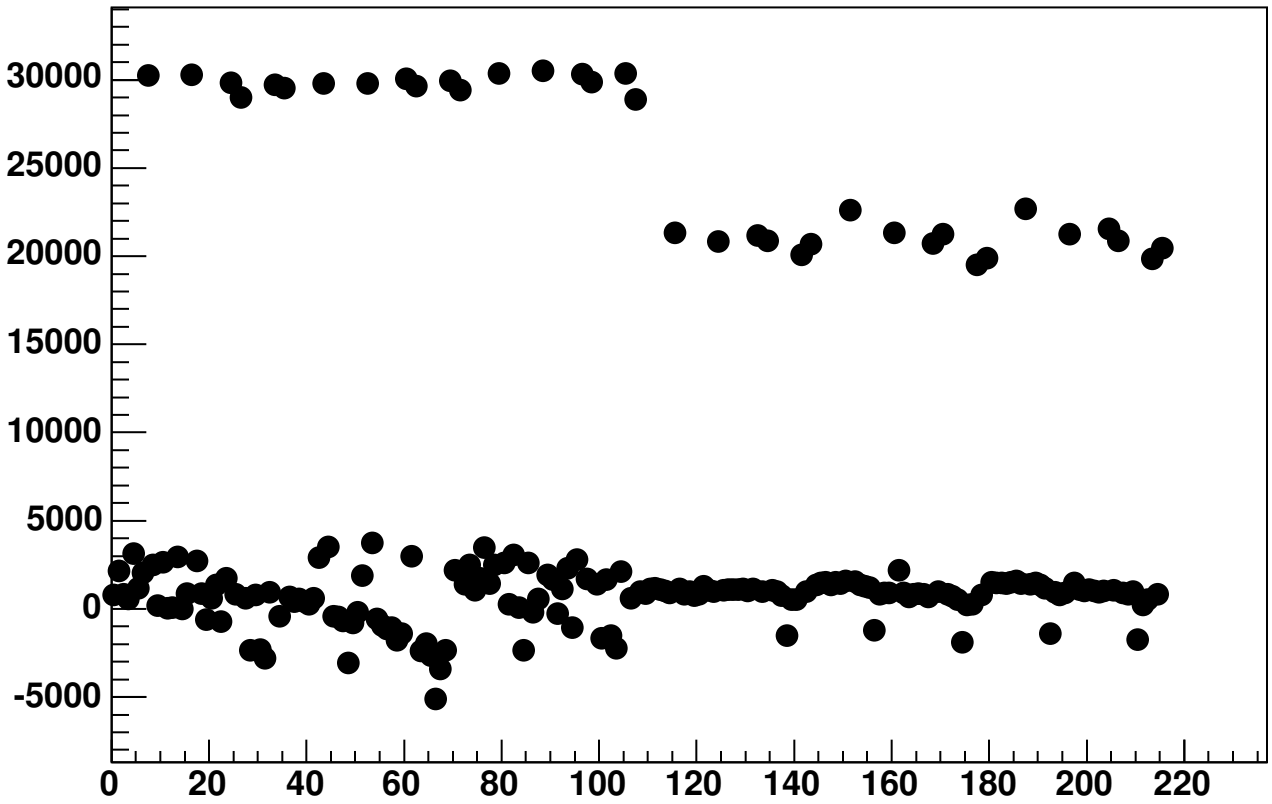
Enable 5, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



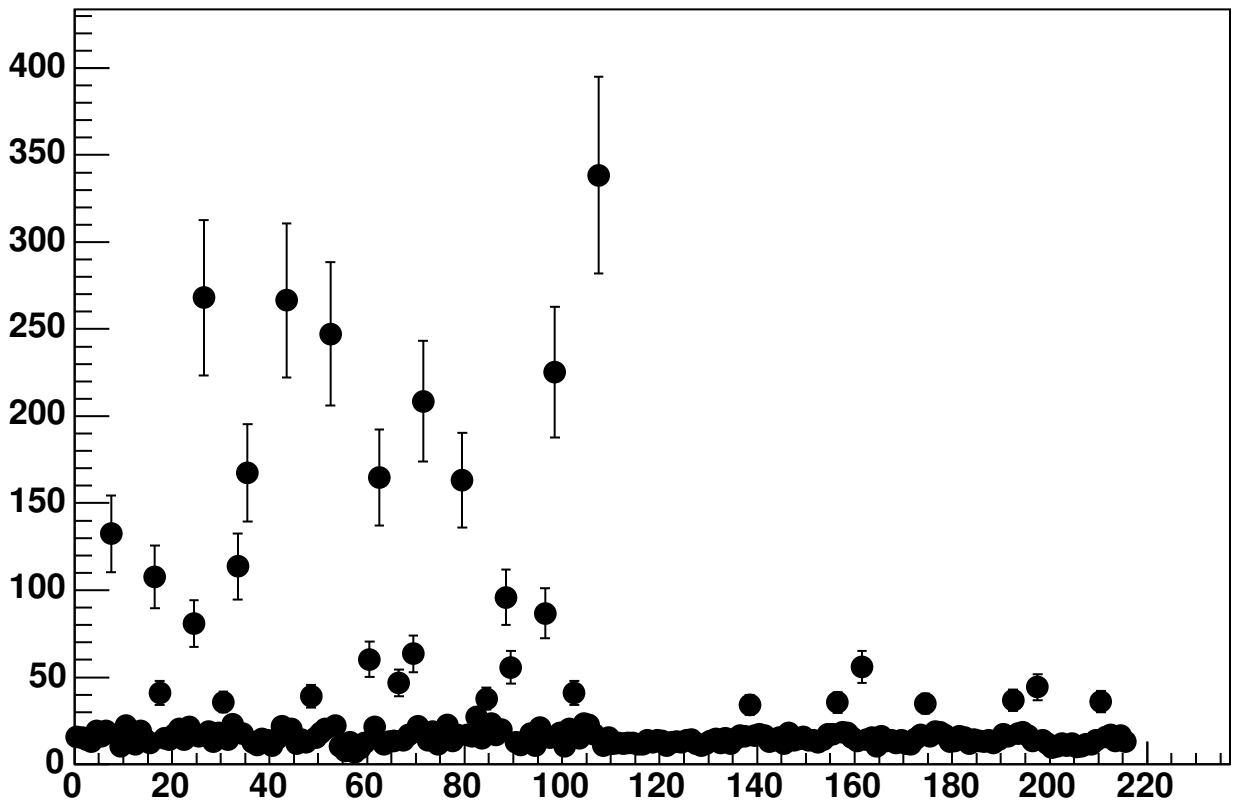
Enable 5, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



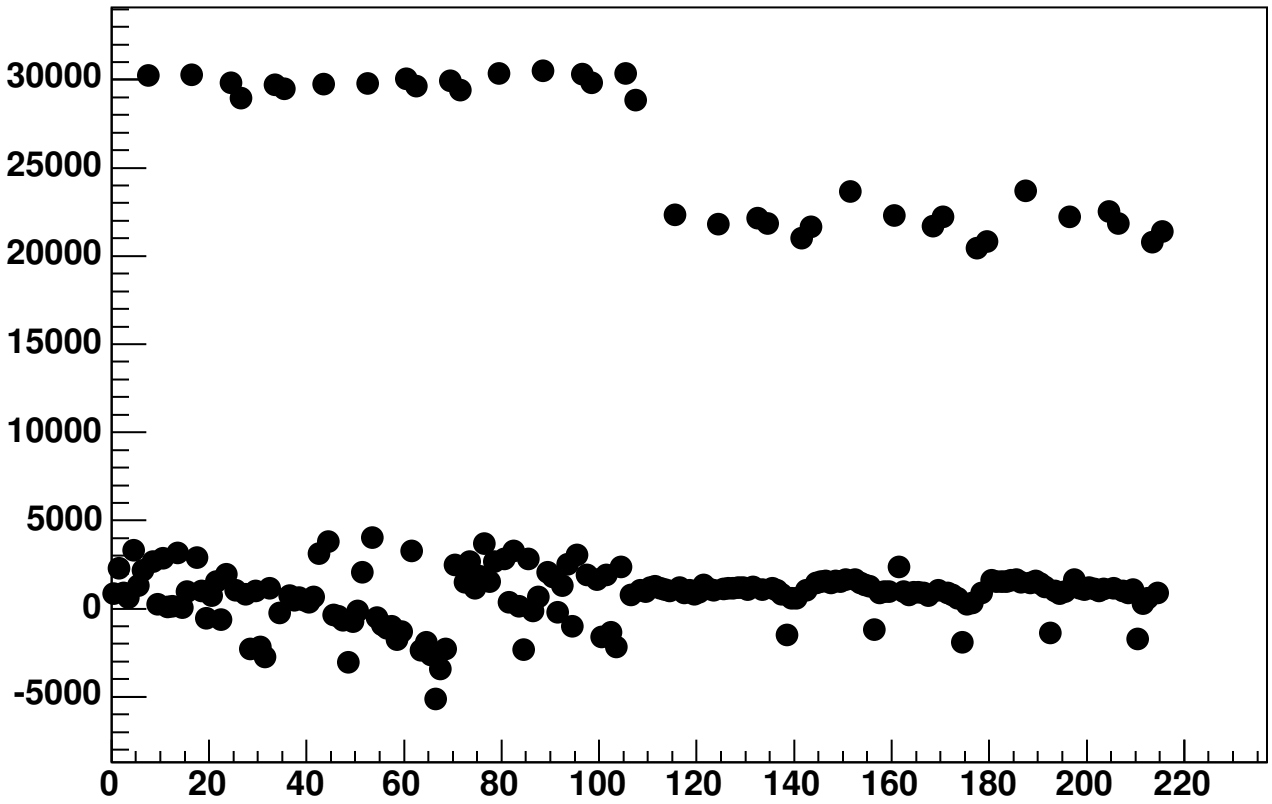
Enable 5, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



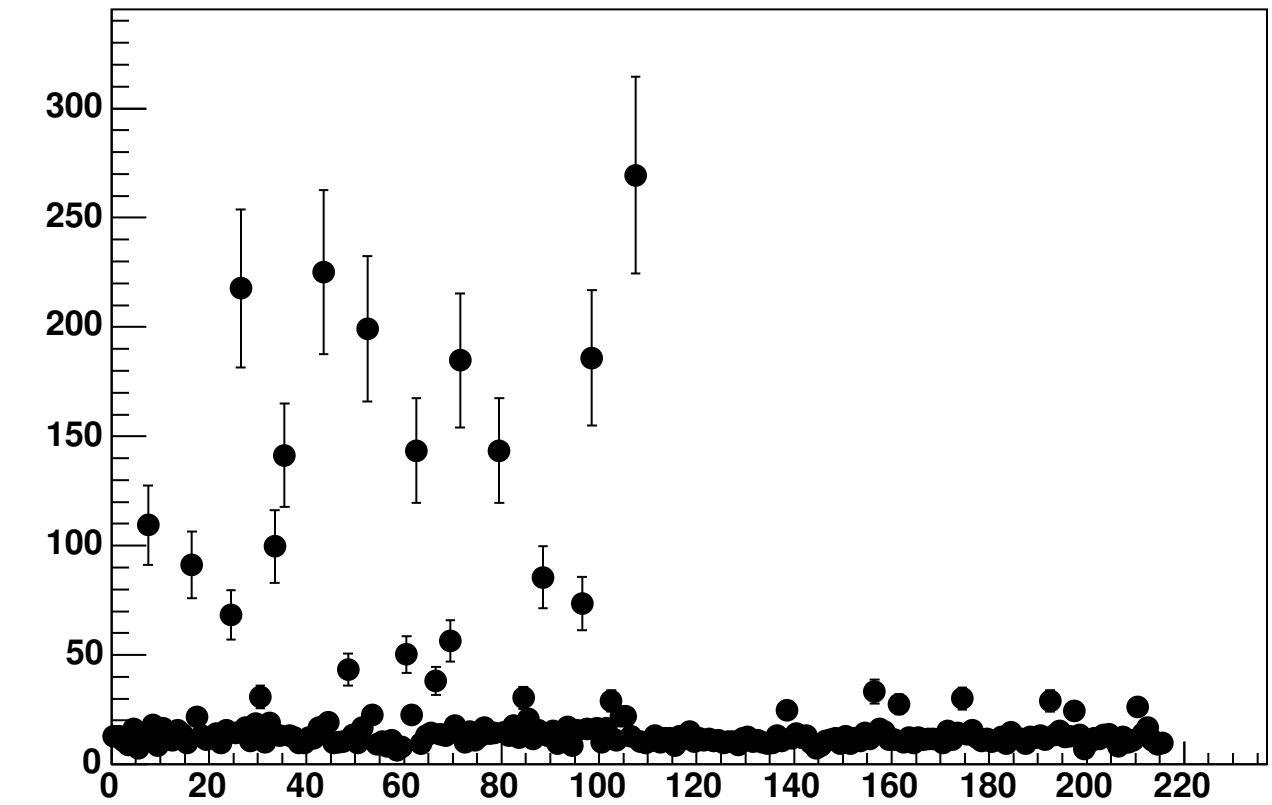
Enable 5, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



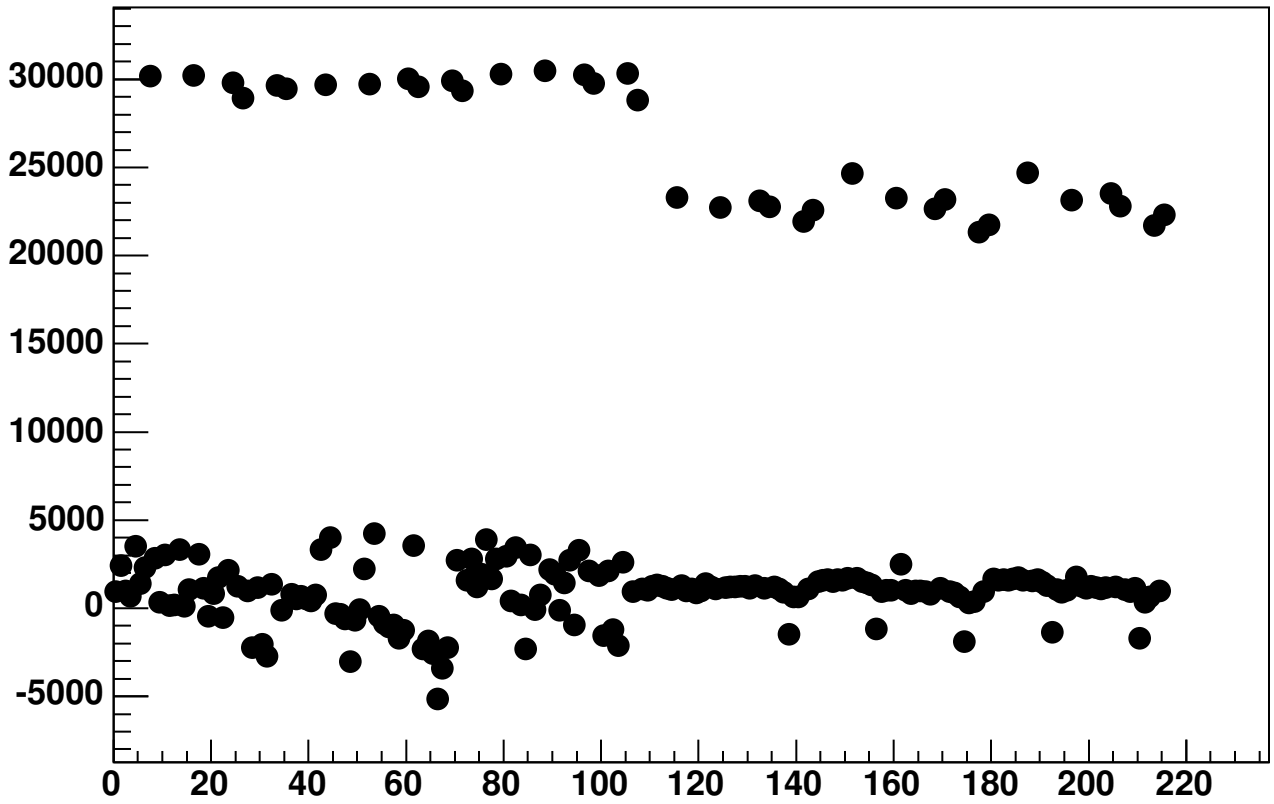
Enable 5, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



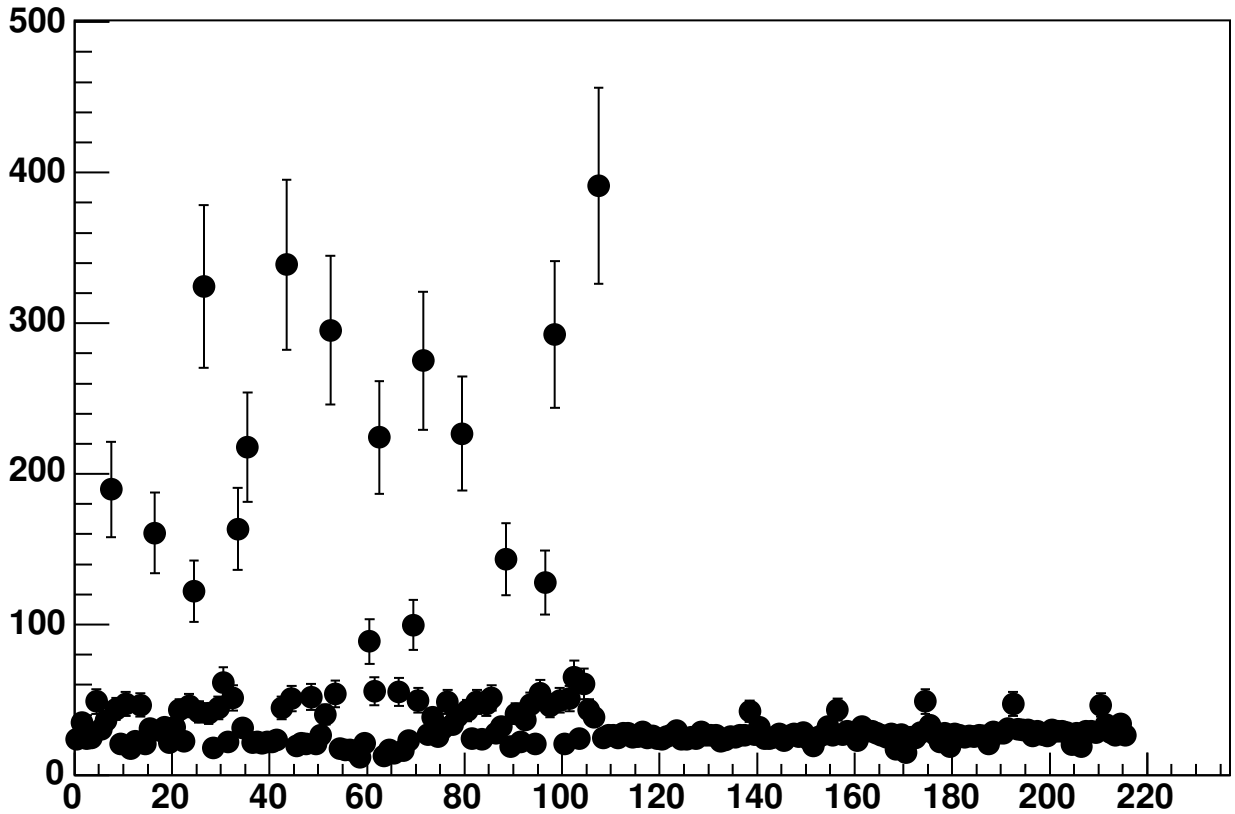
Enable 5, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



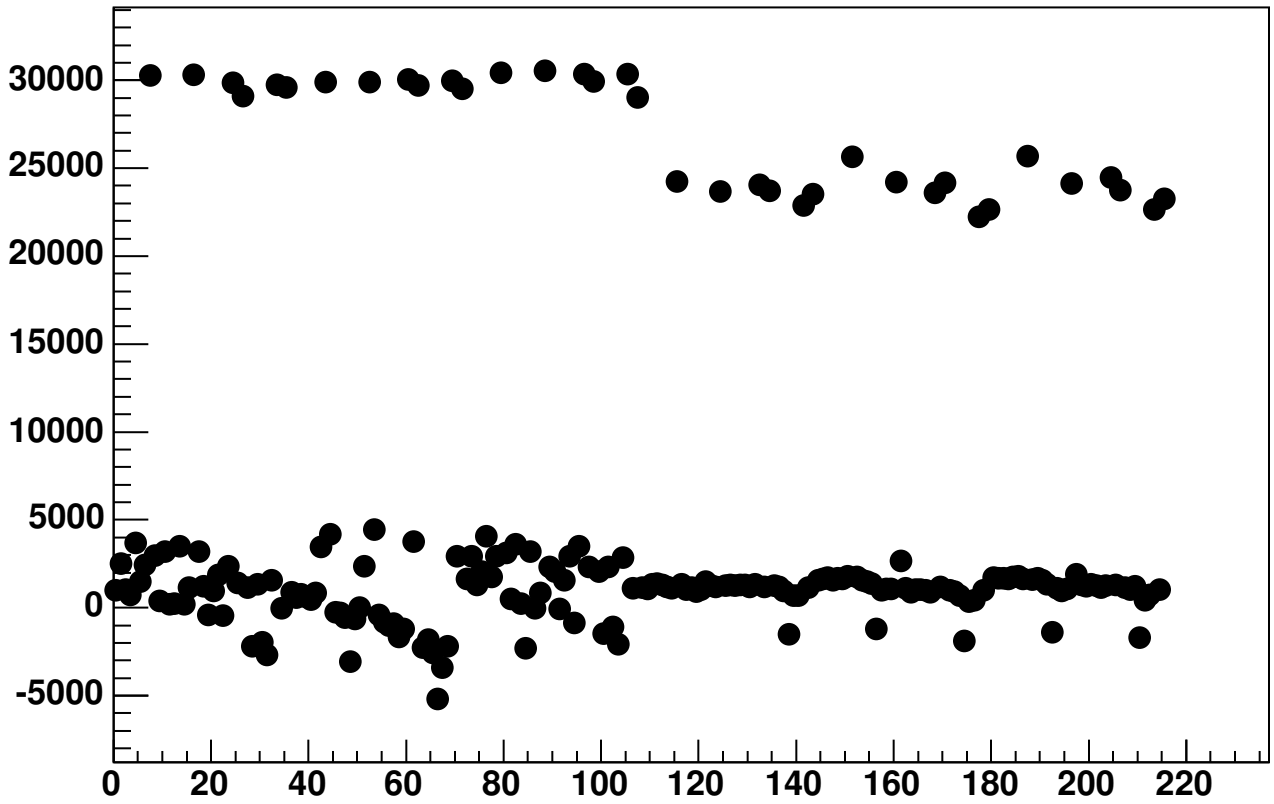
Enable 5, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



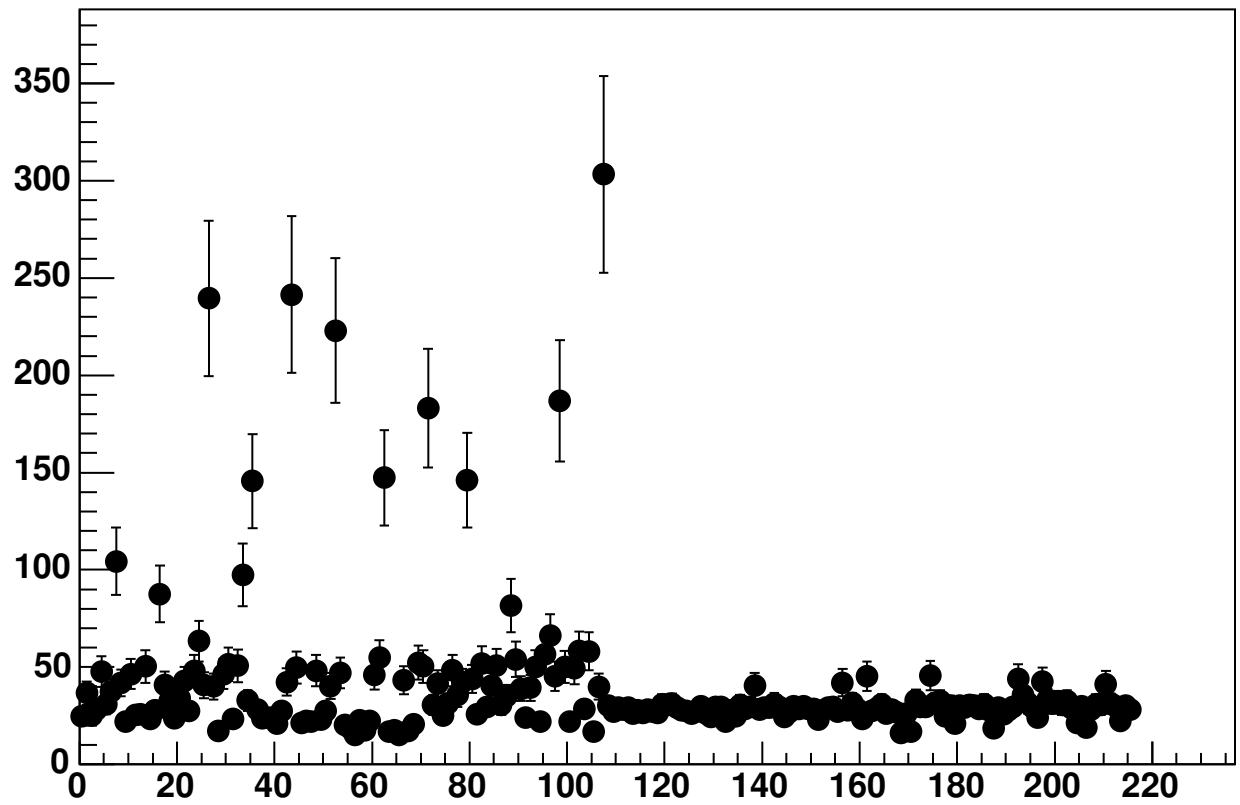
Enable 5, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



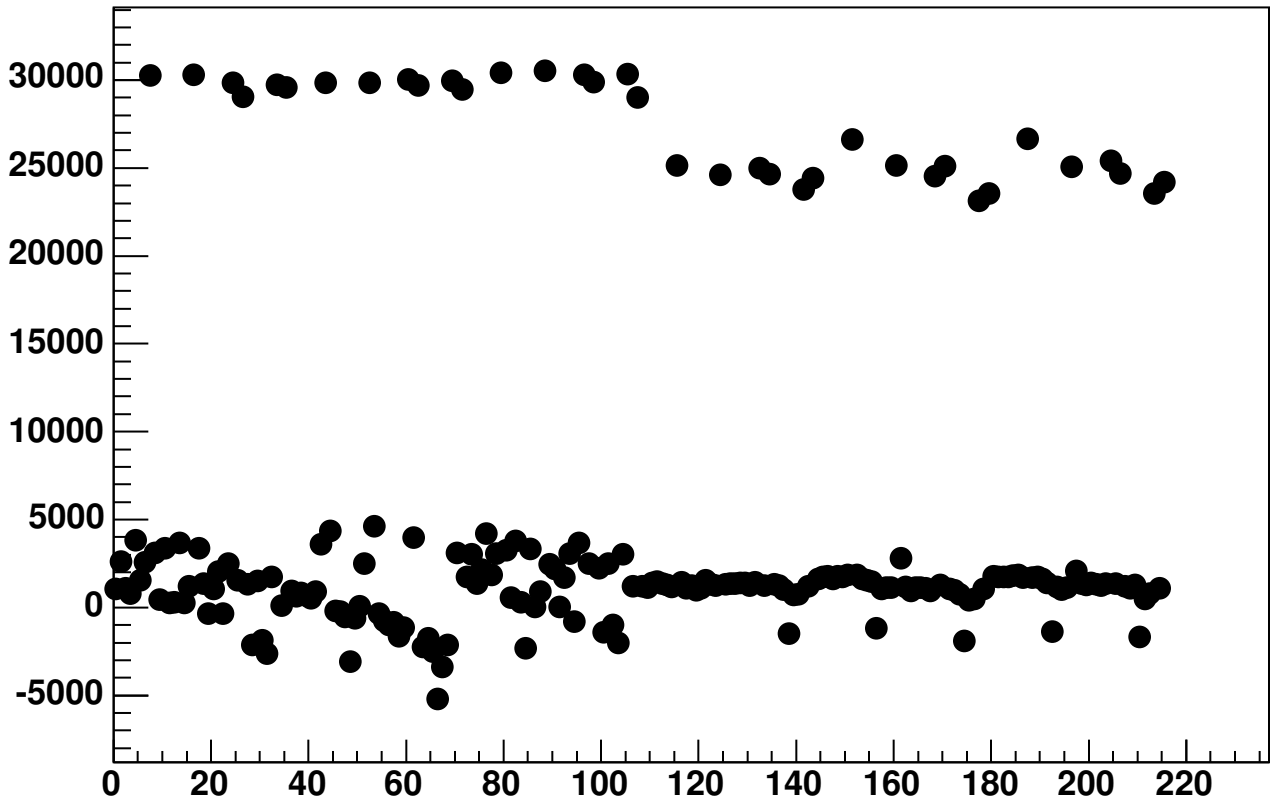
Enable 5, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



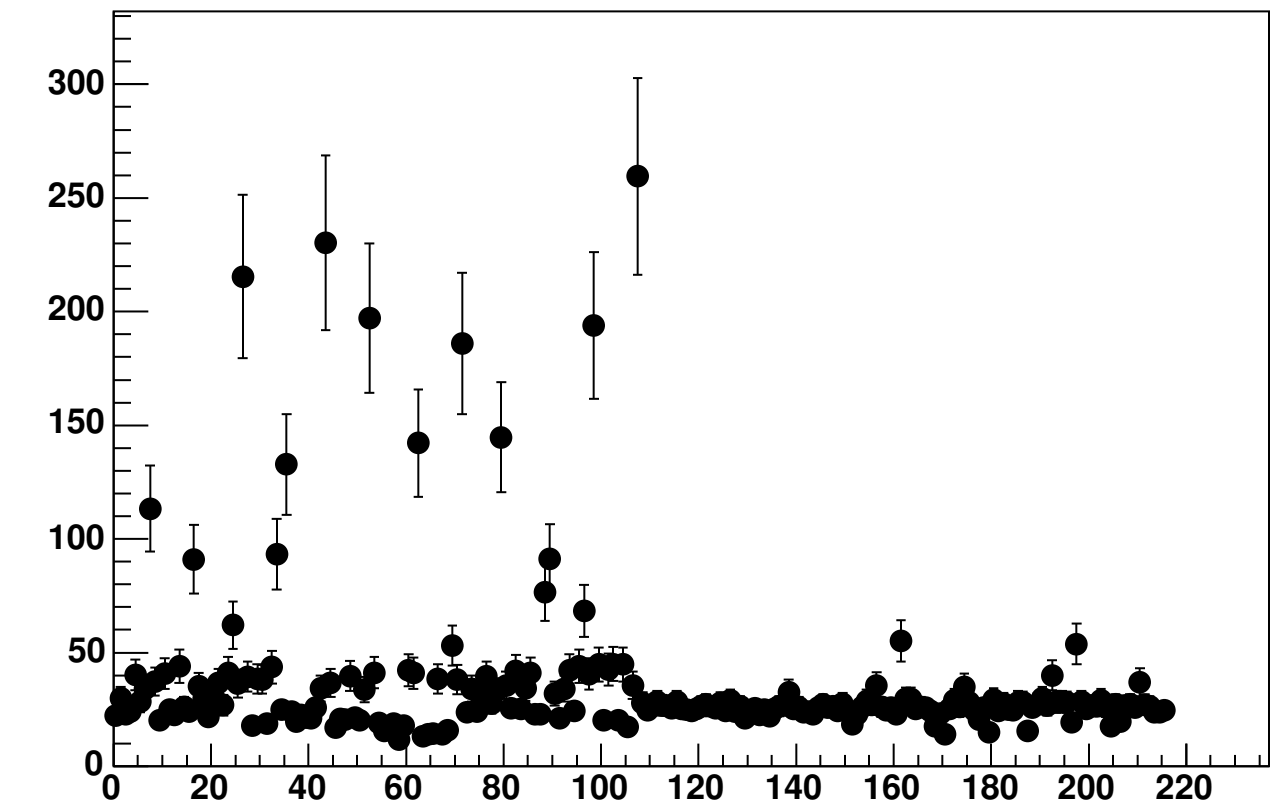
Enable 5, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



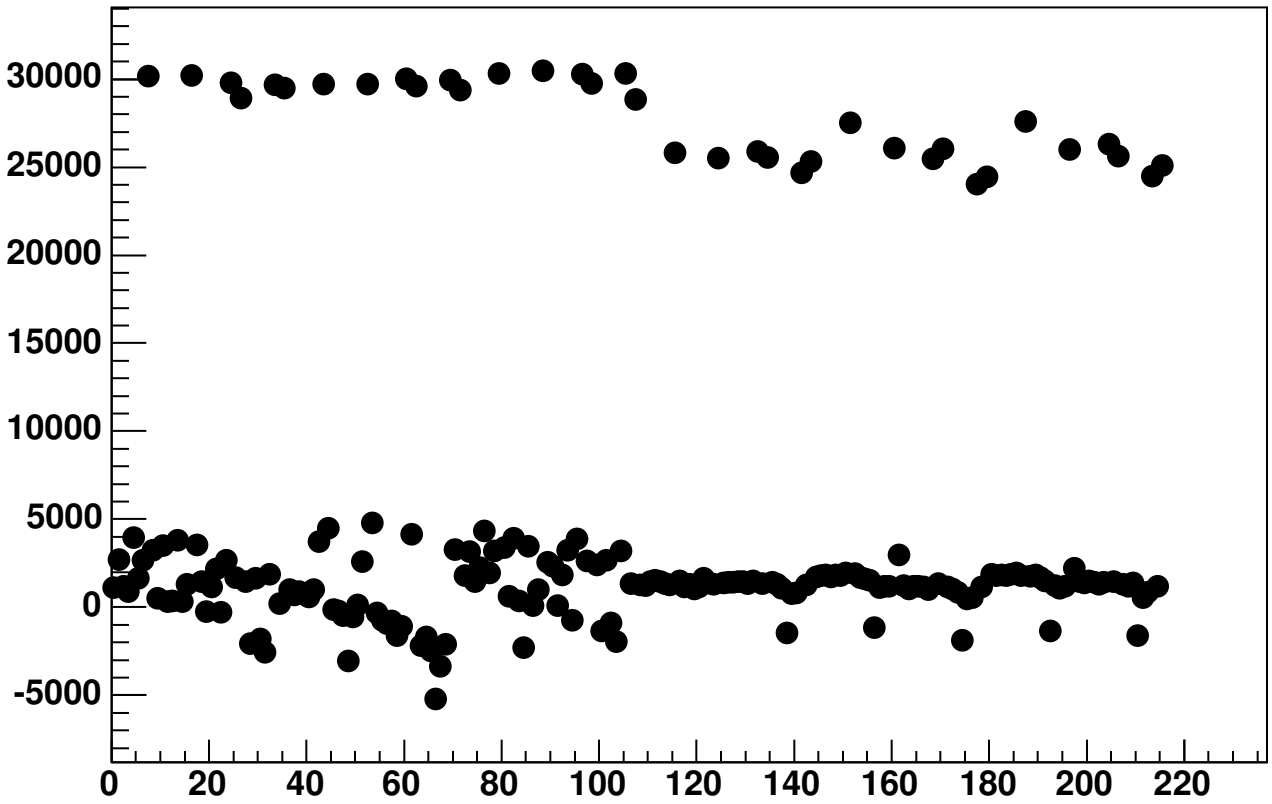
Enable 5, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



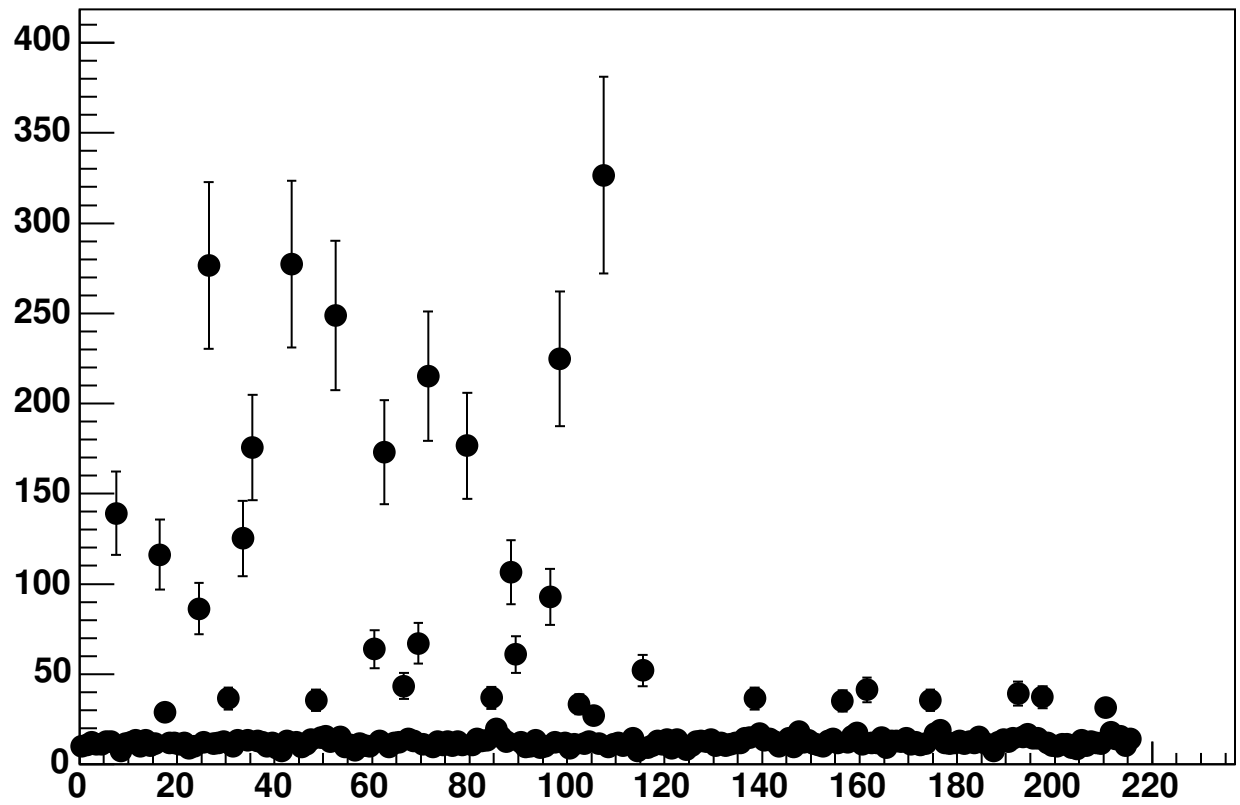
Enable 5, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



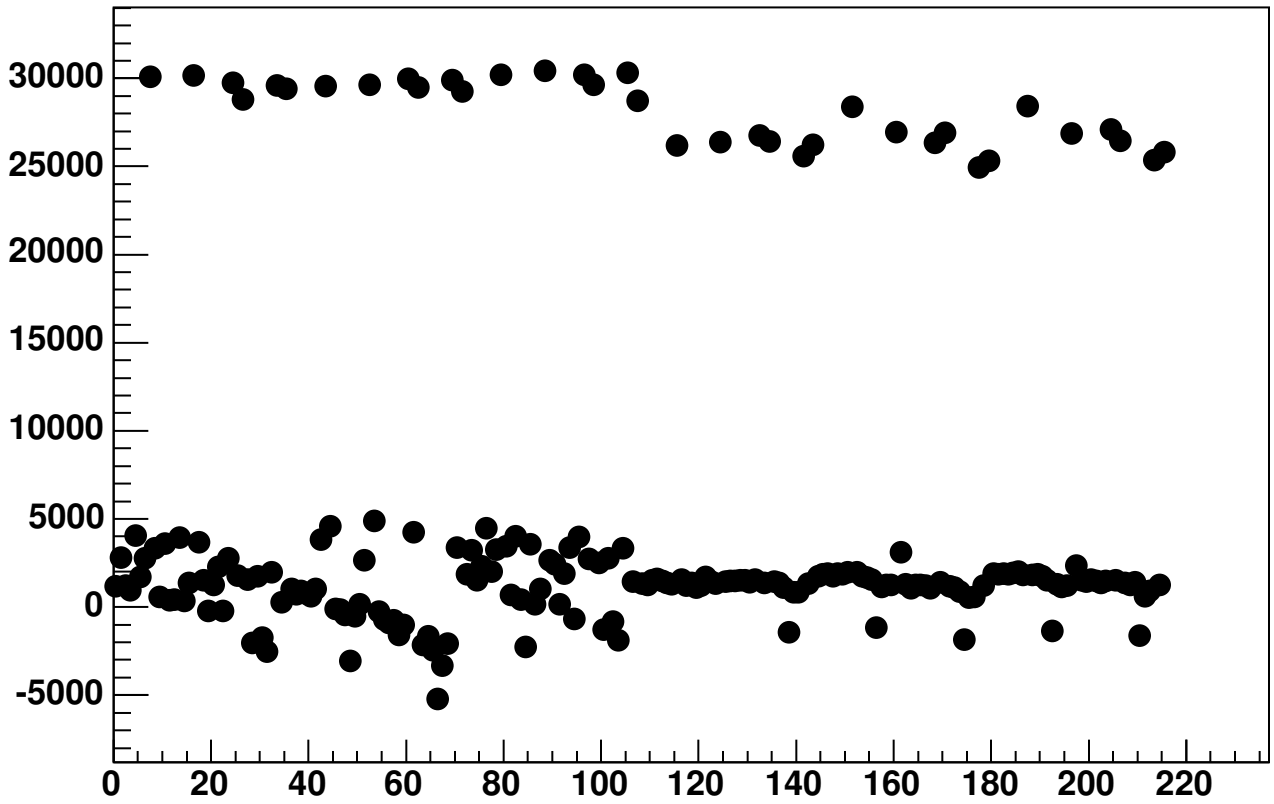
Enable 5, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



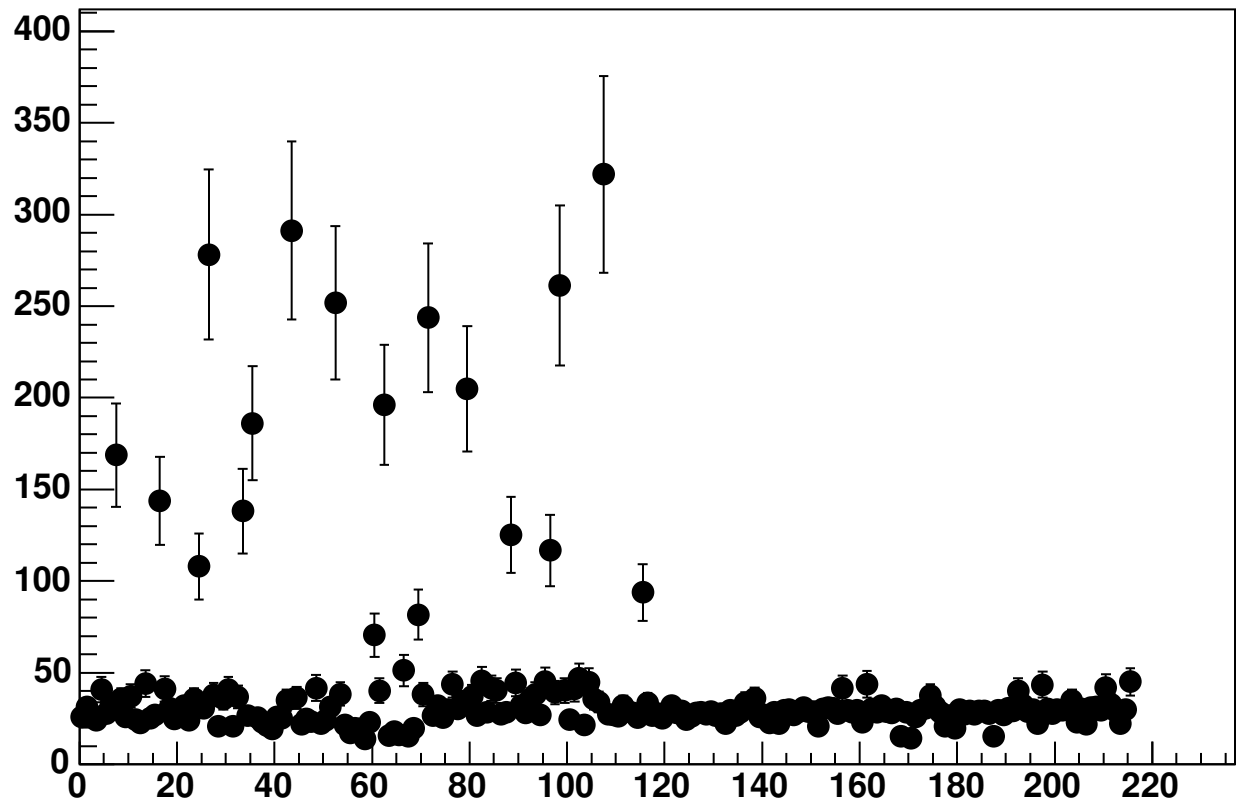
Enable 5, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



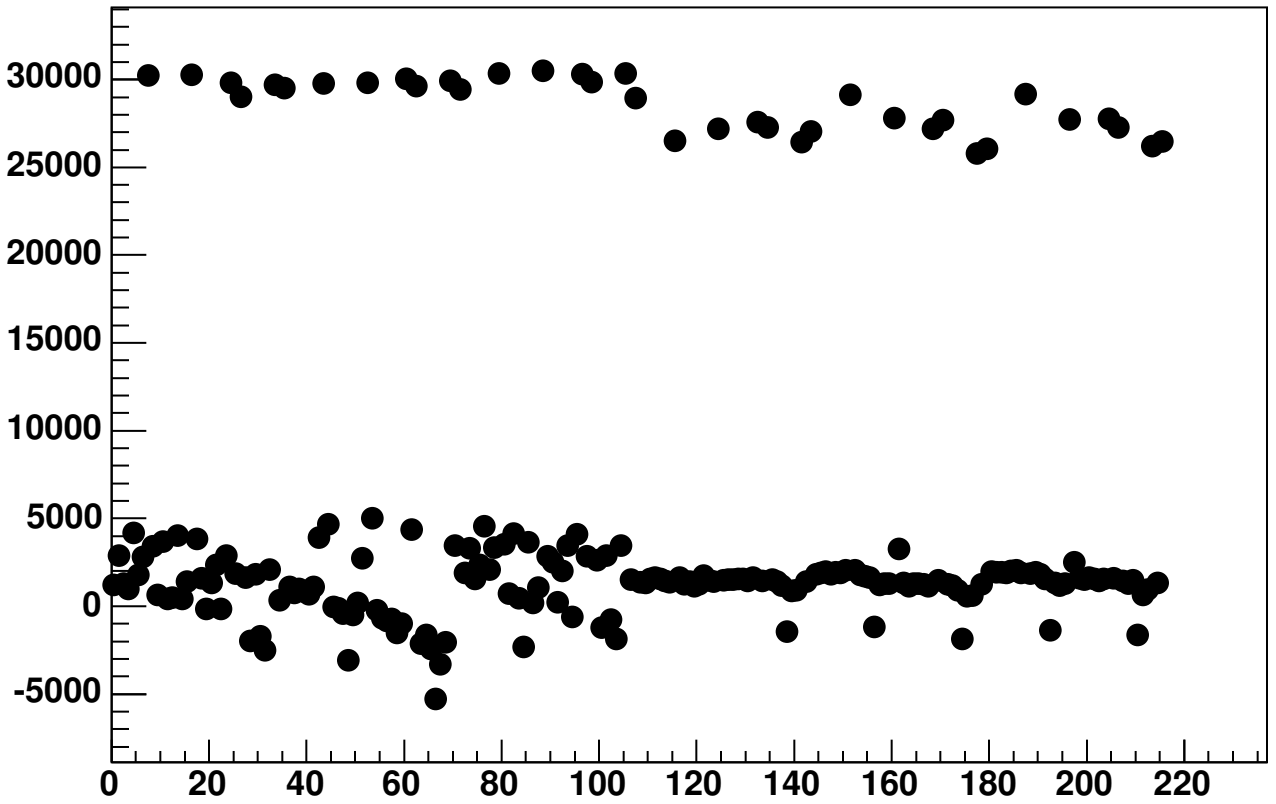
Enable 5, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



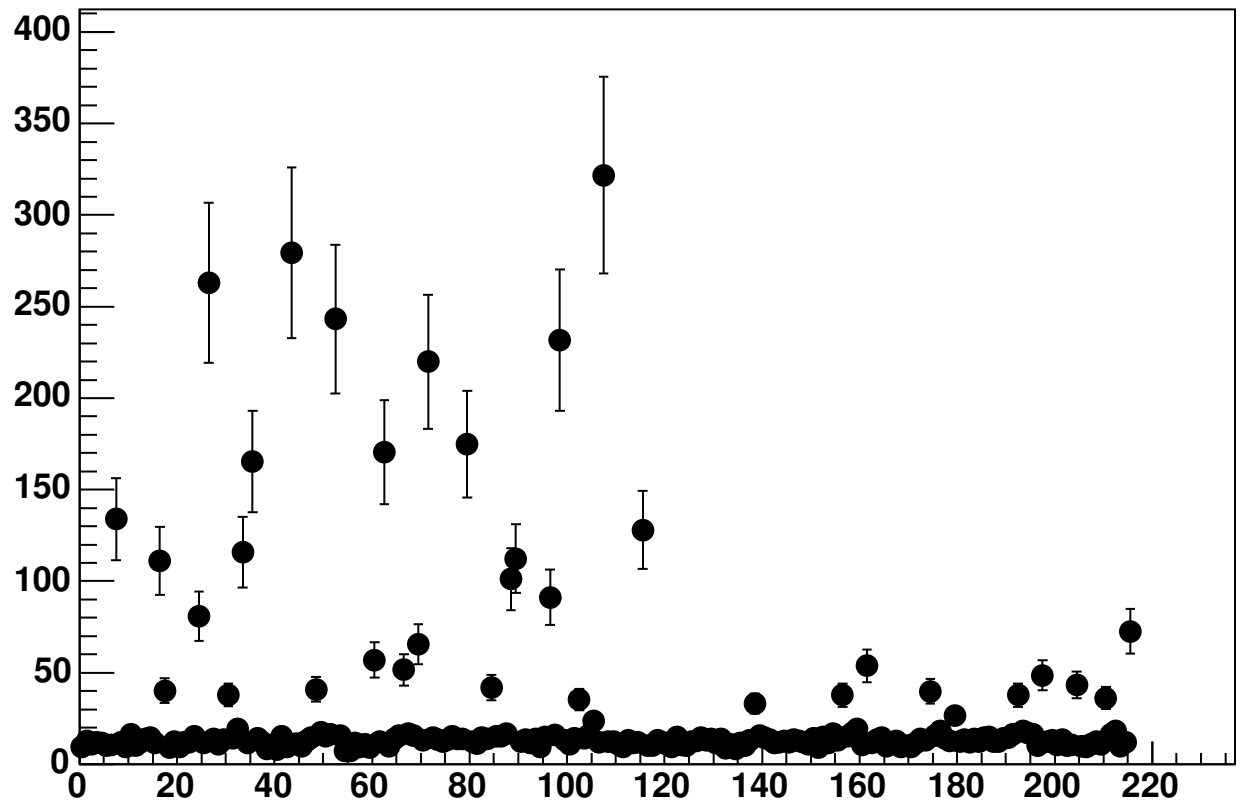
Enable 5, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



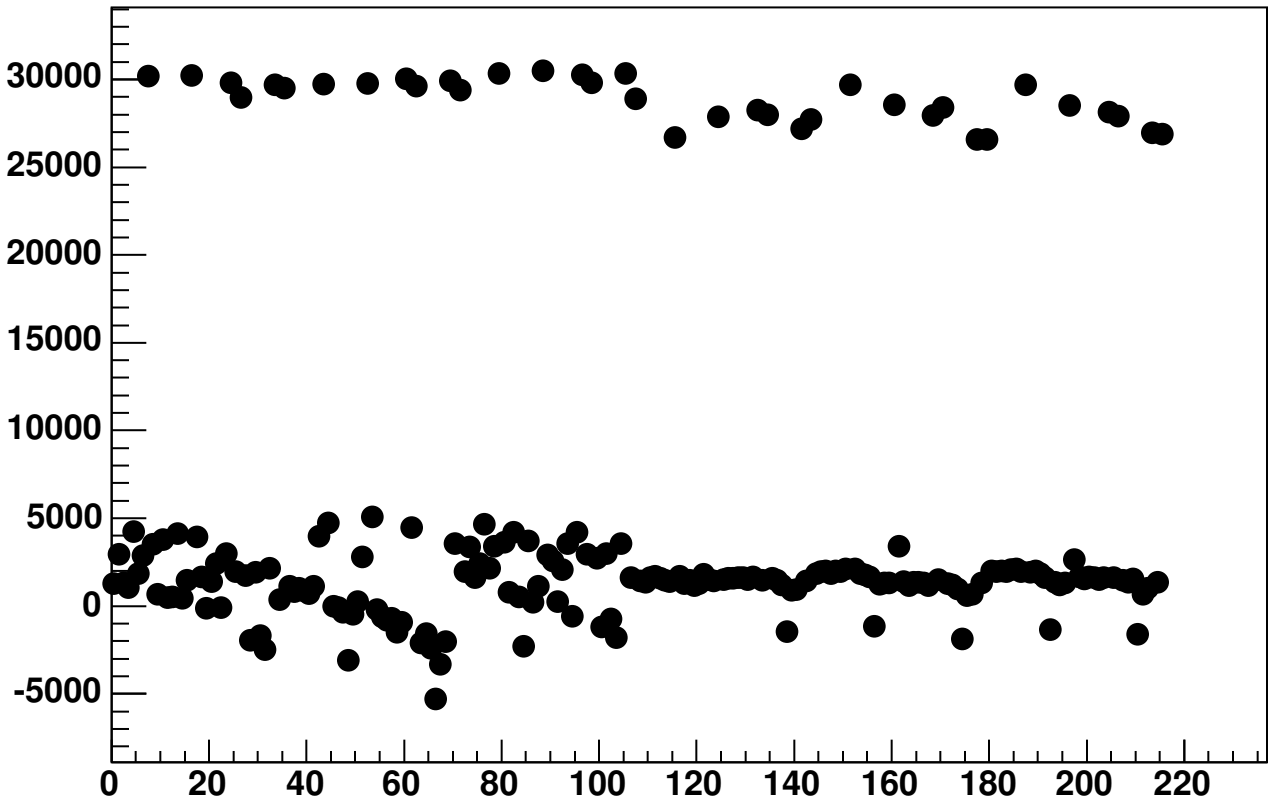
Enable 5, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



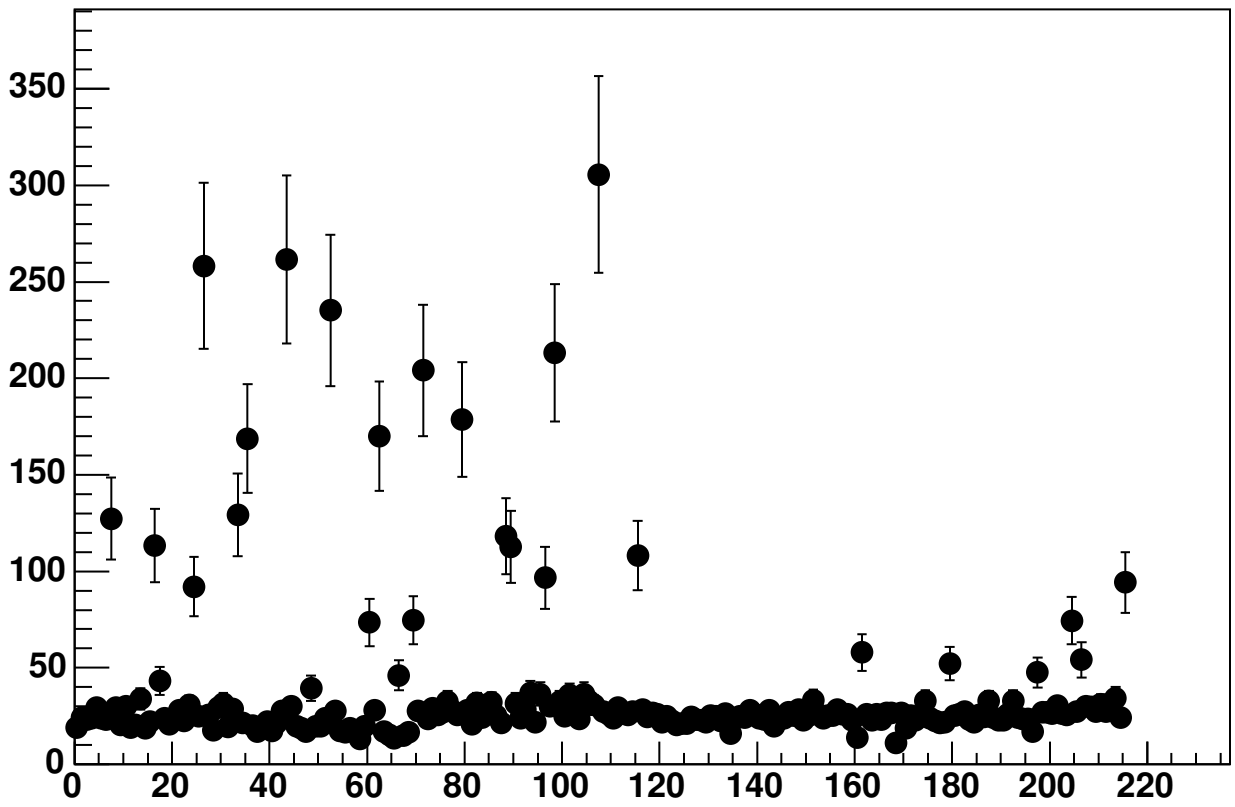
Enable 5, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



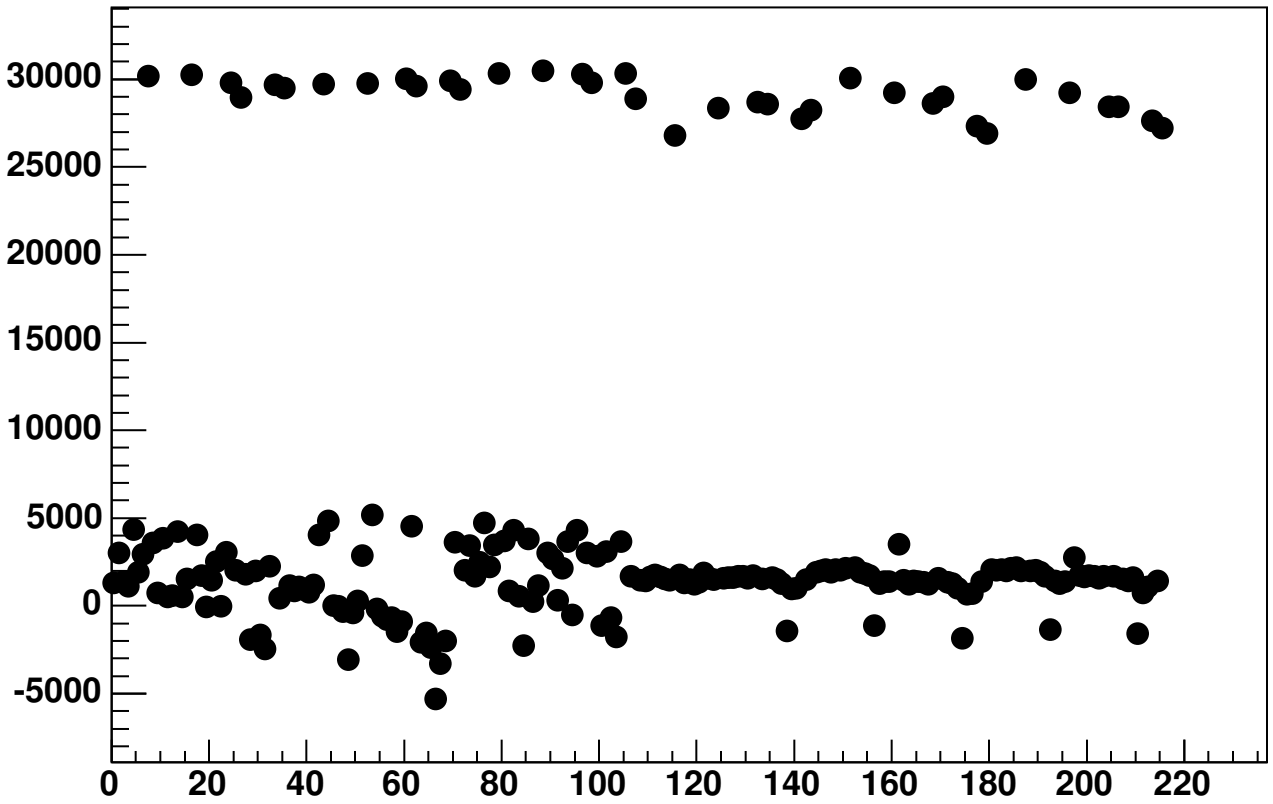
Enable 5, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



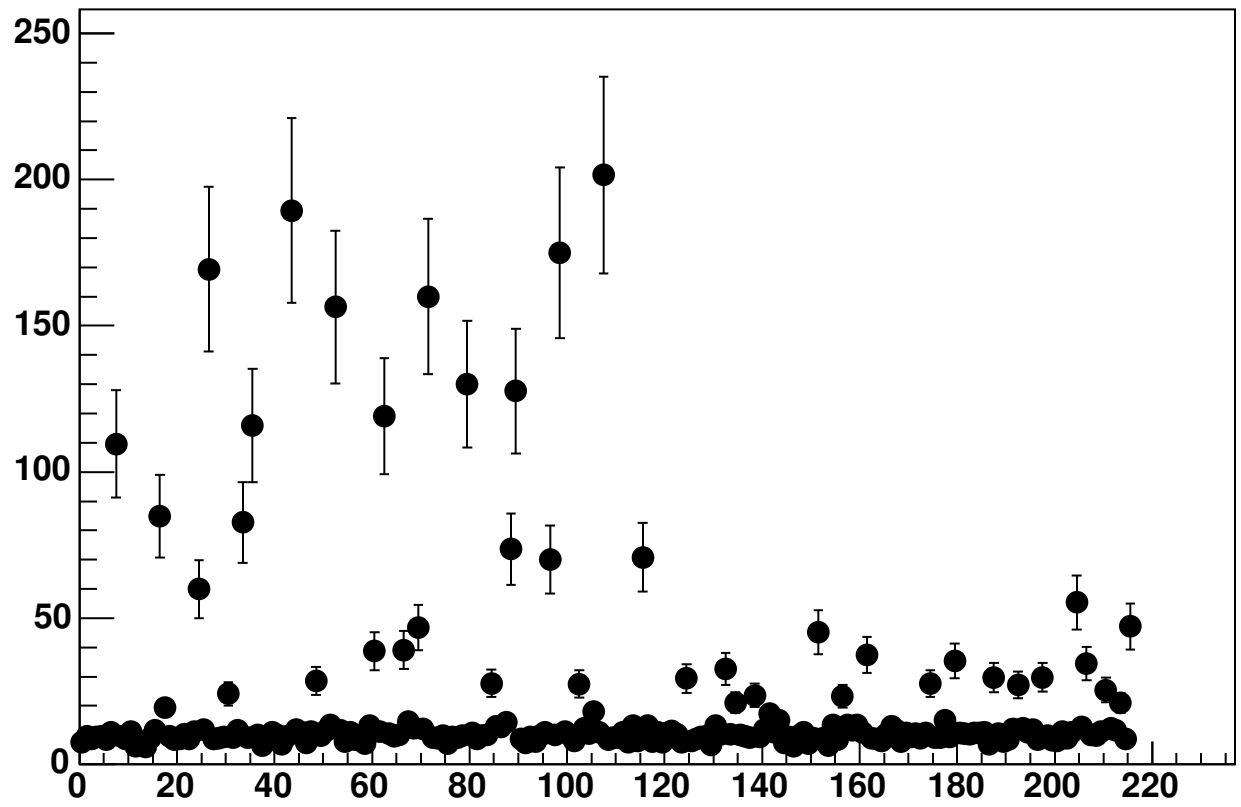
Enable 5, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



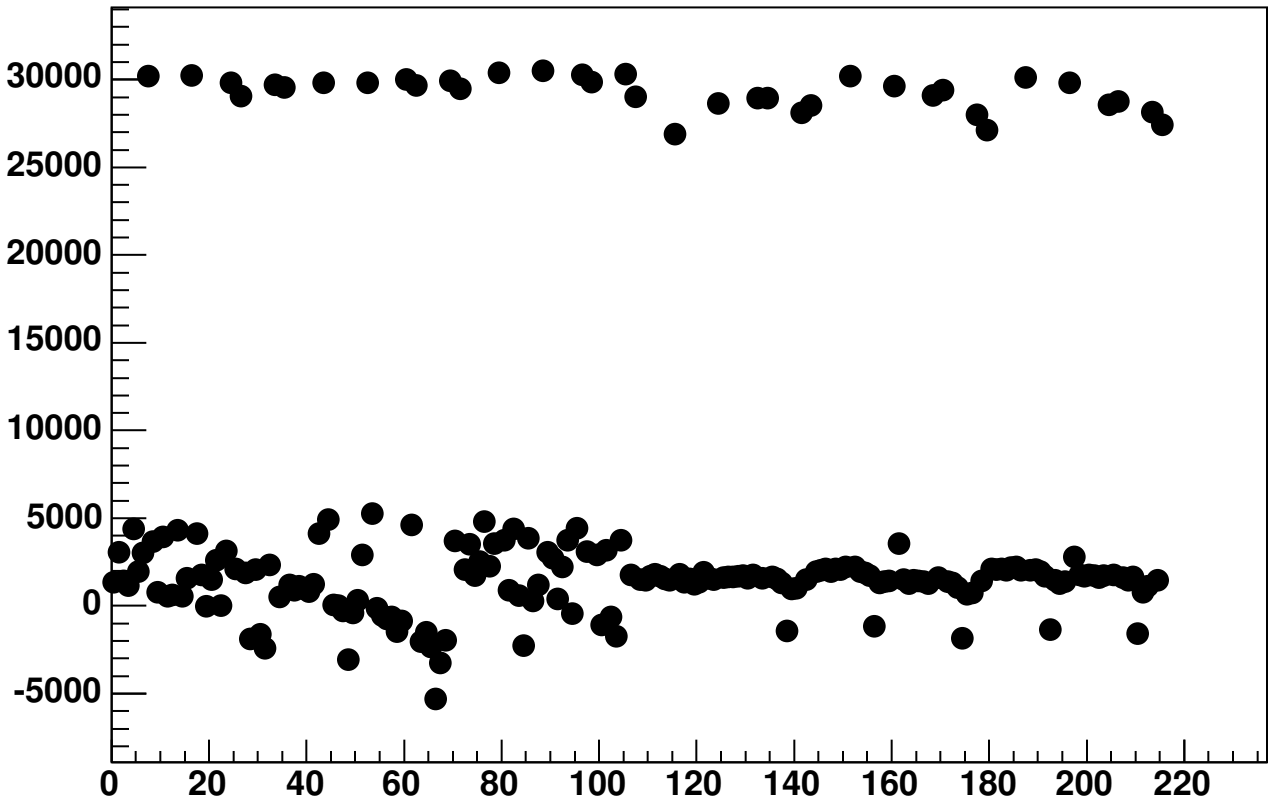
Enable 5, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



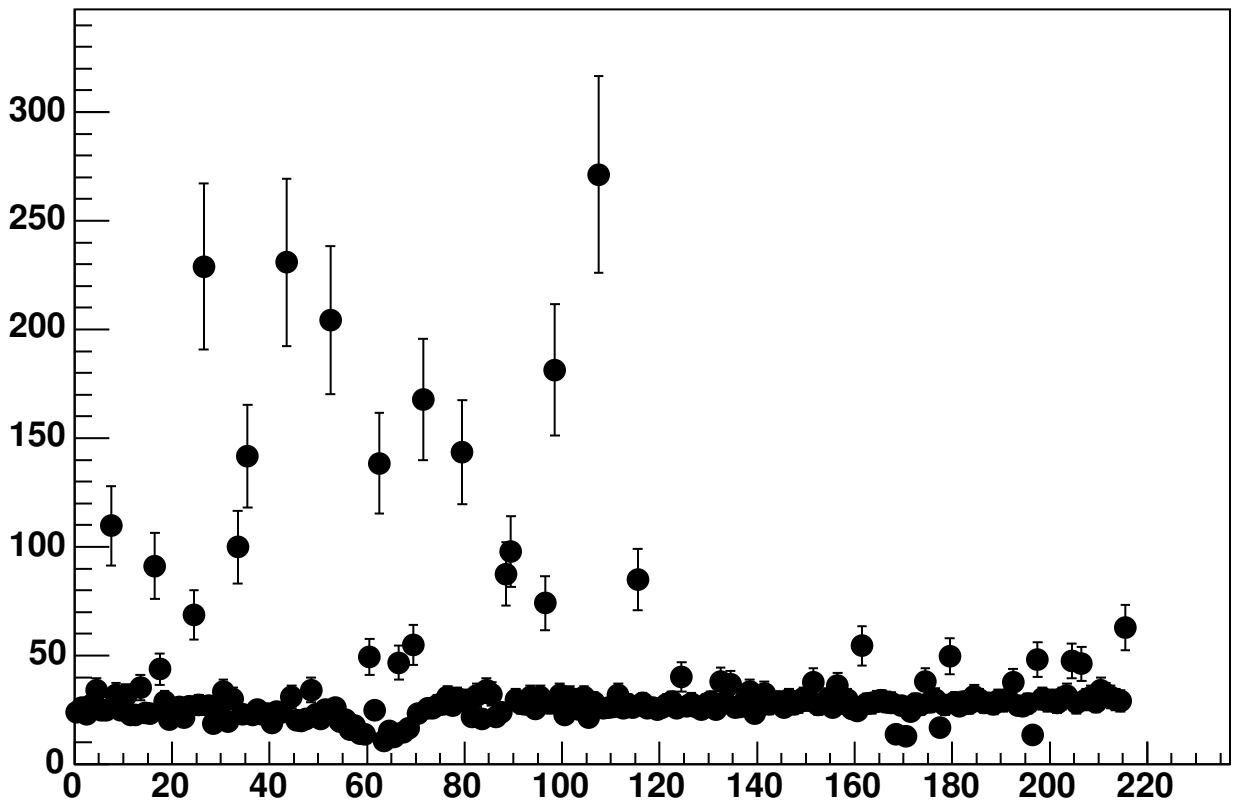
Enable 5, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



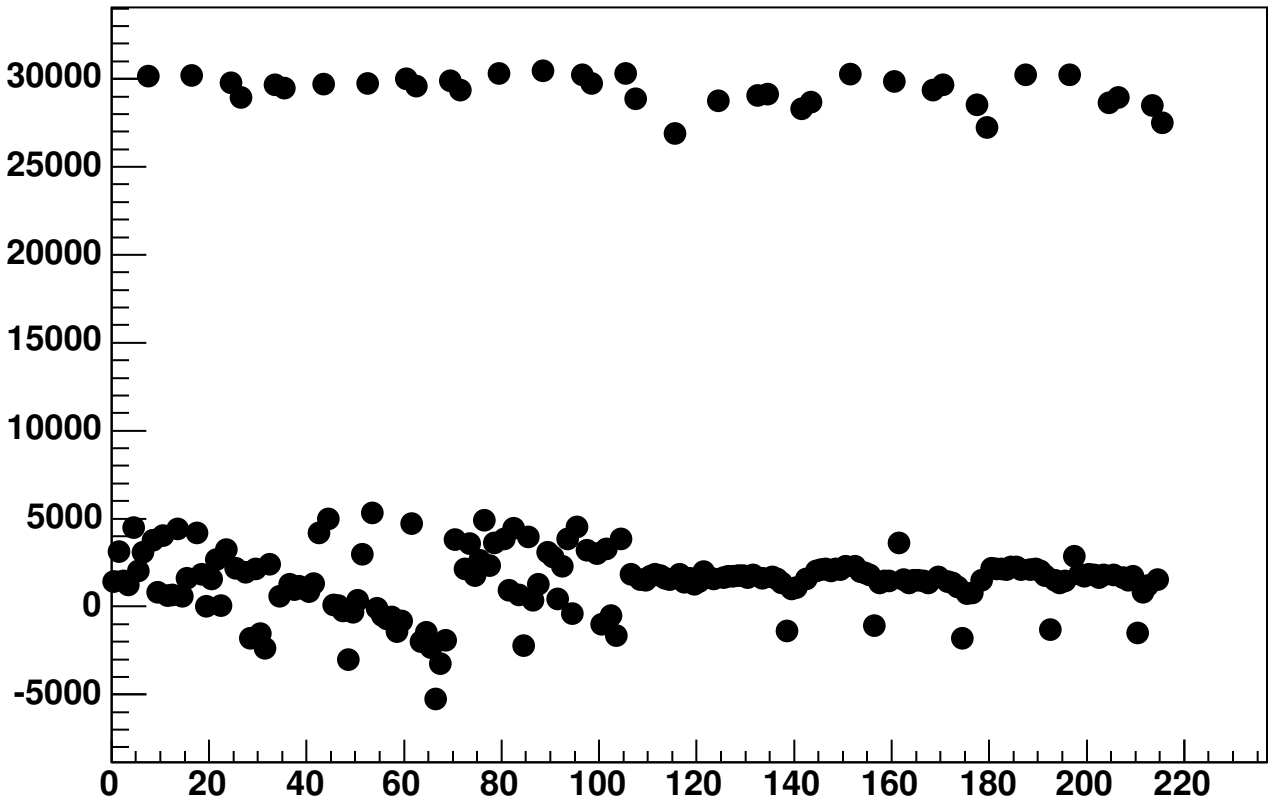
Enable 5, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



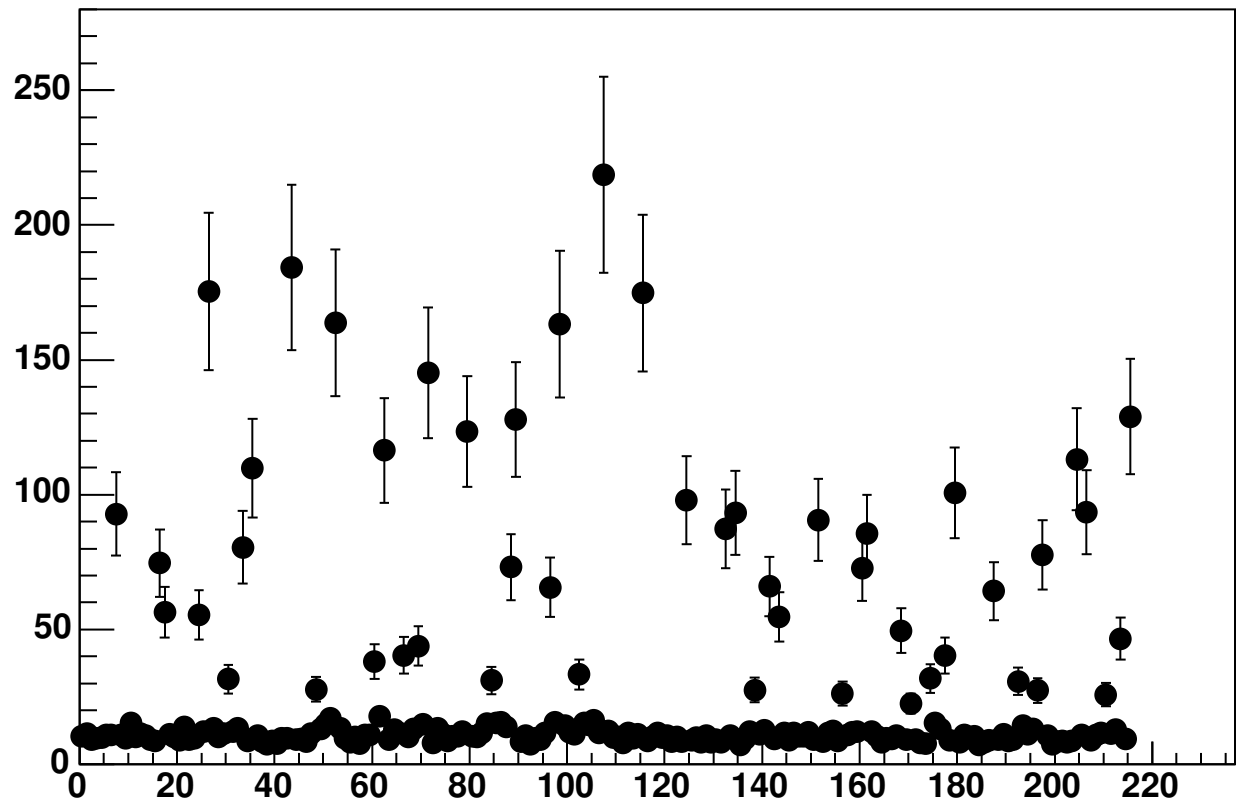
Enable 5, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan

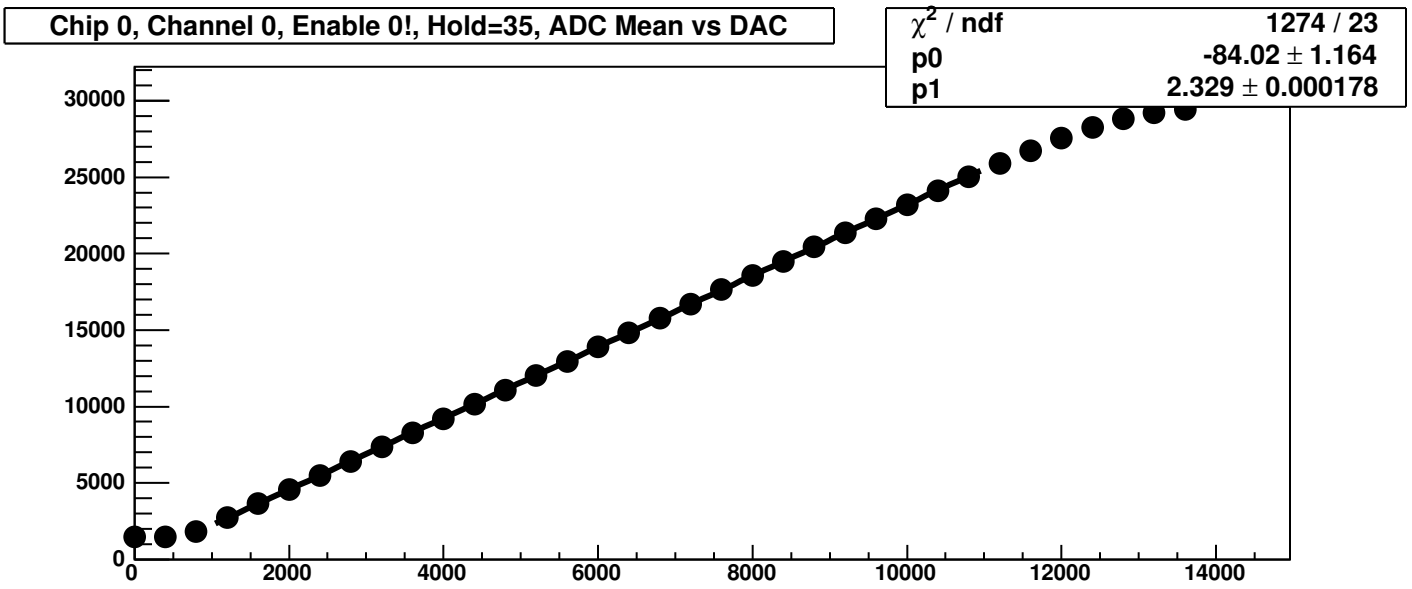


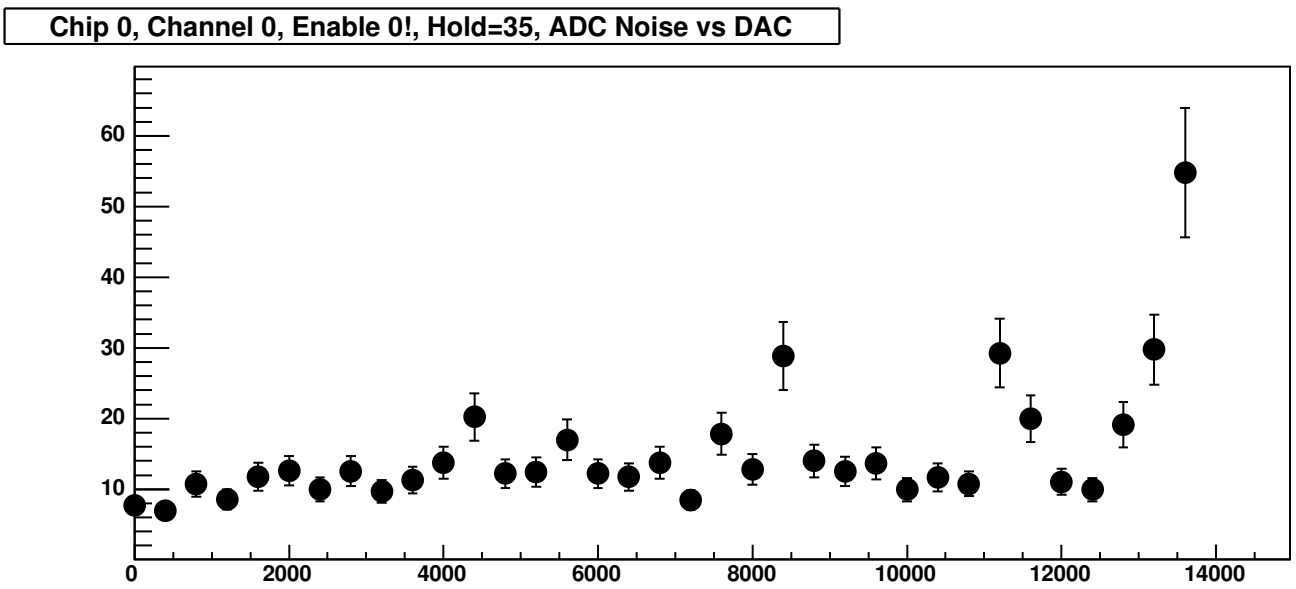
Enable 5, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan

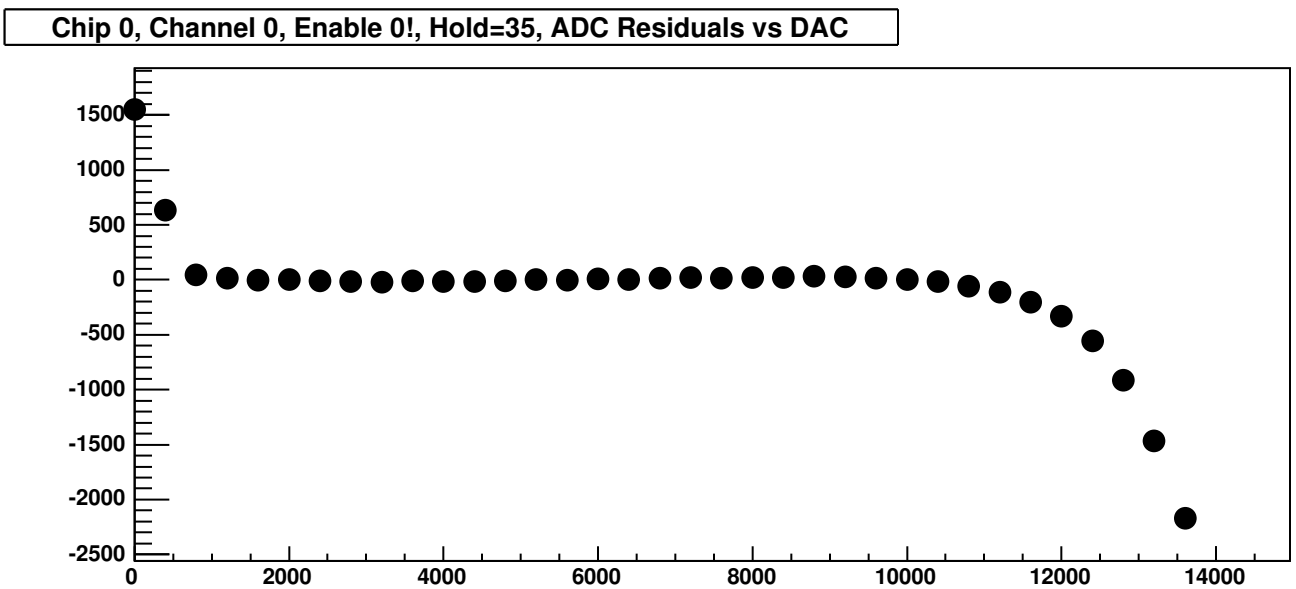


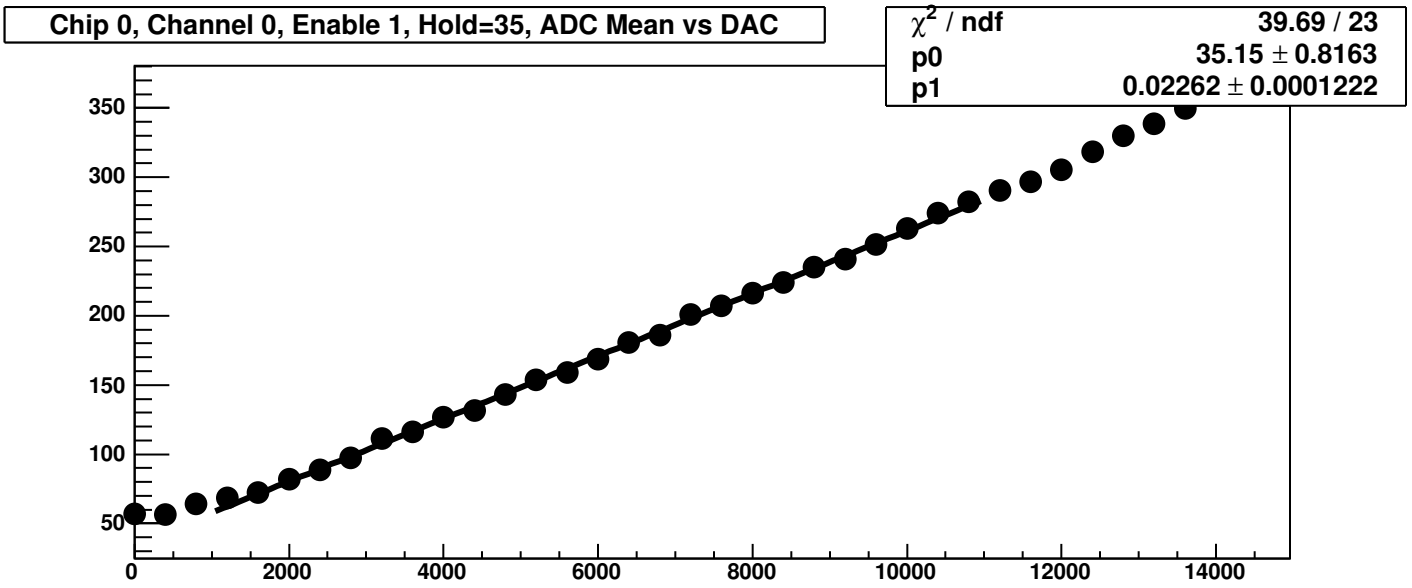
Enable 5, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan

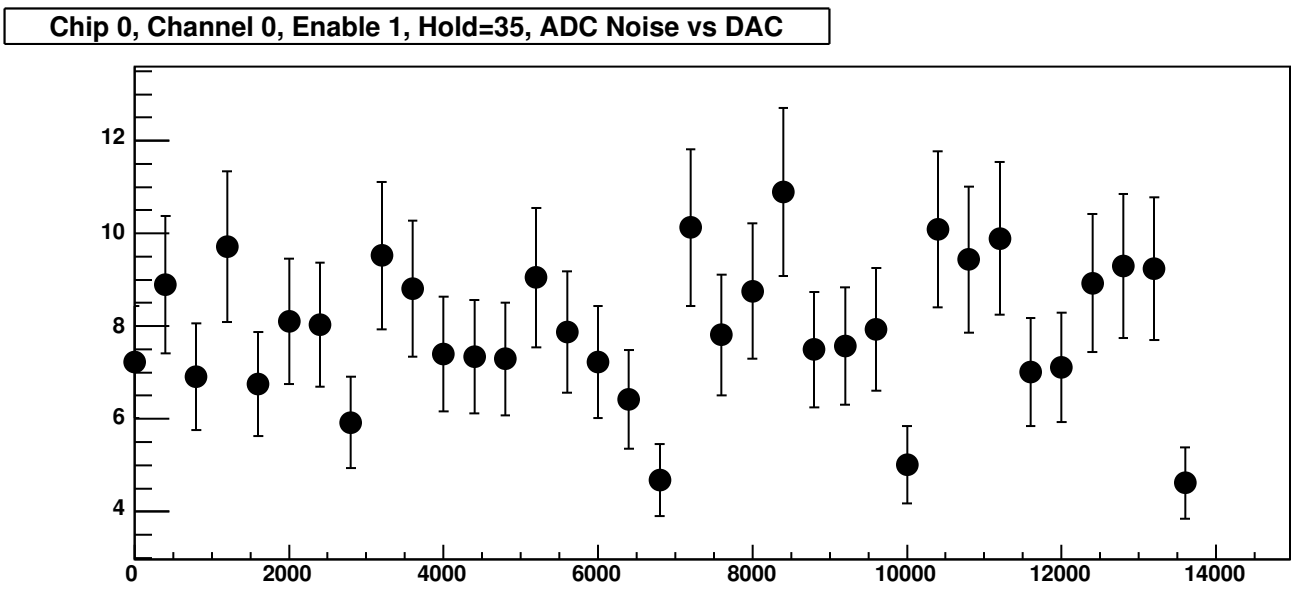


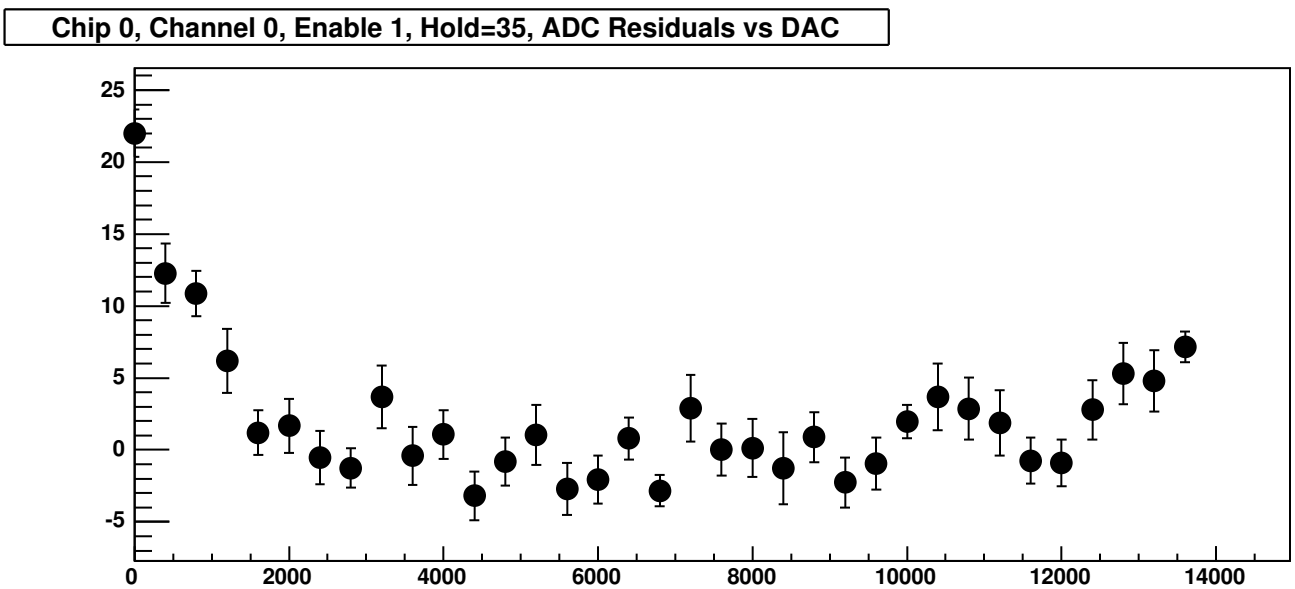

Chip 0, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1274 / 23
p0 -84.02 ± 1.164
p1 2.329 ± 0.000178
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

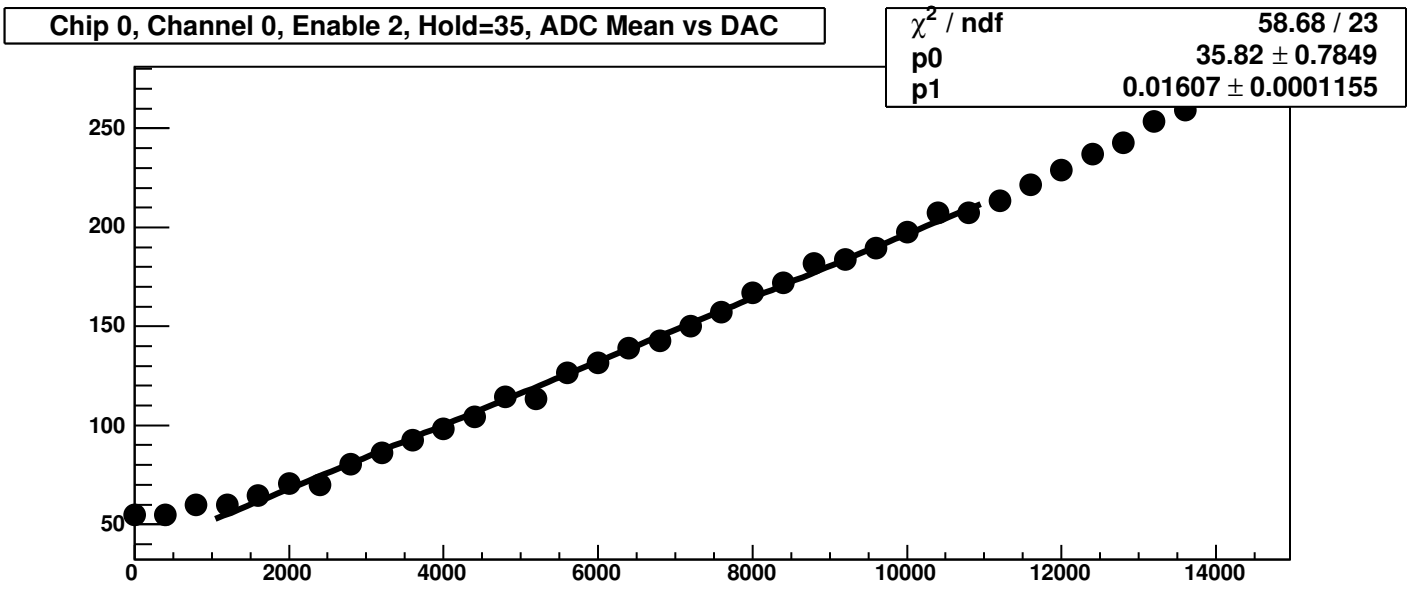

Chip 0, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

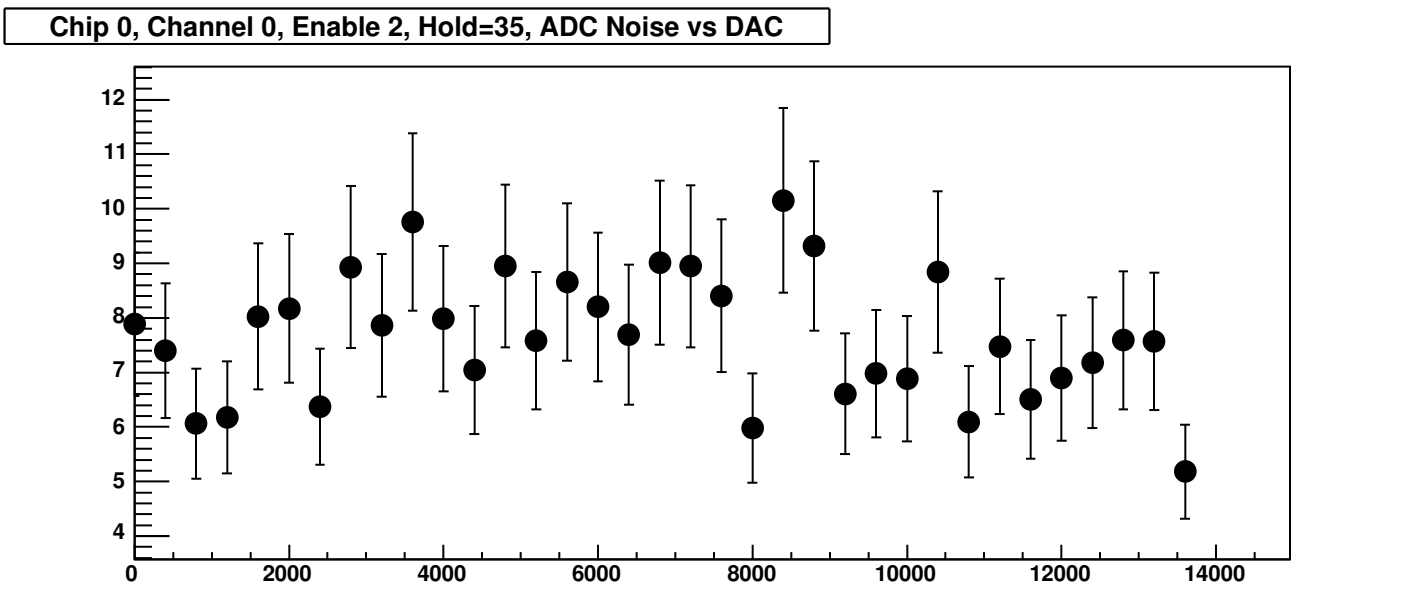

Chip 0, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

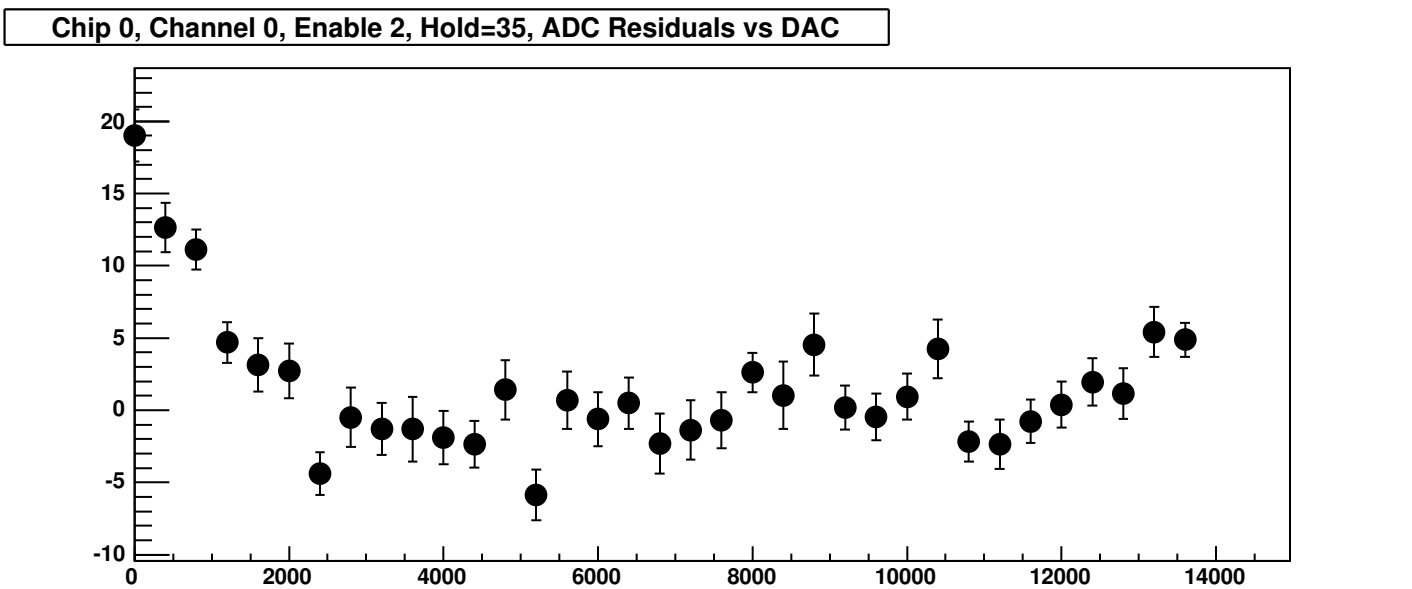

Chip 0, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 39.69 / 23
p0 35.15 ± 0.8163
p1 0.02262 ± 0.0001222
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

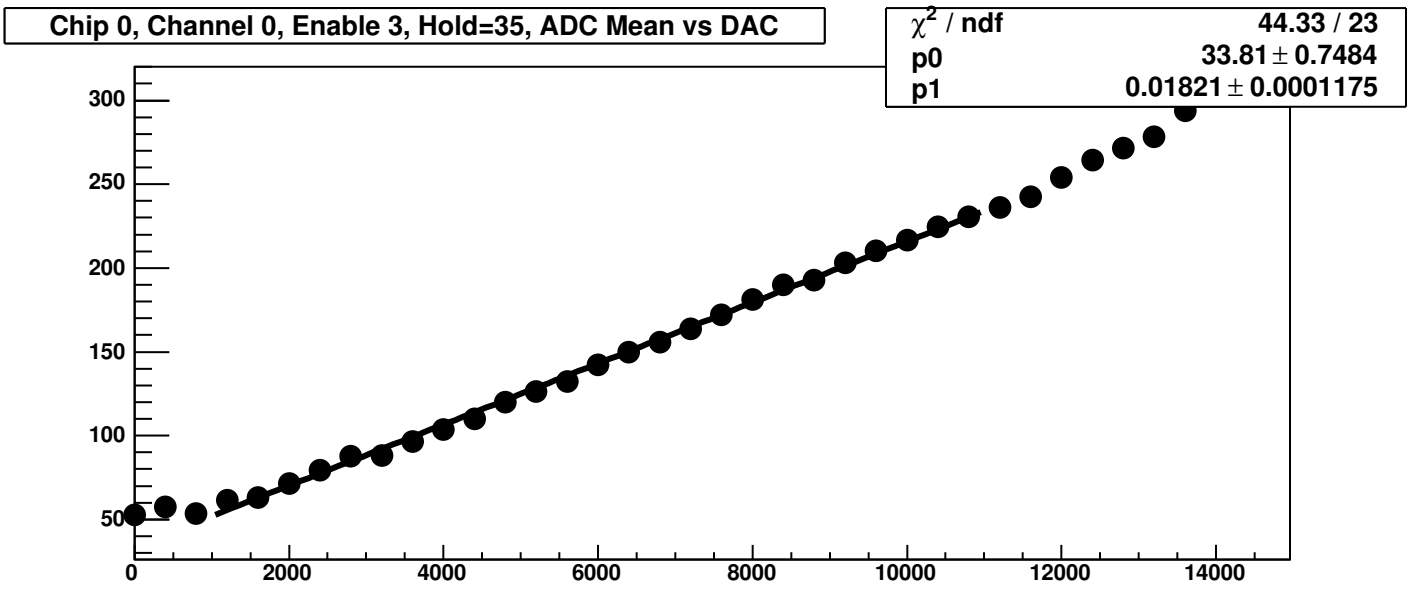

Chip 0, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

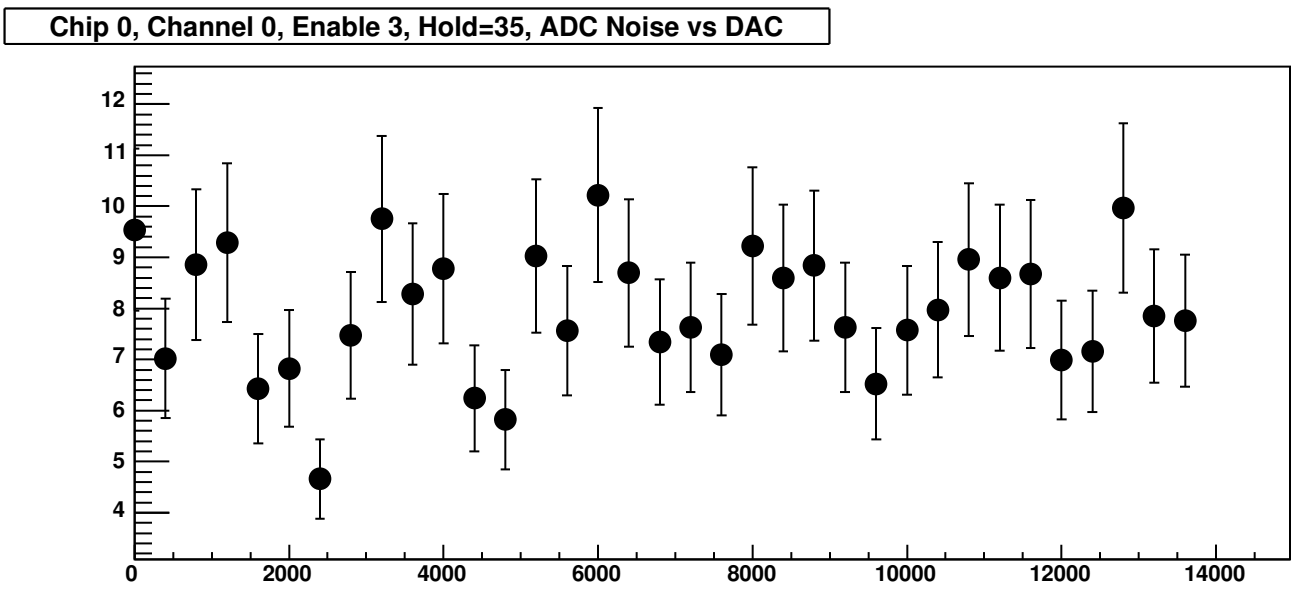

Chip 0, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

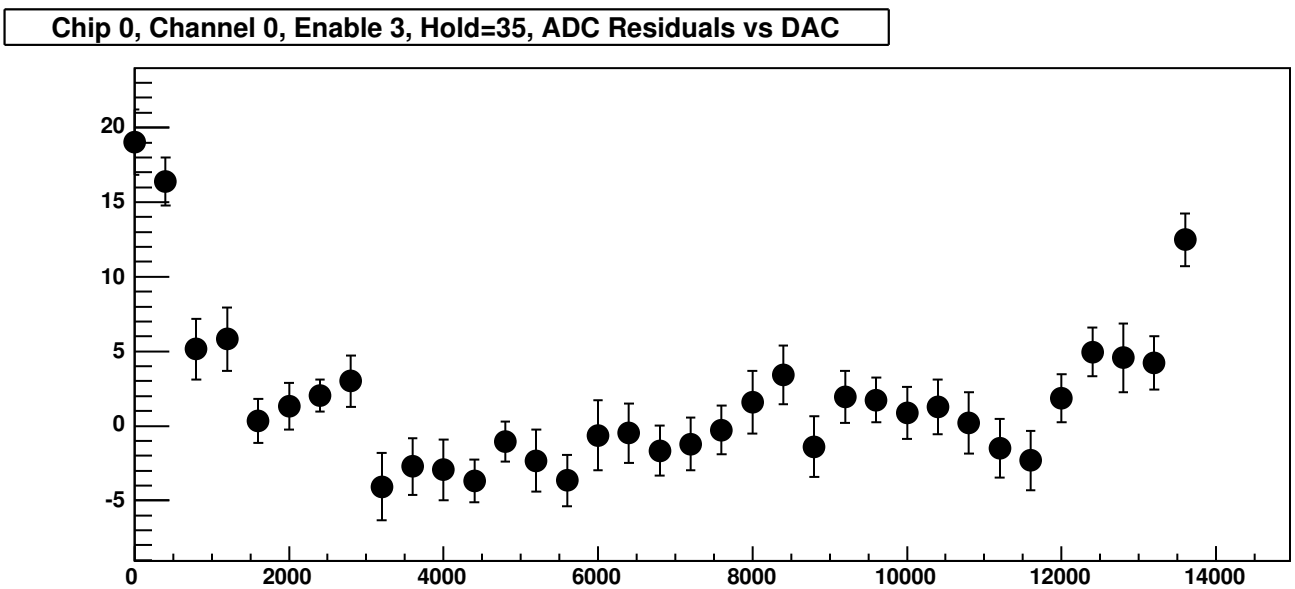

Chip 0, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 58.68 / 23
p0 35.82 ± 0.7849
p1 0.01607 ± 0.0001155
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

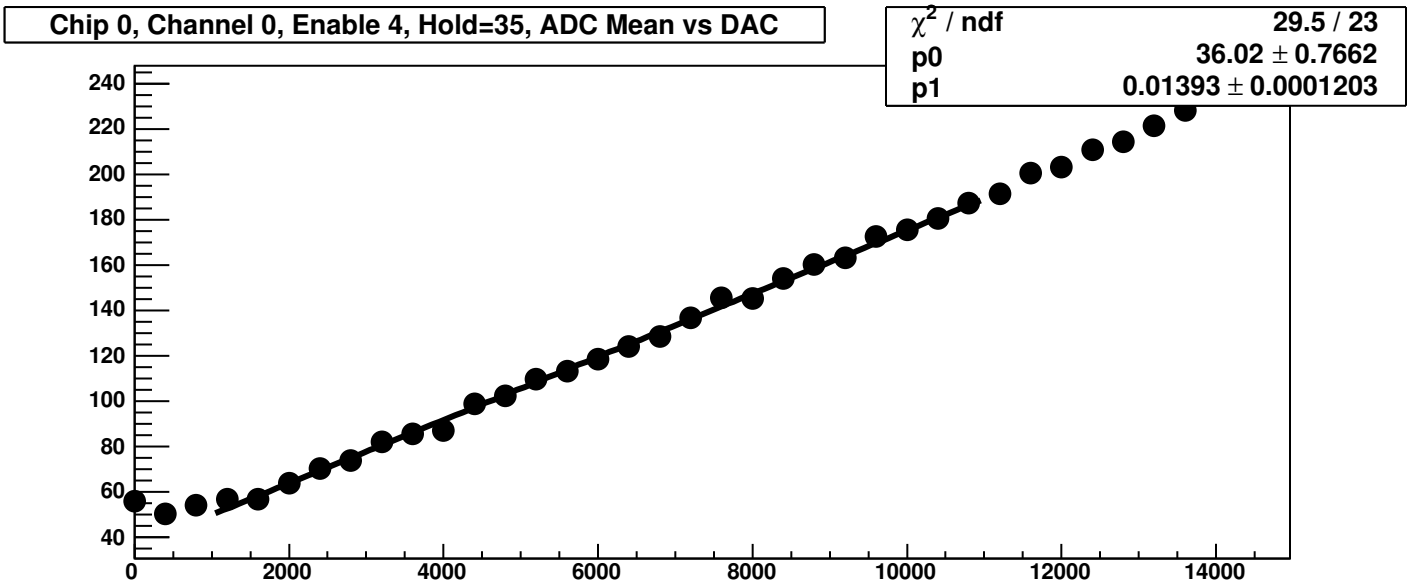

Chip 0, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

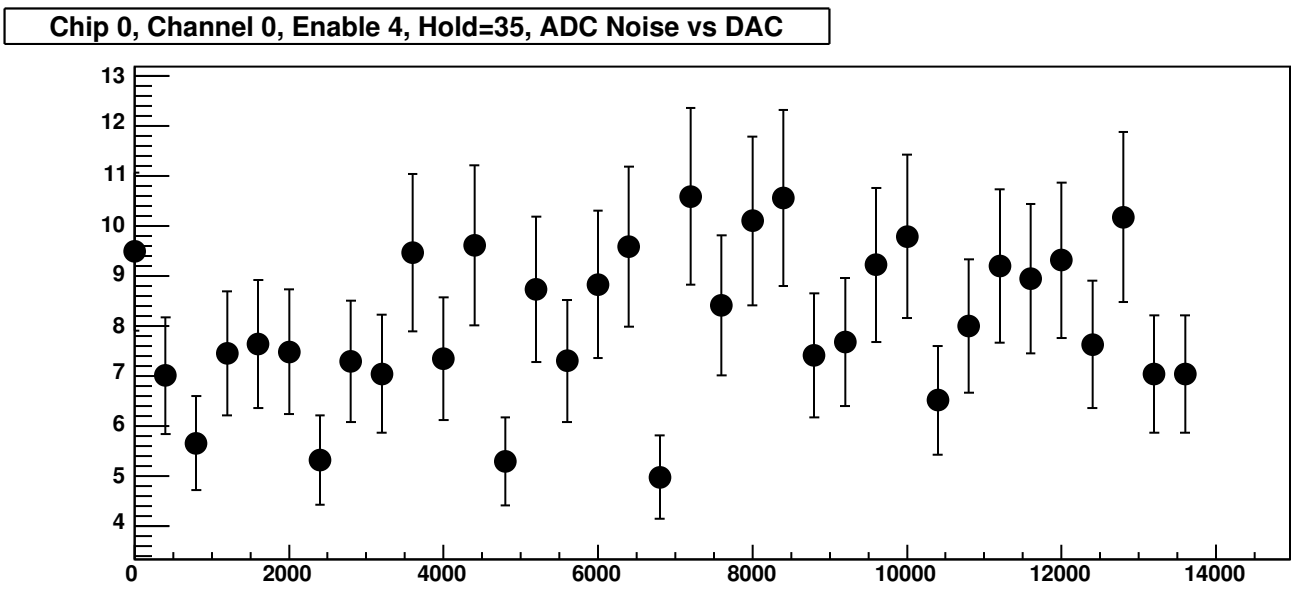

Chip 0, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

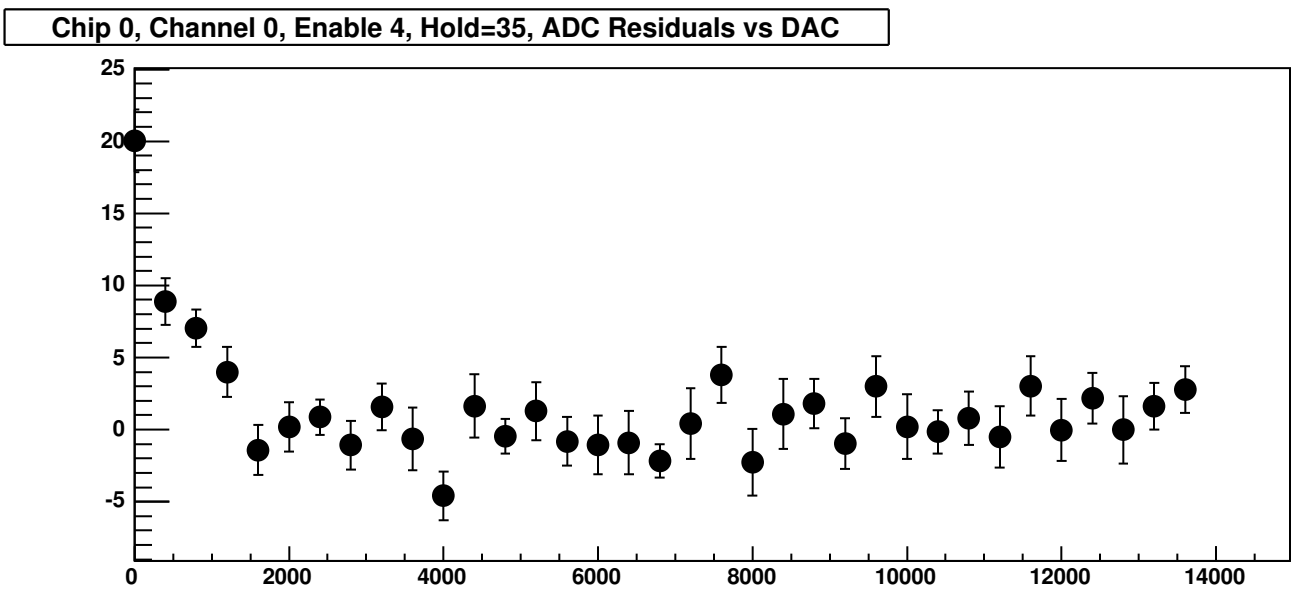

Chip 0, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 44.33 / 23
p0 33.81 ± 0.7484
p1 0.01821 ± 0.0001175
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

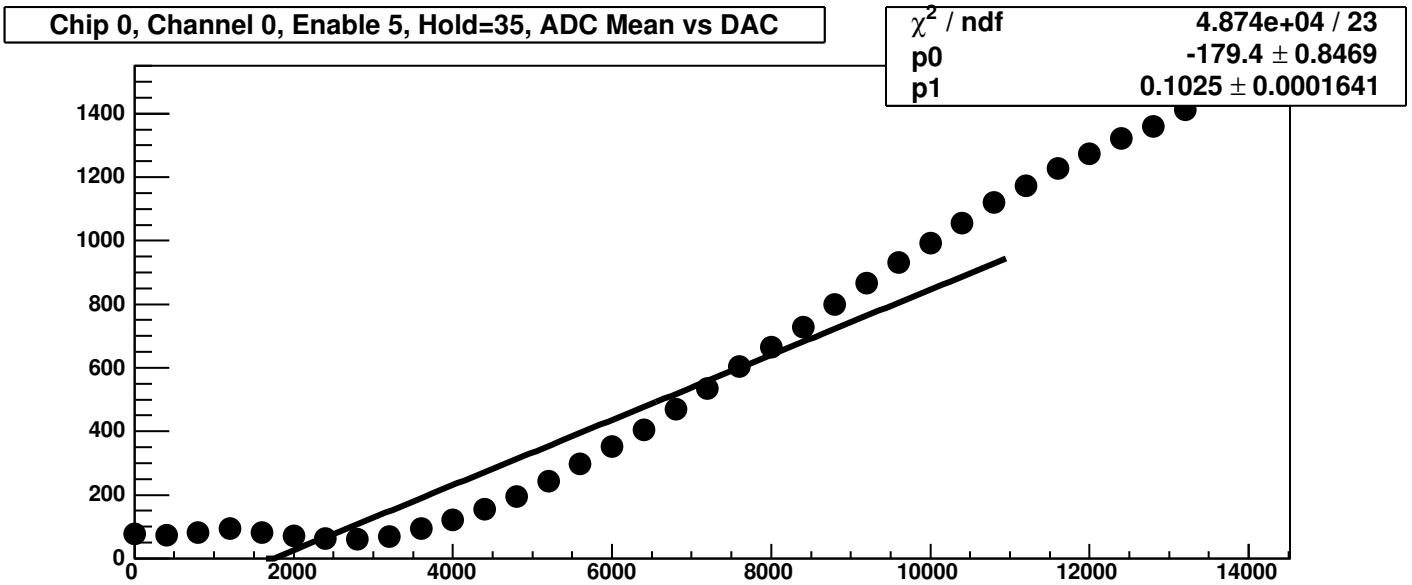

Chip 0, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

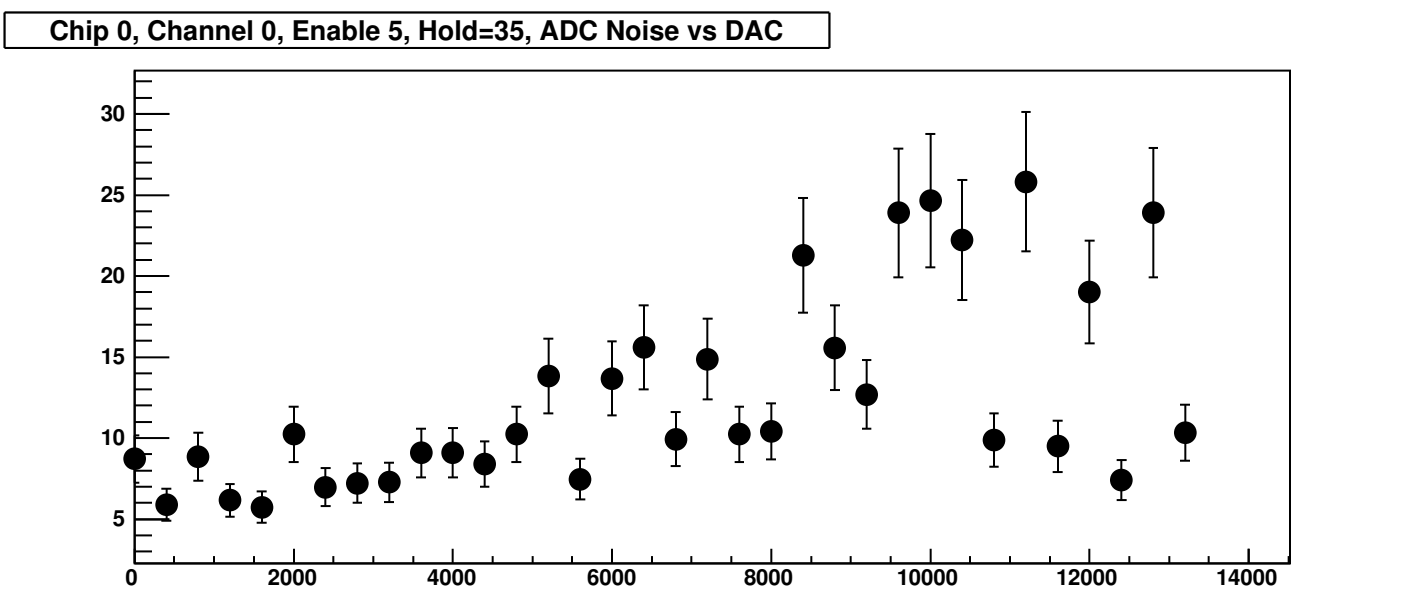

Chip 0, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

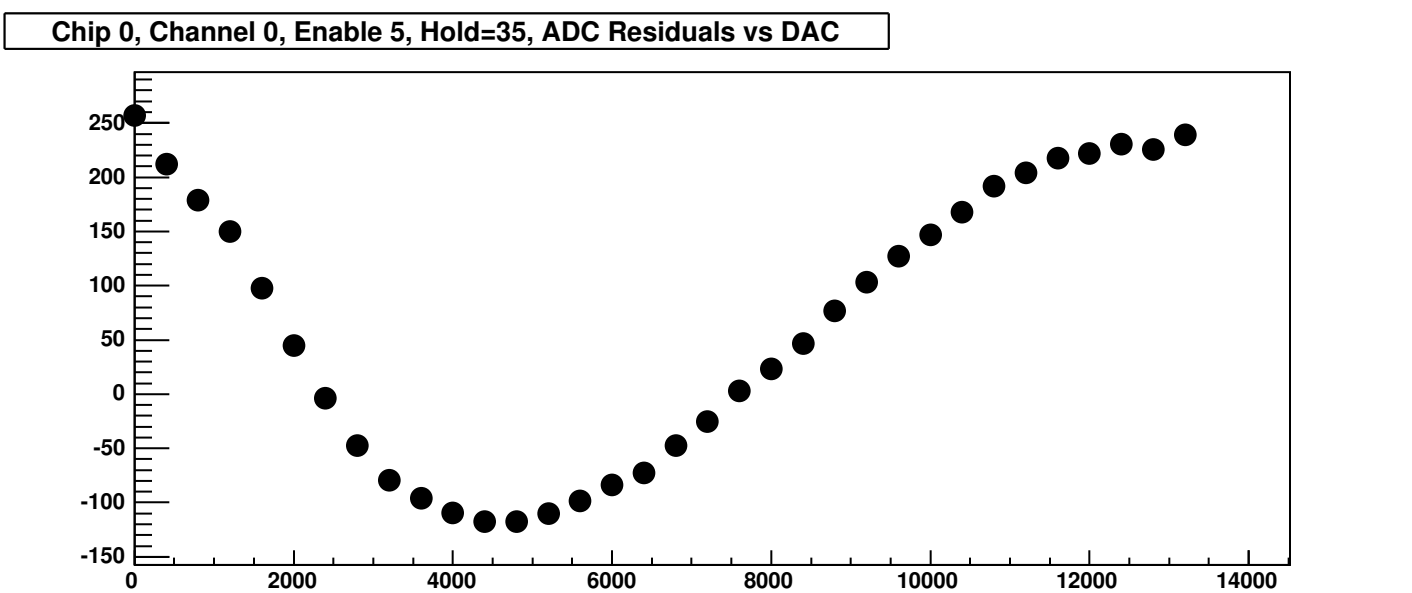

Chip 0, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.5 / 23
p0 36.02 ± 0.7662
p1 0.01393 ± 0.0001203
240
220
200
180
160
140
120
100
80
60
40
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

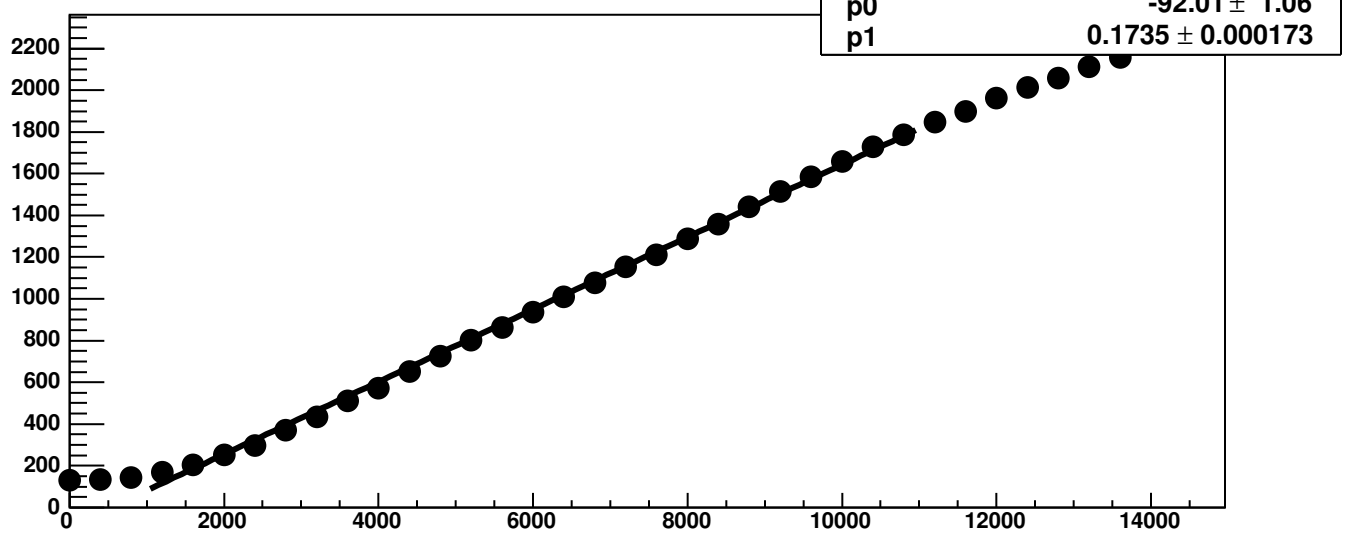

Chip 0, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4.874e+04 / 23
p0 -179.4 ± 0.8469
p1 0.1025 ± 0.0001641
0
200
400
600
800
1000
1200
1400
0
2000
4000
6000
8000
10000
12000
14000

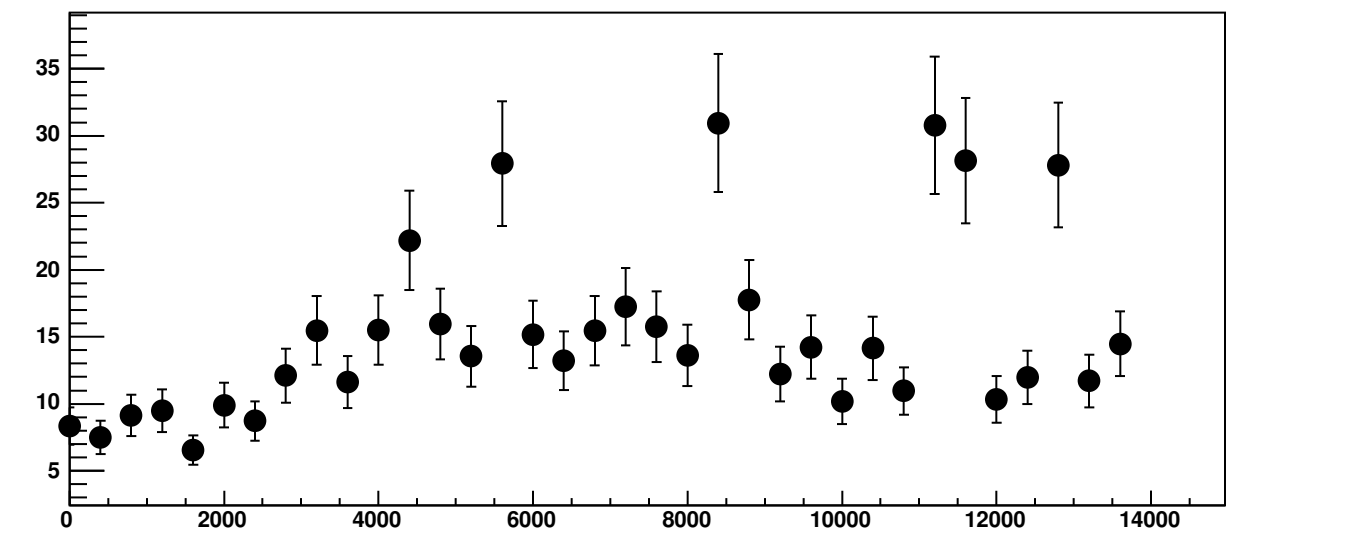

Chip 0, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
5
10
15
20
25
30
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
-150
-100
-50
0
50
100
150
200
250
0
2000
4000
6000
8000
10000
12000
14000

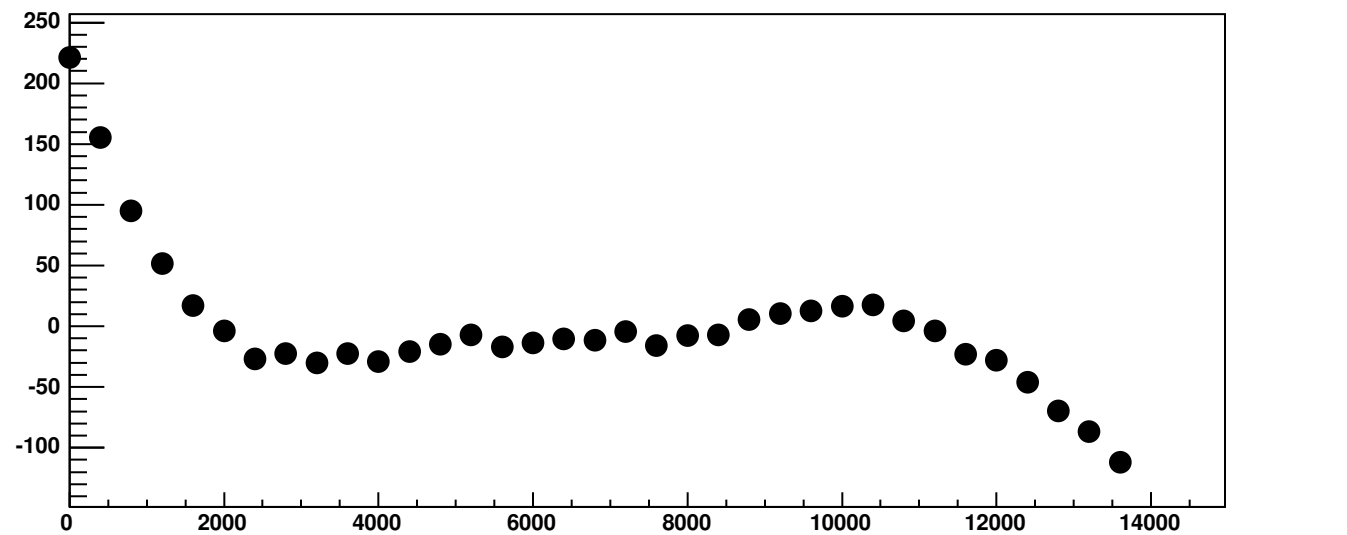
Chip 0, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC

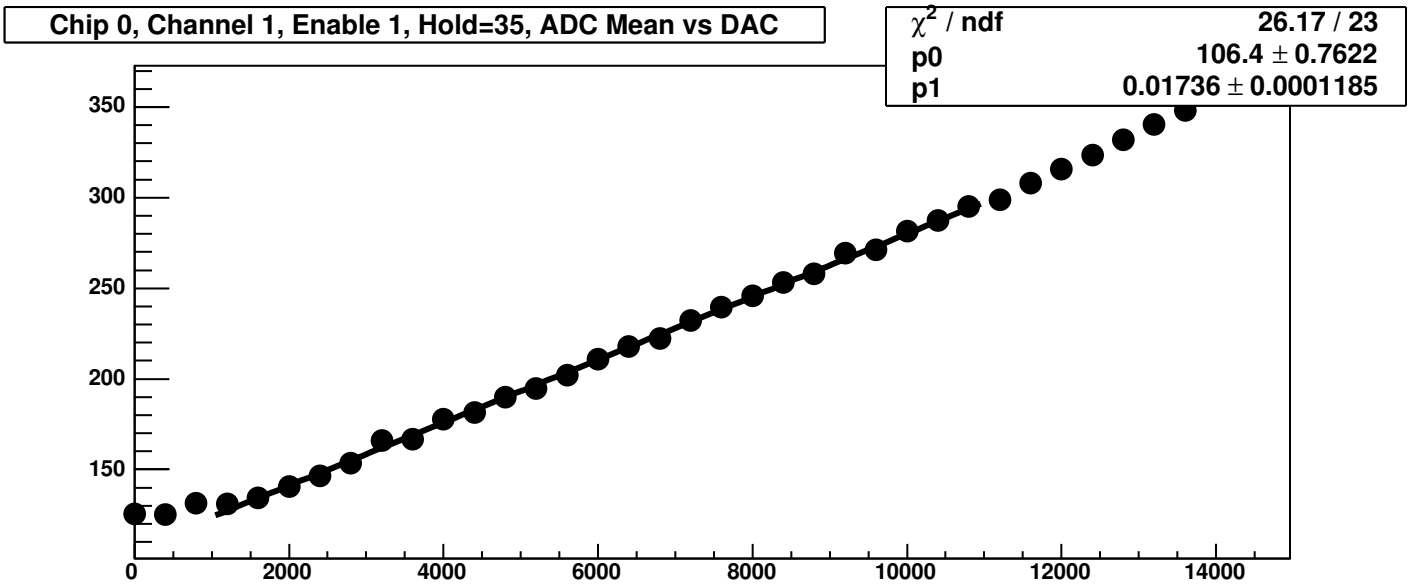


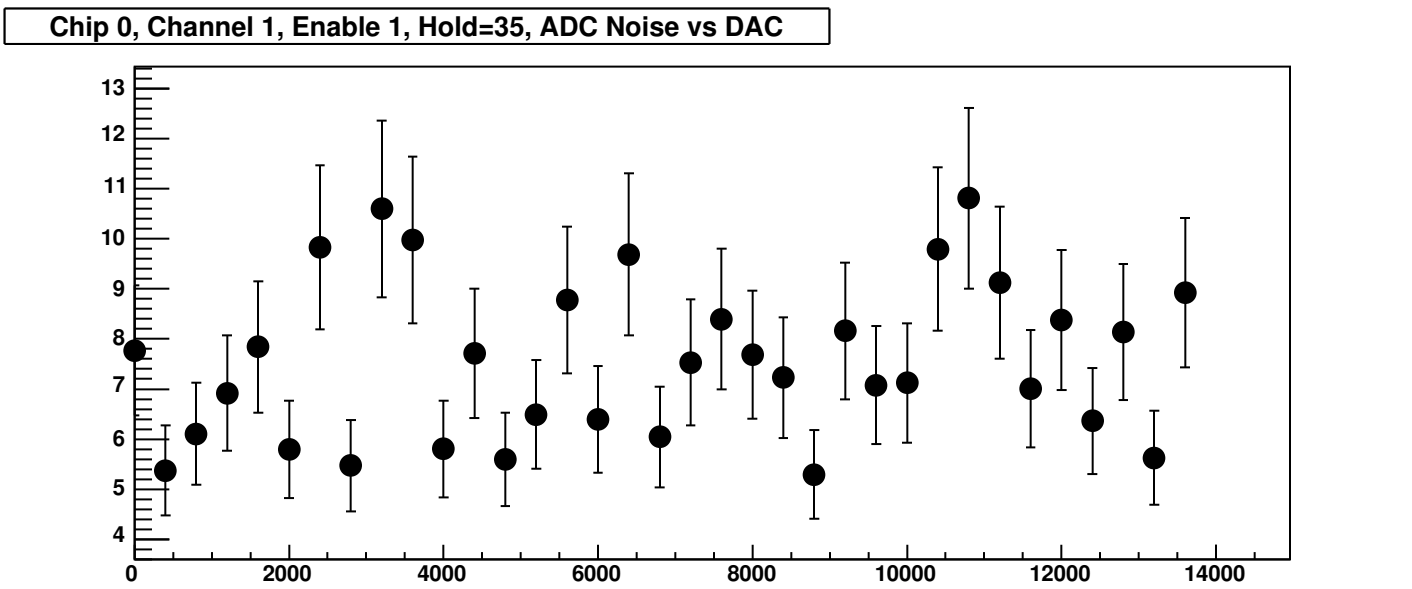
Chip 0, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC

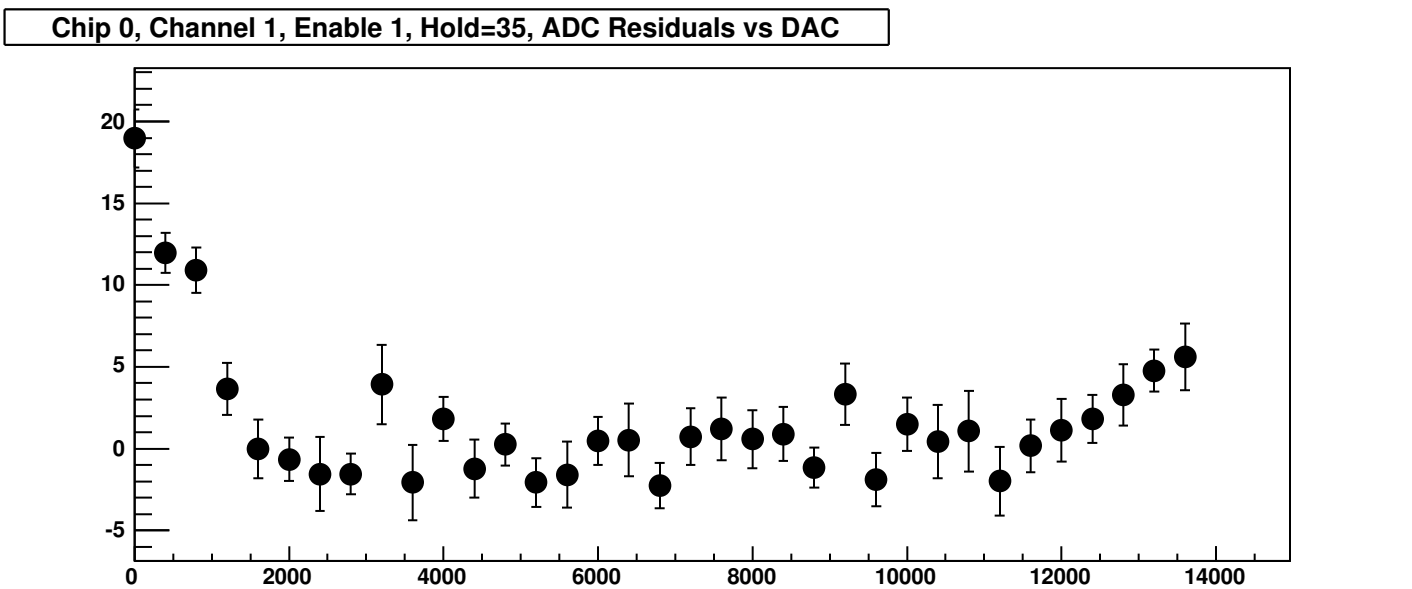


Chip 0, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC

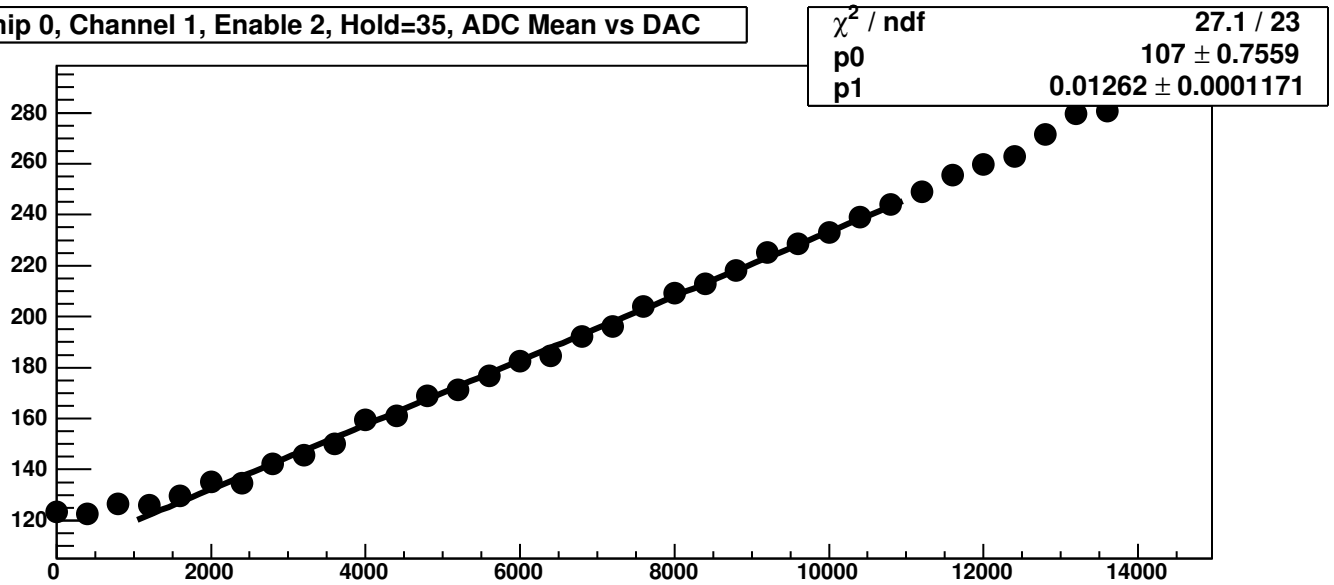



Chip 0, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 26.17 / 23
p0 106.4 ± 0.7622
p1 0.01736 ± 0.0001185
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

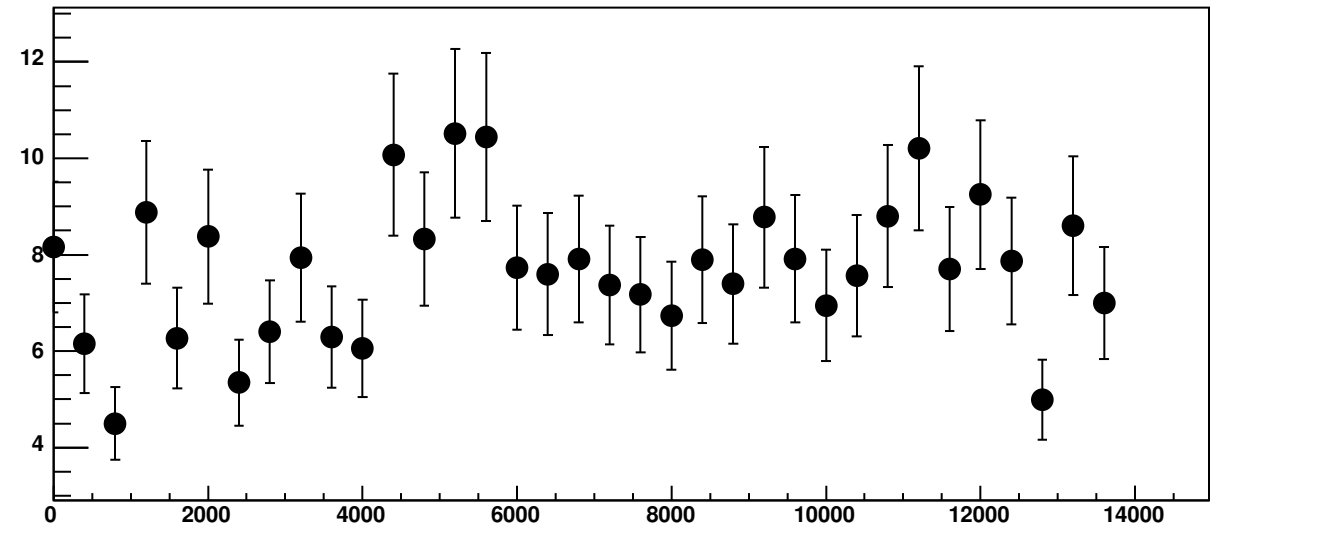

Chip 0, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

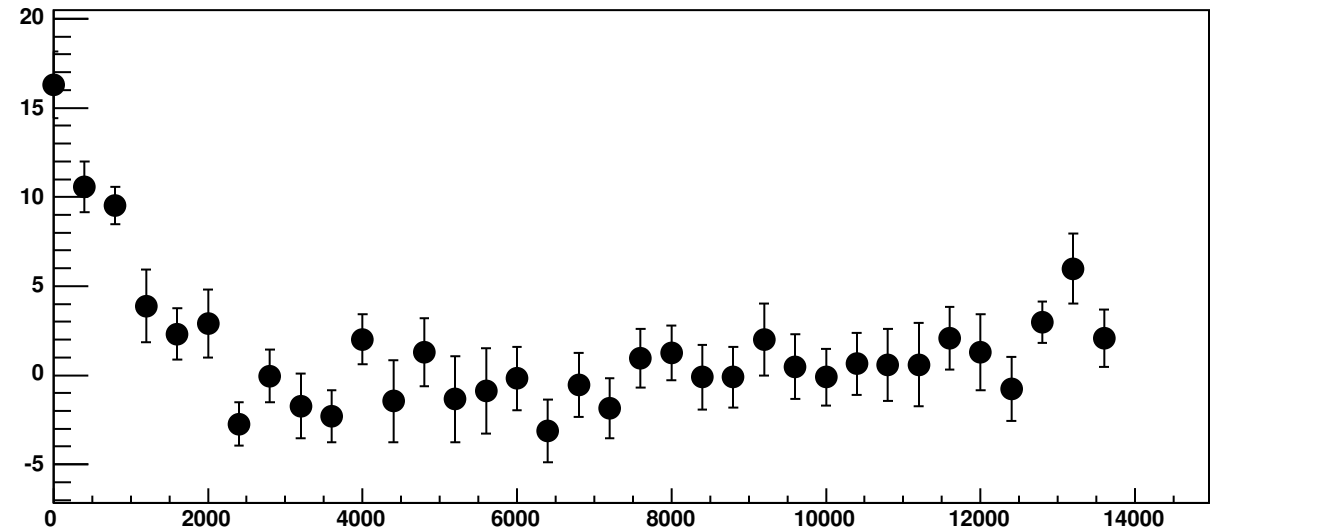
Chip 0, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

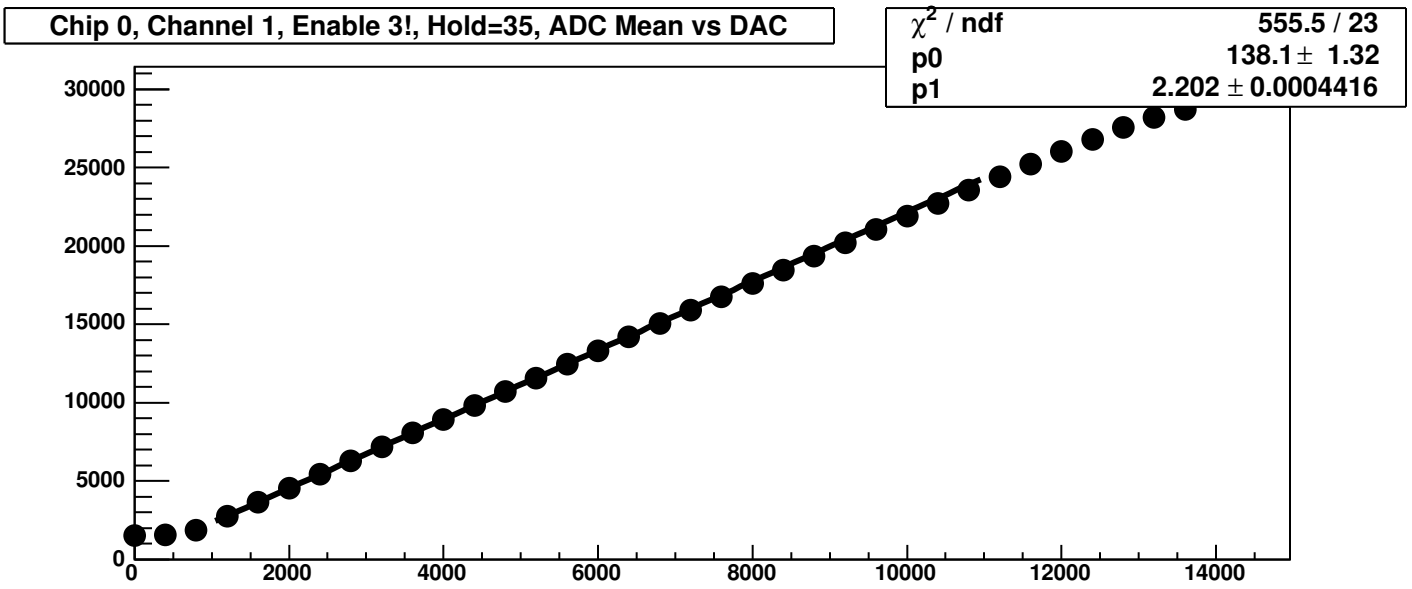


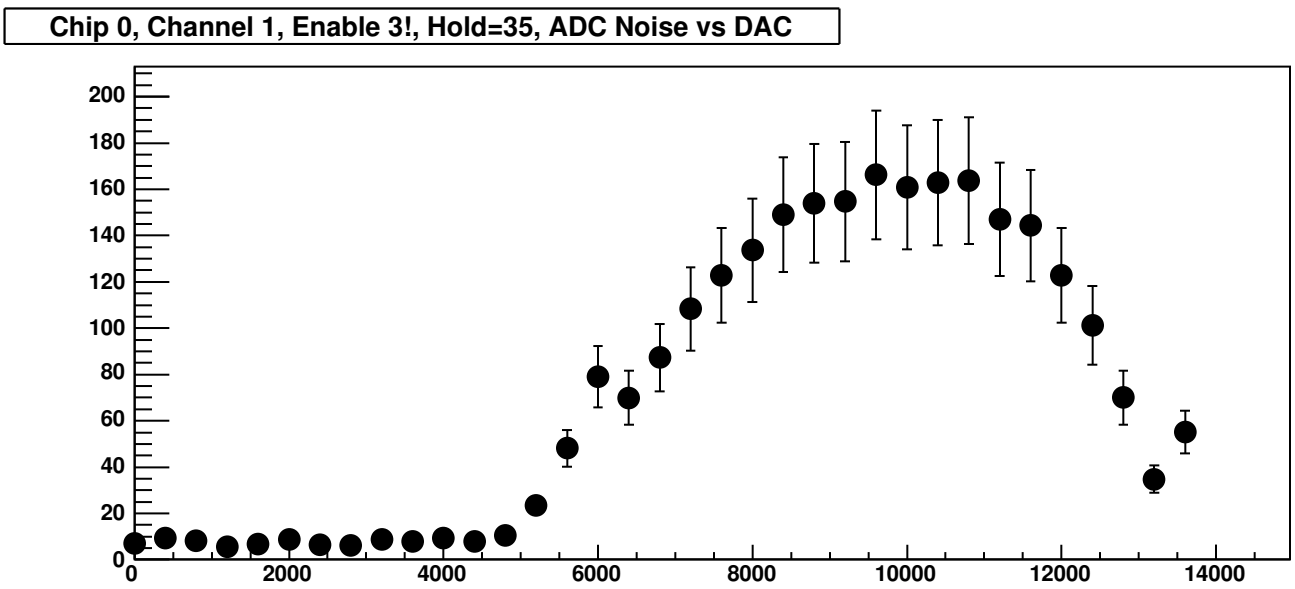
Chip 0, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC

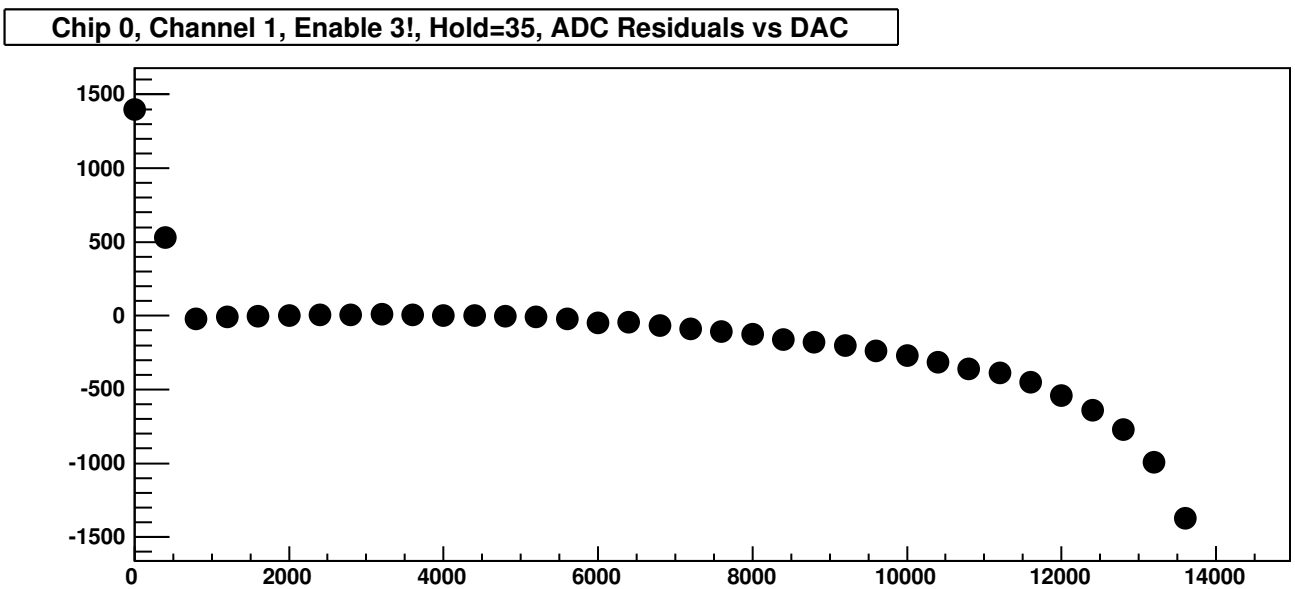


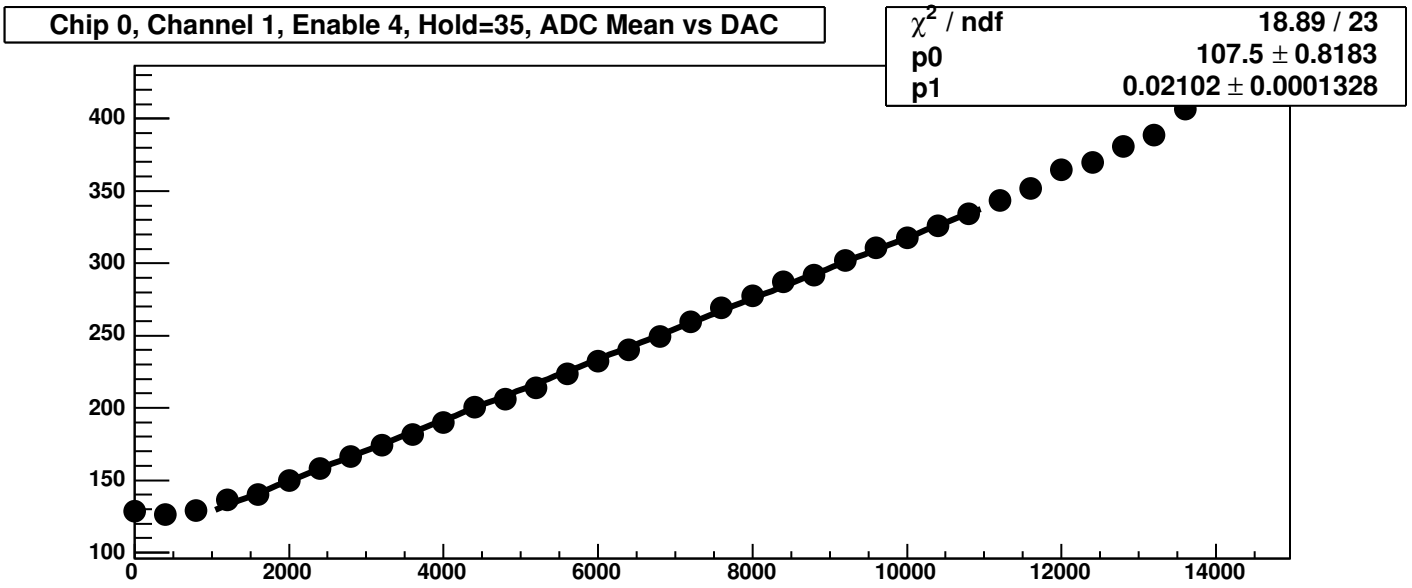
Chip 0, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

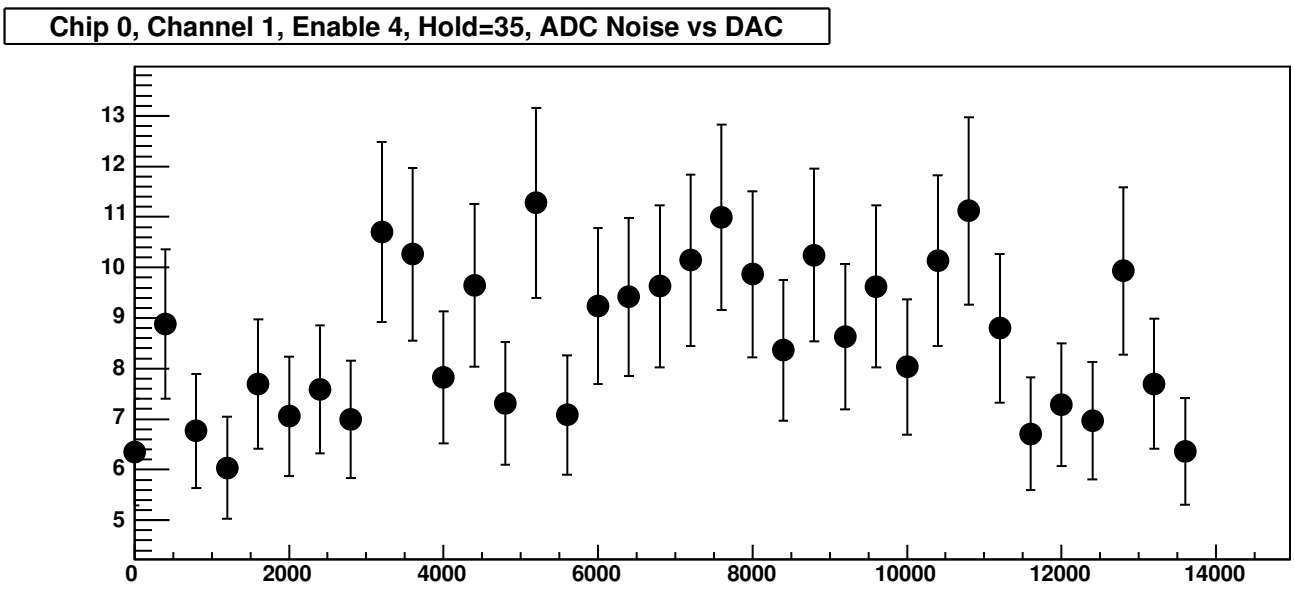


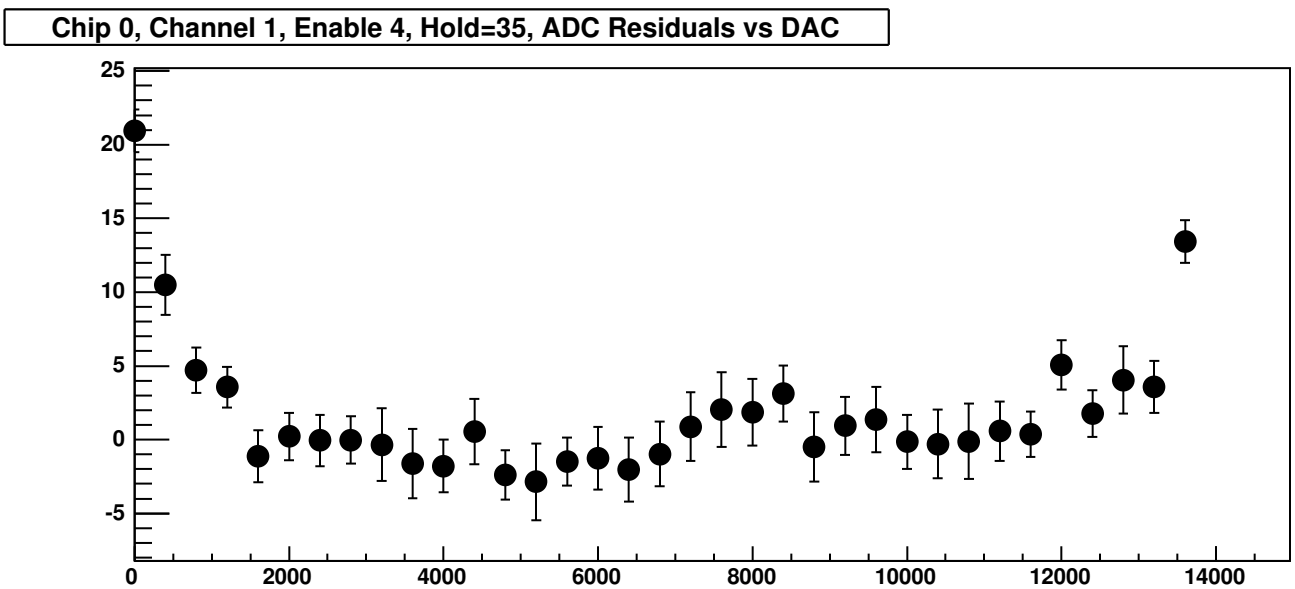

Chip 0, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 555.5 / 23
p0 138.1 ± 1.32
p1 2.202 ± 0.0004416
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

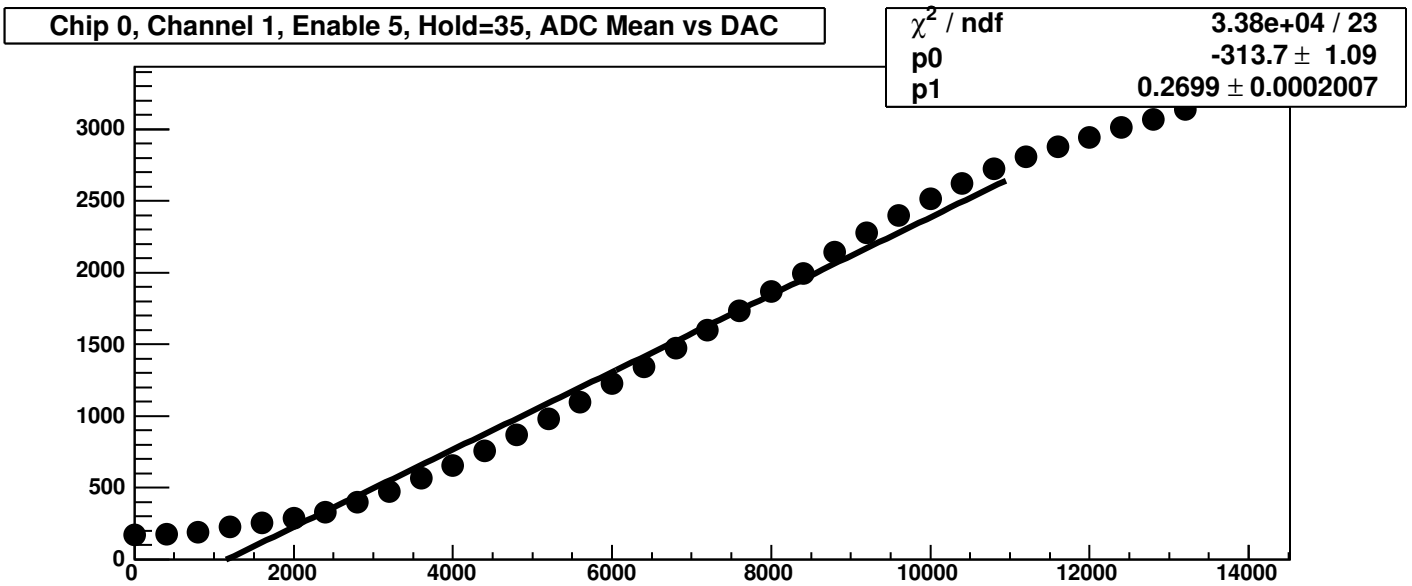

Chip 0, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

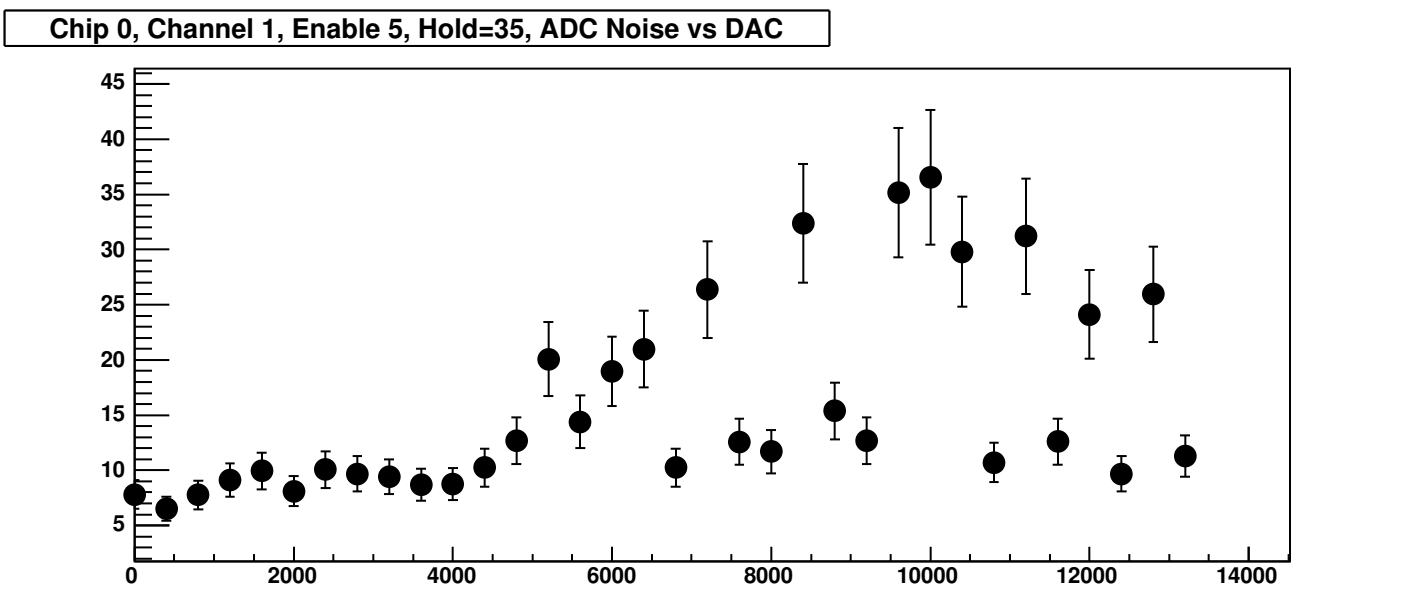

Chip 0, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

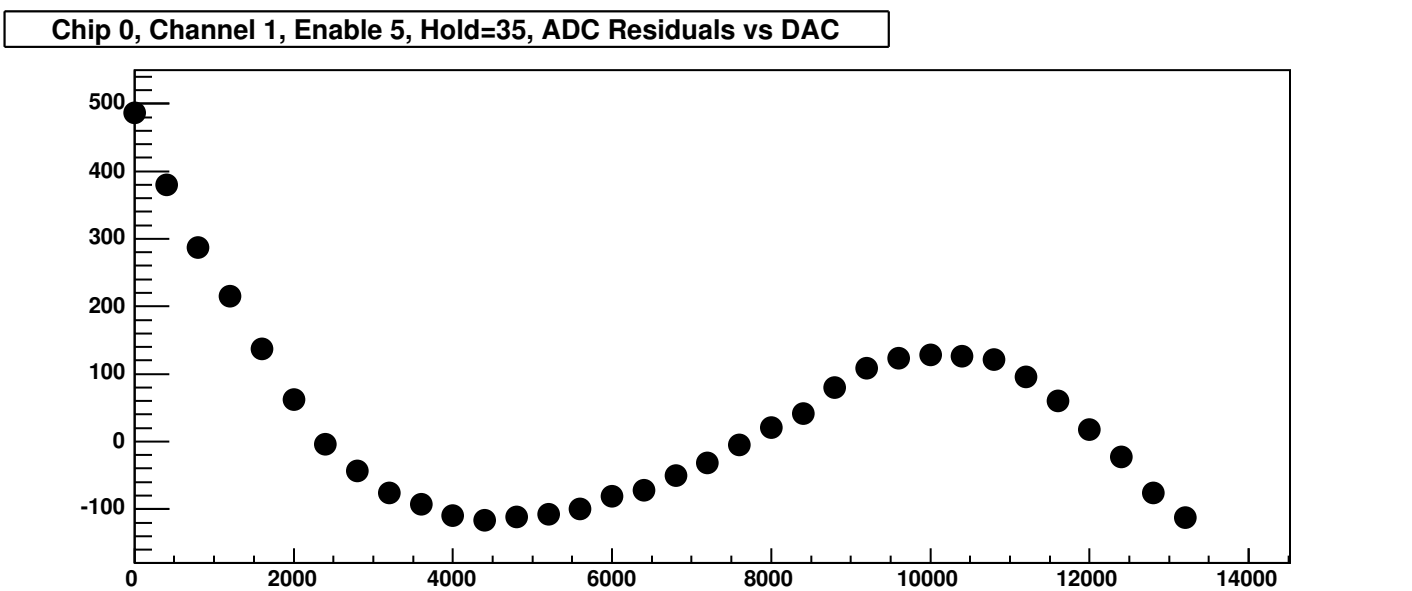

Chip 0, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 18.89 / 23
p0 107.5 ± 0.8183
p1 0.02102 ± 0.0001328
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

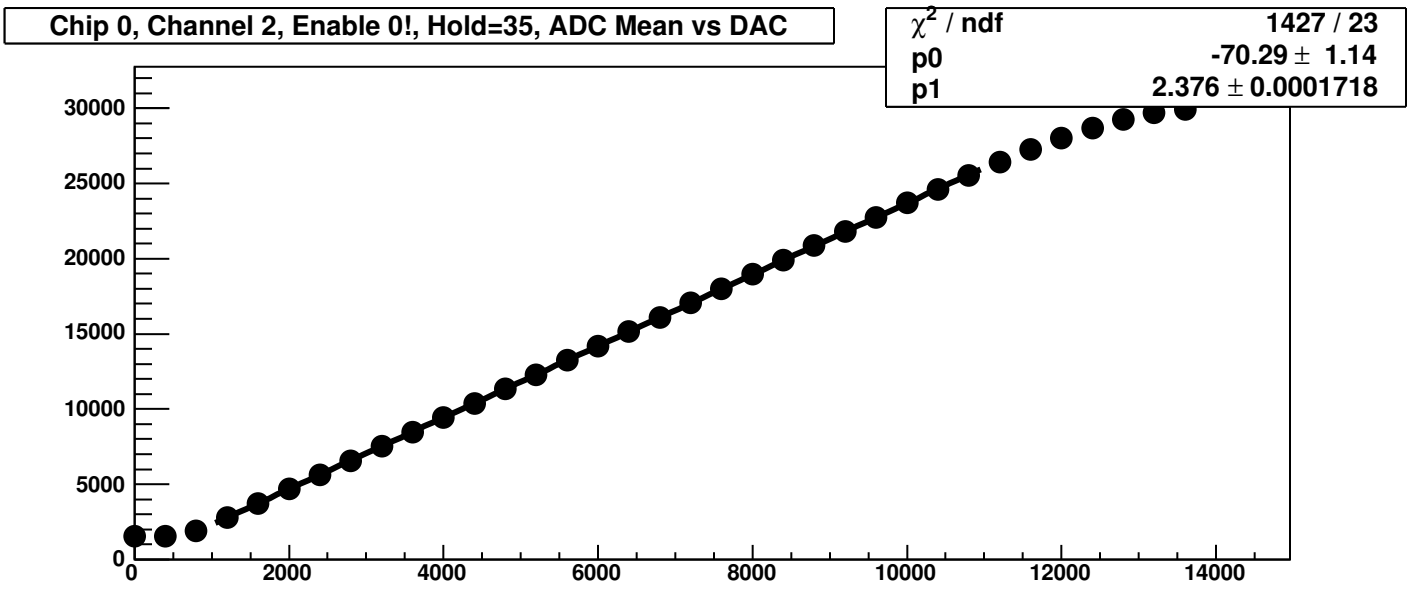

Chip 0, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

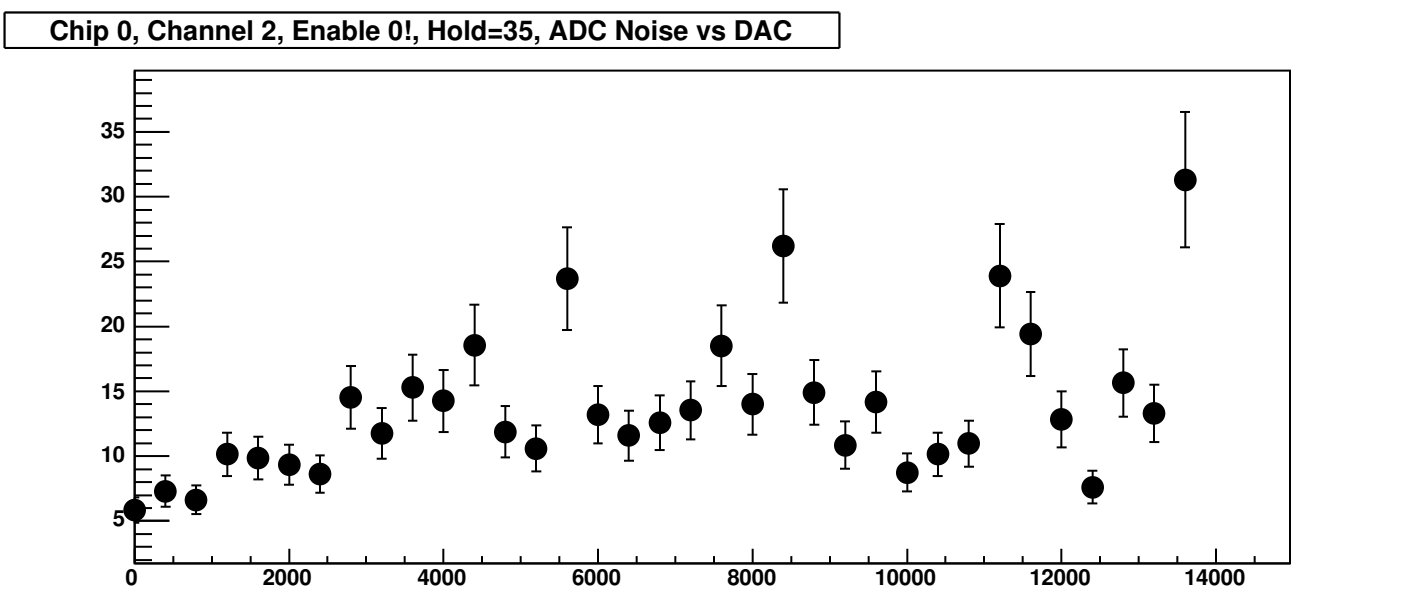

Chip 0, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

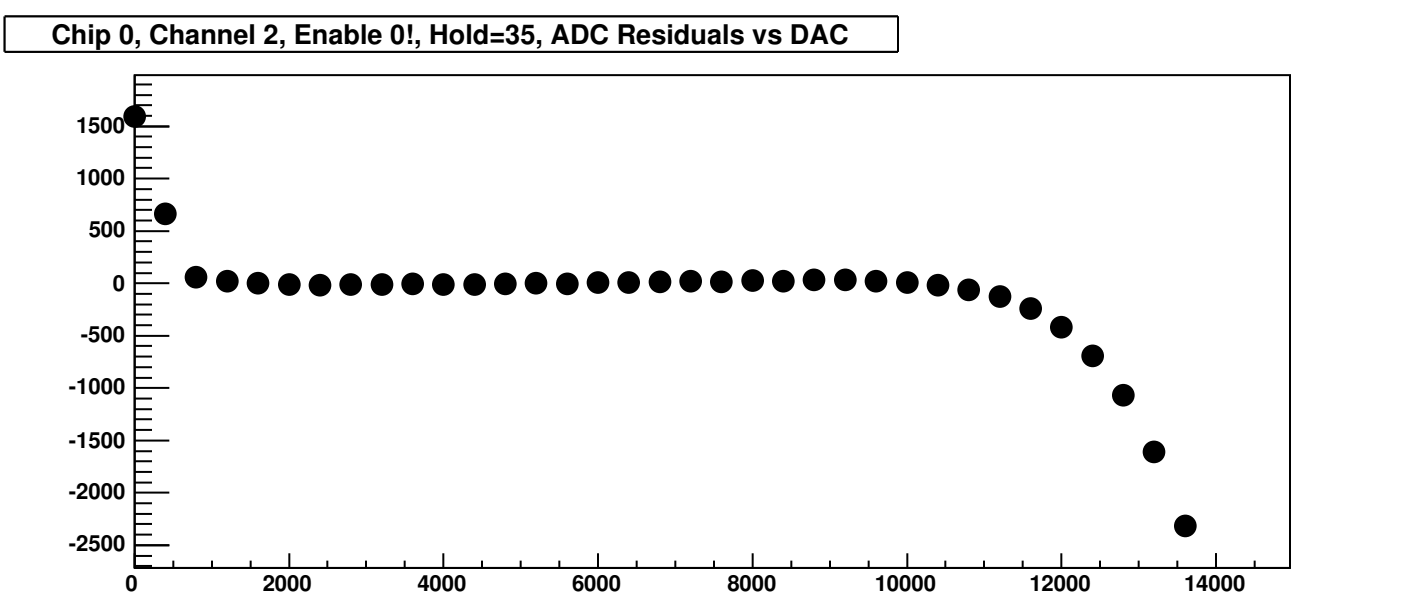

Chip 0, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.38e+04 / 23
p0 -313.7 ± 1.09
p1 0.2699 ± 0.0002007
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

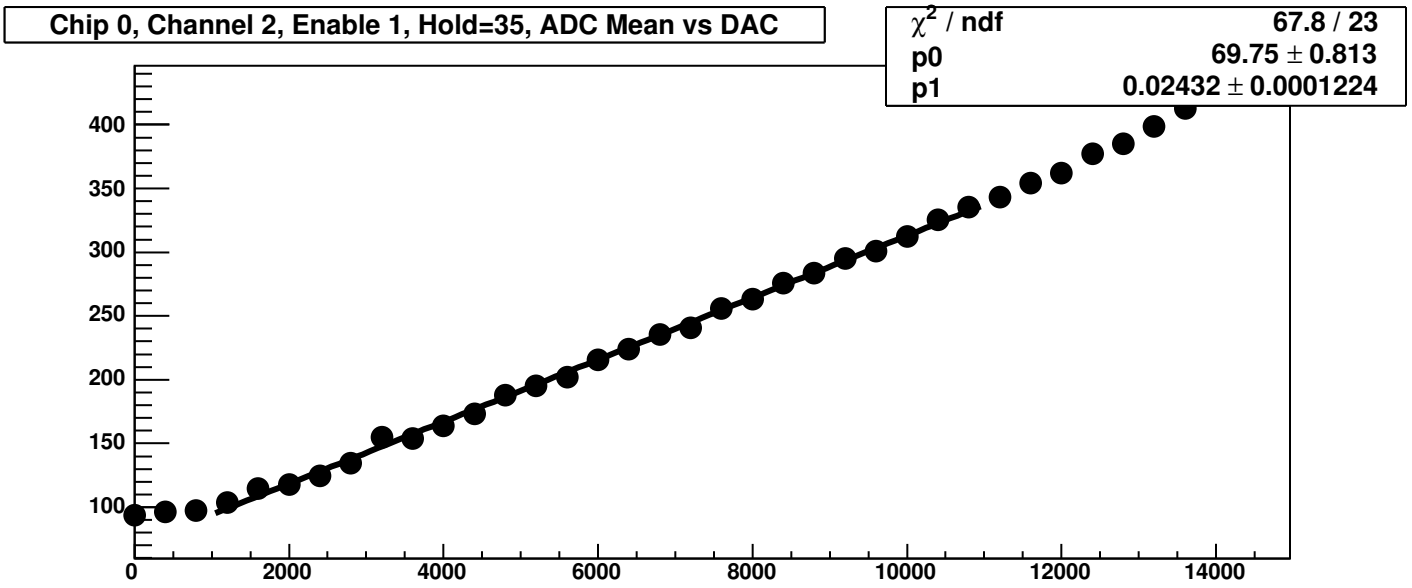

Chip 0, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

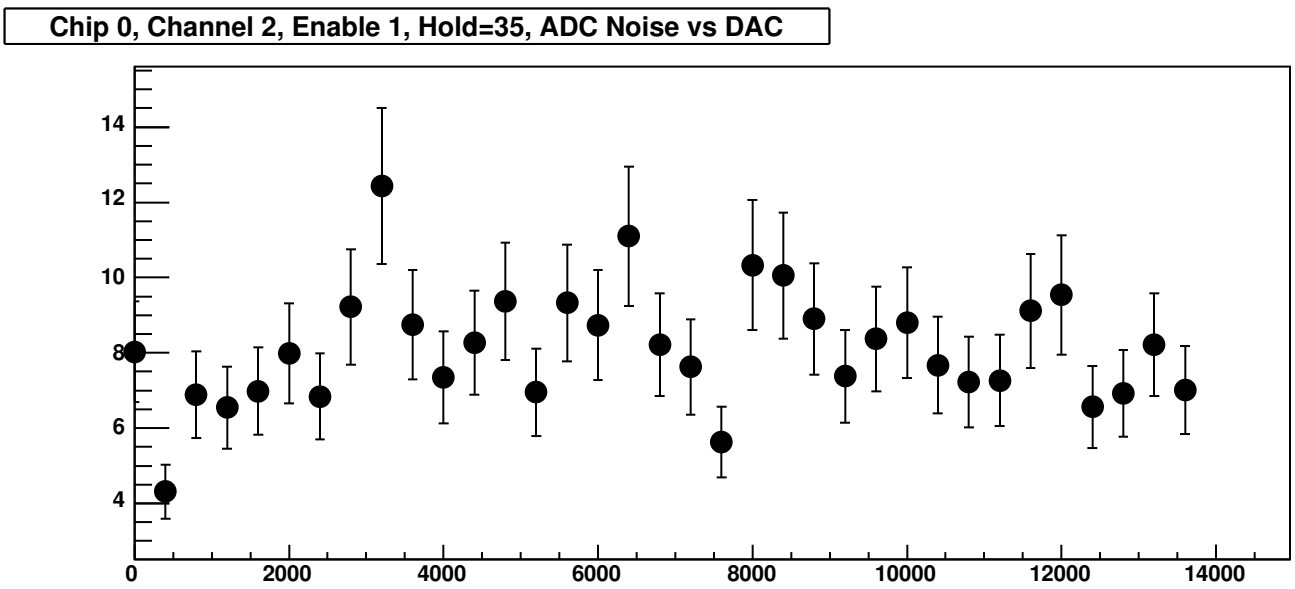

Chip 0, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

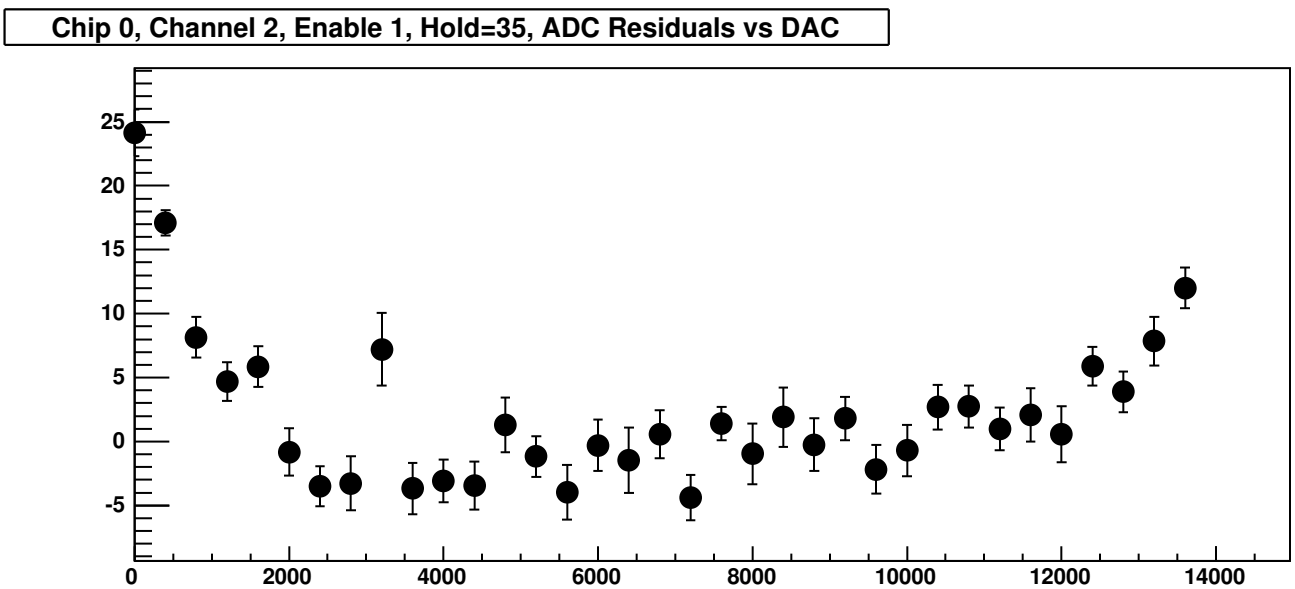

Chip 0, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1427 / 23
p0 -70.29 ± 1.14
p1 2.376 ± 0.0001718
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

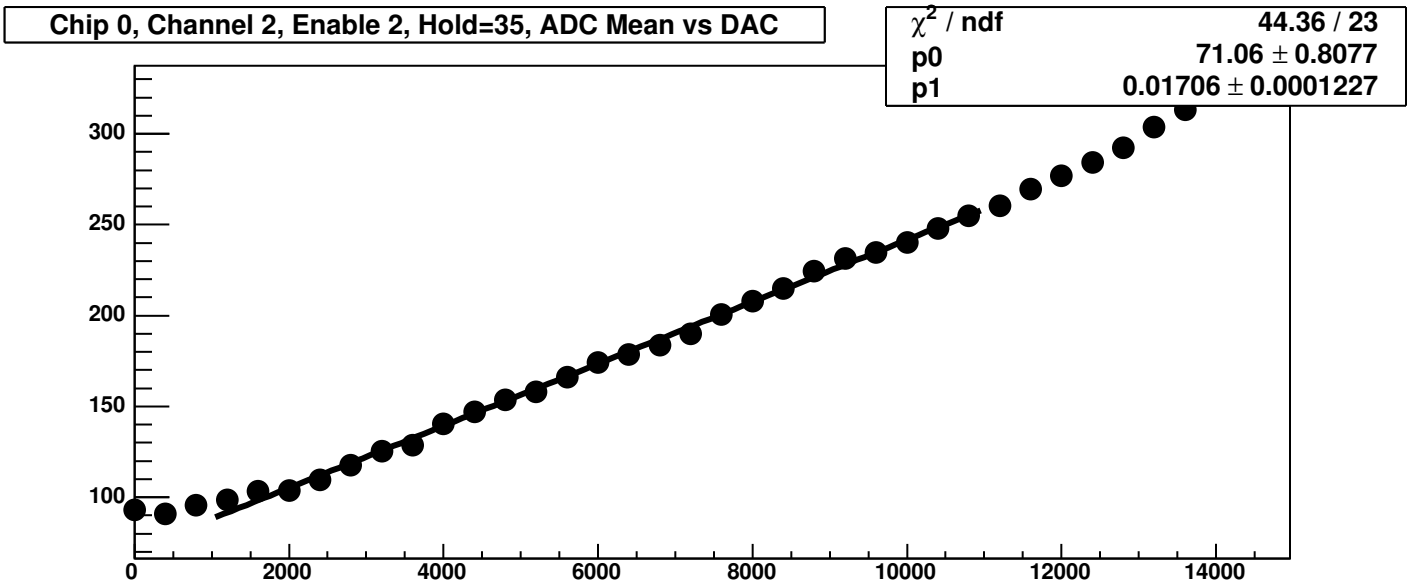

Chip 0, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

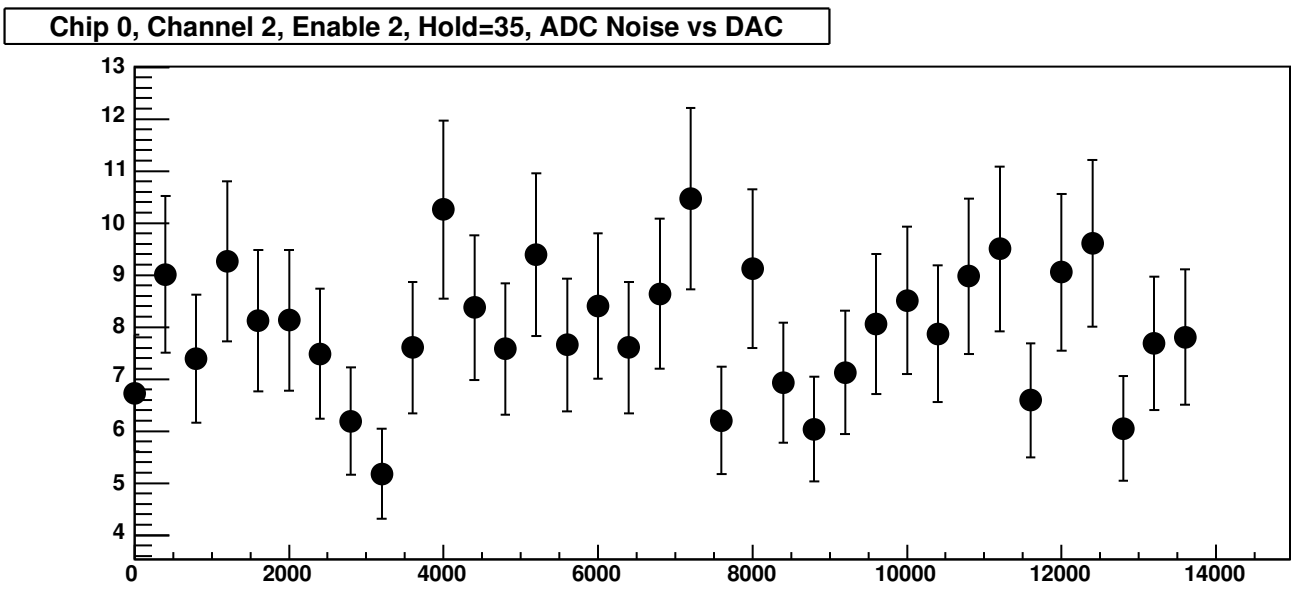

Chip 0, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

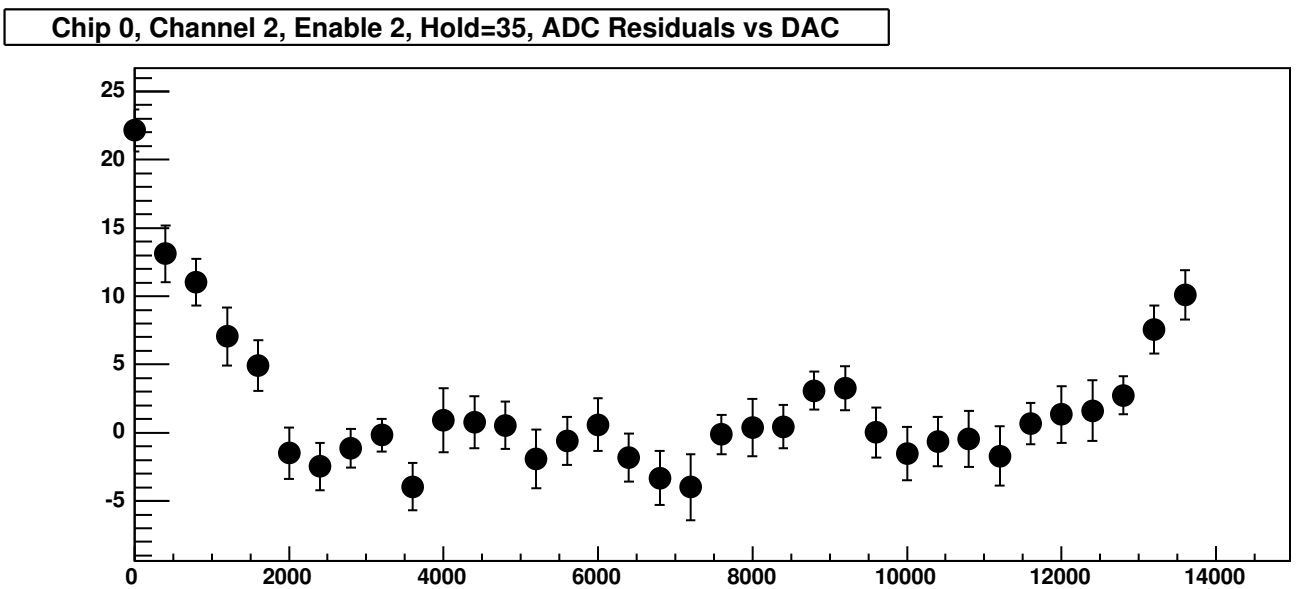

Chip 0, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 67.8 / 23
p0 69.75 ± 0.813
p1 0.02432 ± 0.0001224
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

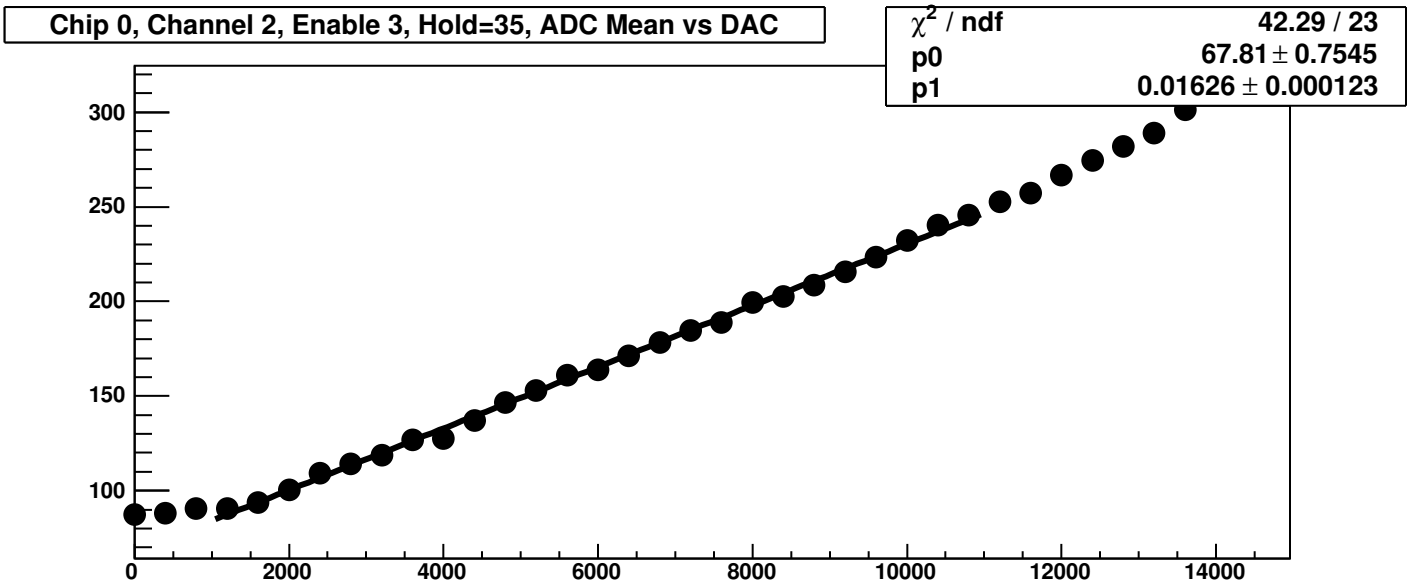

Chip 0, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

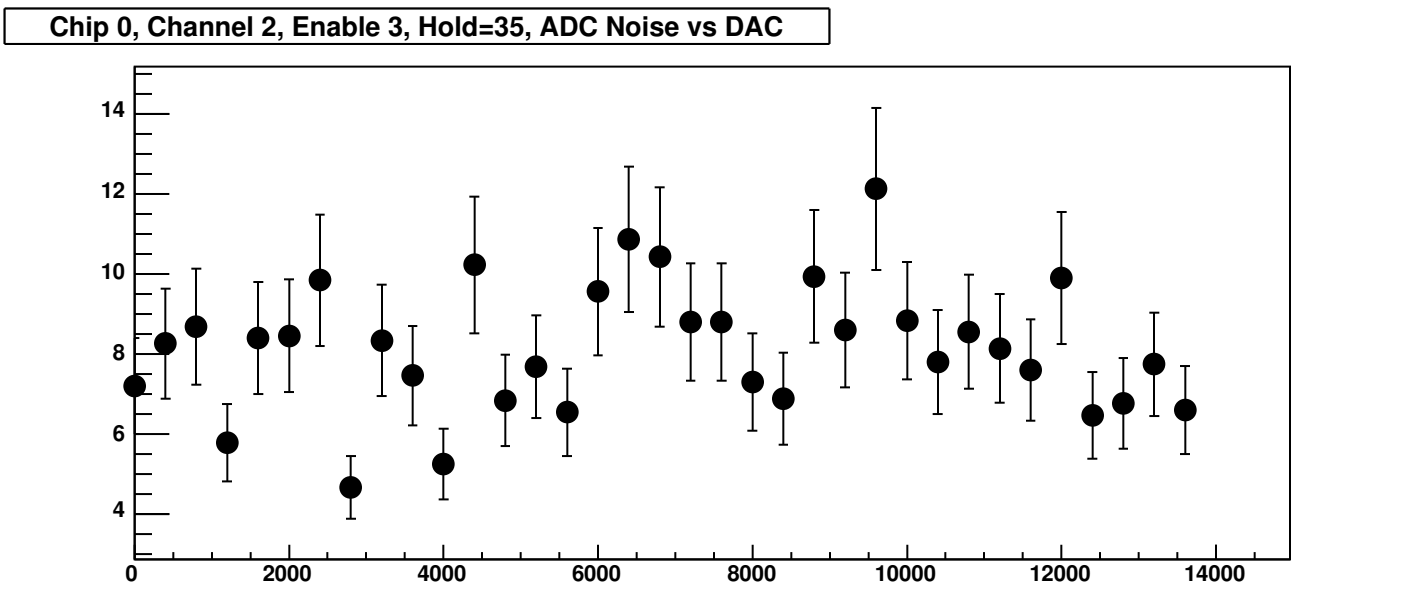

Chip 0, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

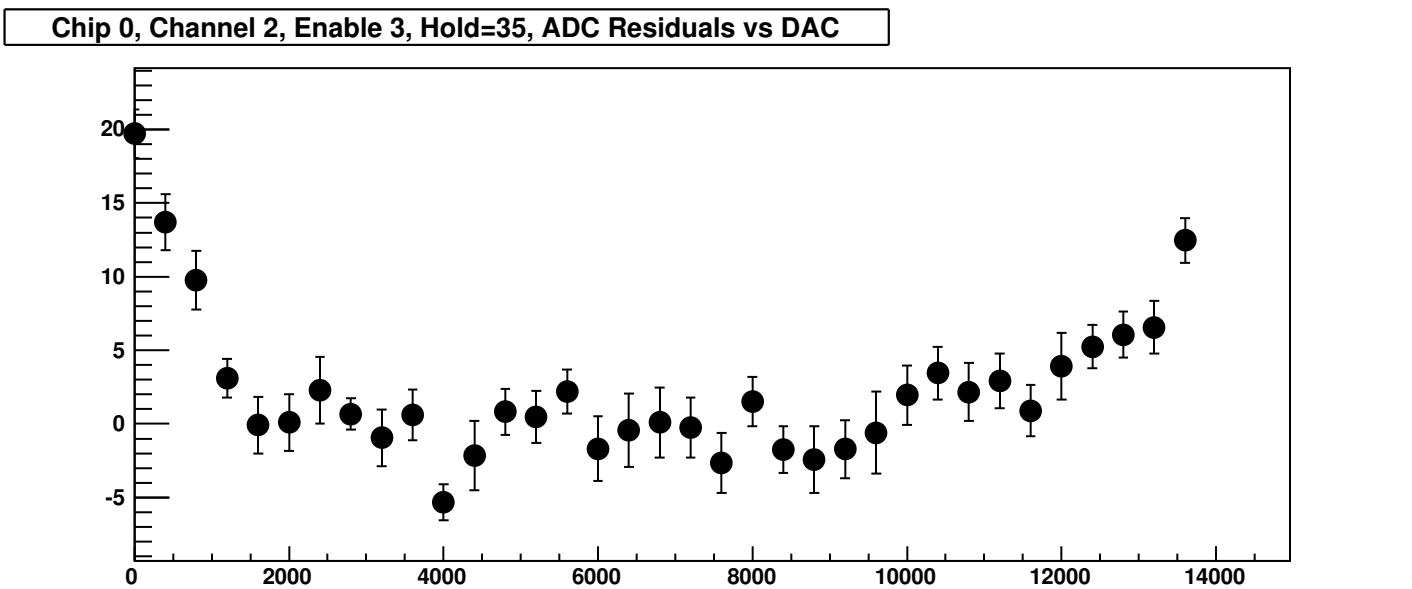

Chip 0, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 44.36 / 23
p0 71.06 ± 0.8077
p1 0.01706 ± 0.0001227
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

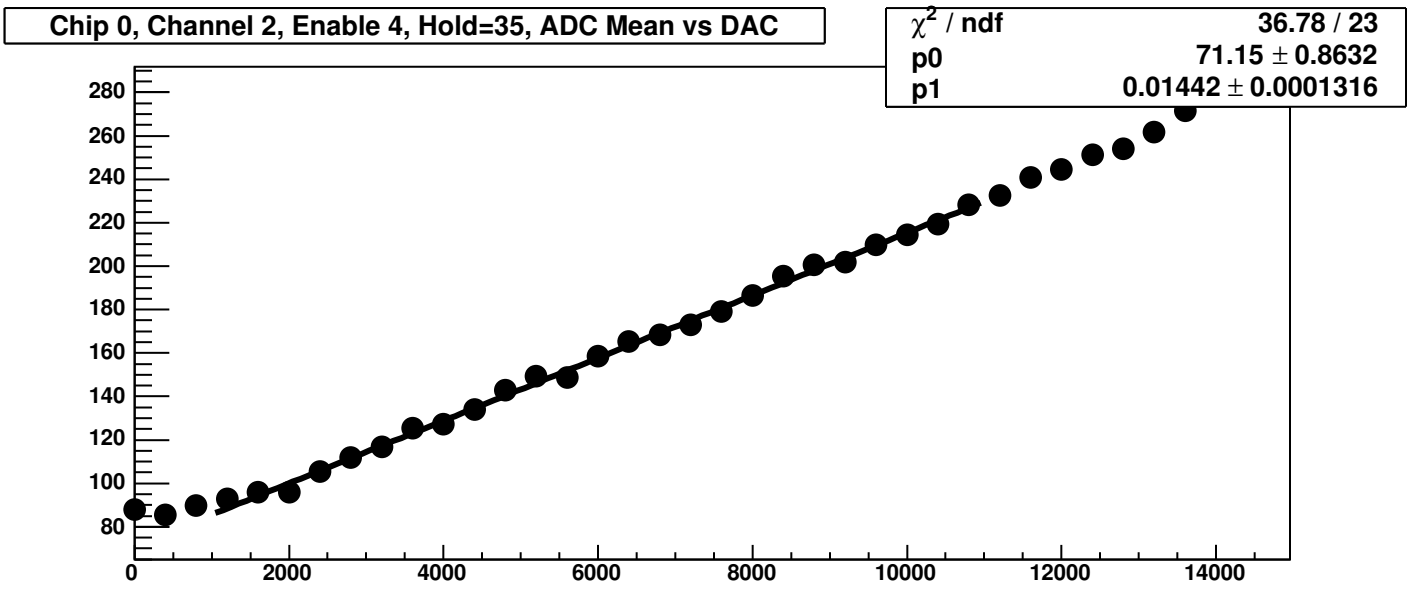

Chip 0, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

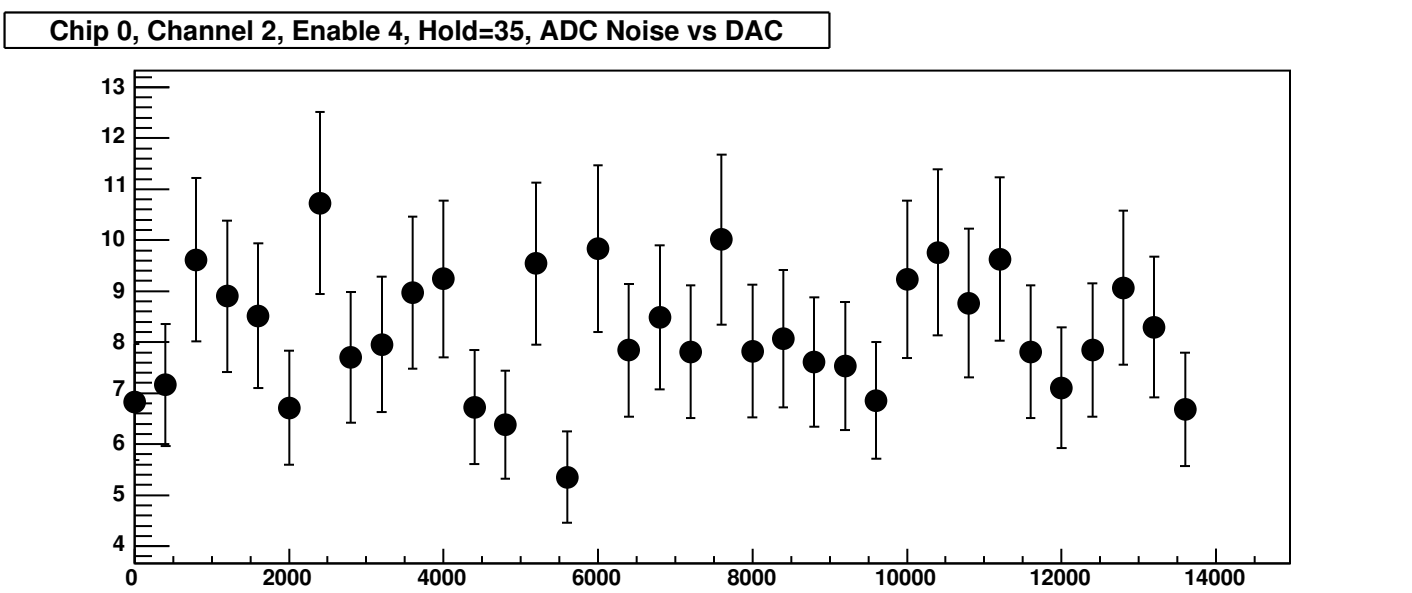

Chip 0, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

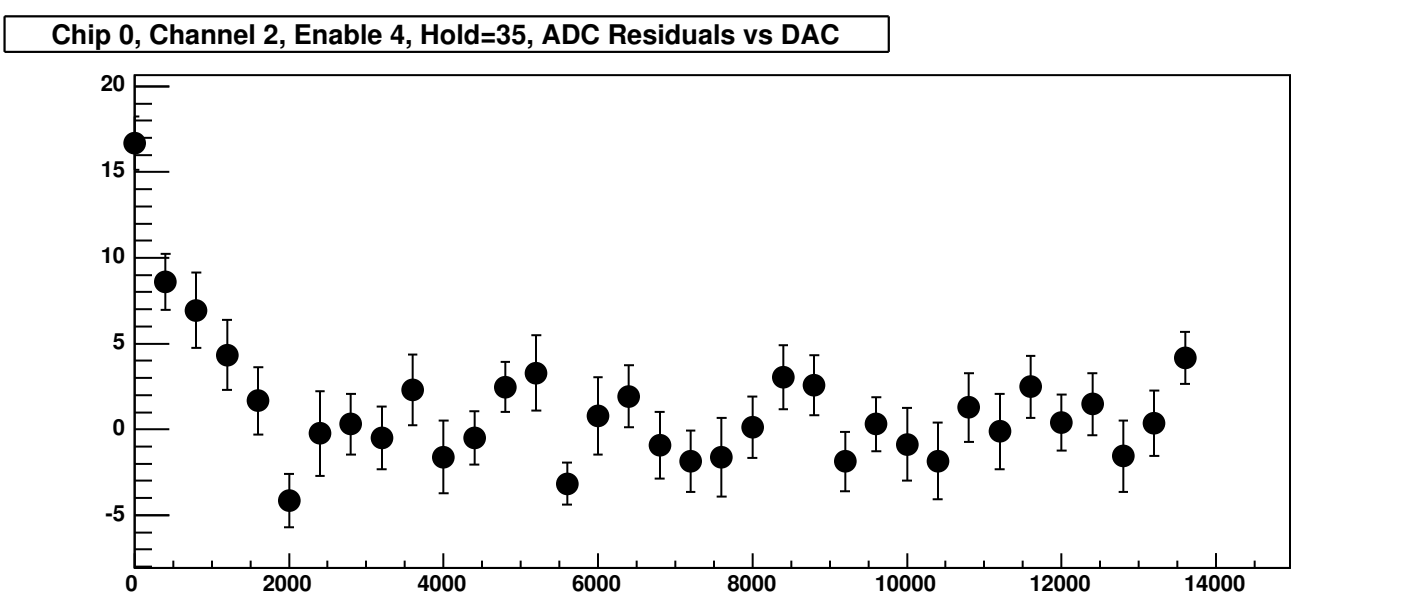

Chip 0, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 42.29 / 23
p0 67.81 ± 0.7545
p1 0.01626 ± 0.000123
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

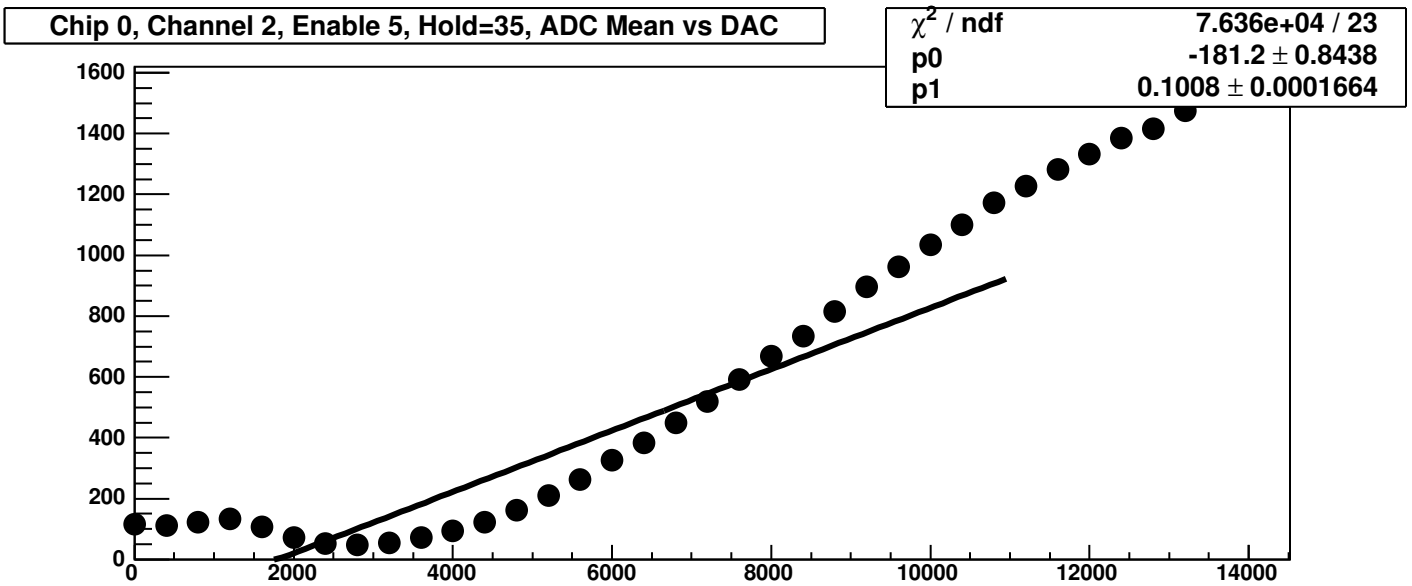

Chip 0, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

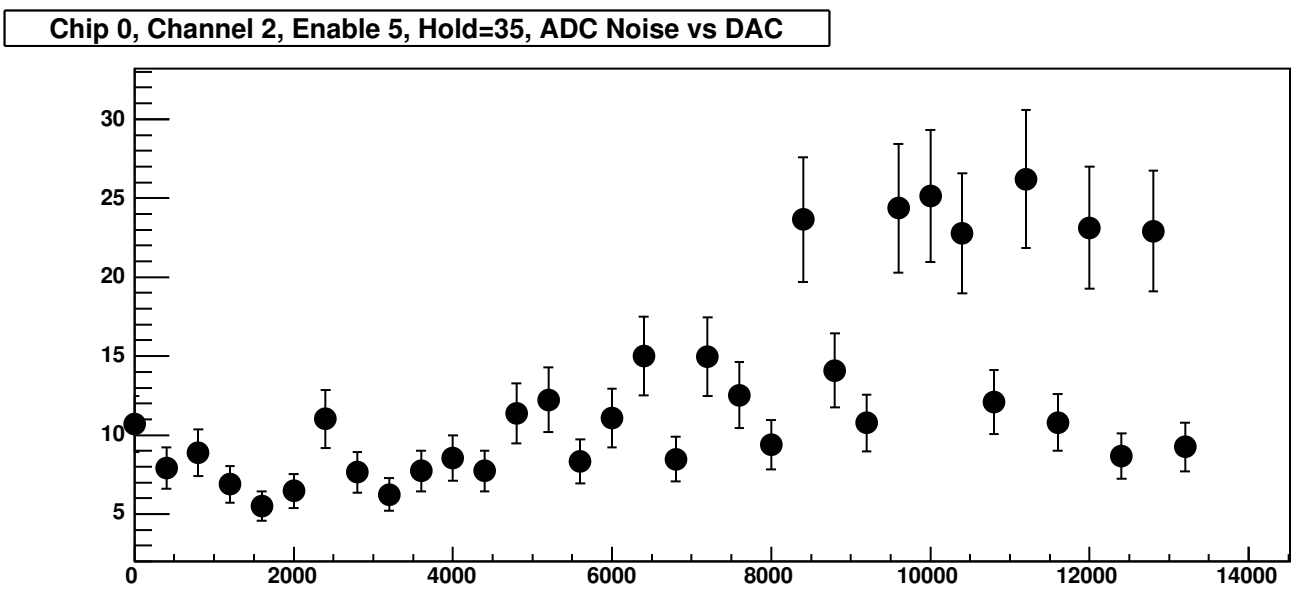

Chip 0, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

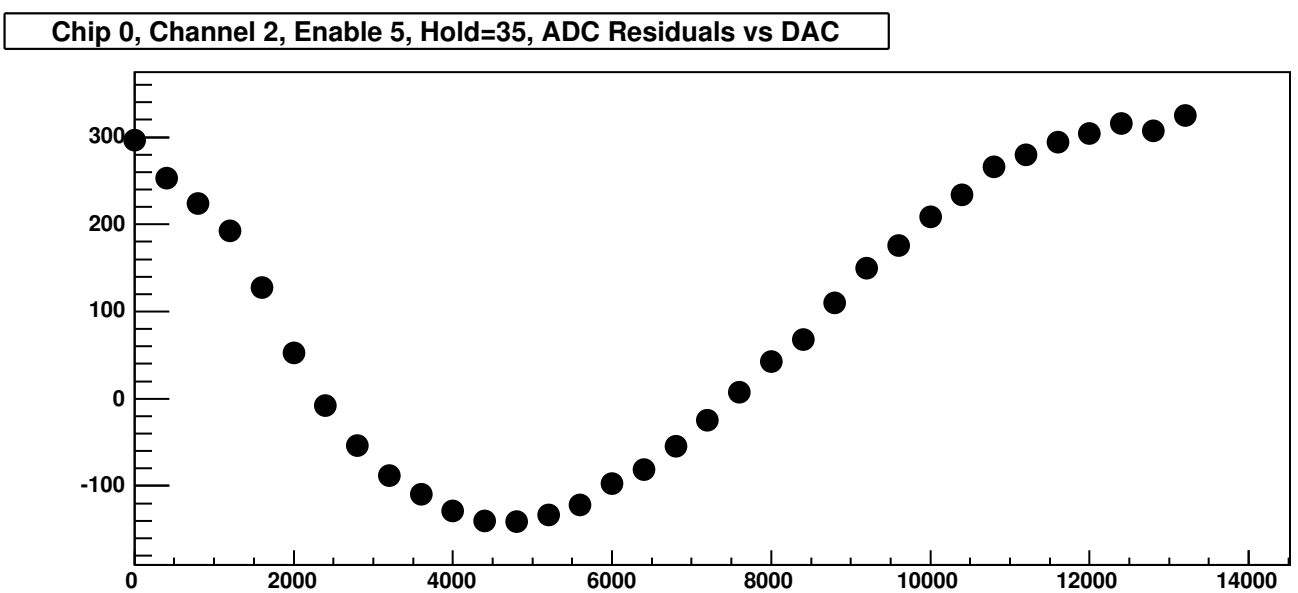

Chip 0, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 36.78 / 23
p0 71.15 ± 0.8632
p1 0.01442 ± 0.0001316
280
260
240
220
200
180
160
140
120
100
80
0
2000
4000
6000
8000
10000
12000
14000

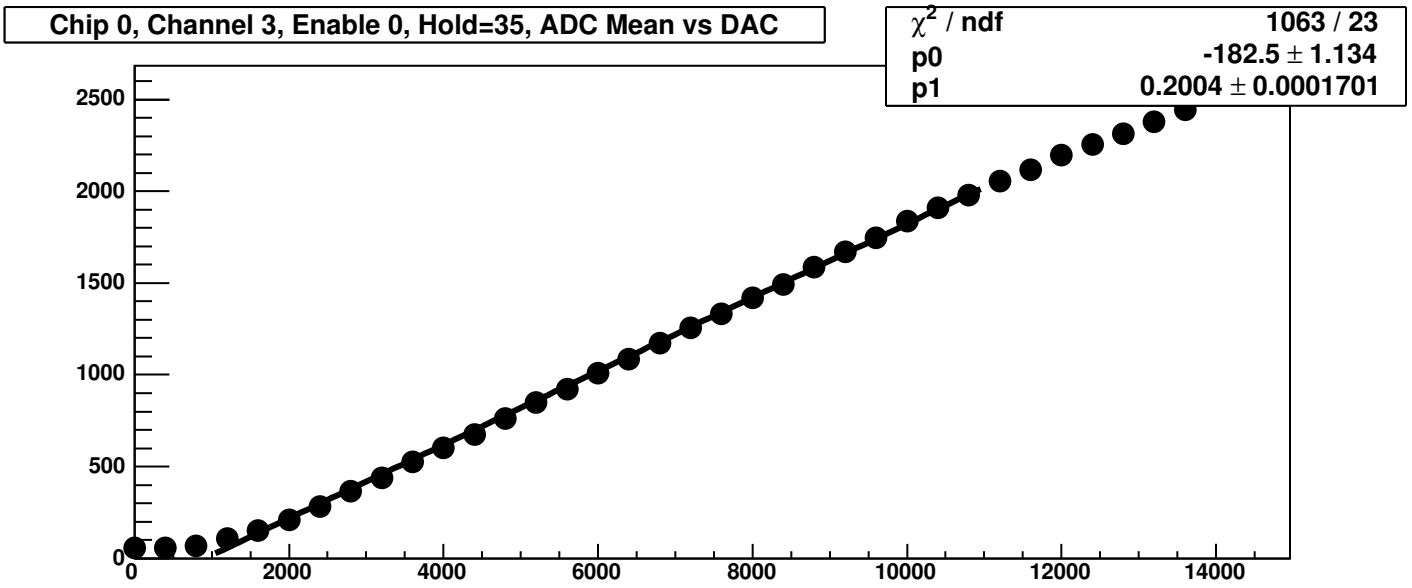

Chip 0, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

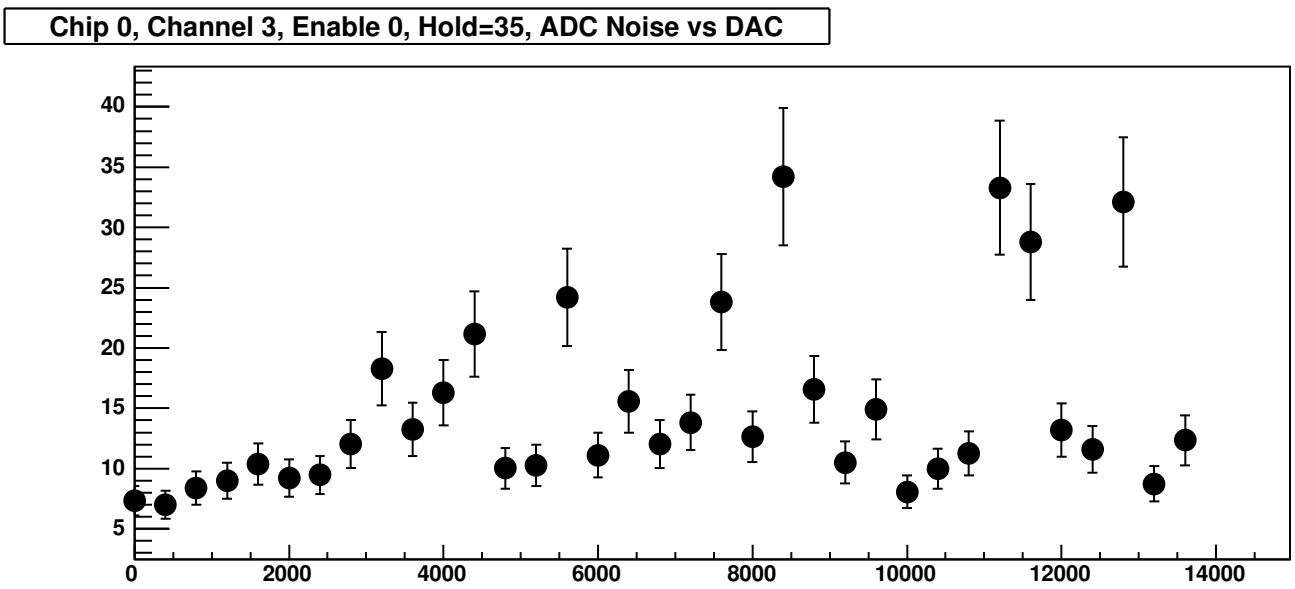

Chip 0, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

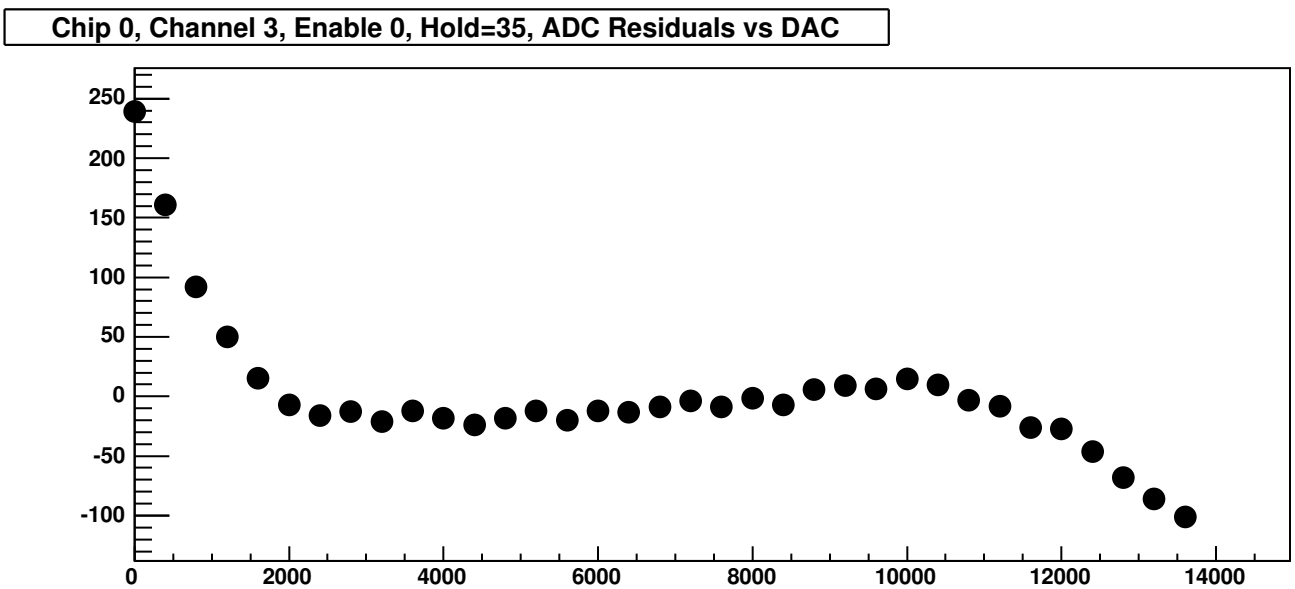

Chip 0, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 7.636e+04 / 23
p0 -181.2 ± 0.8438
p1 0.1008 ± 0.0001664
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

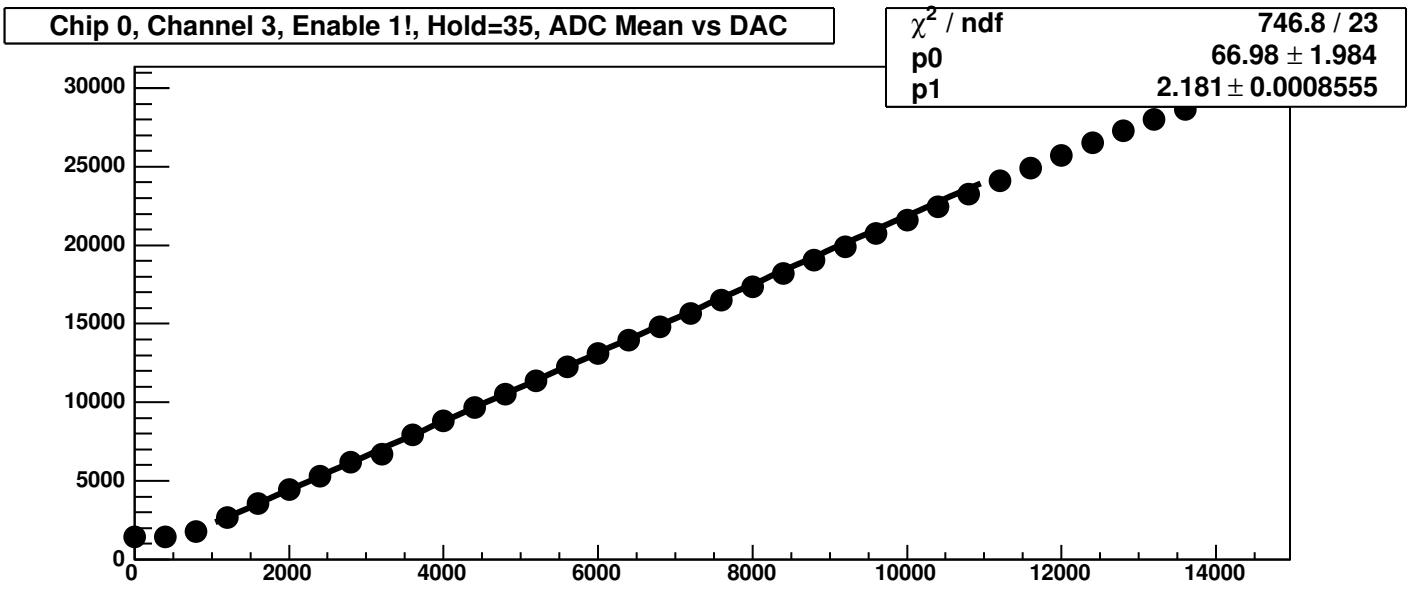

Chip 0, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

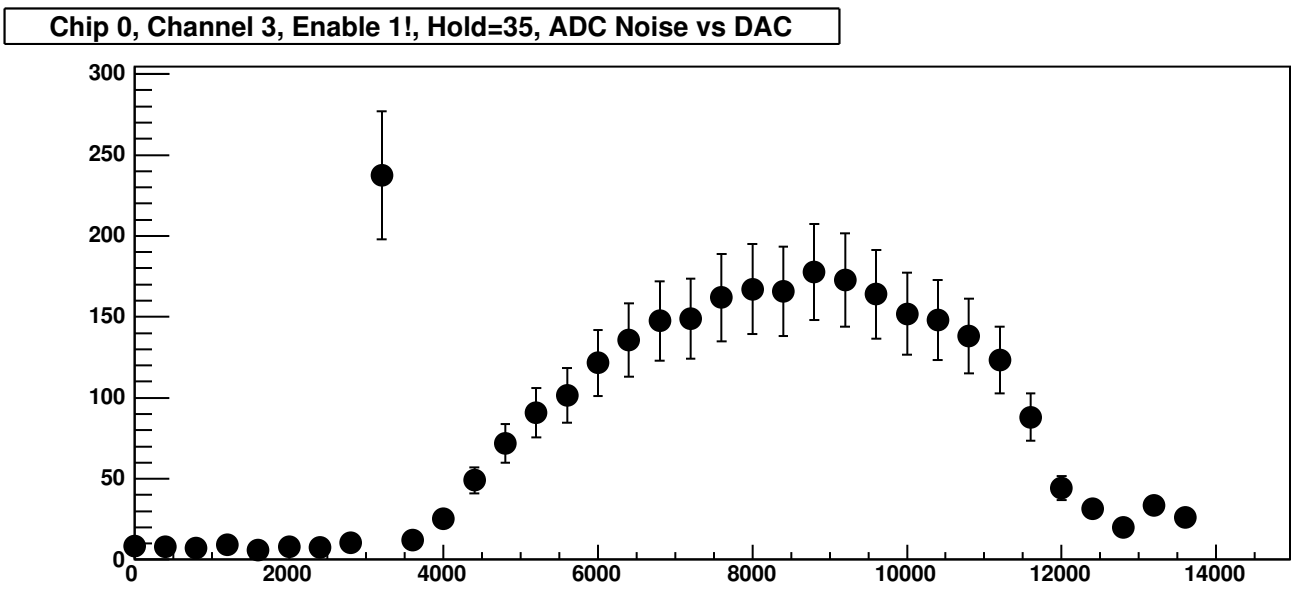

Chip 0, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

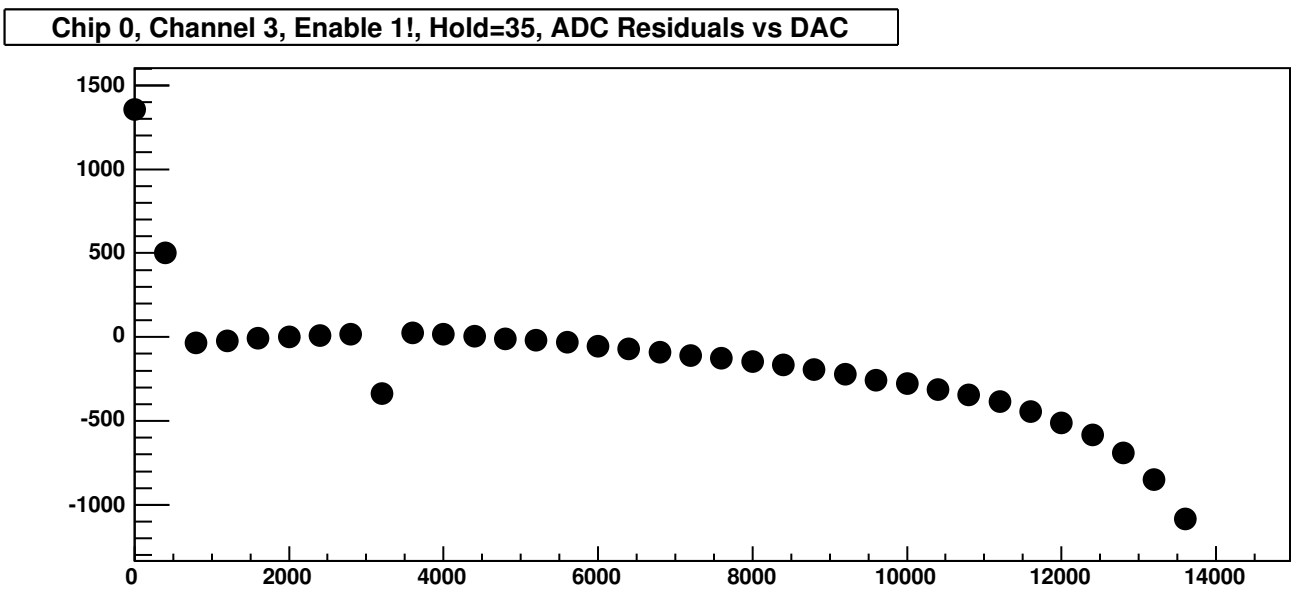

Chip 0, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1063 / 23
p0 -182.5 ± 1.134
p1 0.2004 ± 0.0001701
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

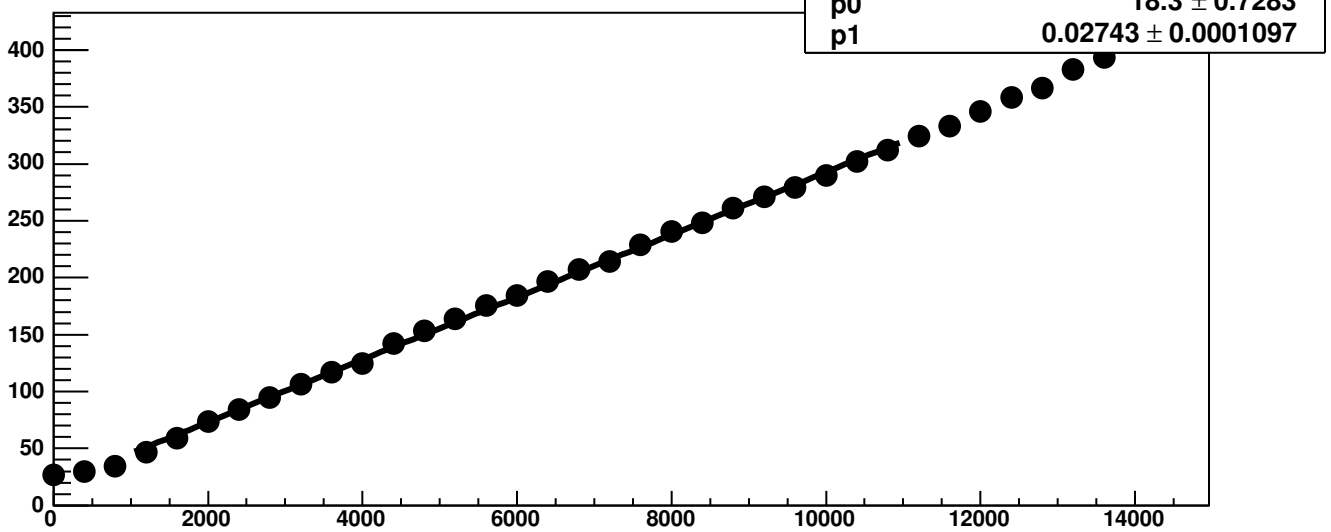

Chip 0, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 746.8 / 23
p0 66.98 ± 1.984
p1 2.181 ± 0.0008555
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

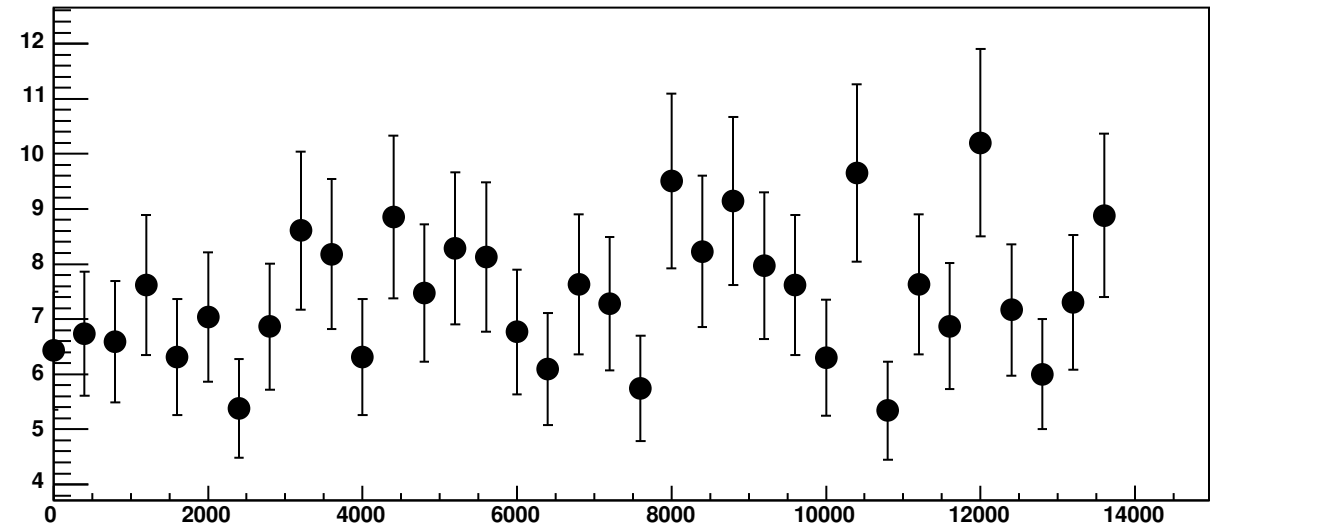

Chip 0, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

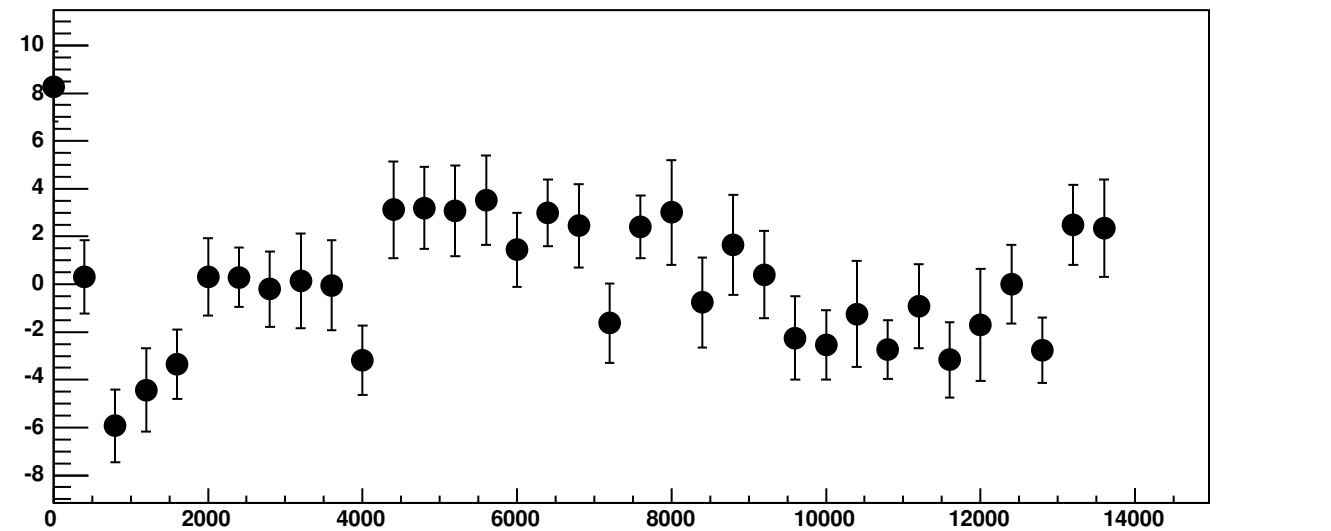
Chip 0, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

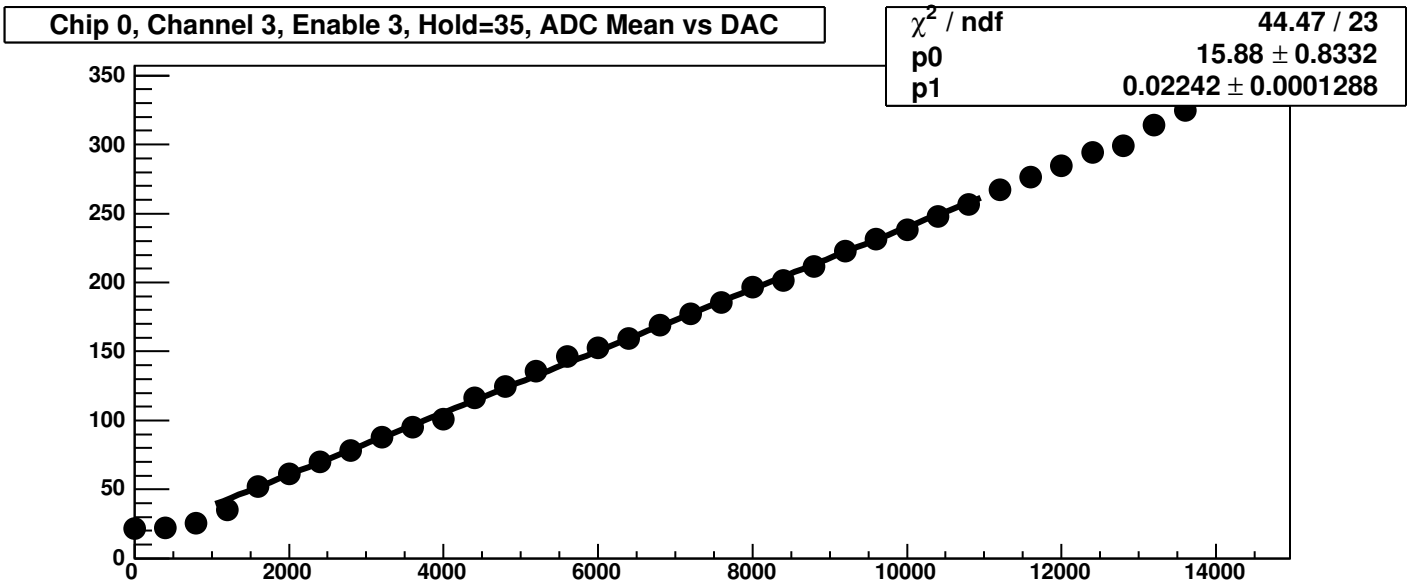


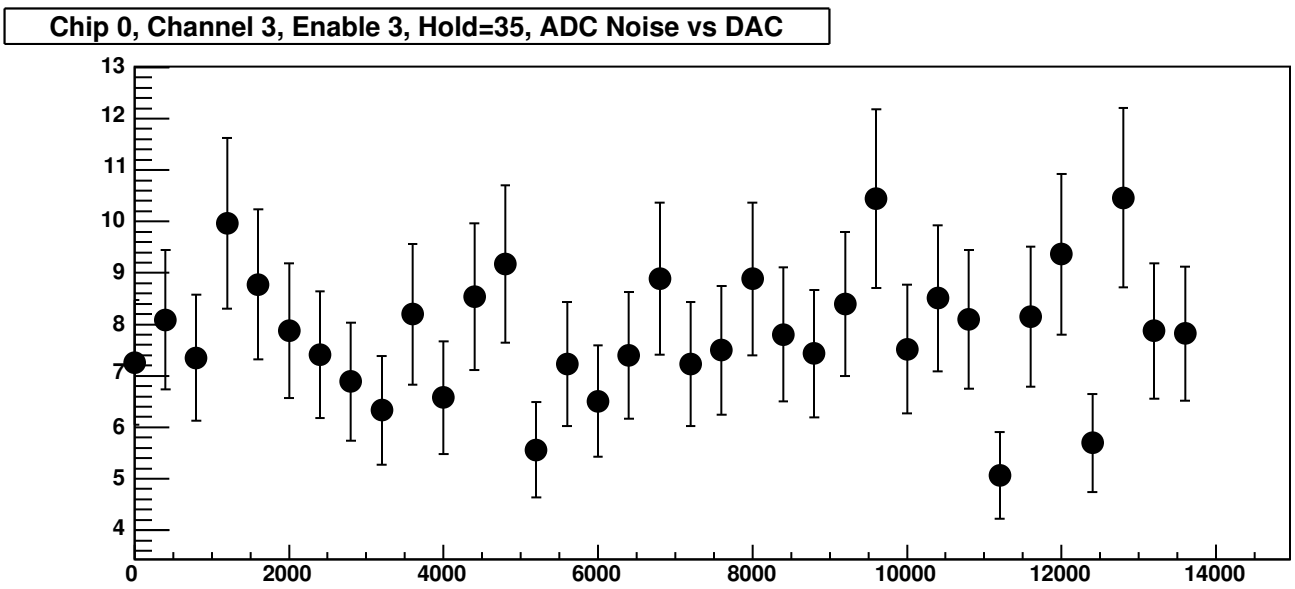
Chip 0, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC

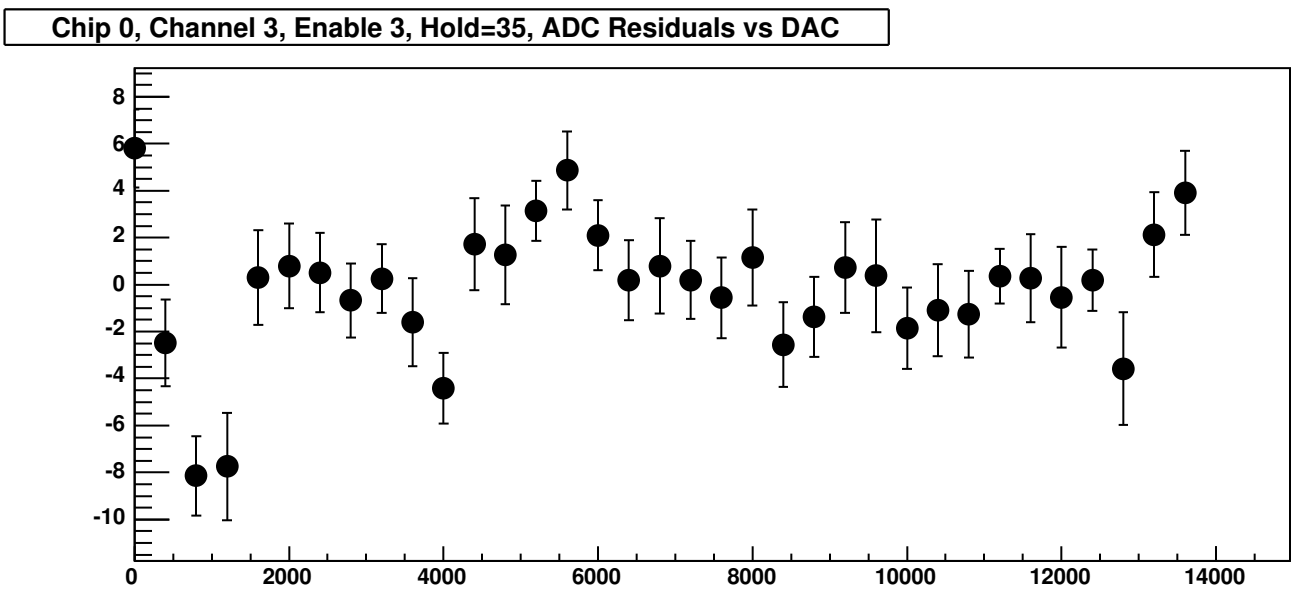


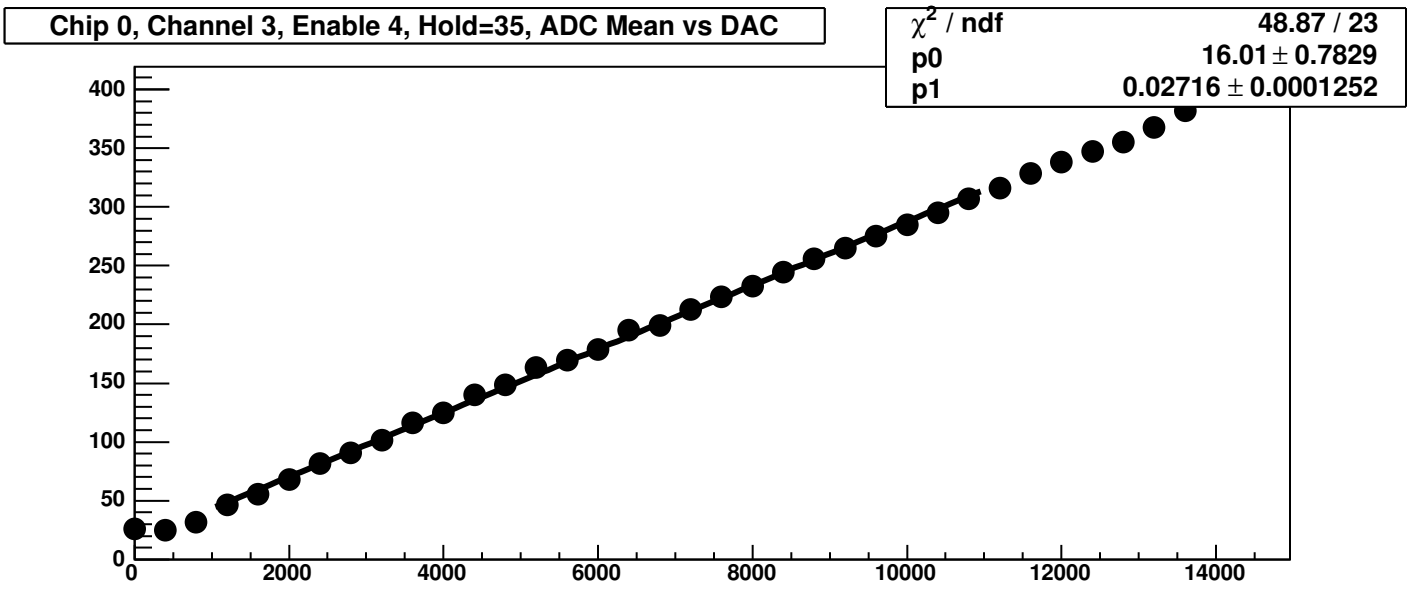
Chip 0, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

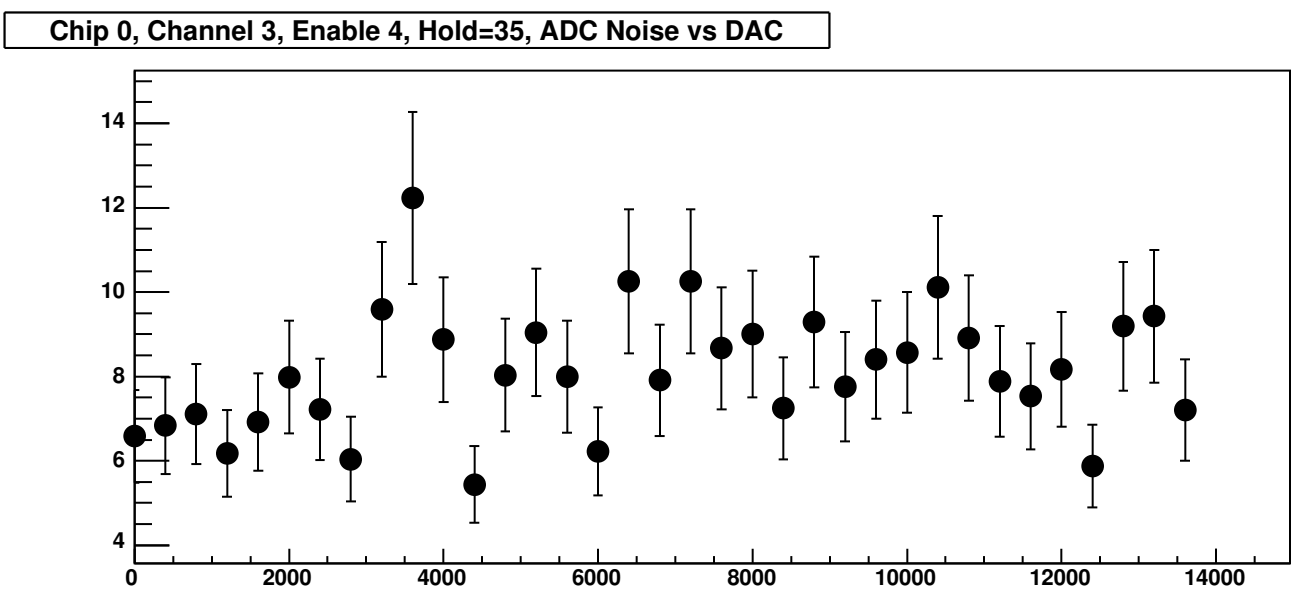


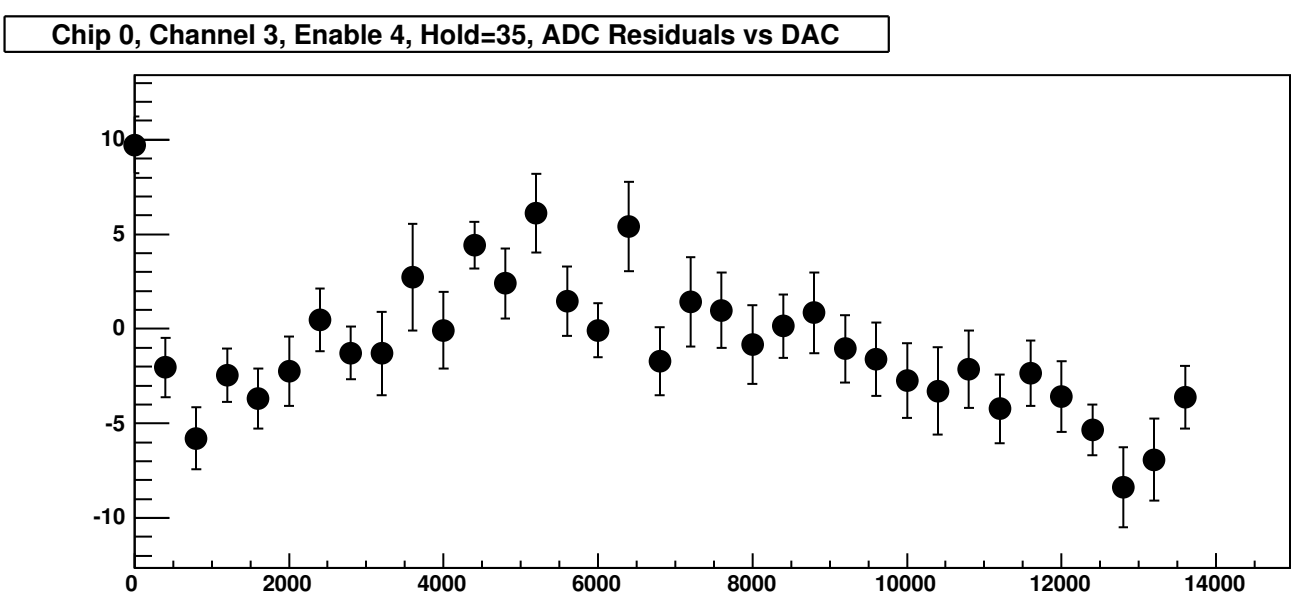

Chip 0, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 44.47 / 23
p0 15.88 ± 0.8332
p1 0.02242 ± 0.0001288
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

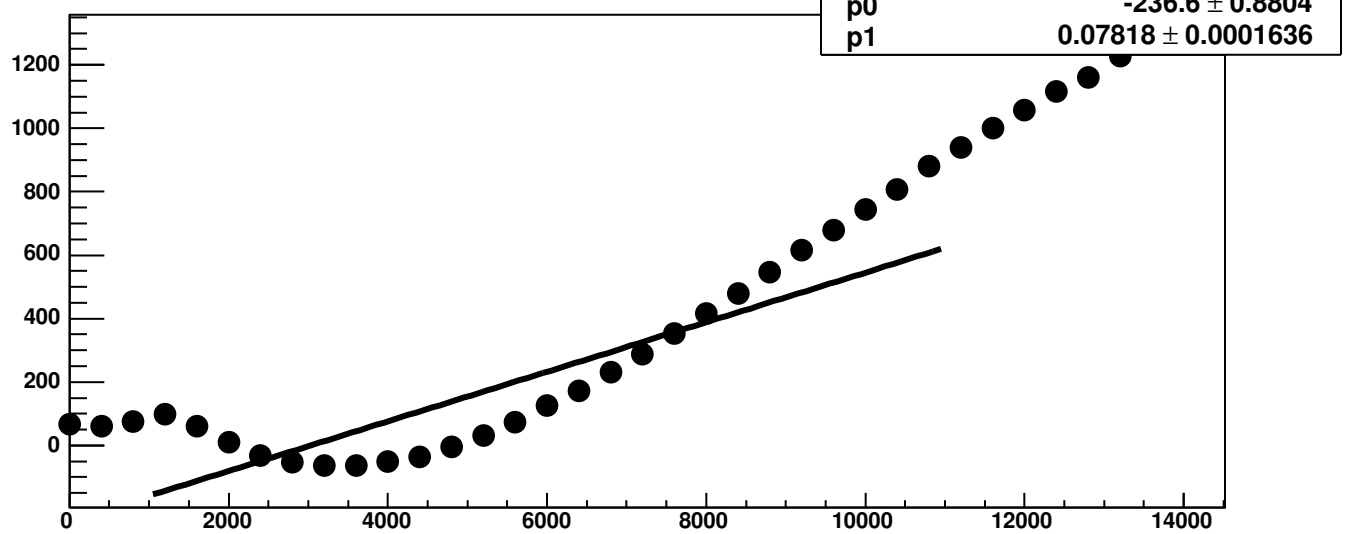

Chip 0, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 48.87 / 23
p0 16.01 ± 0.7829
p1 0.02716 ± 0.0001252
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

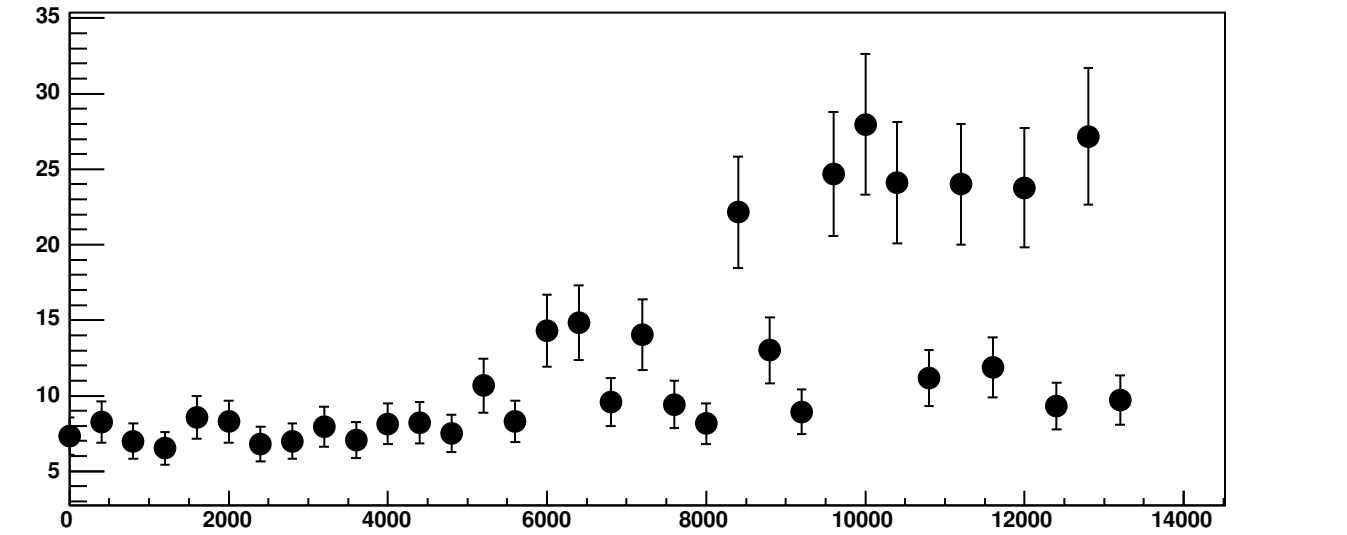

Chip 0, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

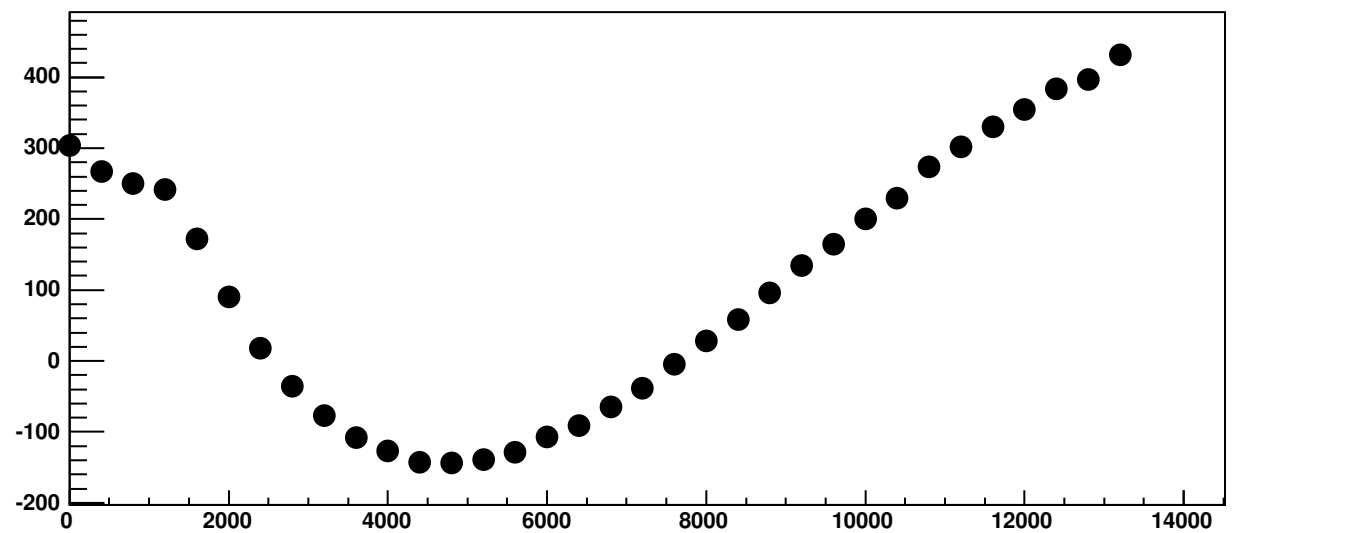
Chip 0, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

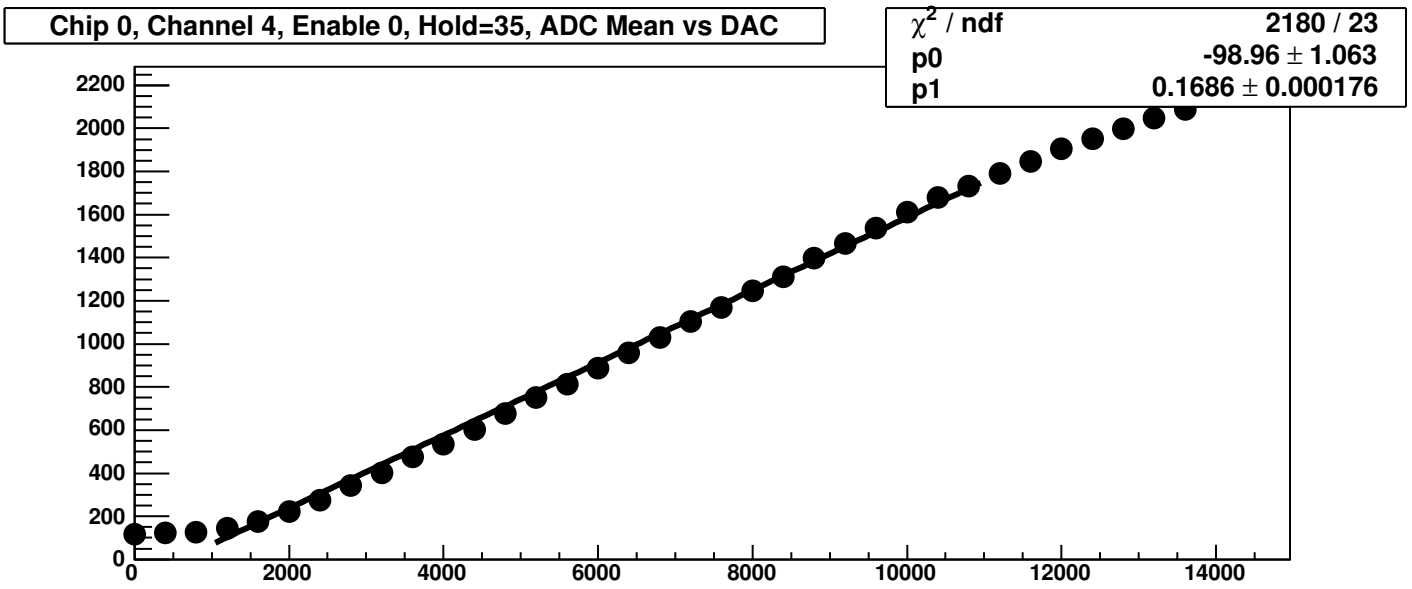


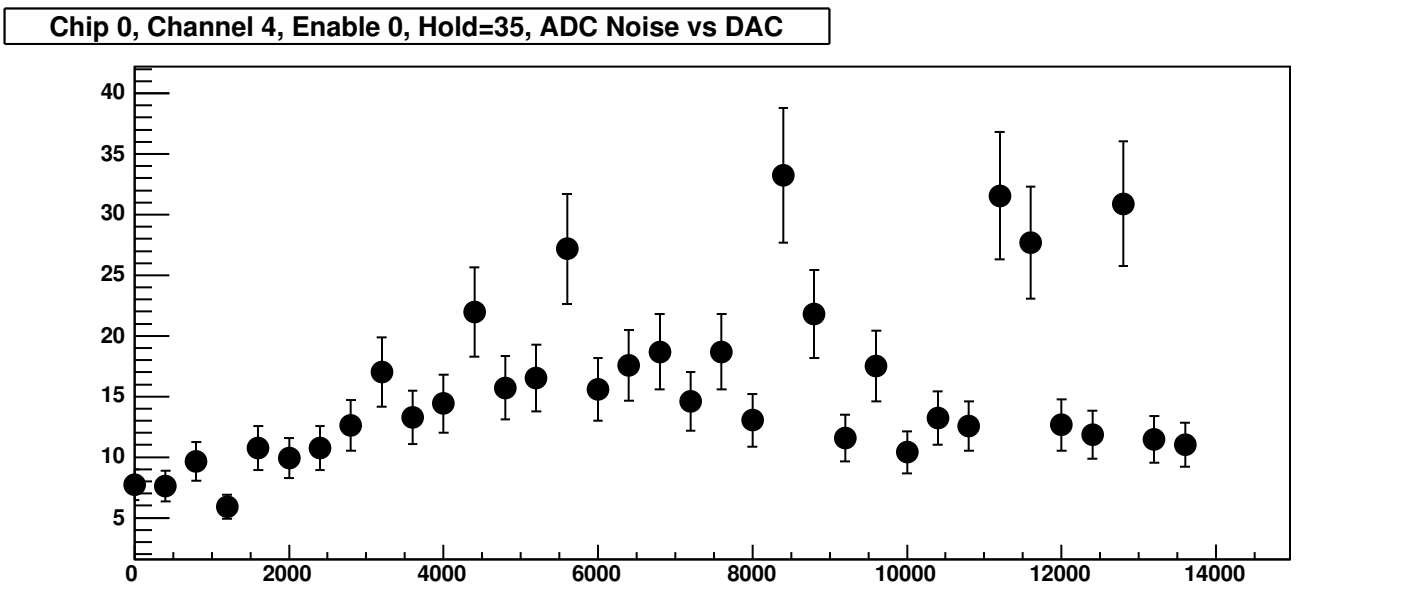
Chip 0, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC

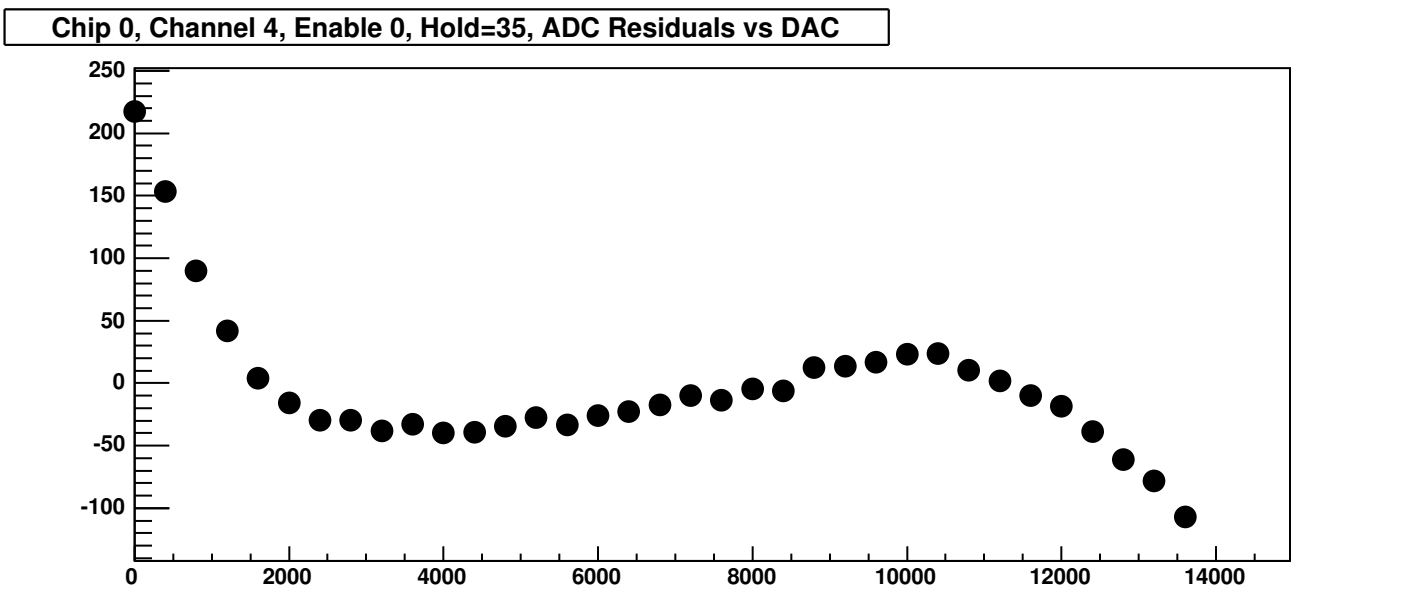


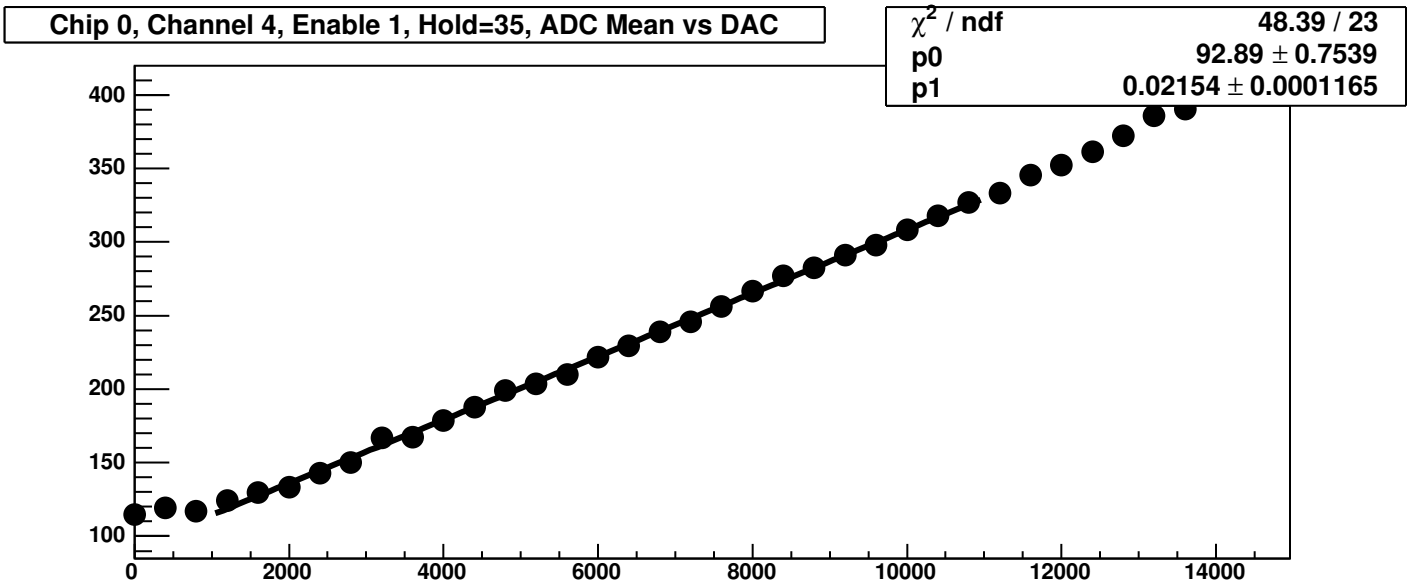
Chip 0, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

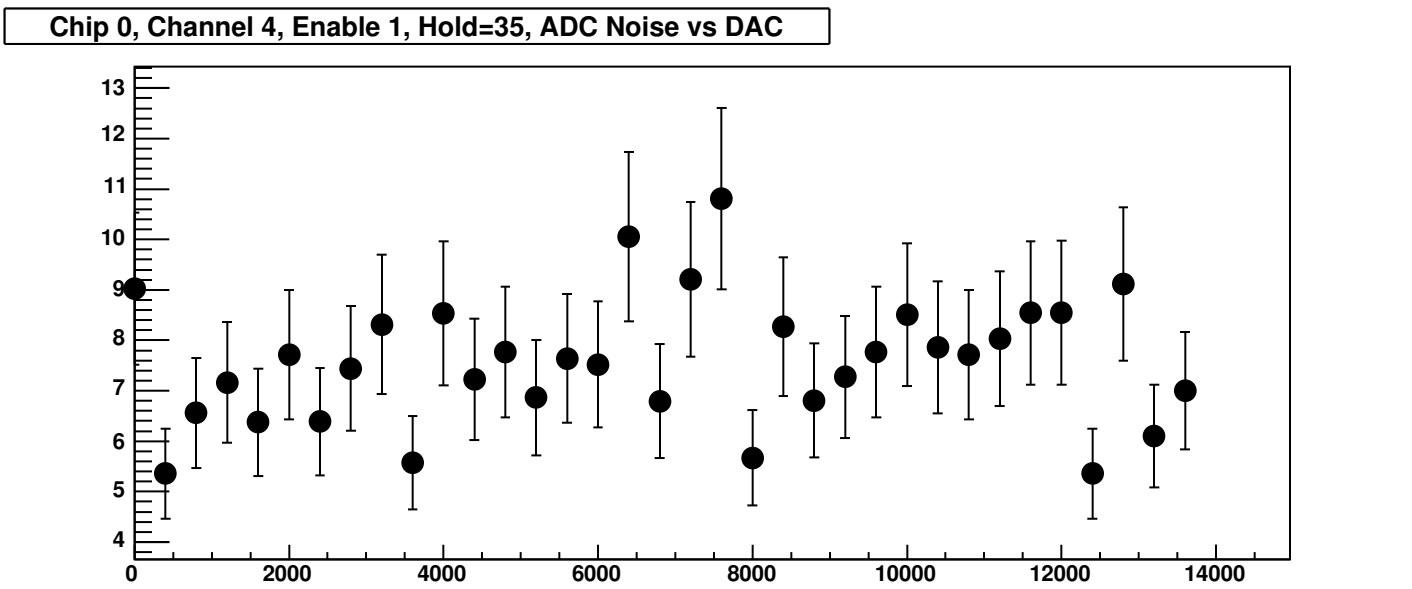


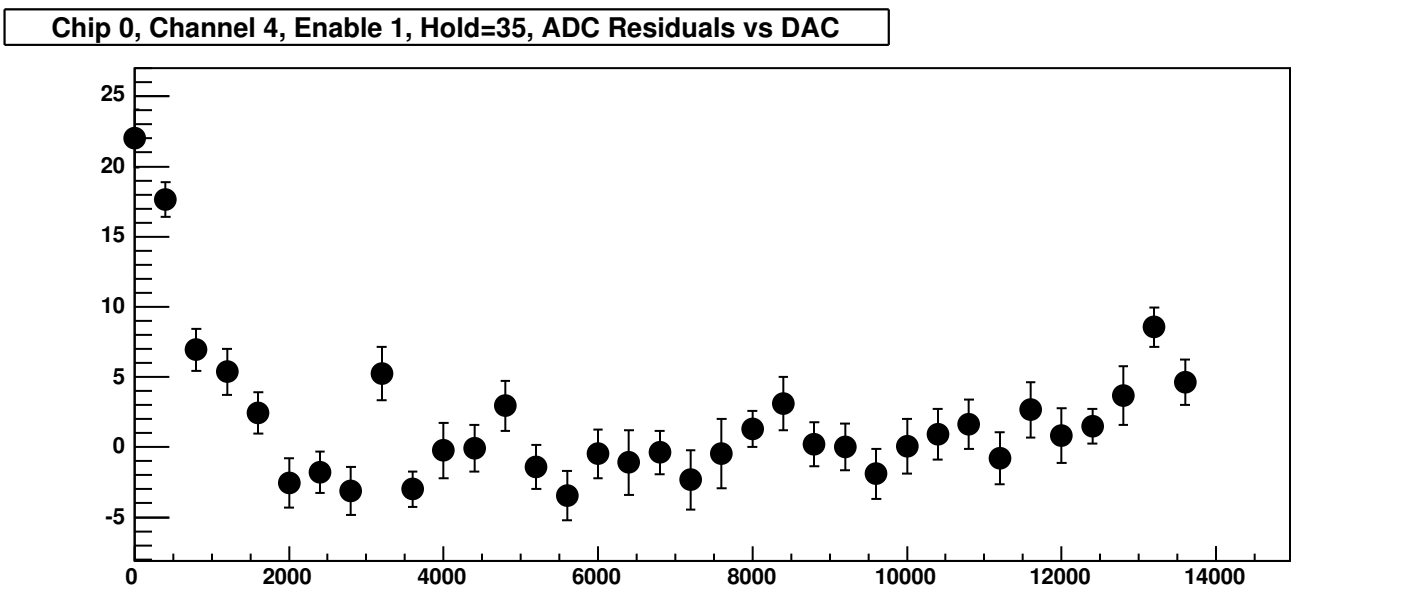

Chip 0, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2180 / 23
p0 -98.96 ± 1.063
p1 0.1686 ± 0.000176
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

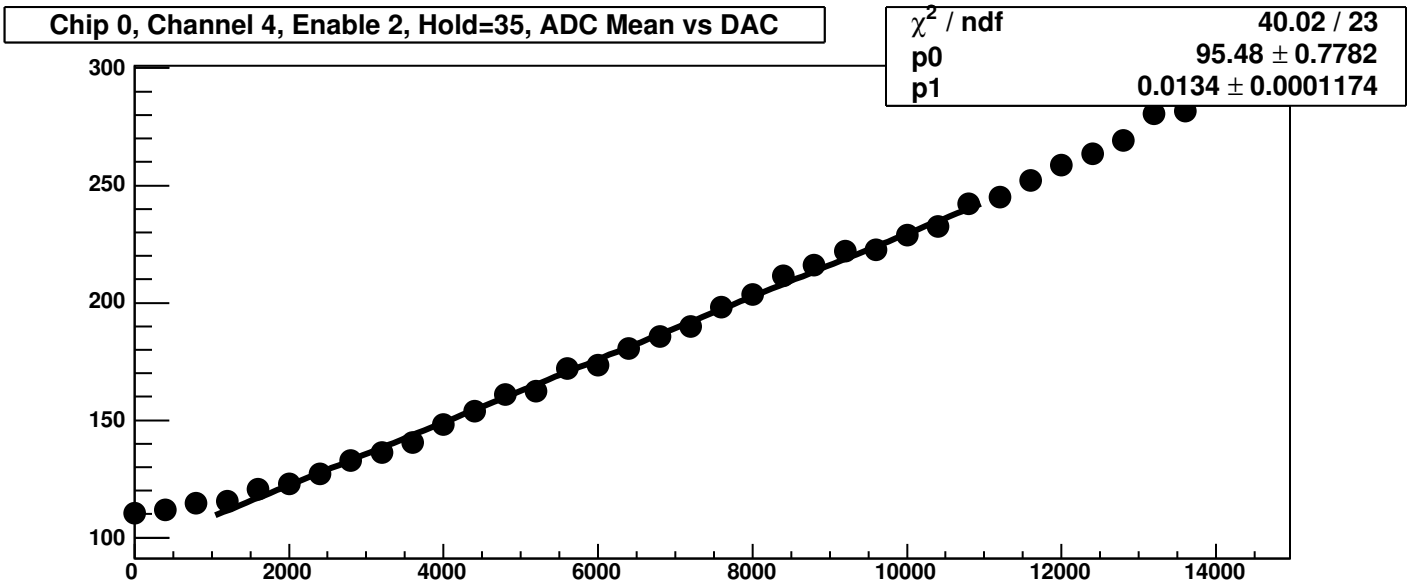

Chip 0, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

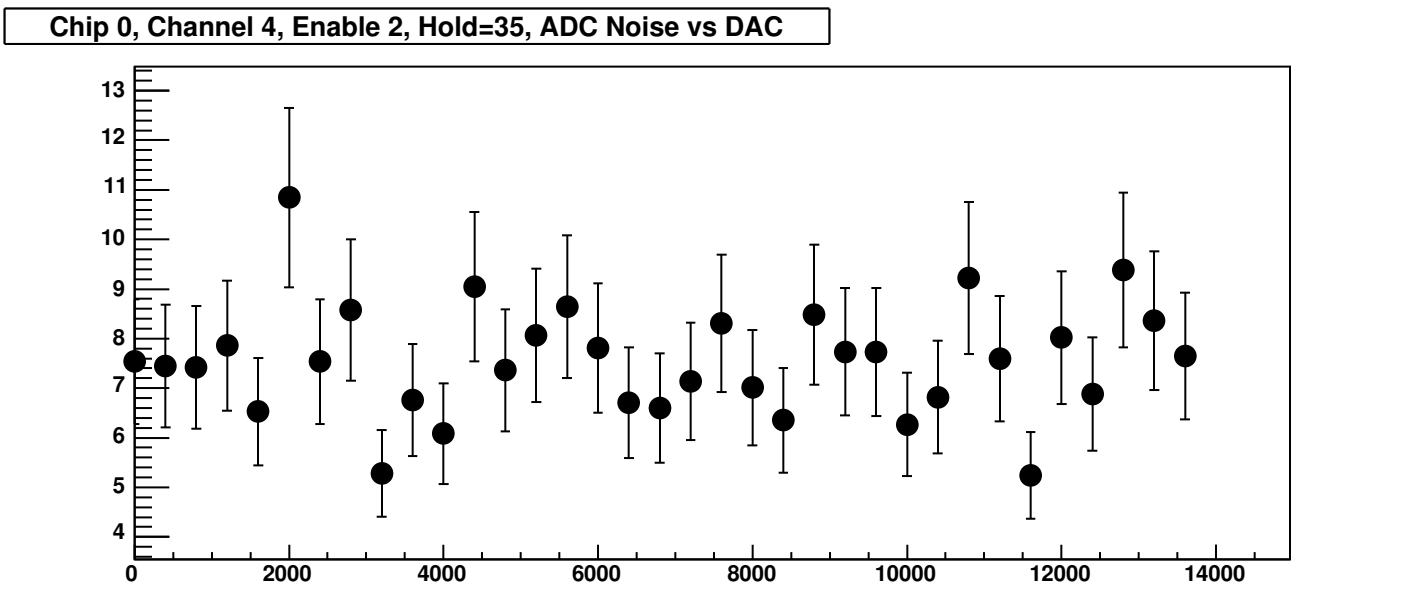

Chip 0, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

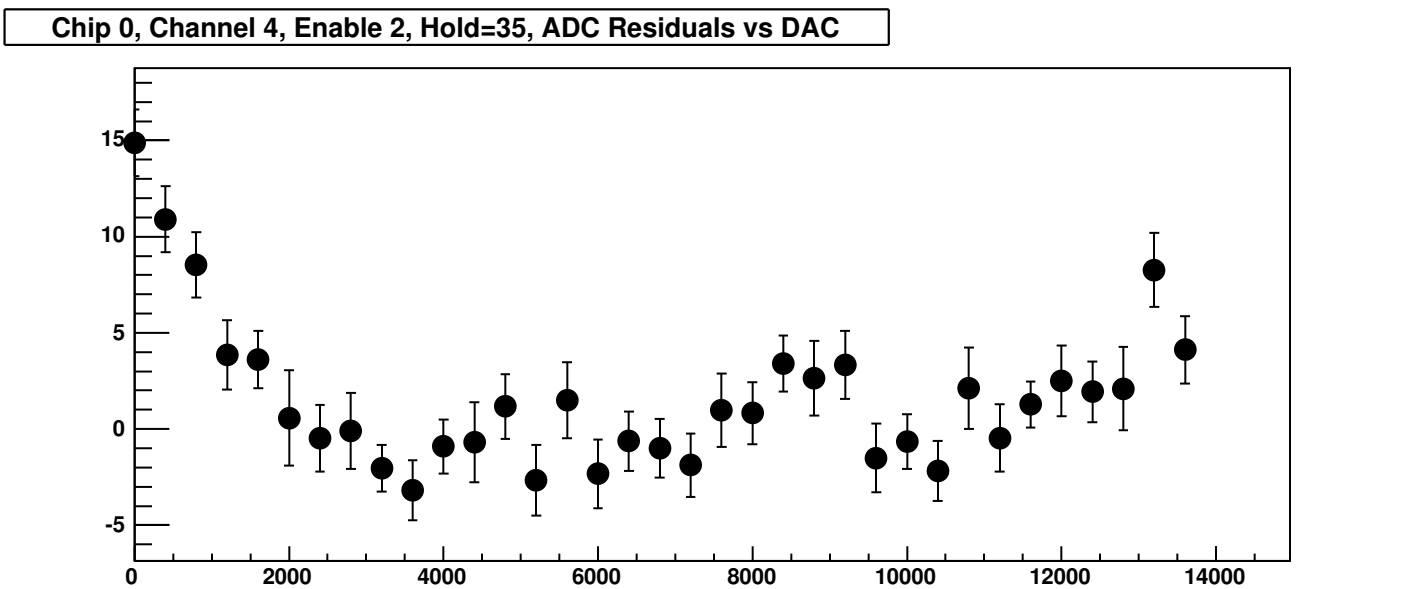

Chip 0, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 48.39 / 23
p0 92.89 ± 0.7539
p1 0.02154 ± 0.0001165
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

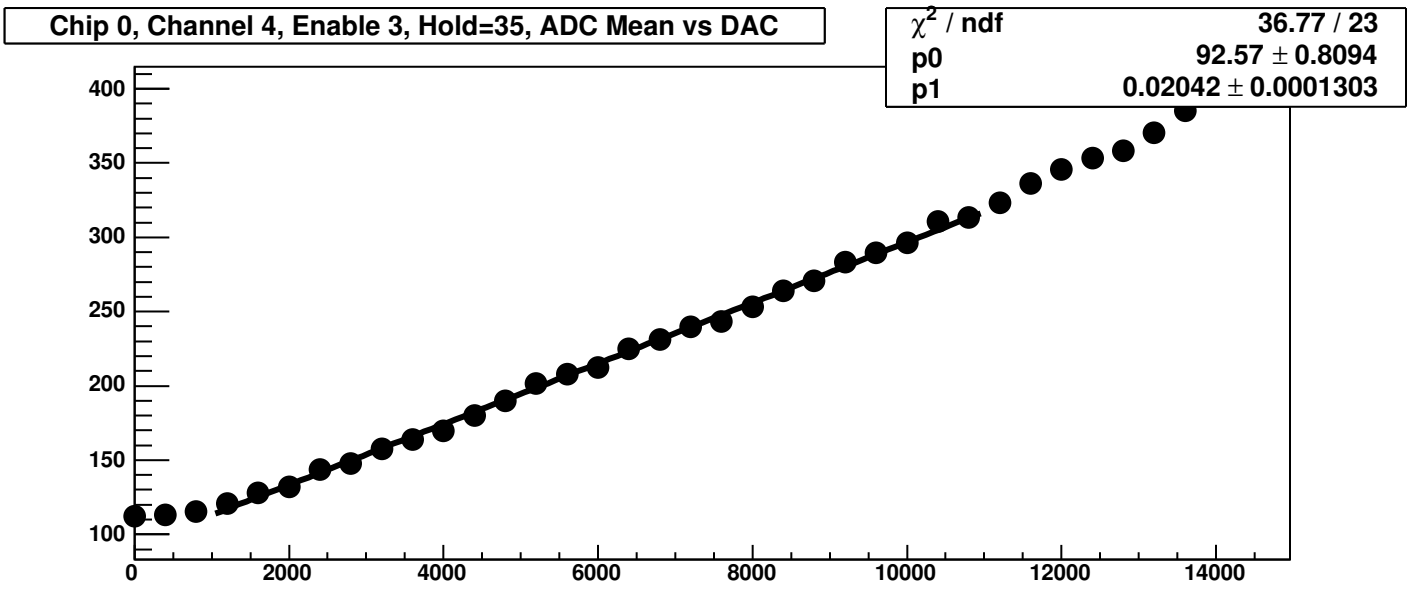

Chip 0, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

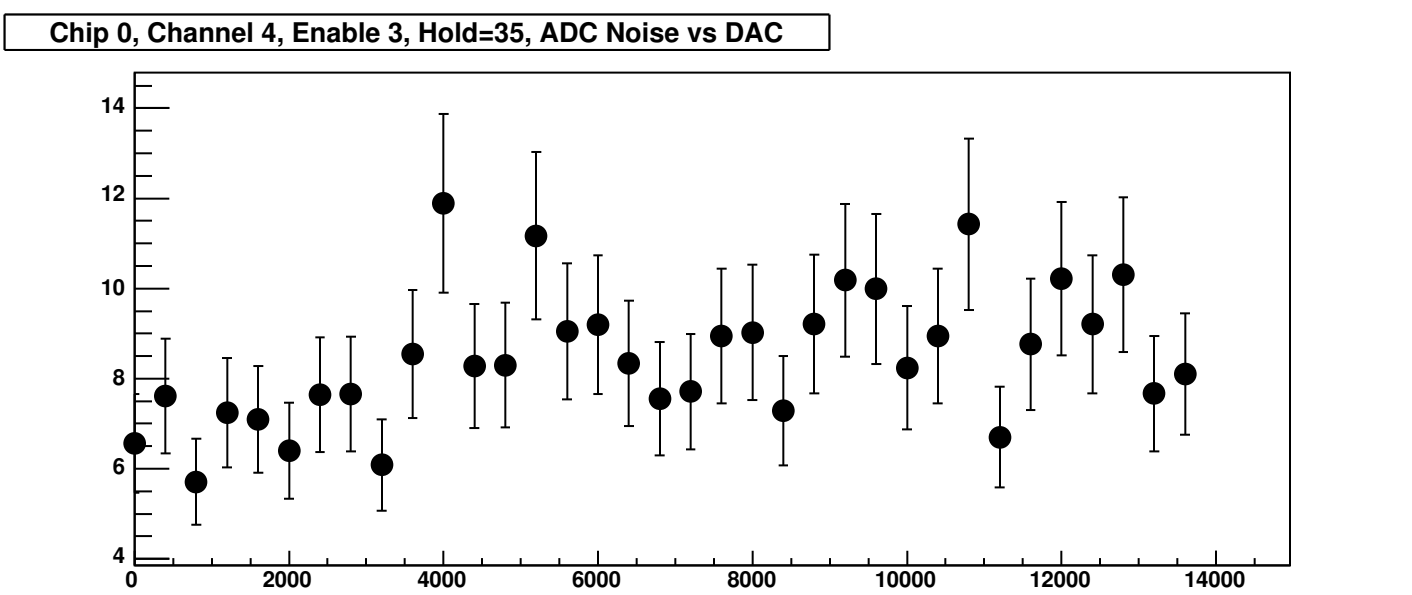

Chip 0, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

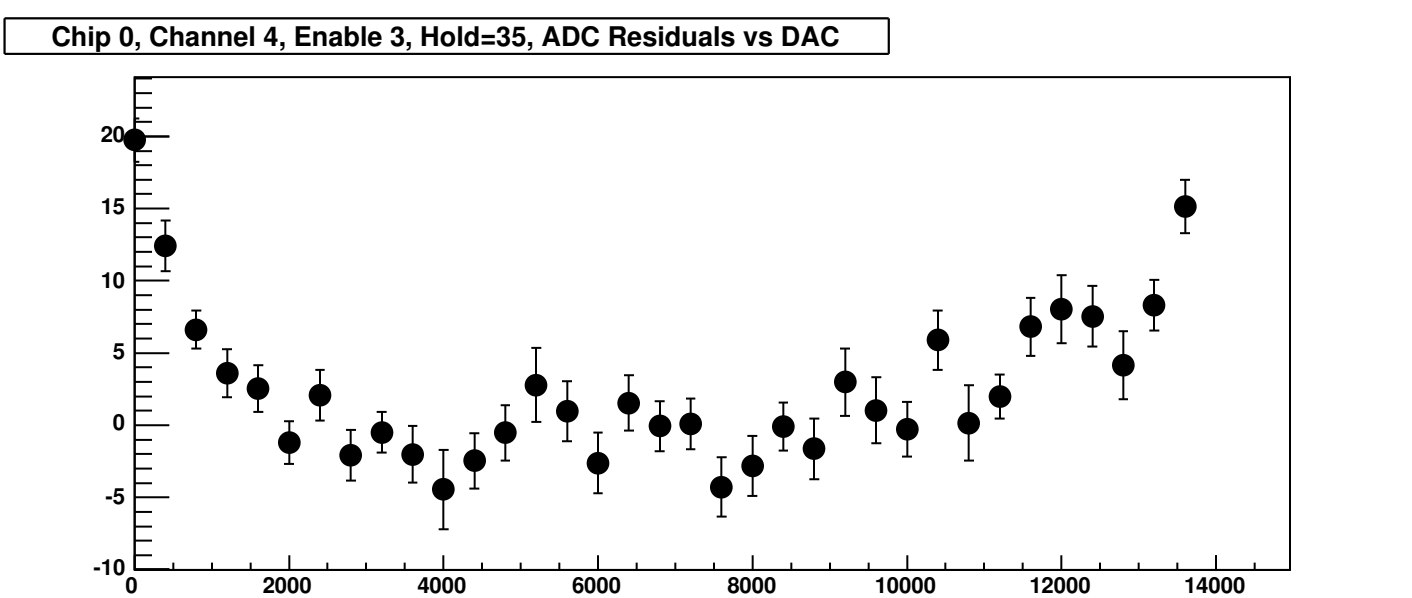

Chip 0, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 40.02 / 23
p0 95.48 ± 0.7782
p1 0.0134 ± 0.0001174
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

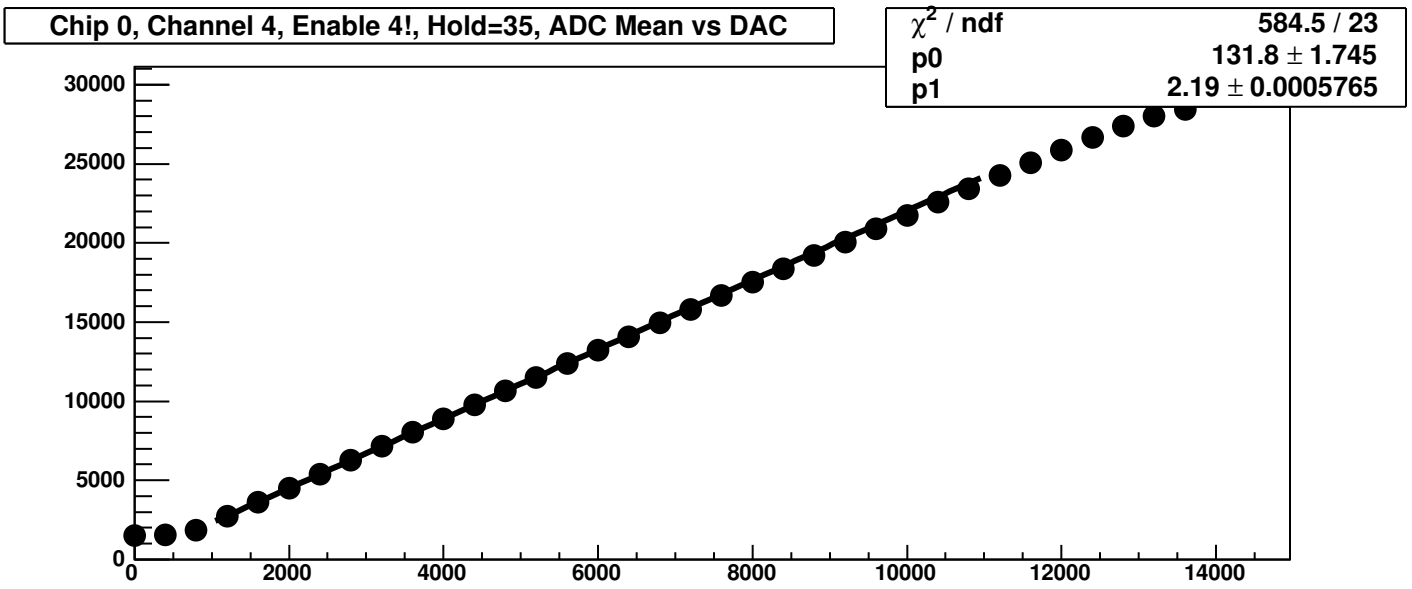

Chip 0, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

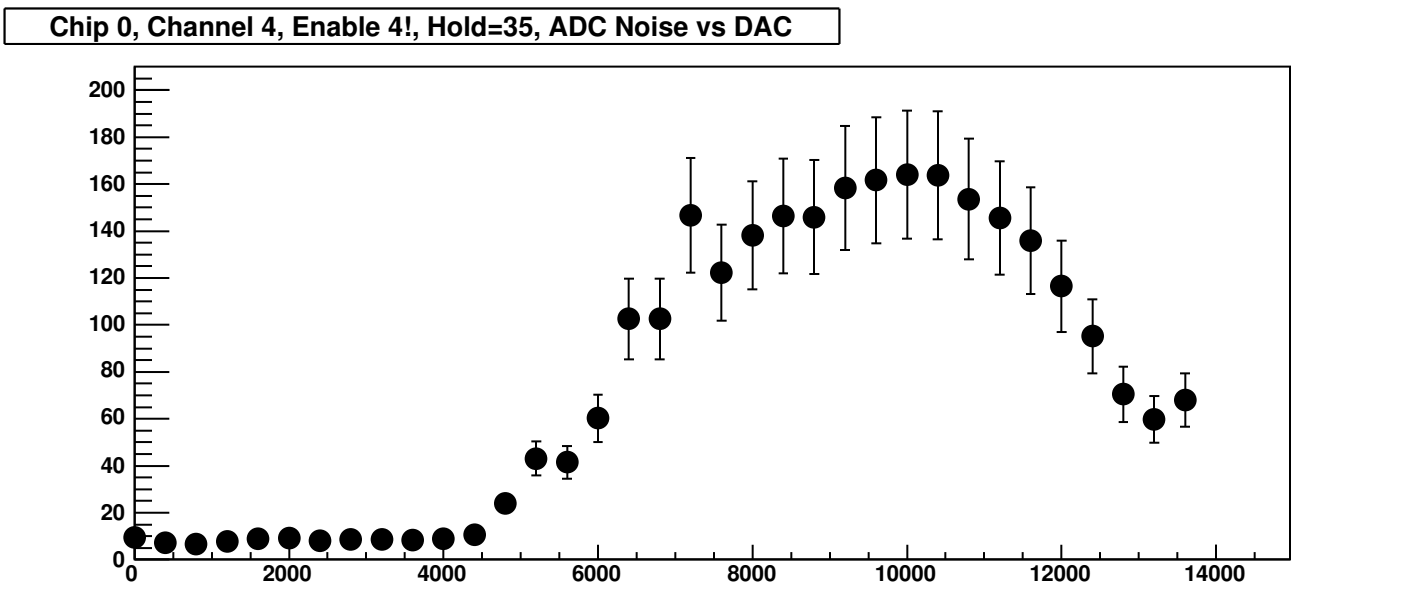

Chip 0, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

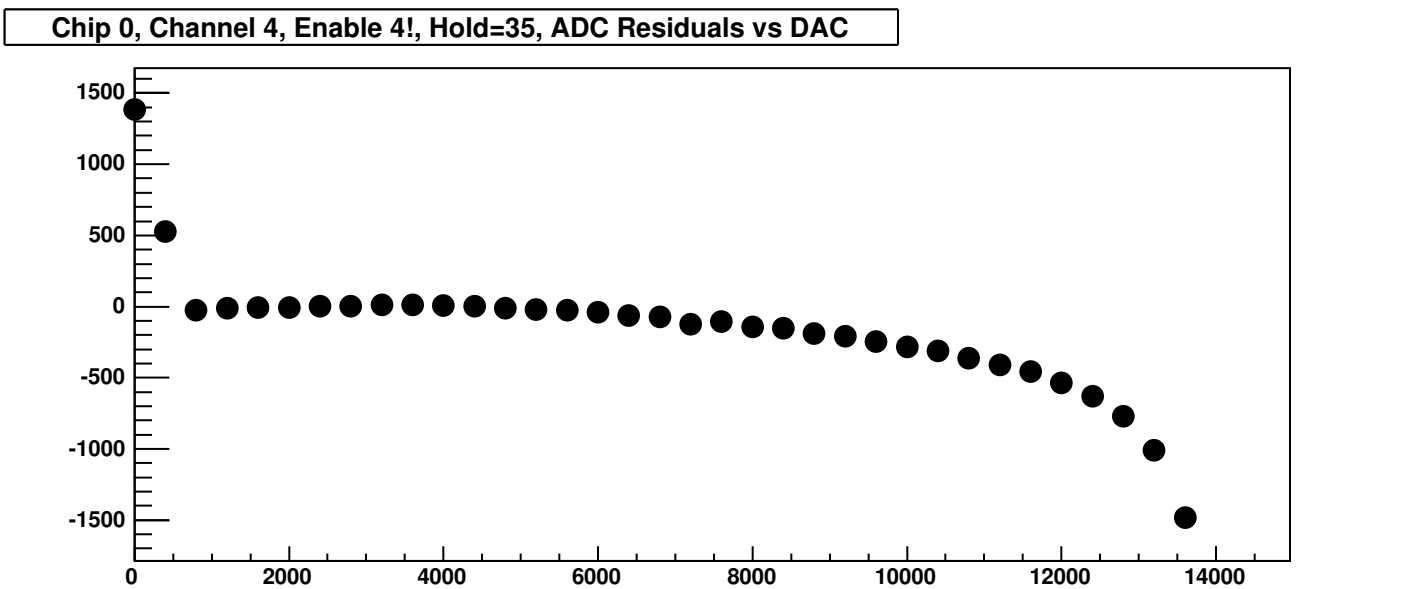

Chip 0, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.77 / 23
p0 92.57 ± 0.8094
p1 0.02042 ± 0.0001303
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

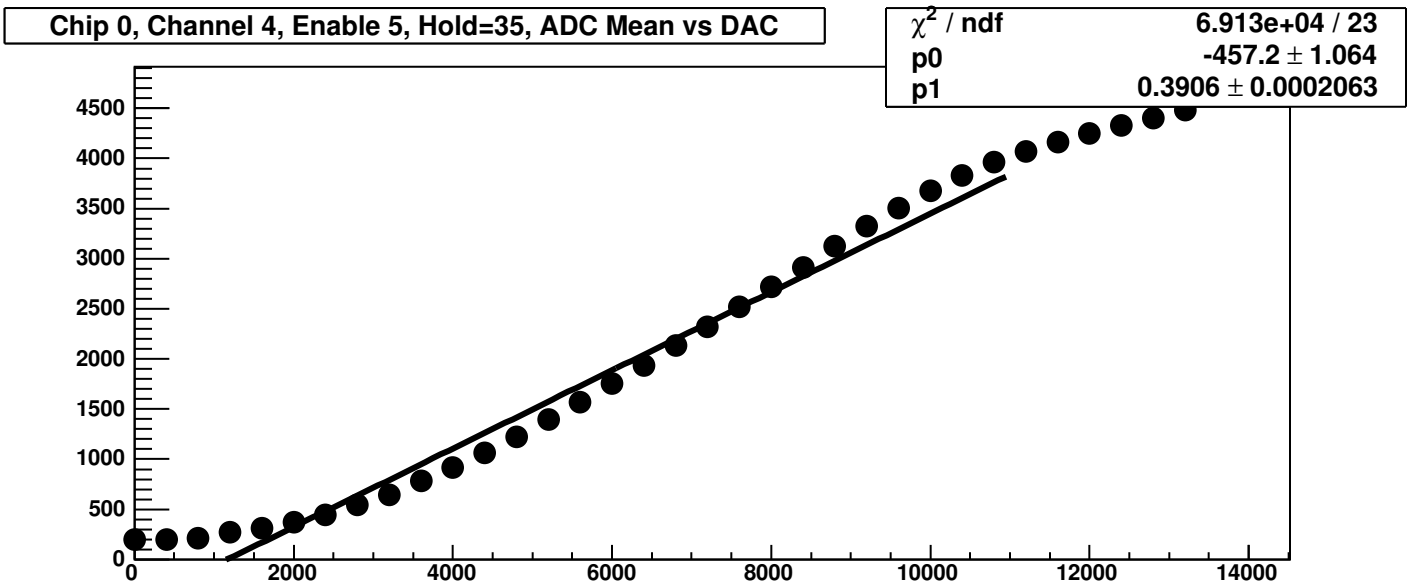

Chip 0, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

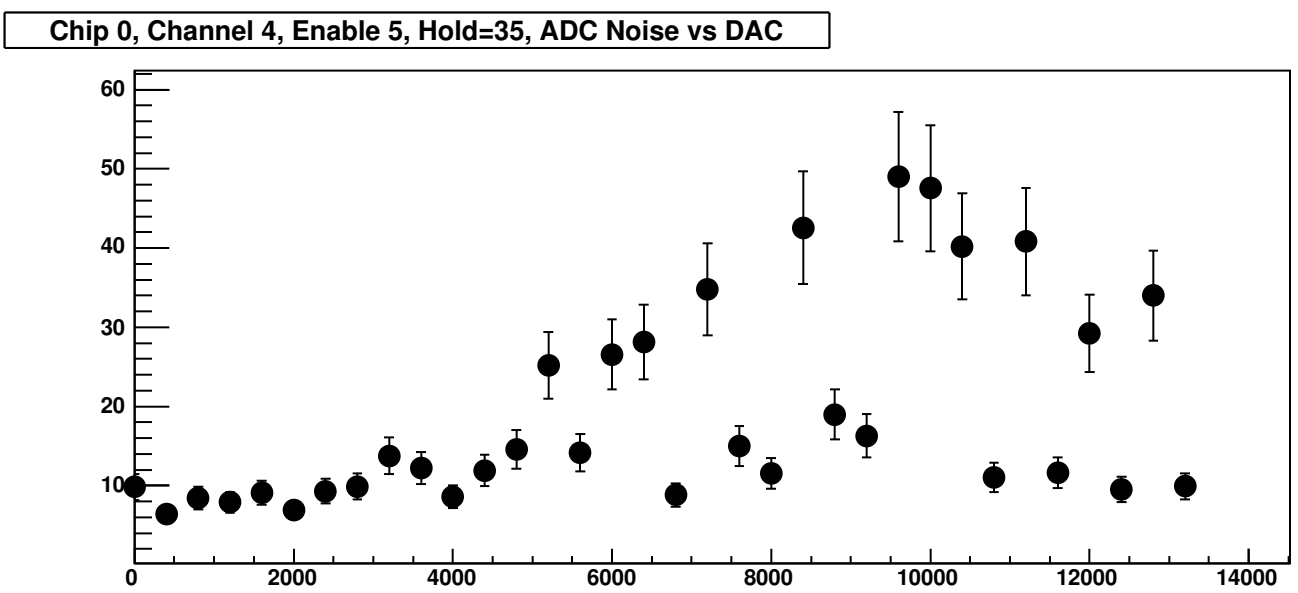

Chip 0, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

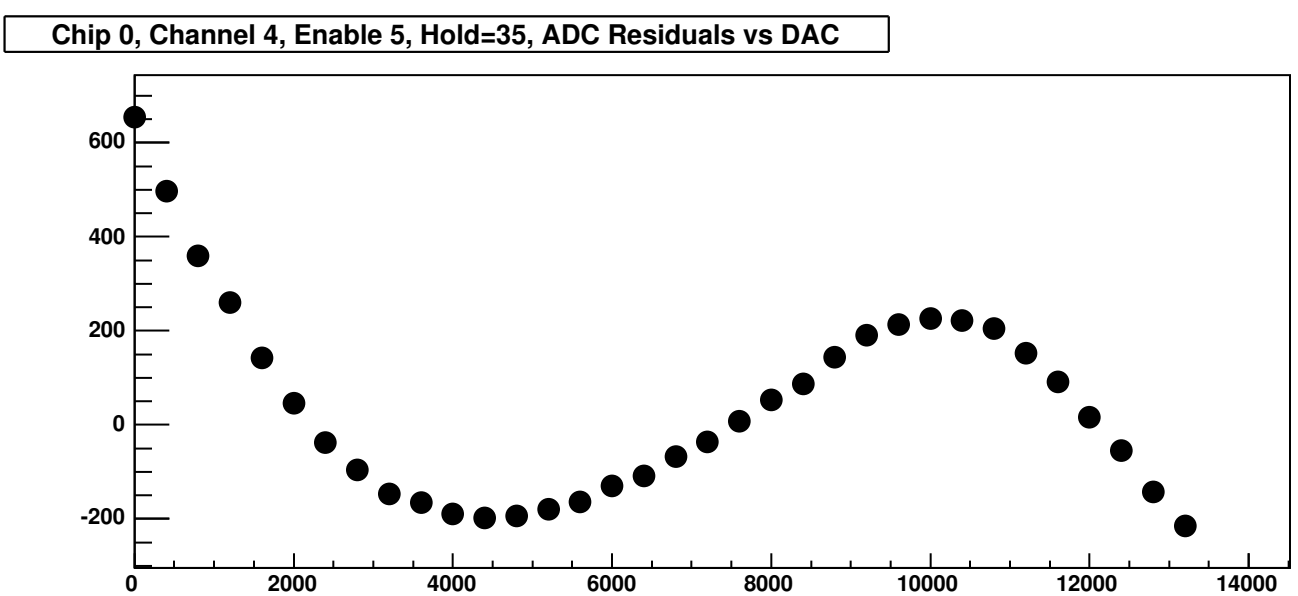

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 584.5 / 23
p0 131.8 ± 1.745
p1 2.19 ± 0.0005765
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

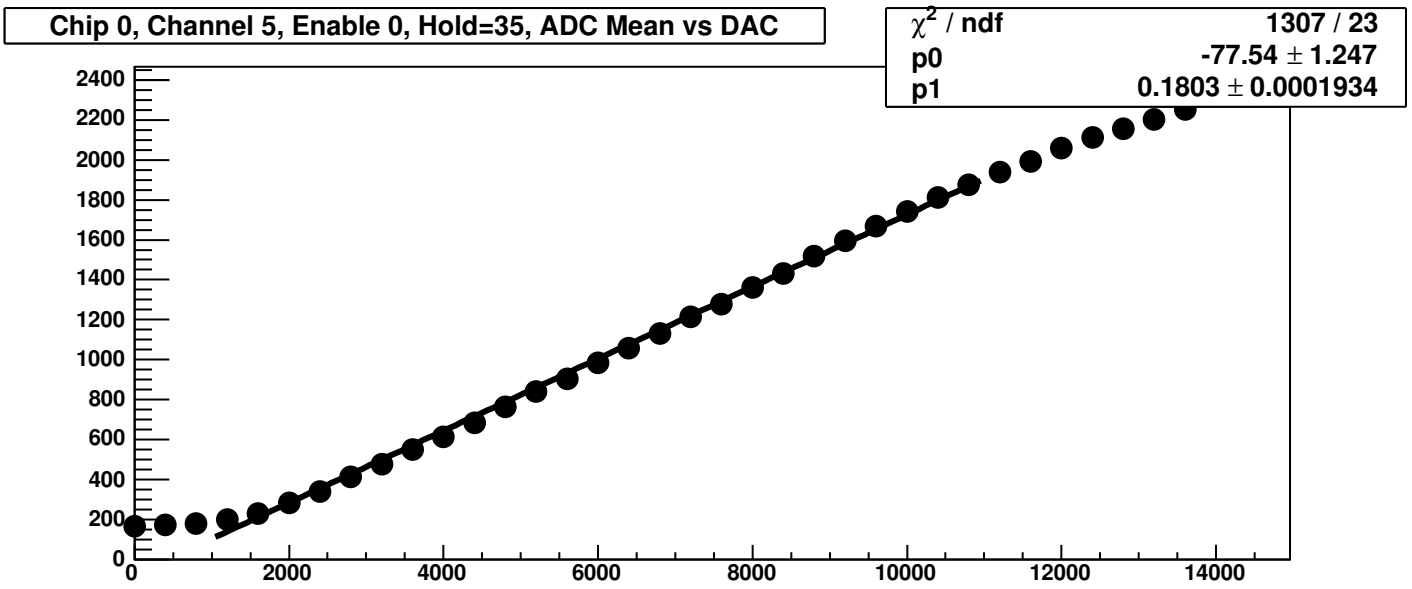

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

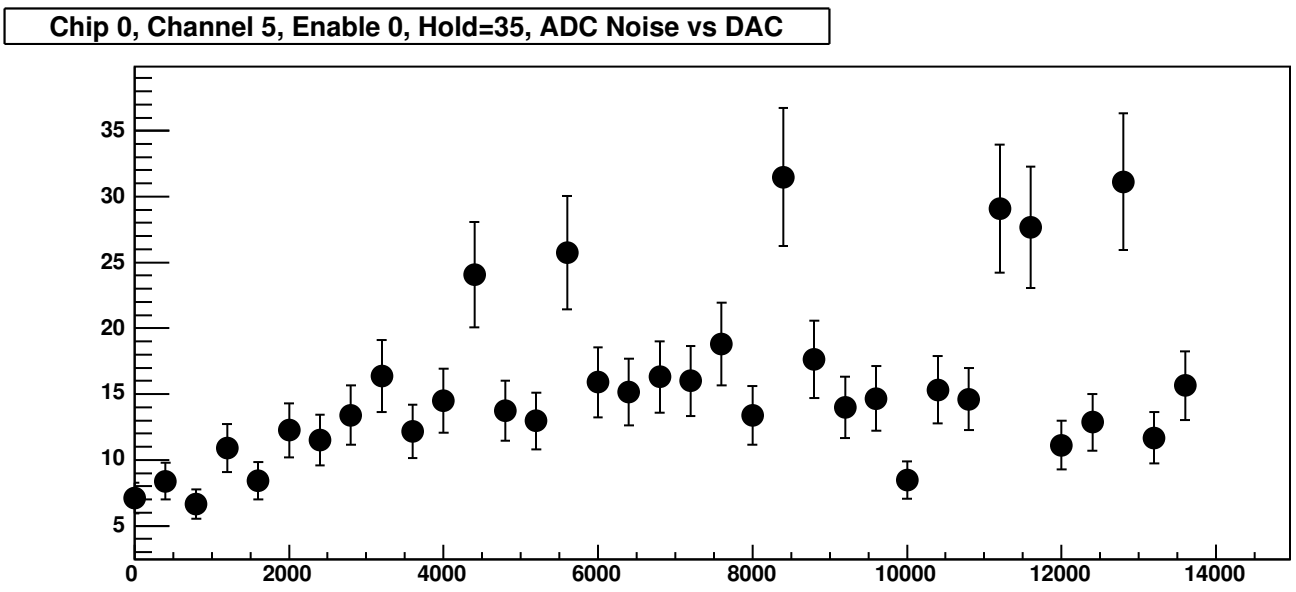

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

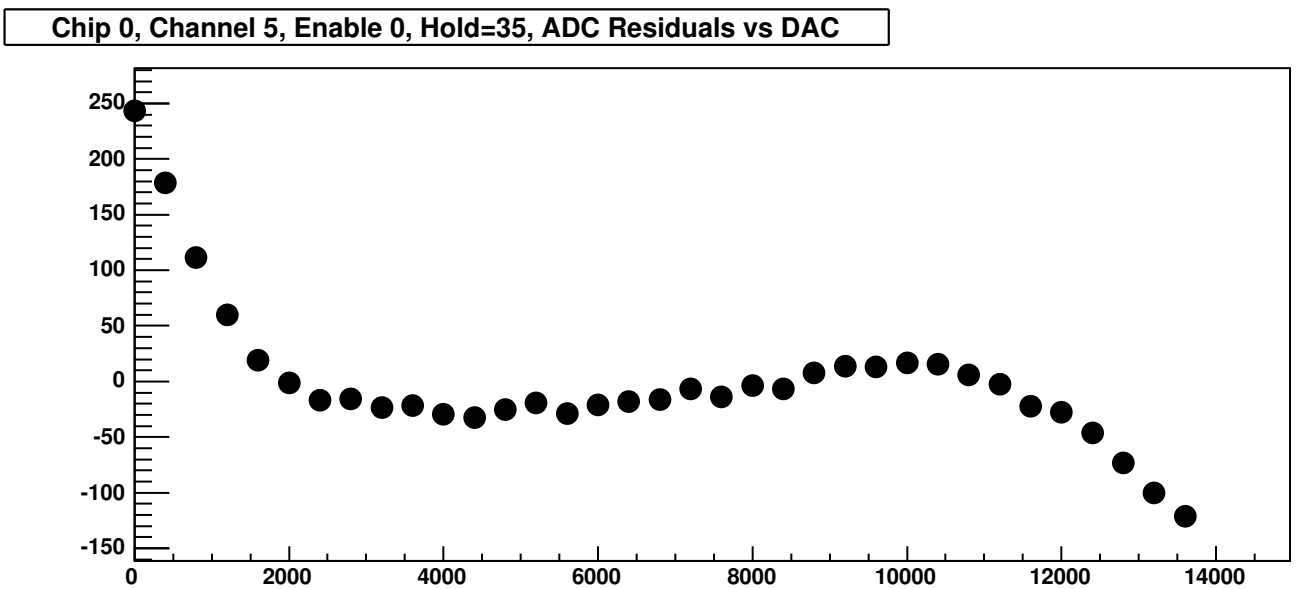

Chip 0, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 6.913e+04 / 23
p0 -457.2 ± 1.064
p1 0.3906 ± 0.0002063
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

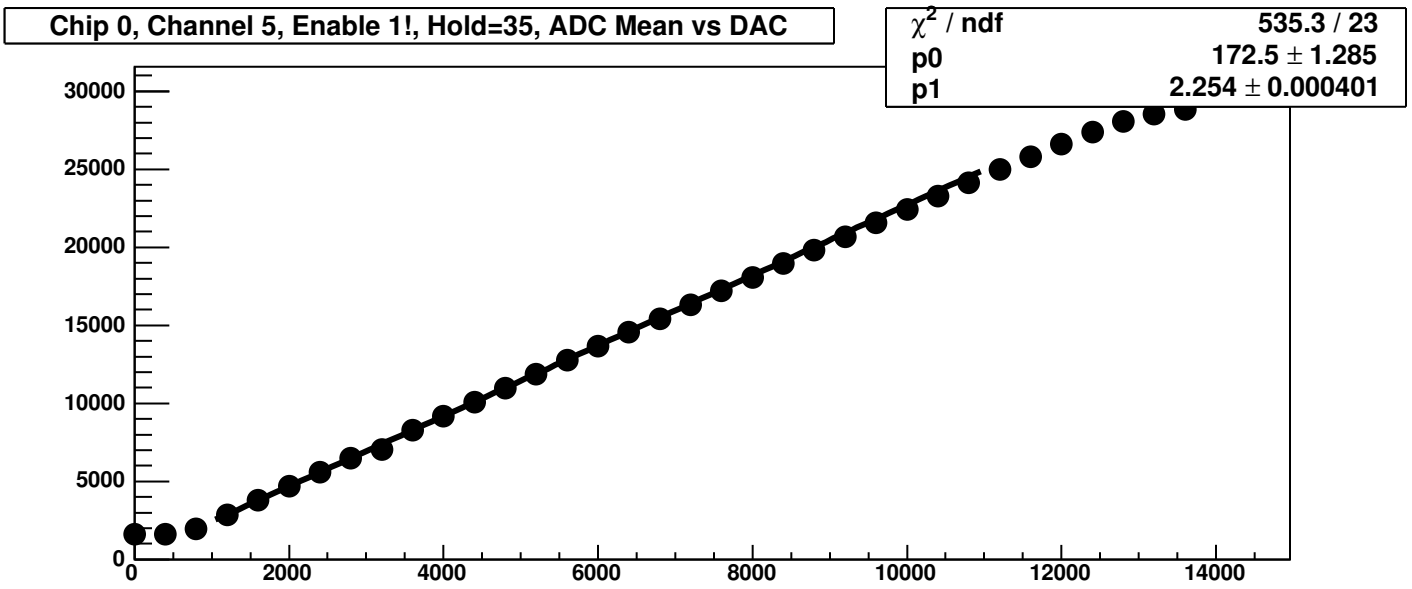

Chip 0, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

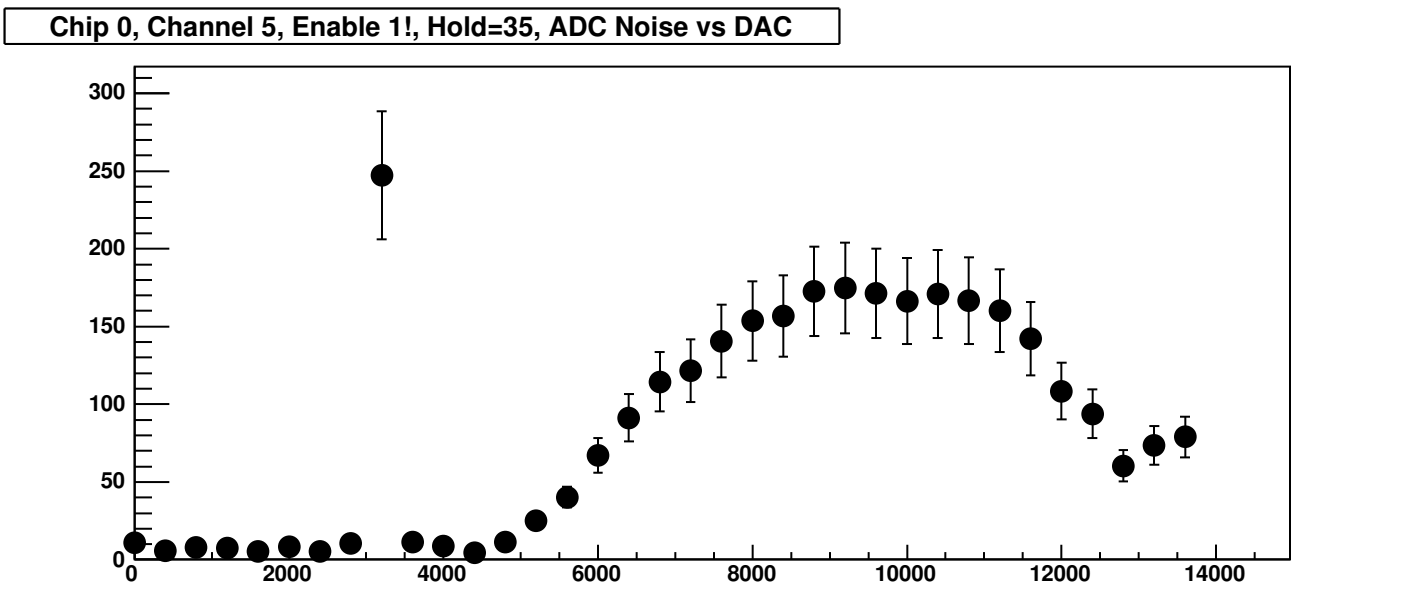

Chip 0, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

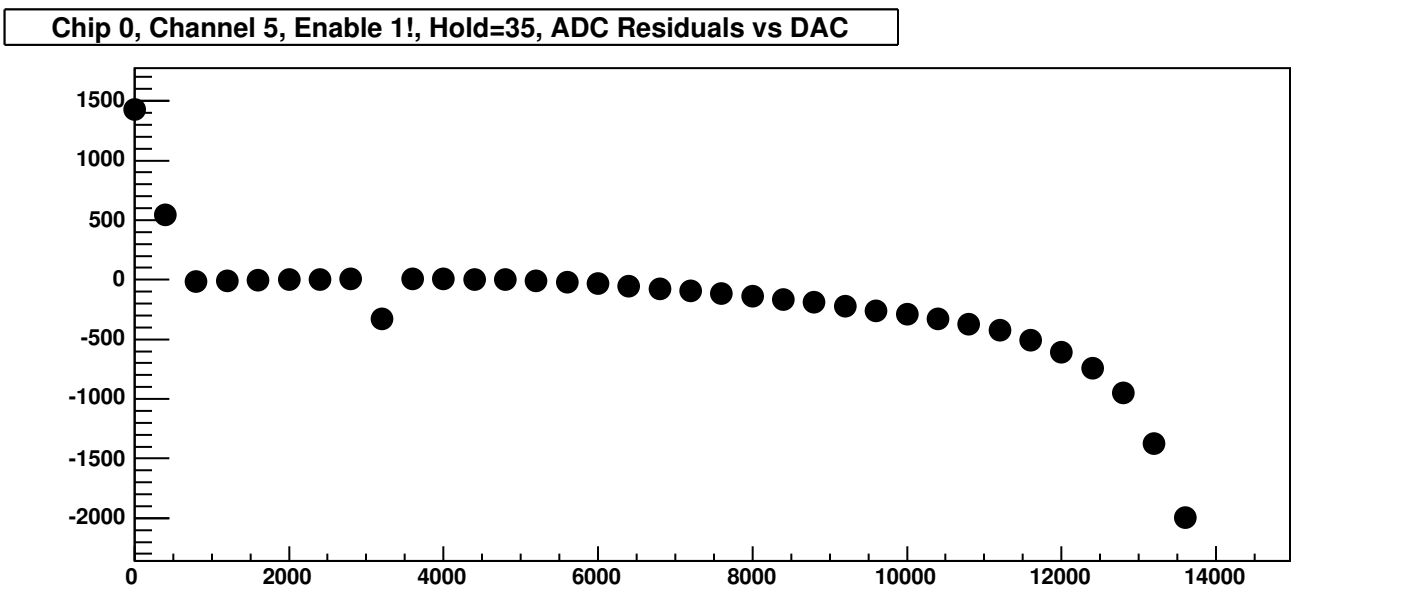

Chip 0, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1307 / 23
p0 -77.54 ± 1.247
p1 0.1803 ± 0.0001934
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

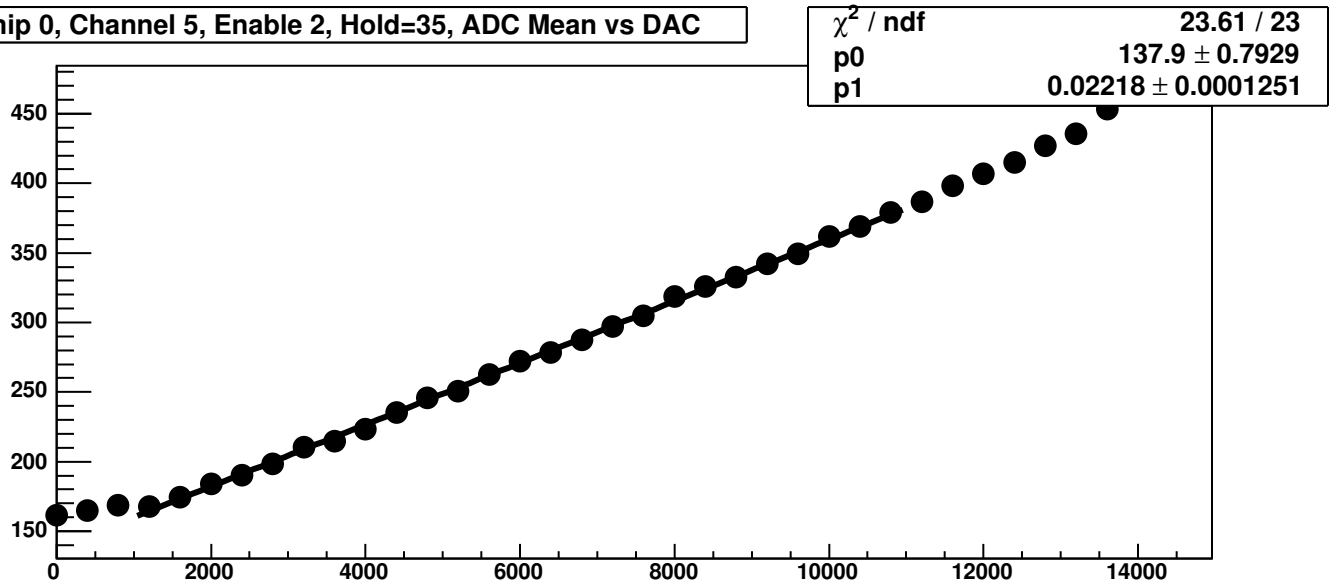

Chip 0, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 535.3 / 23
p0 172.5 ± 1.285
p1 2.254 ± 0.000401
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

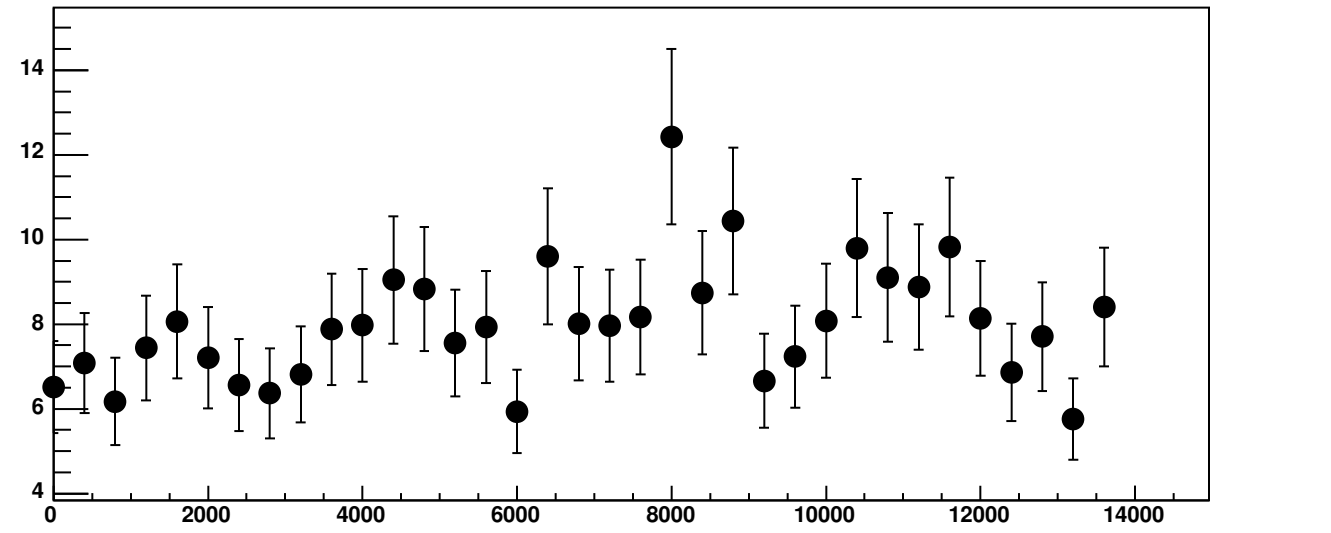

Chip 0, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

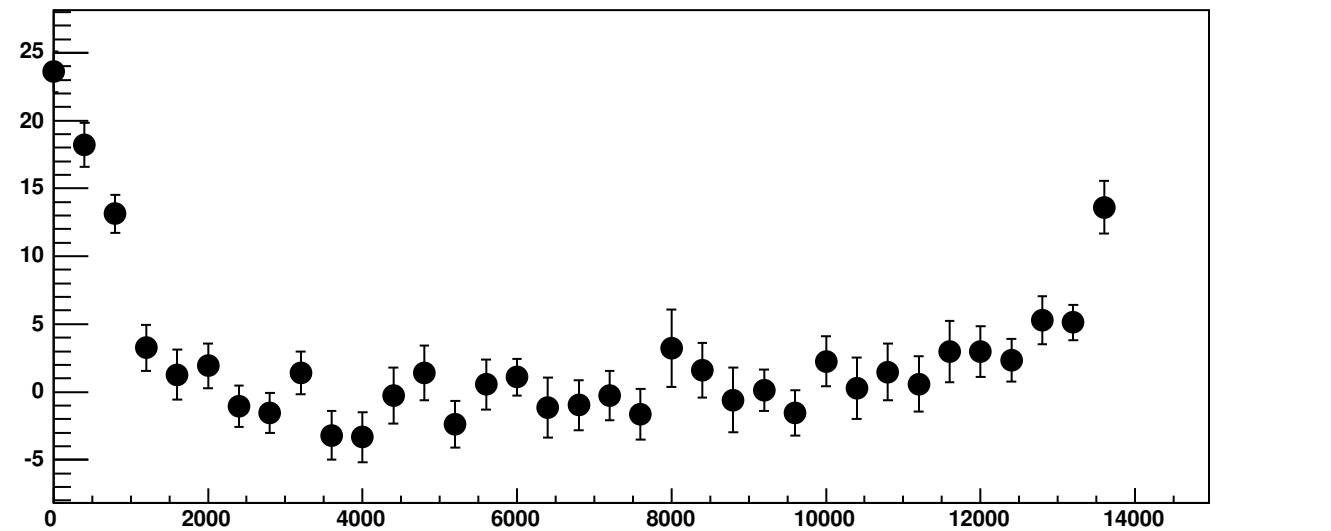
Chip 0, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

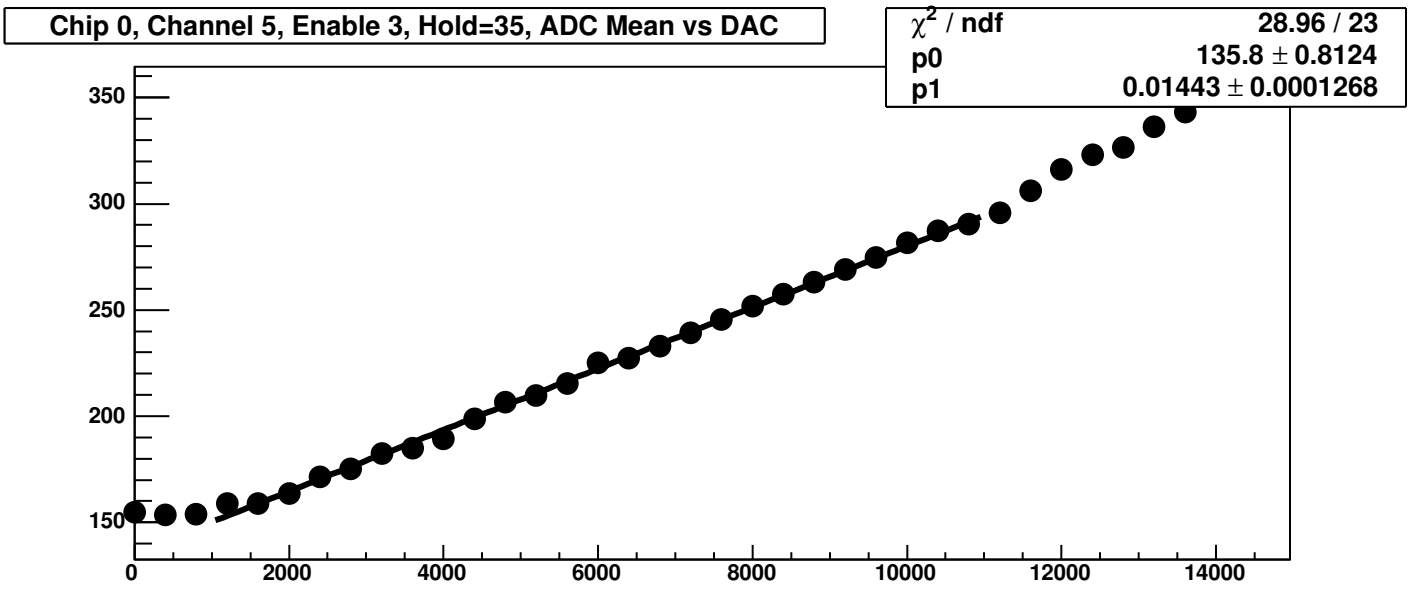


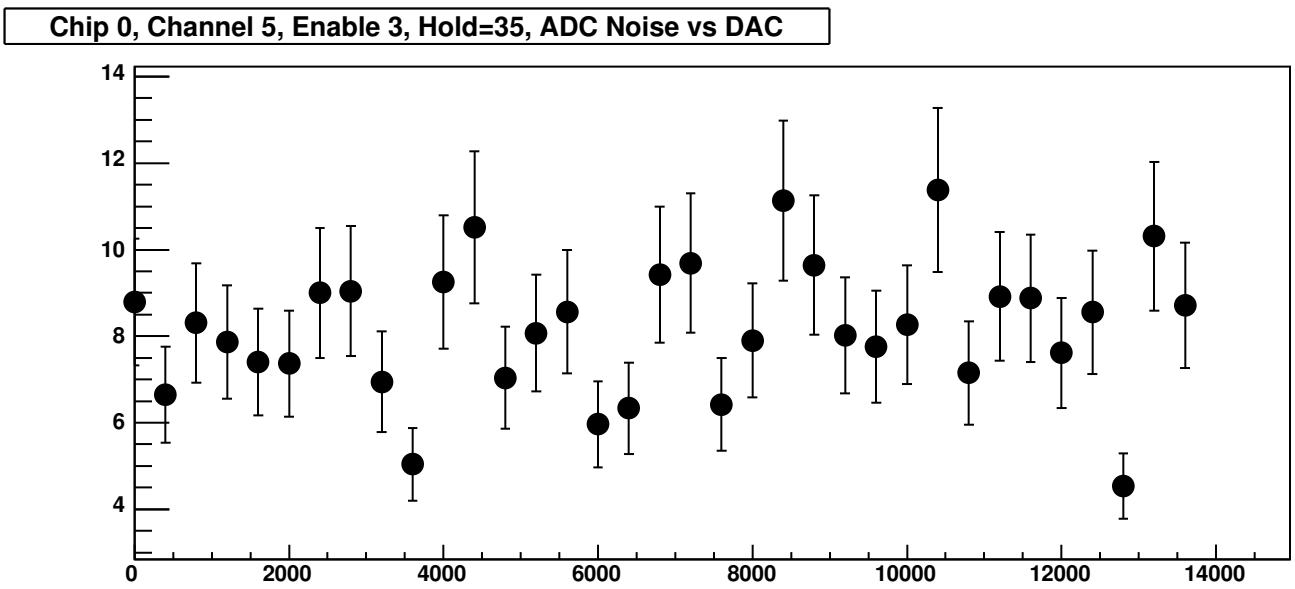
Chip 0, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC

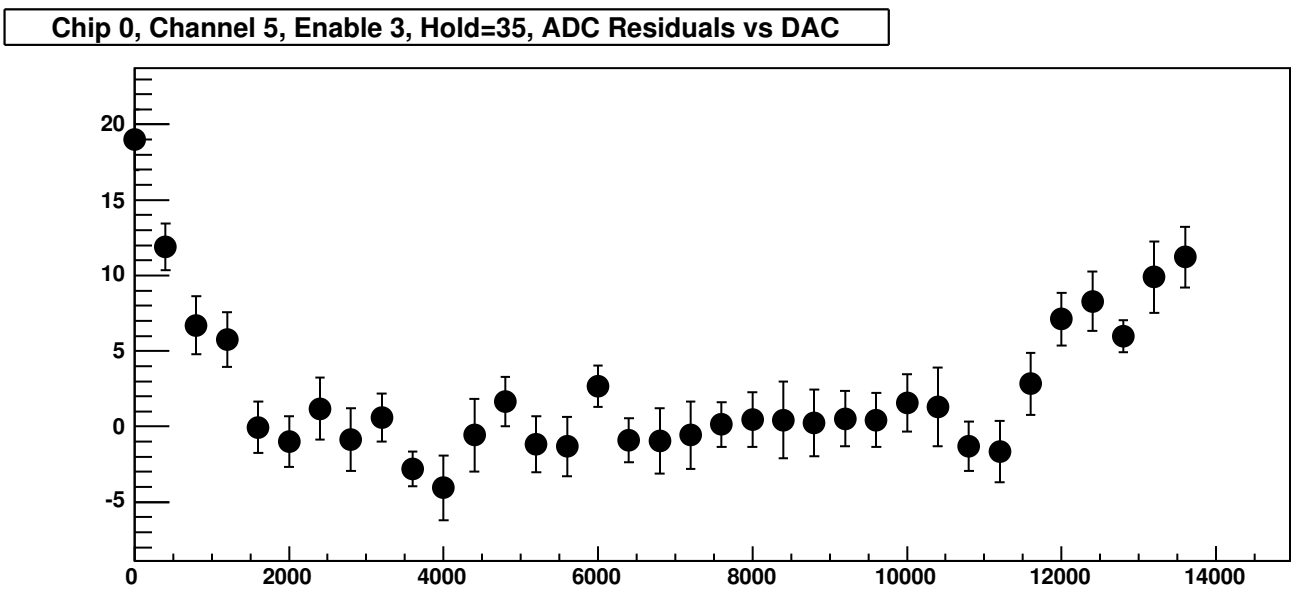


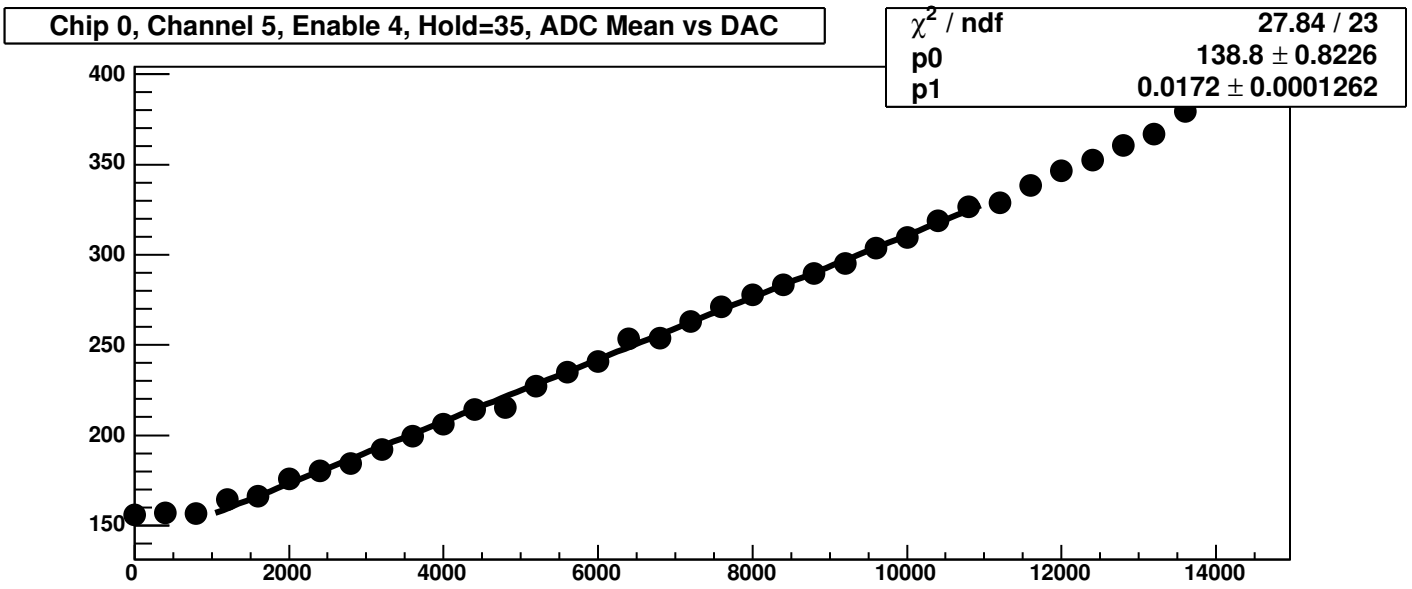
Chip 0, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

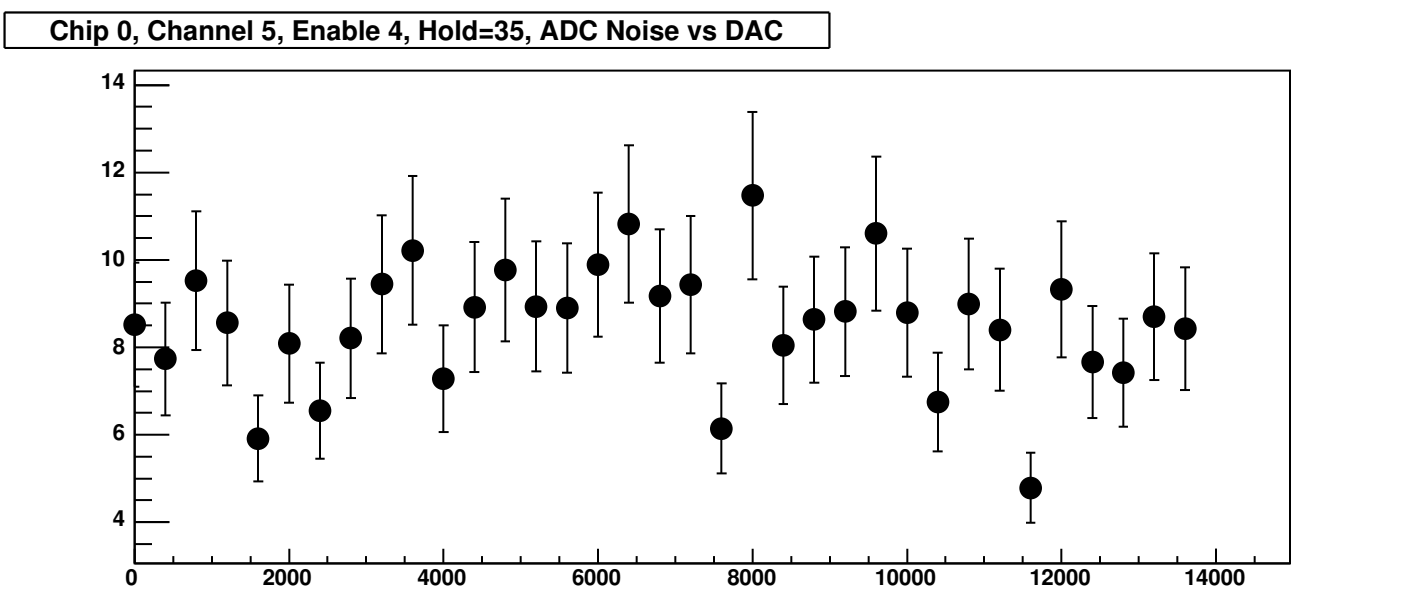


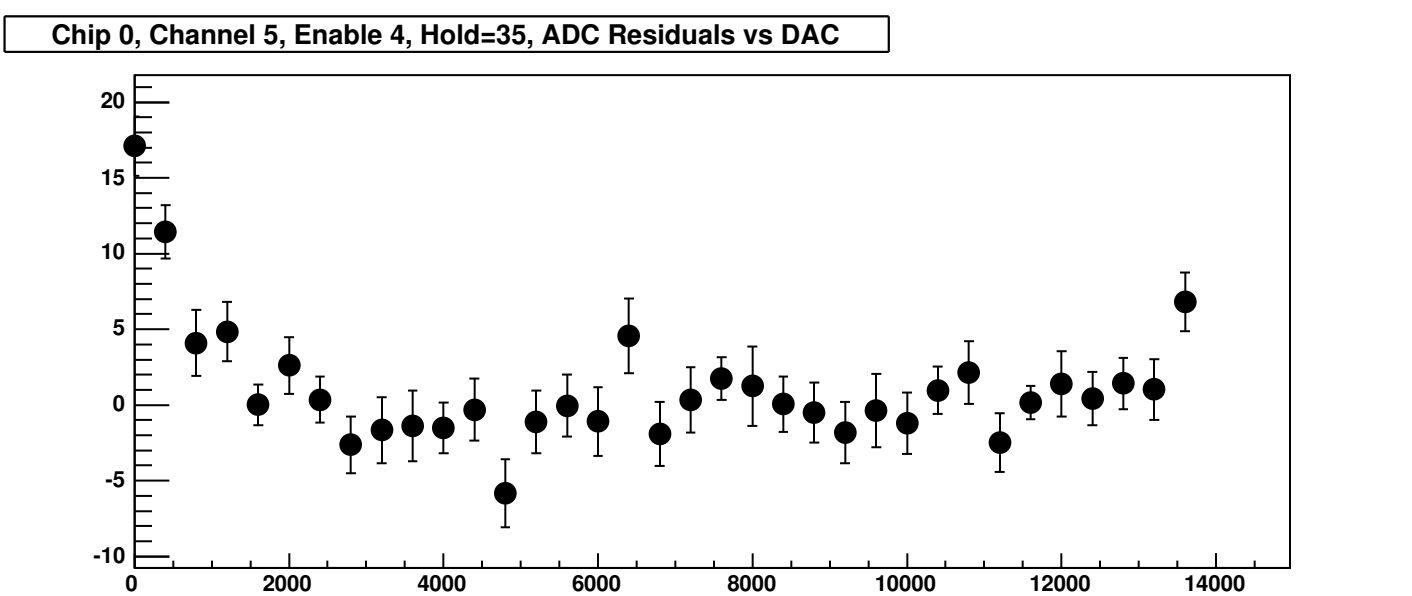

Chip 0, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.96 / 23
p0 135.8 ± 0.8124
p1 0.01443 ± 0.0001268
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

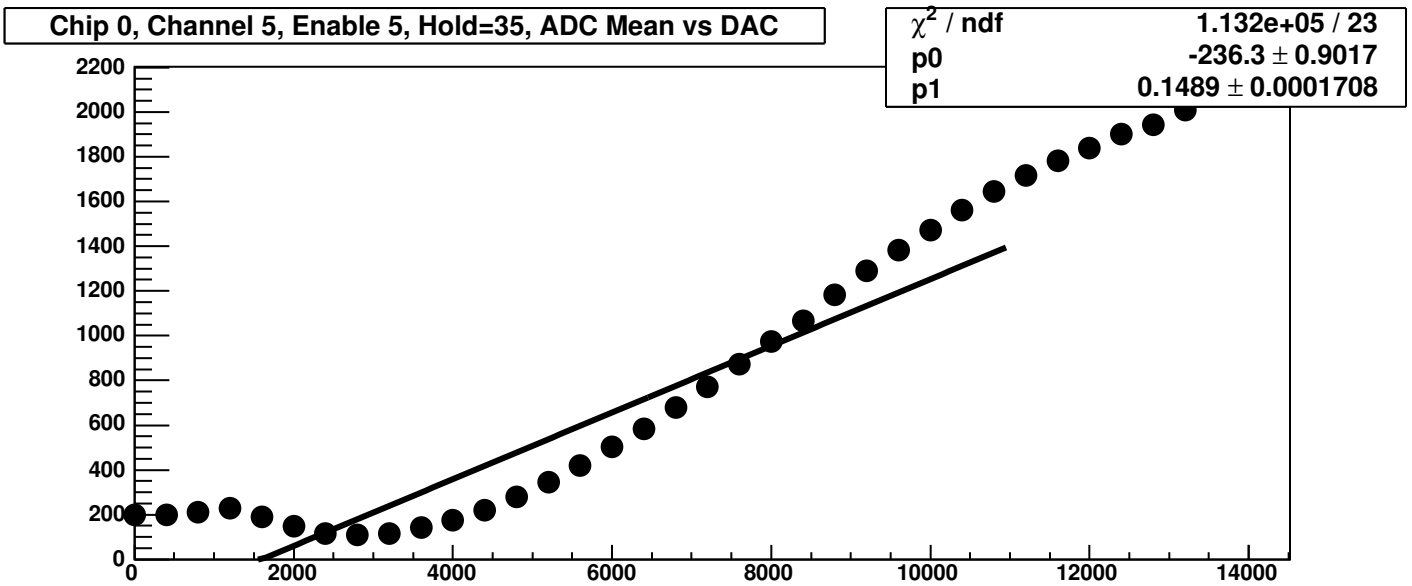

Chip 0, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

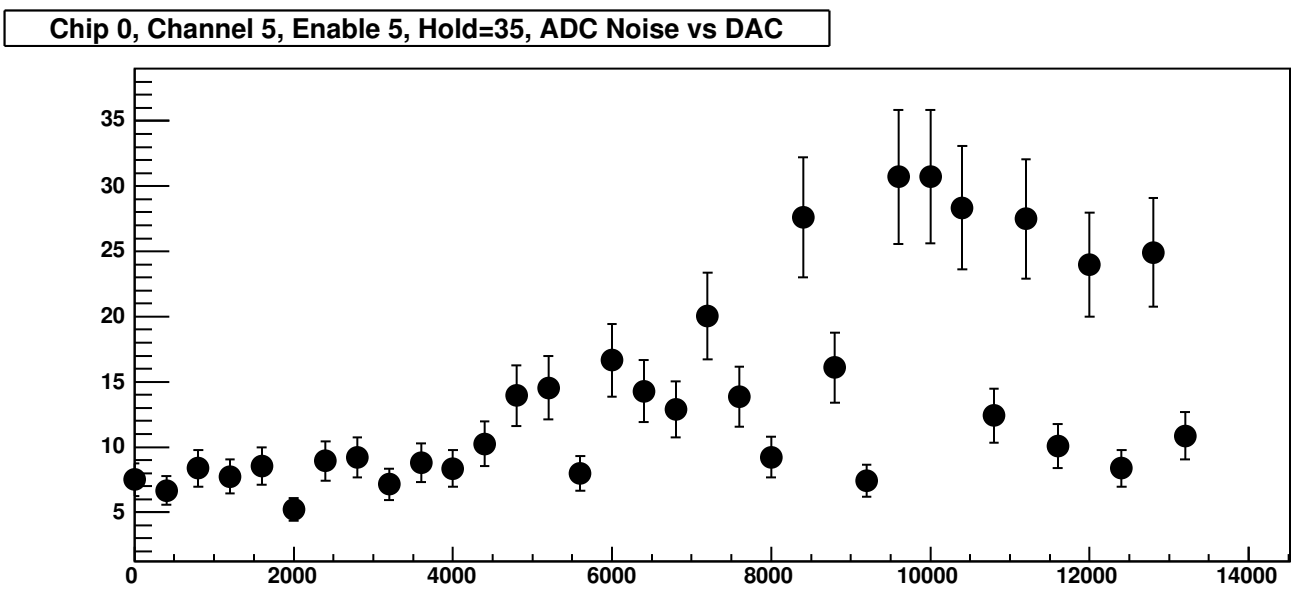

Chip 0, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

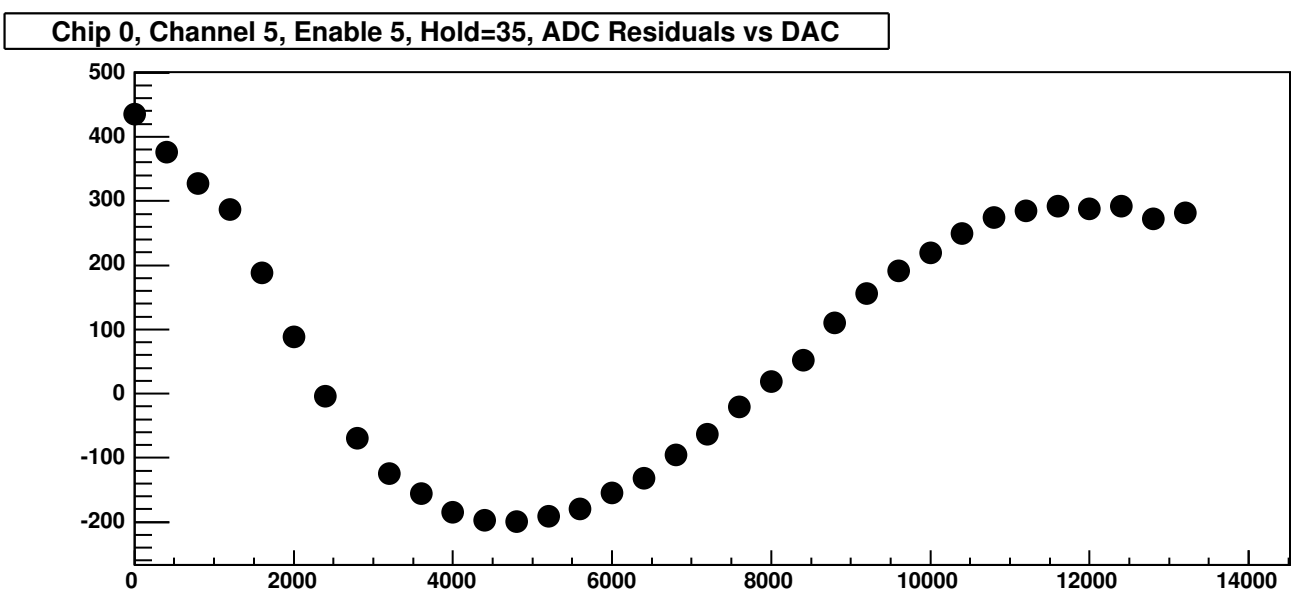

Chip 0, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.84 / 23
p0 138.8 ± 0.8226
p1 0.0172 ± 0.0001262
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

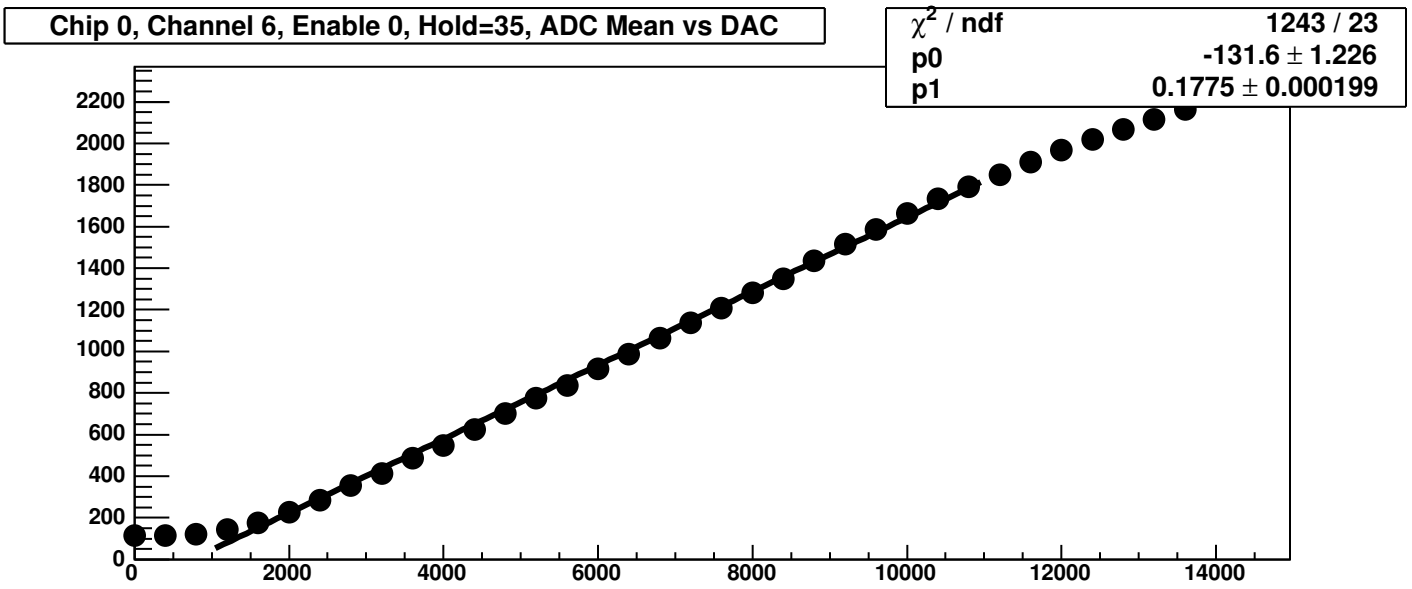

Chip 0, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

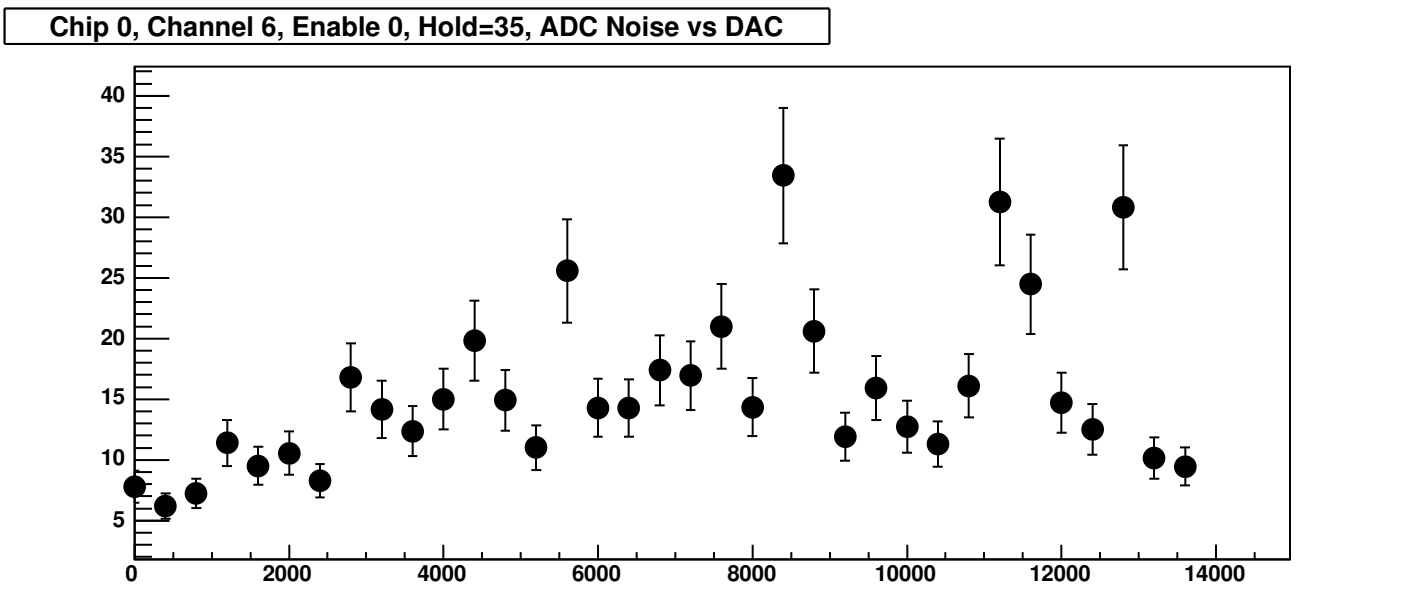

Chip 0, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

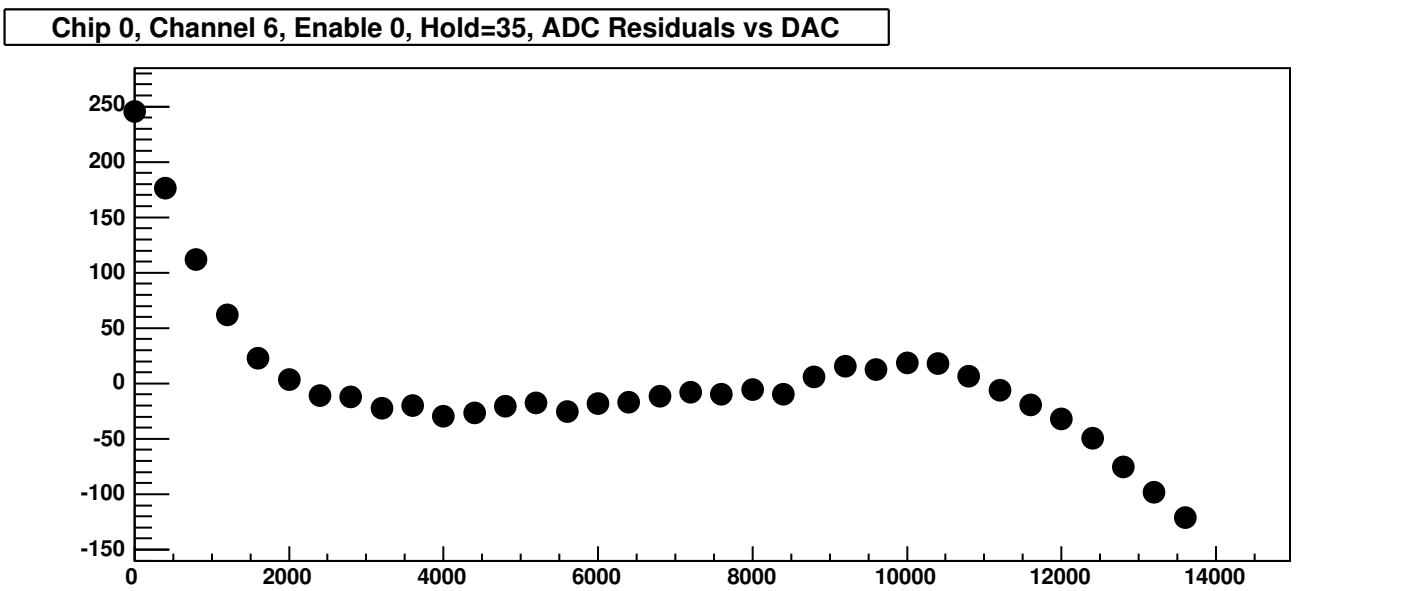

Chip 0, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.132e+05 / 23
p0 -236.3 ± 0.9017
p1 0.1489 ± 0.0001708
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

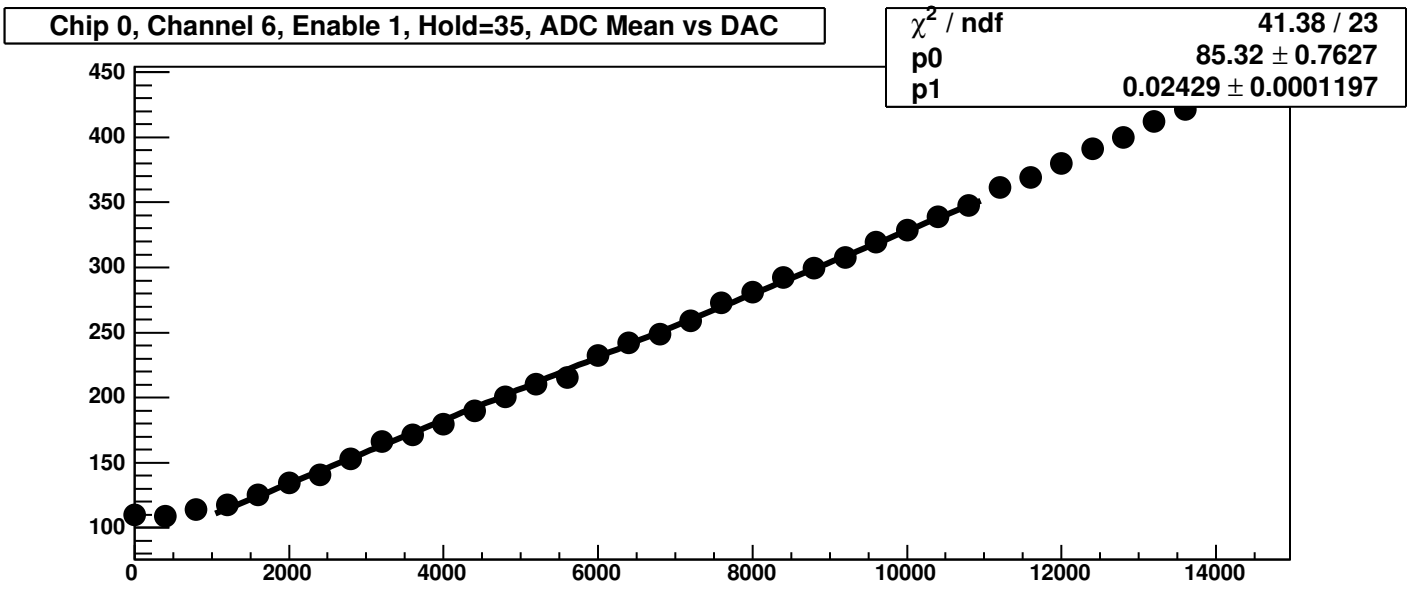

Chip 0, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

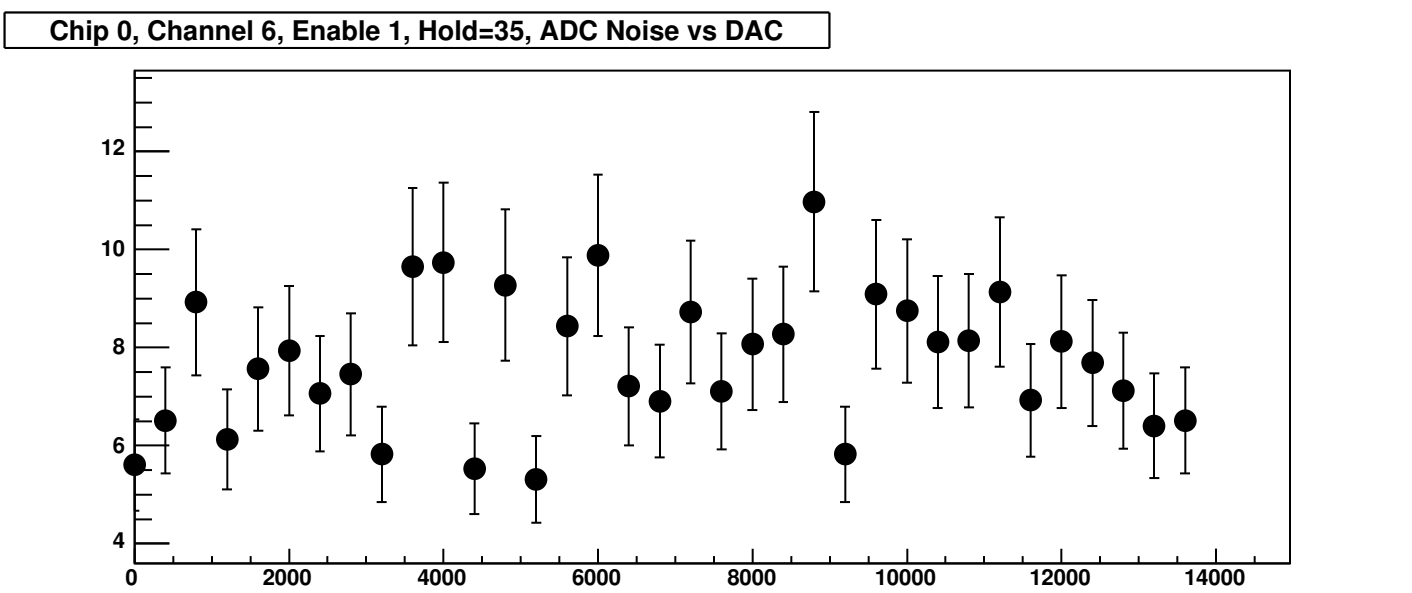

Chip 0, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

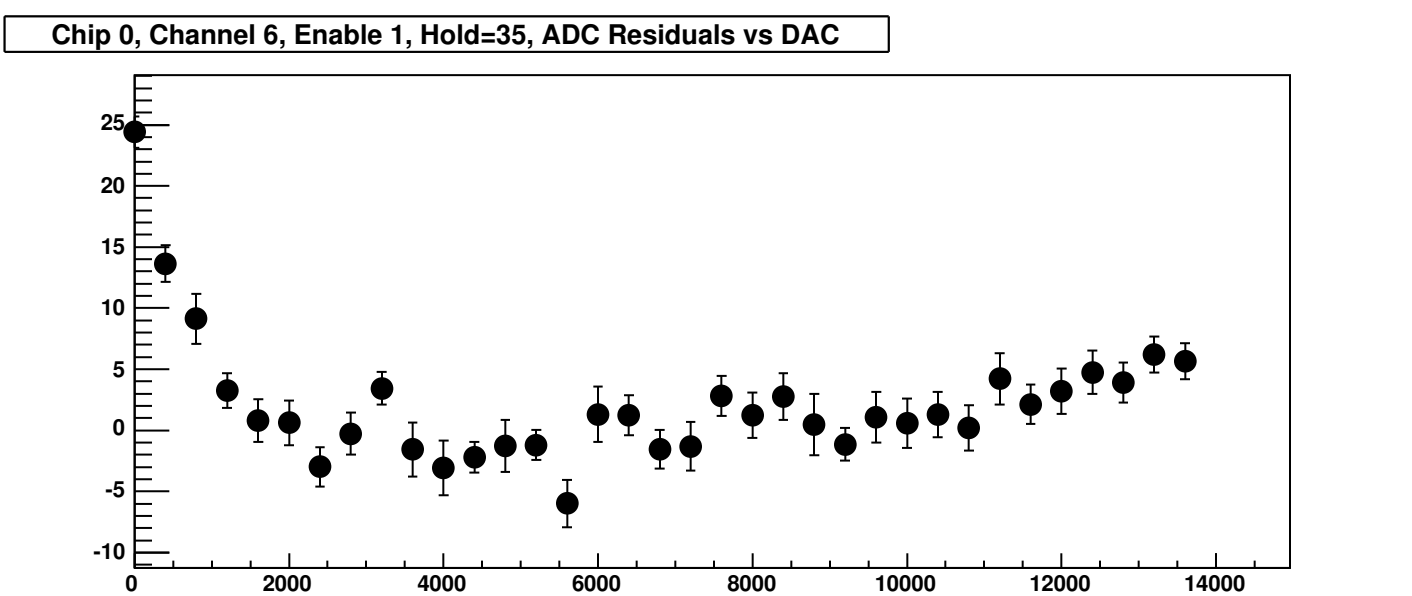

Chip 0, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1243 / 23
p0 -131.6 ± 1.226
p1 0.1775 ± 0.000199
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

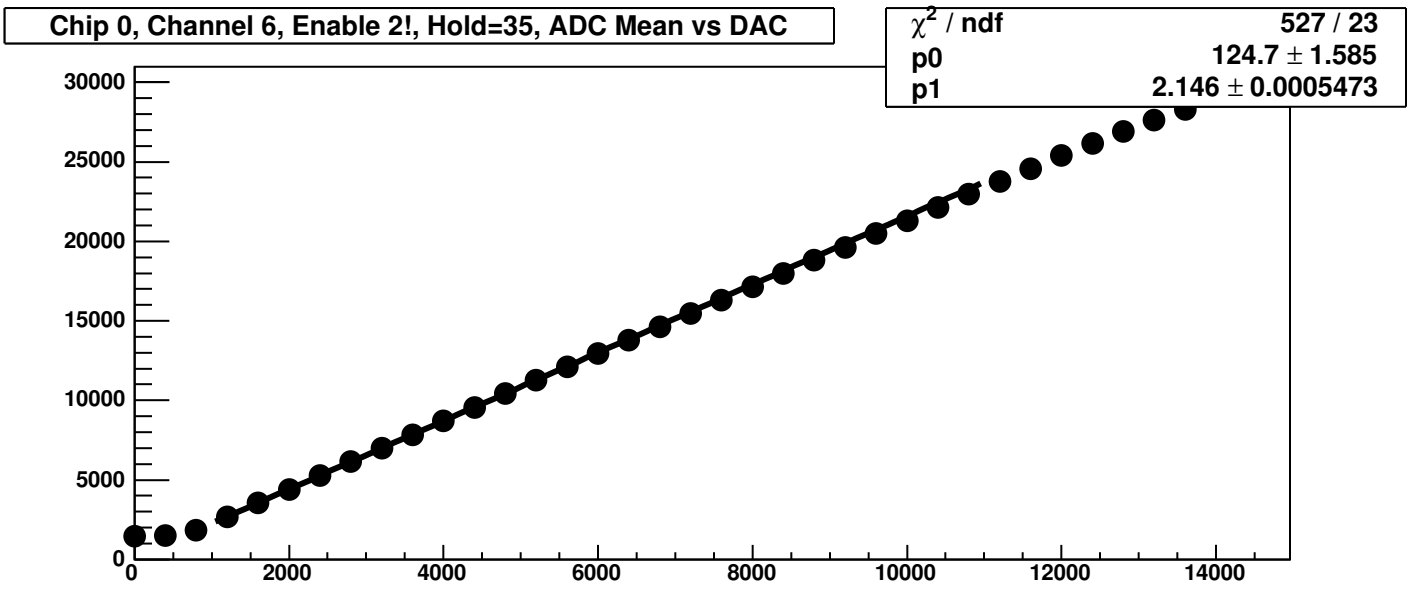

Chip 0, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

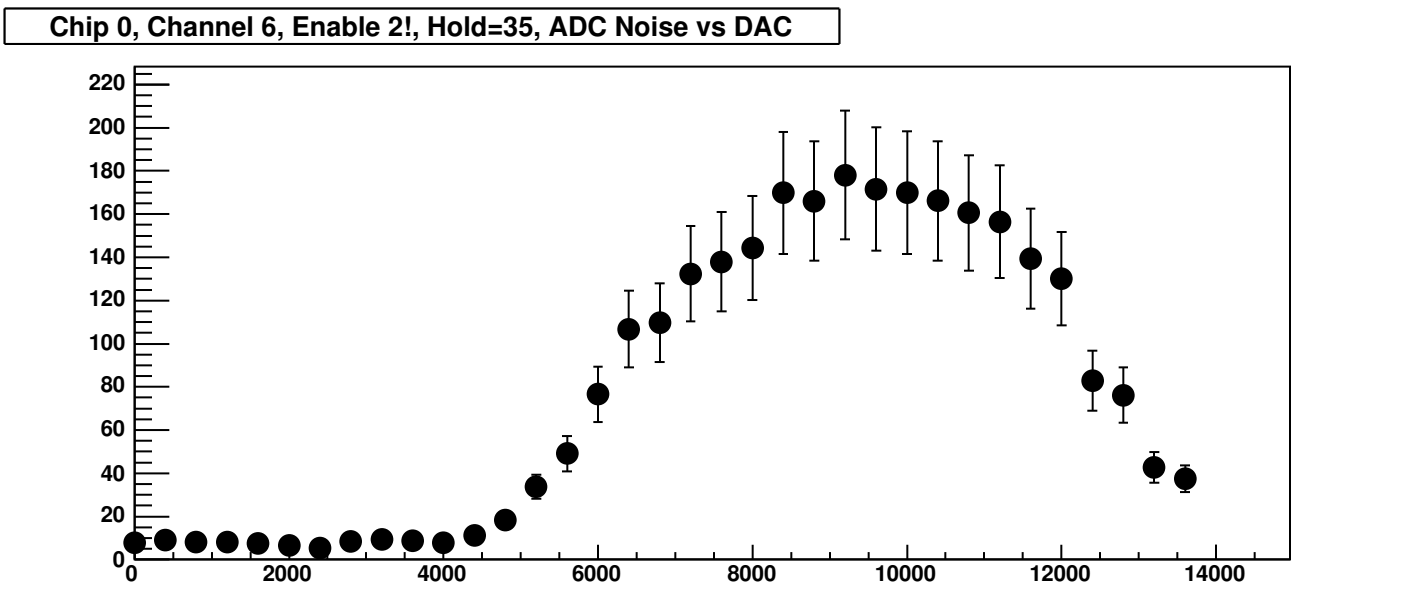

Chip 0, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

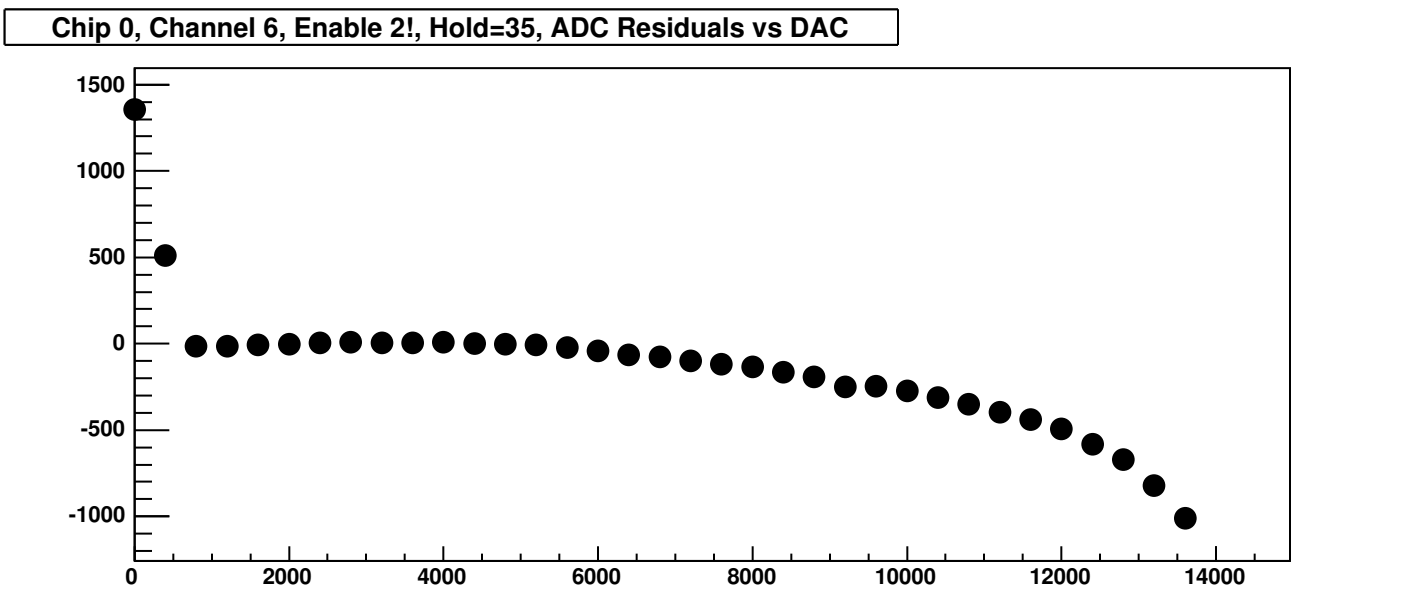

Chip 0, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 41.38 / 23
p0 85.32 ± 0.7627
p1 0.02429 ± 0.0001197
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

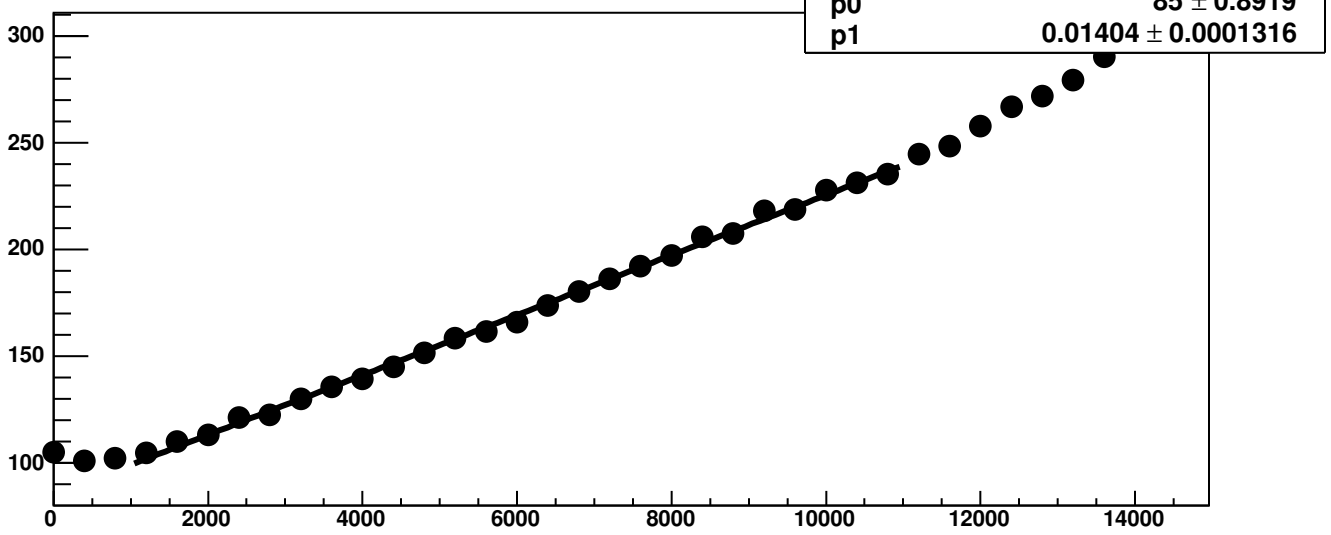

Chip 0, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 527 / 23
p0 124.7 ± 1.585
p1 2.146 ± 0.0005473
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

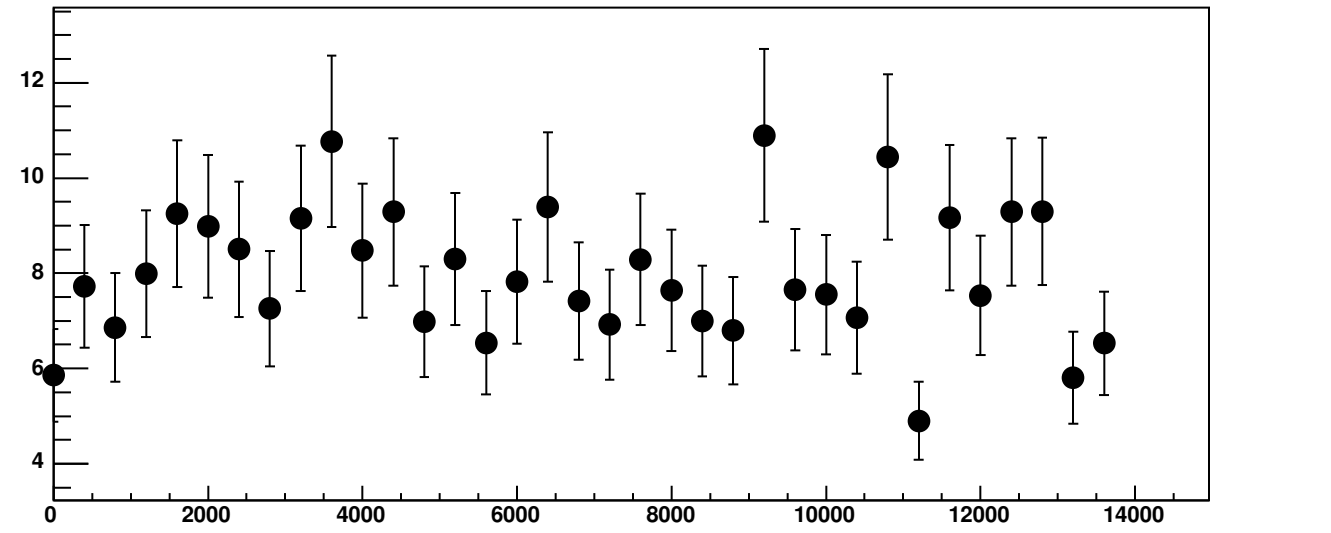

Chip 0, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

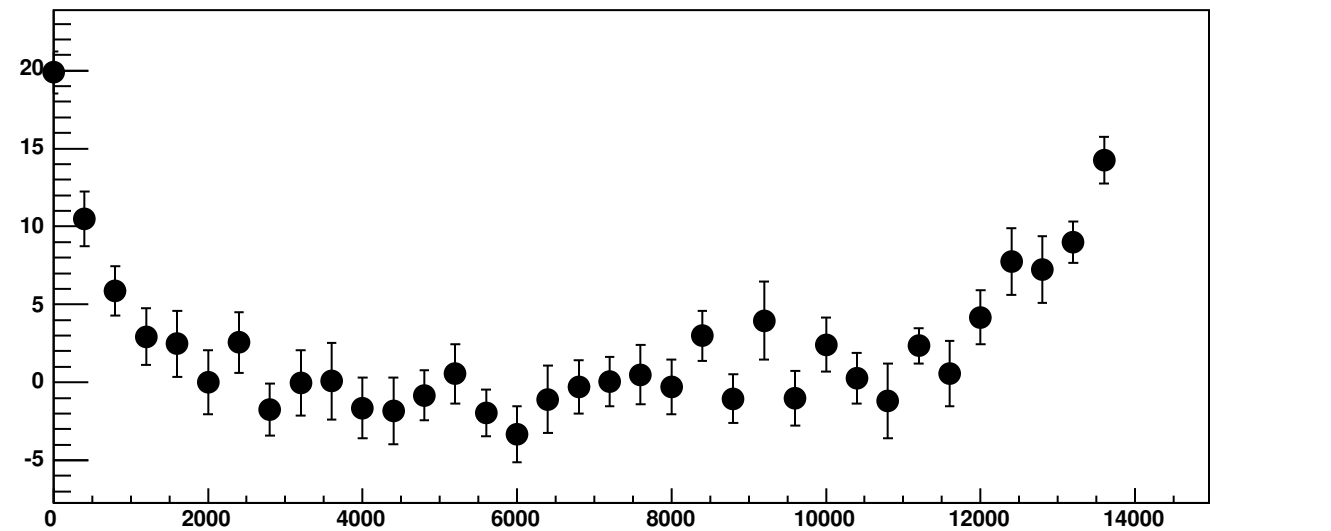
Chip 0, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

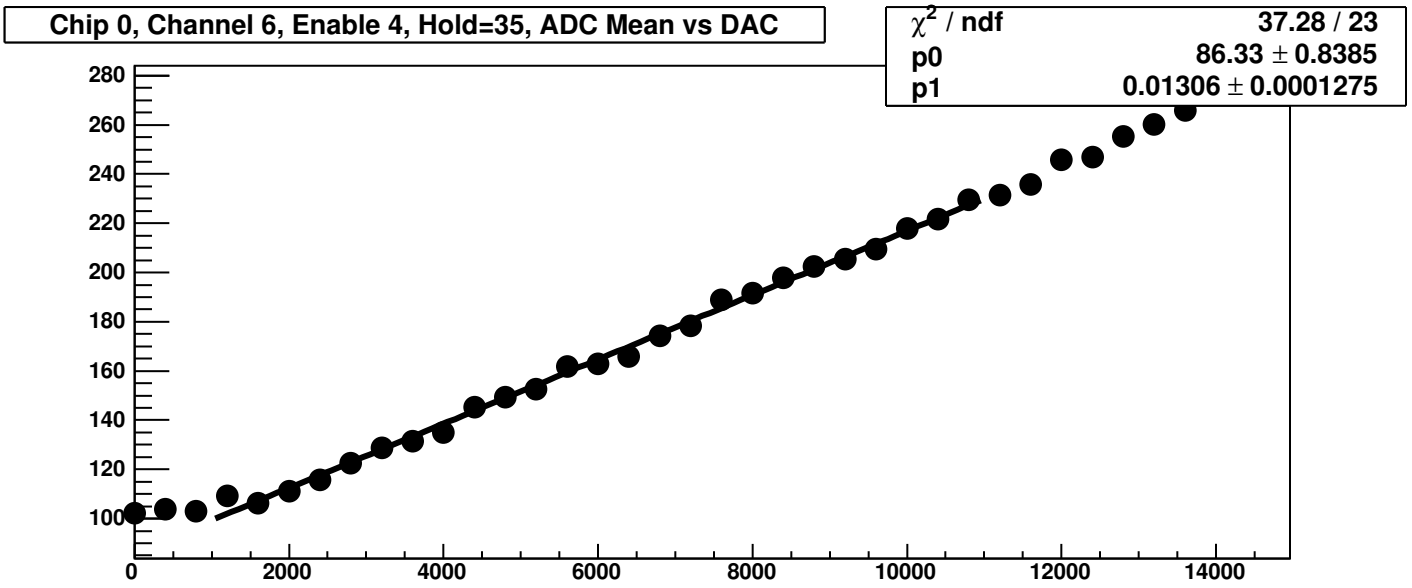


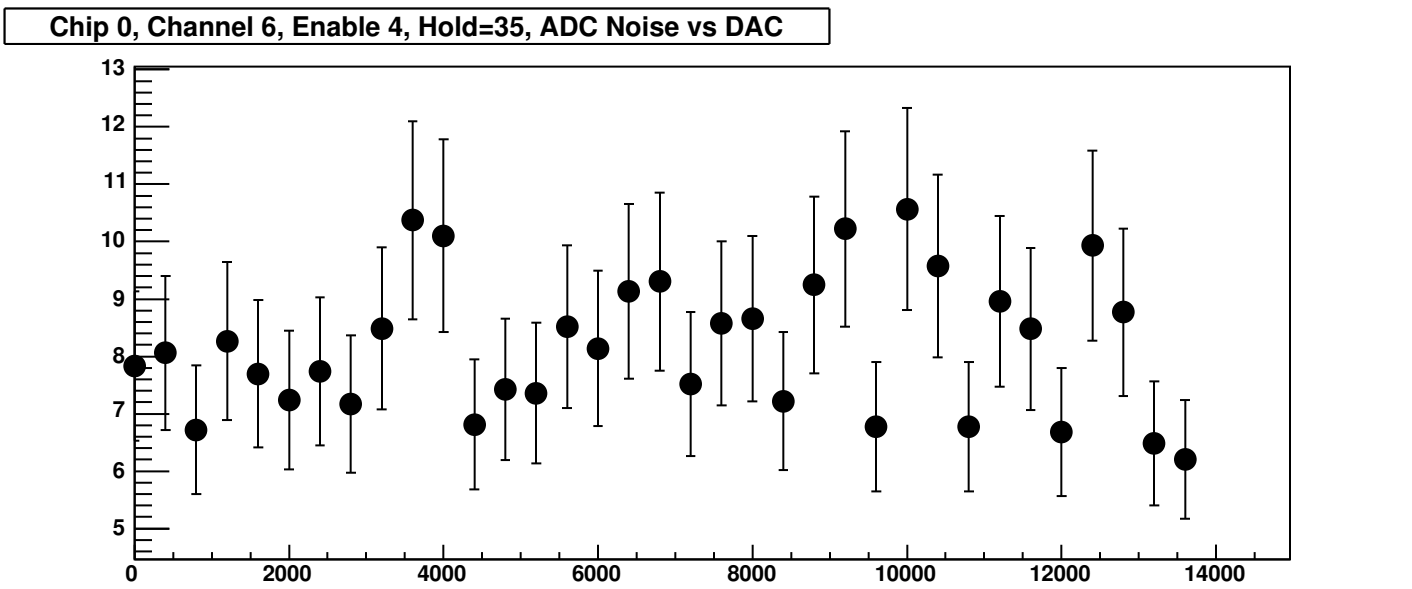
Chip 0, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC

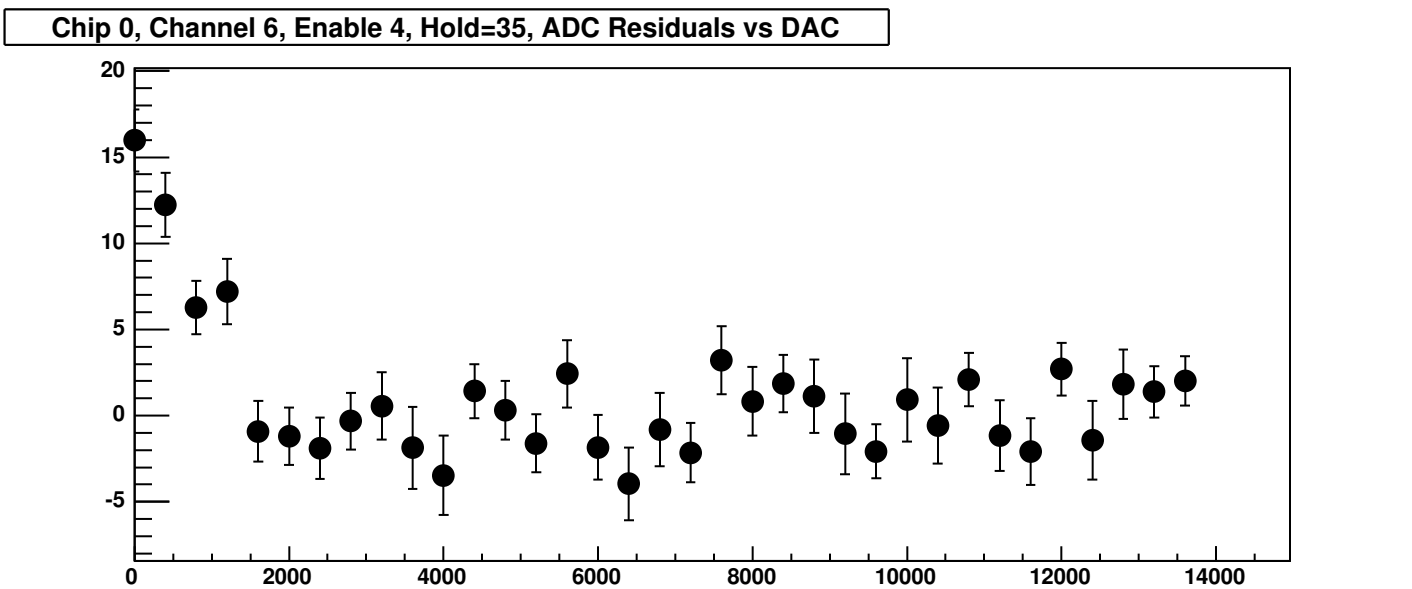


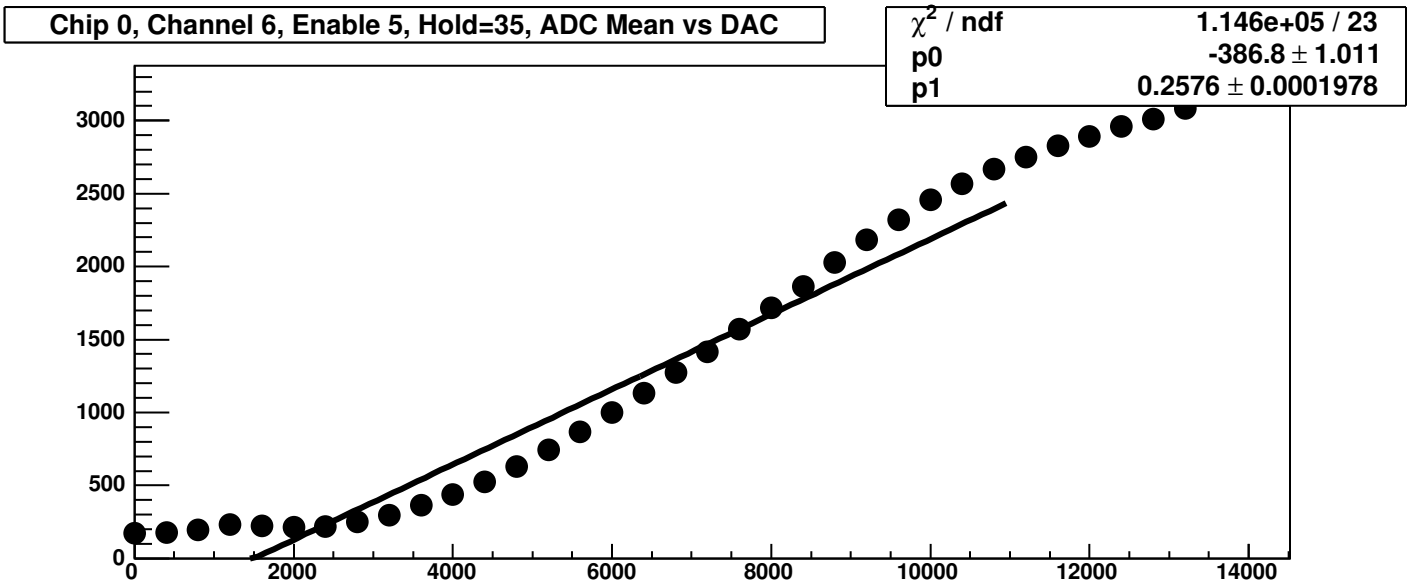
Chip 0, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

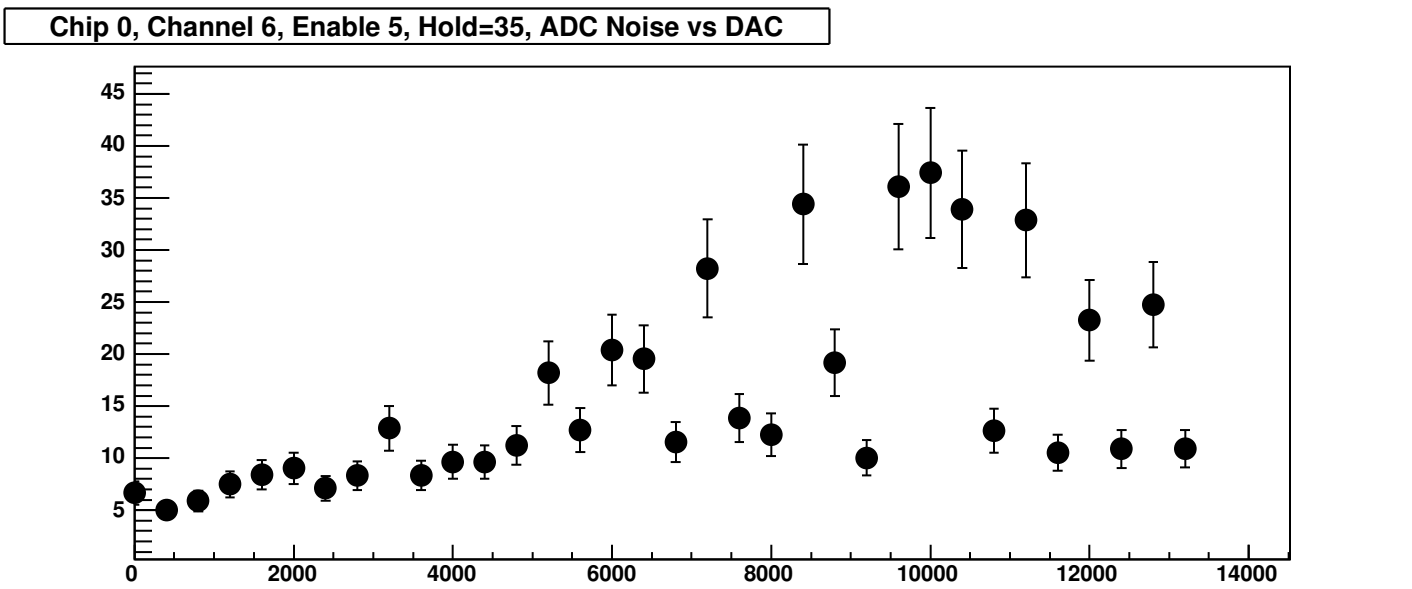


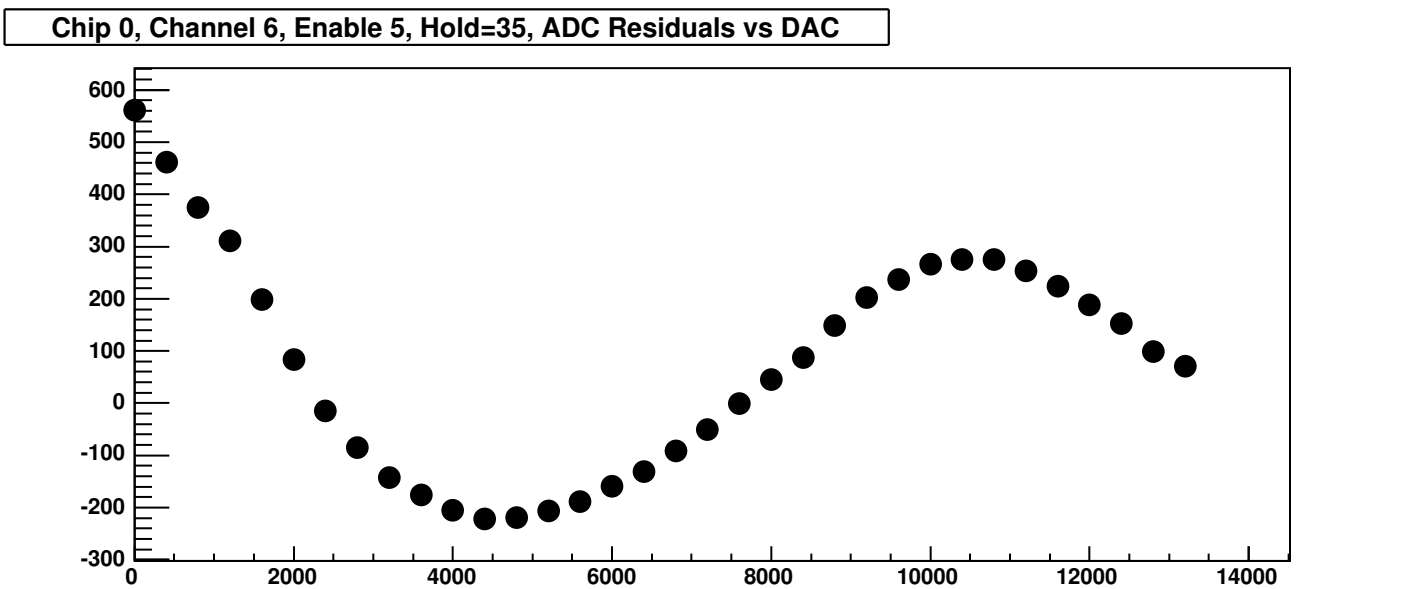

Chip 0, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 37.28 / 23
p0 86.33 ± 0.8385
p1 0.01306 ± 0.0001275
280
260
240
220
200
180
160
140
120
100
0
2000
4000
6000
8000
10000
12000
14000

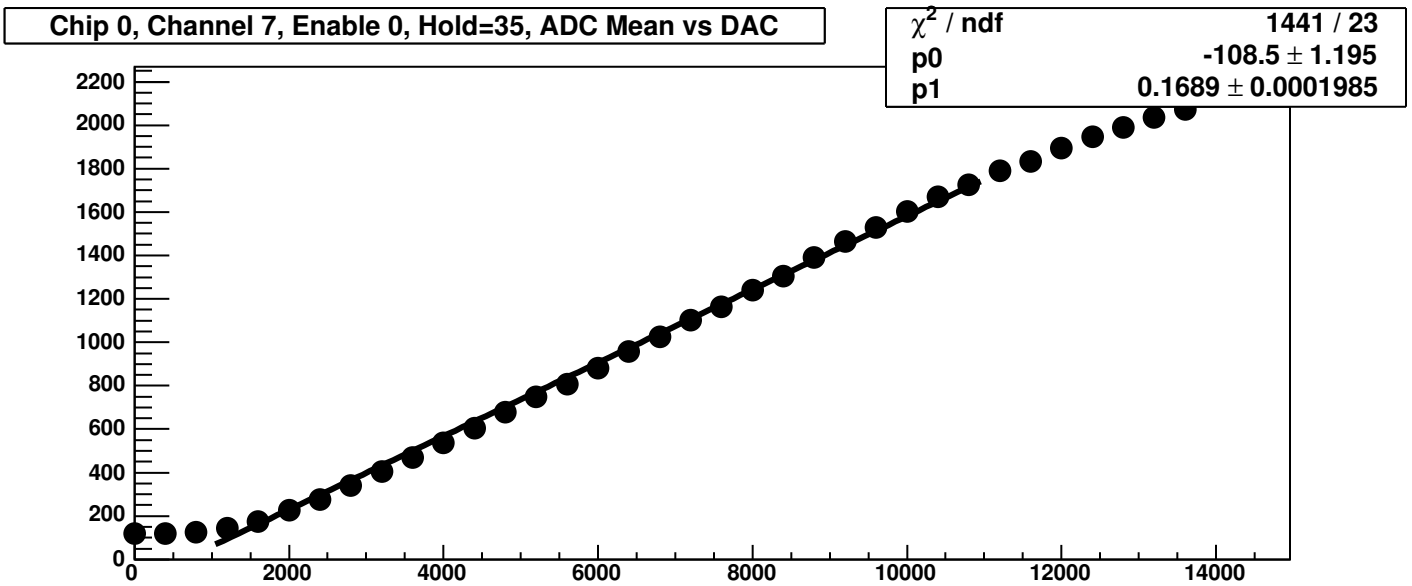

Chip 0, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

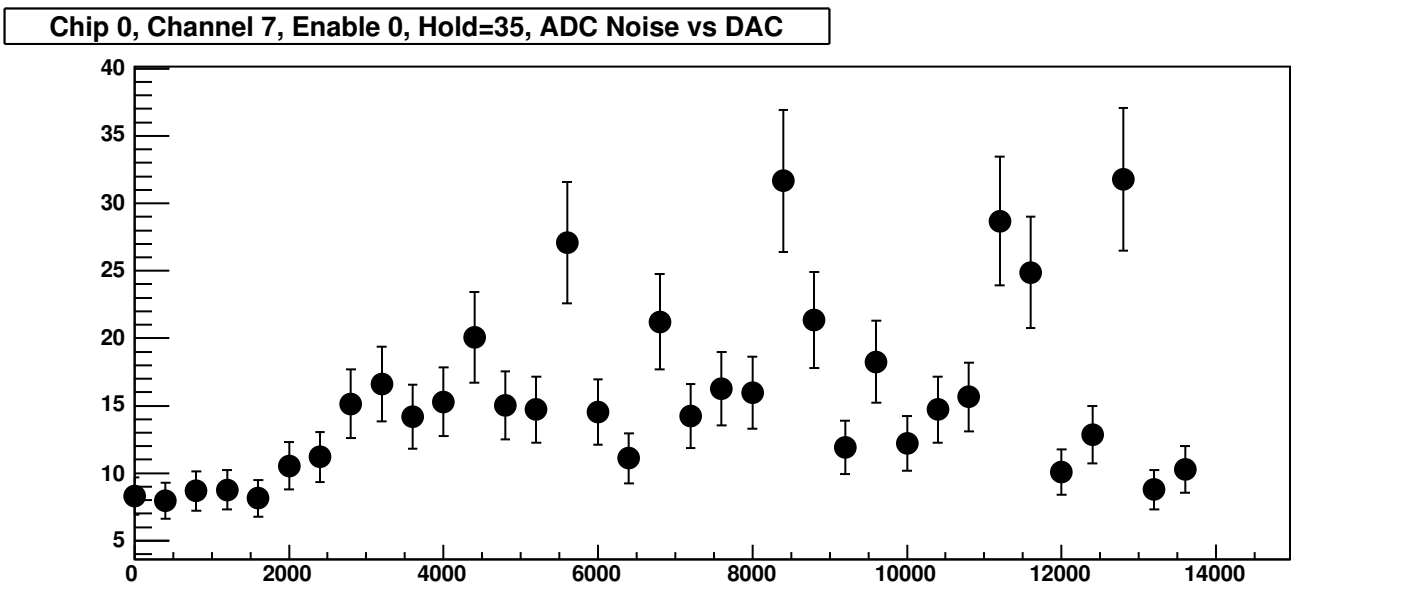

Chip 0, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

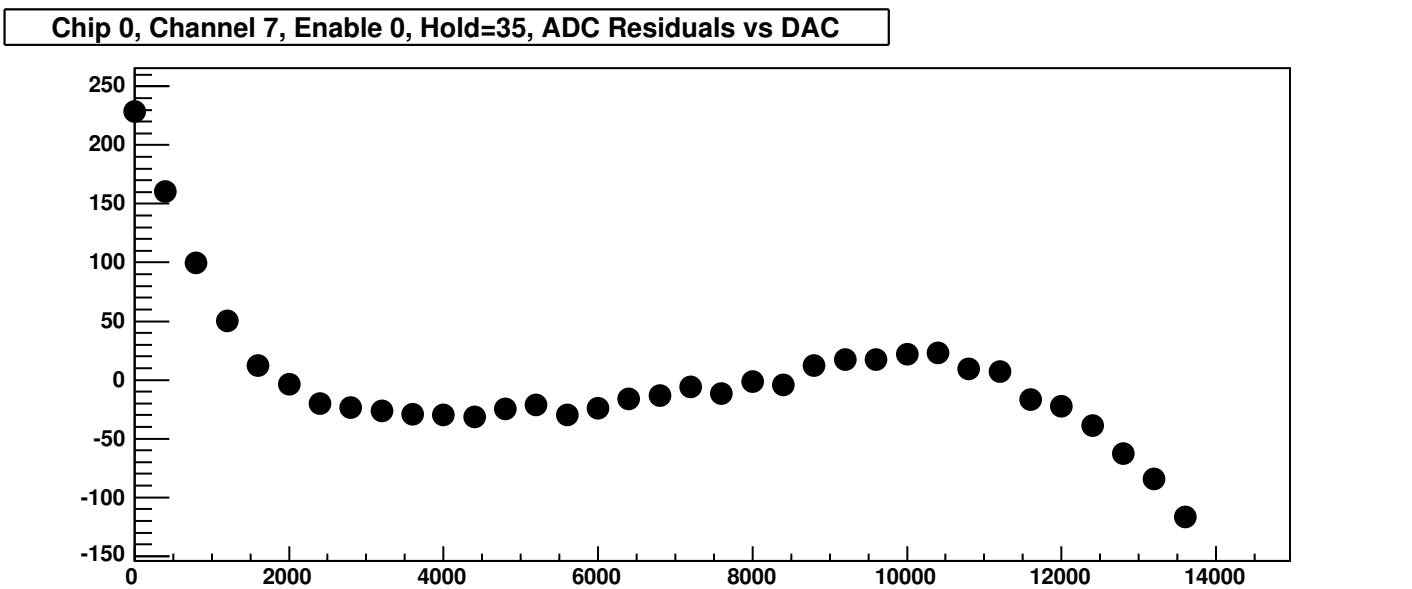

Chip 0, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.146e+05 / 23
p0 -386.8 ± 1.011
p1 0.2576 ± 0.0001978
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

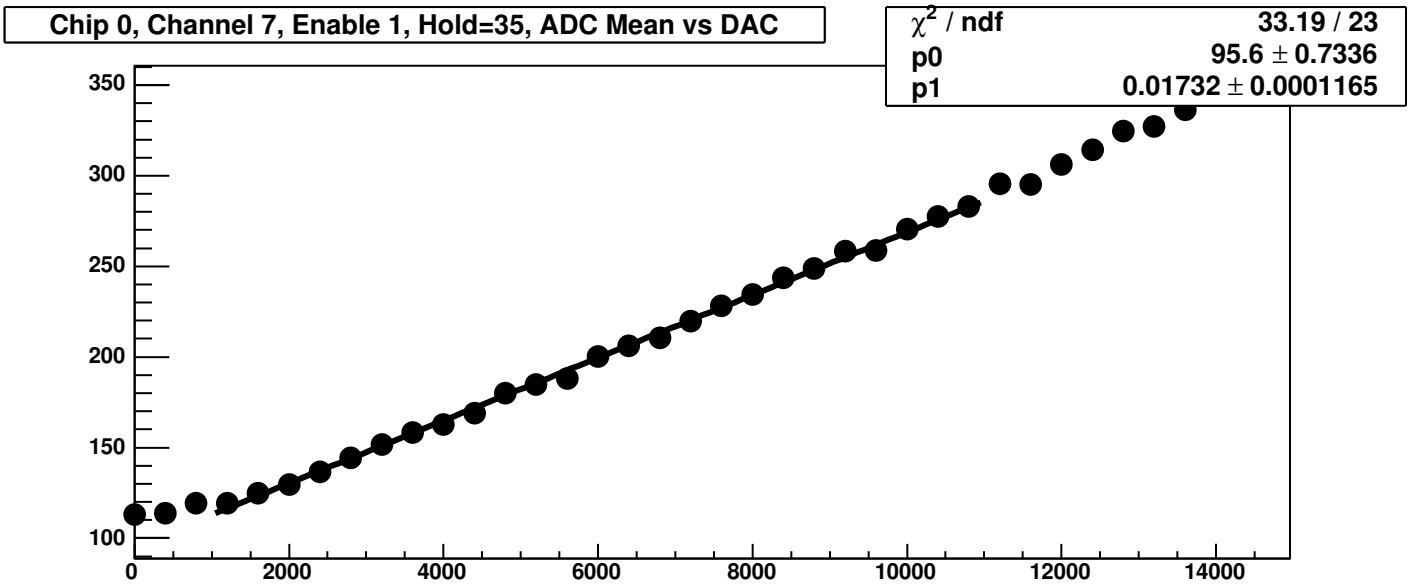

Chip 0, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

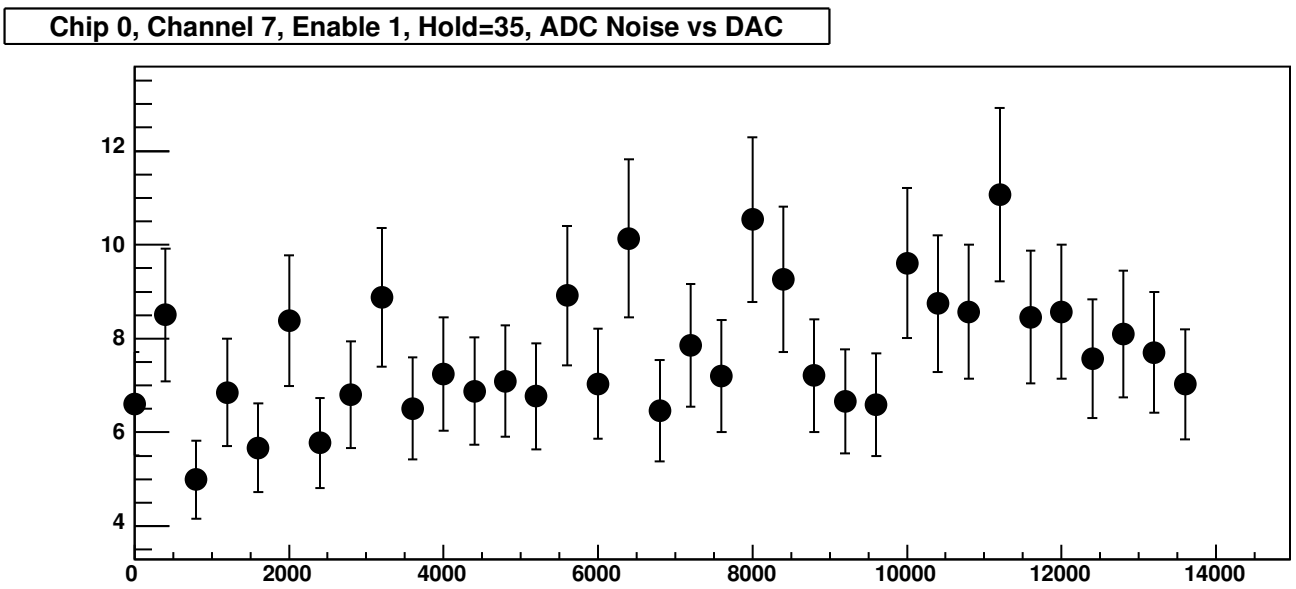

Chip 0, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

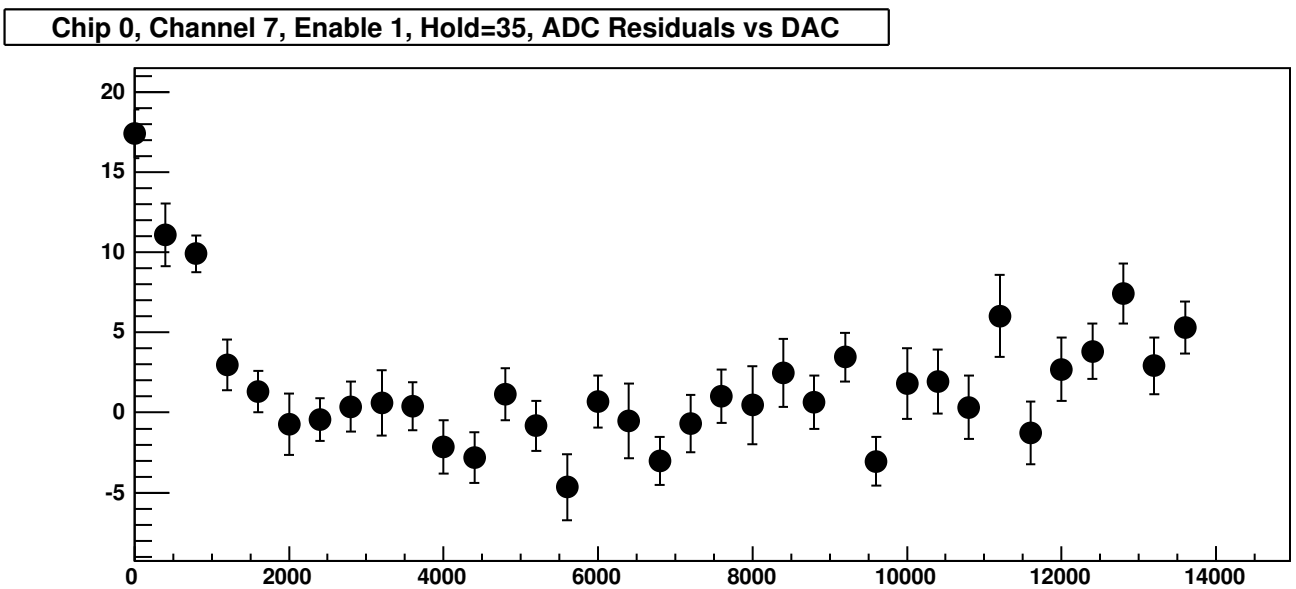

Chip 0, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1441 / 23
p0 -108.5 ± 1.195
p1 0.1689 ± 0.0001985
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

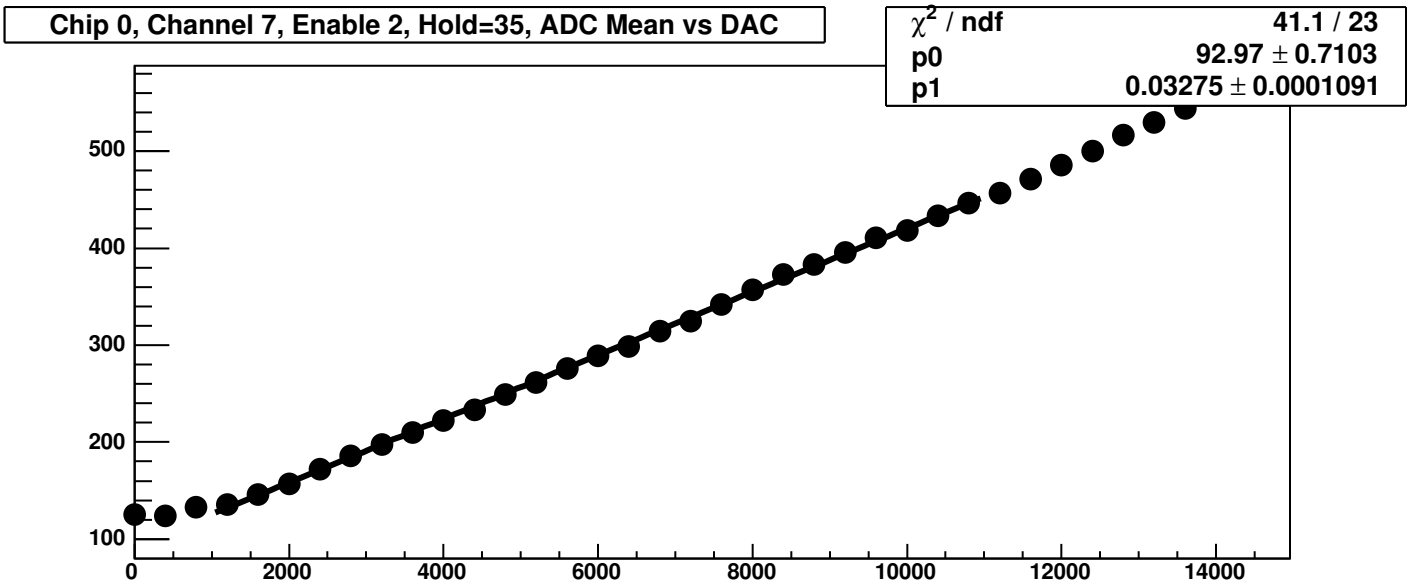

Chip 0, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

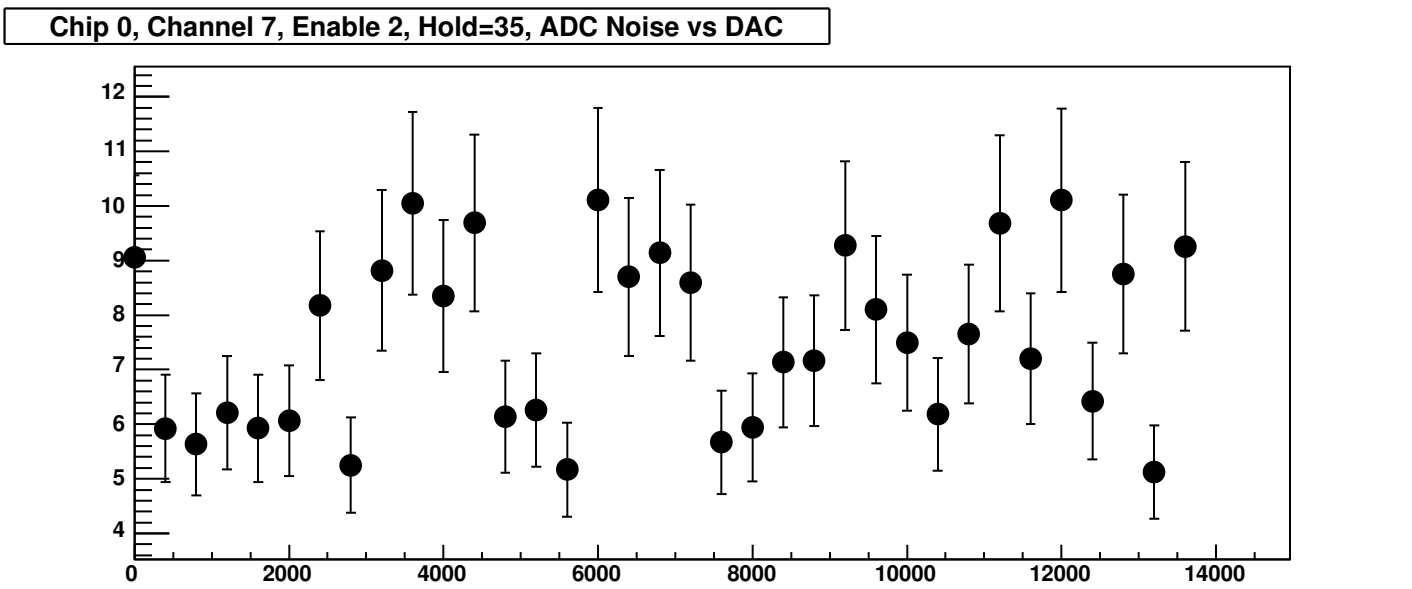

Chip 0, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

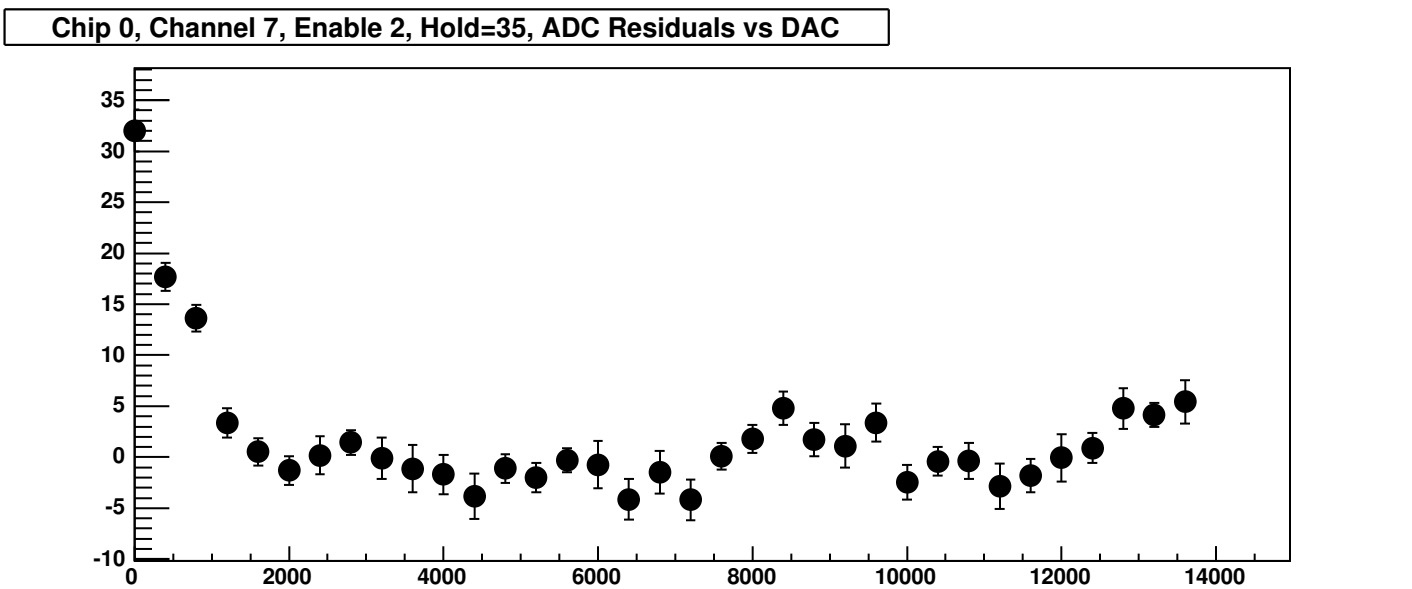

Chip 0, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.19 / 23
p0 95.6 ± 0.7336
p1 0.01732 ± 0.0001165
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

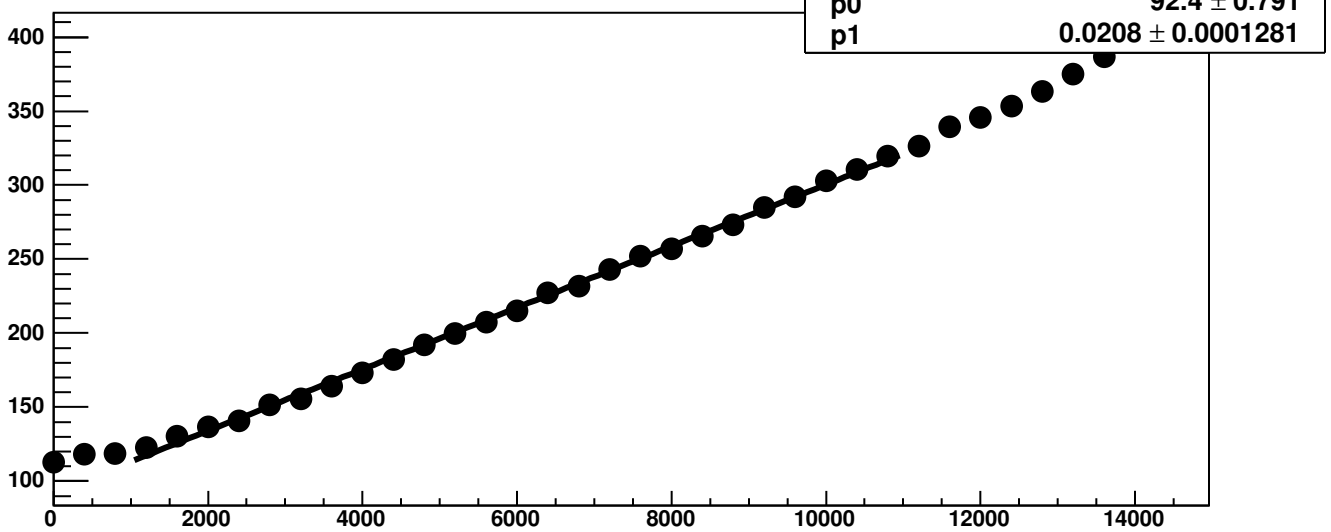

Chip 0, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 41.1 / 23
p0 92.97 ± 0.7103
p1 0.03275 ± 0.0001091
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000

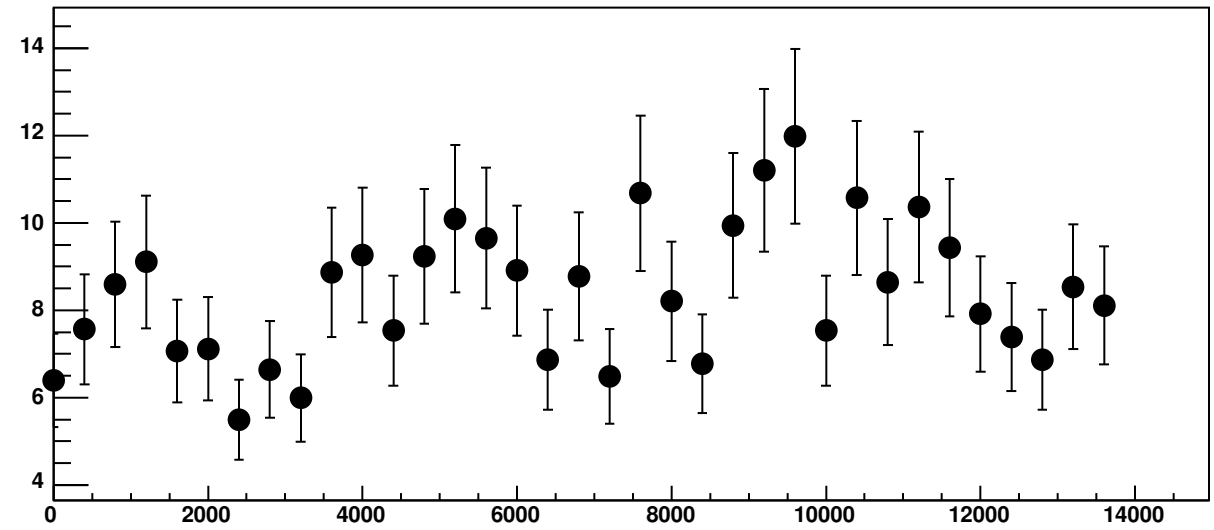

Chip 0, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

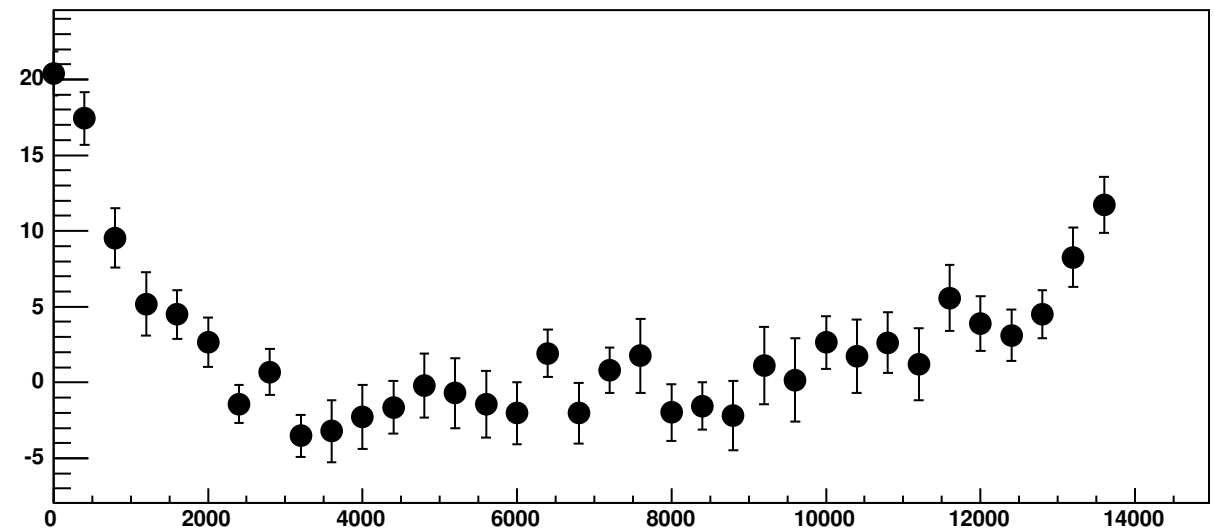
Chip 0, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

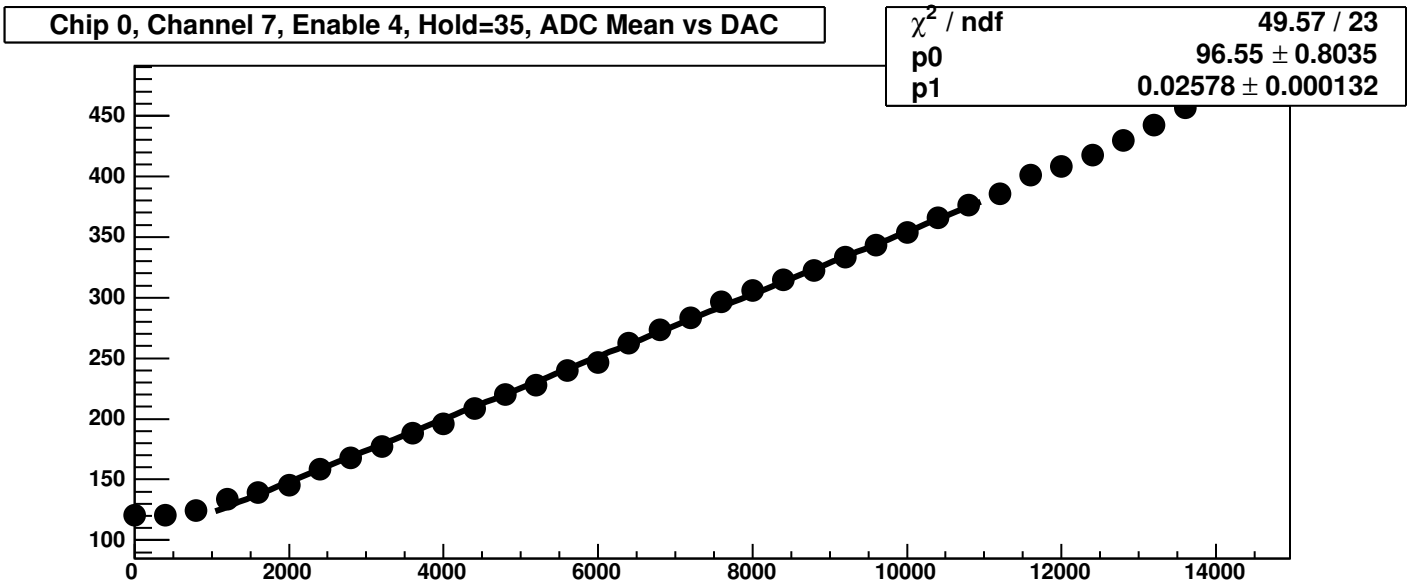


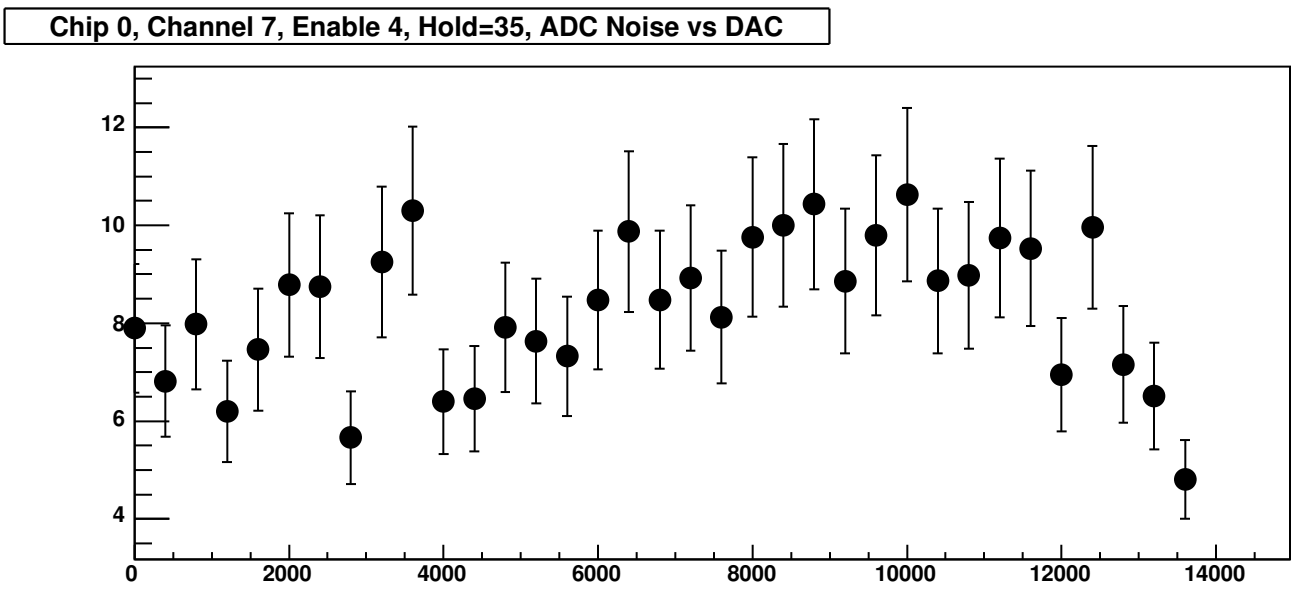
Chip 0, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC

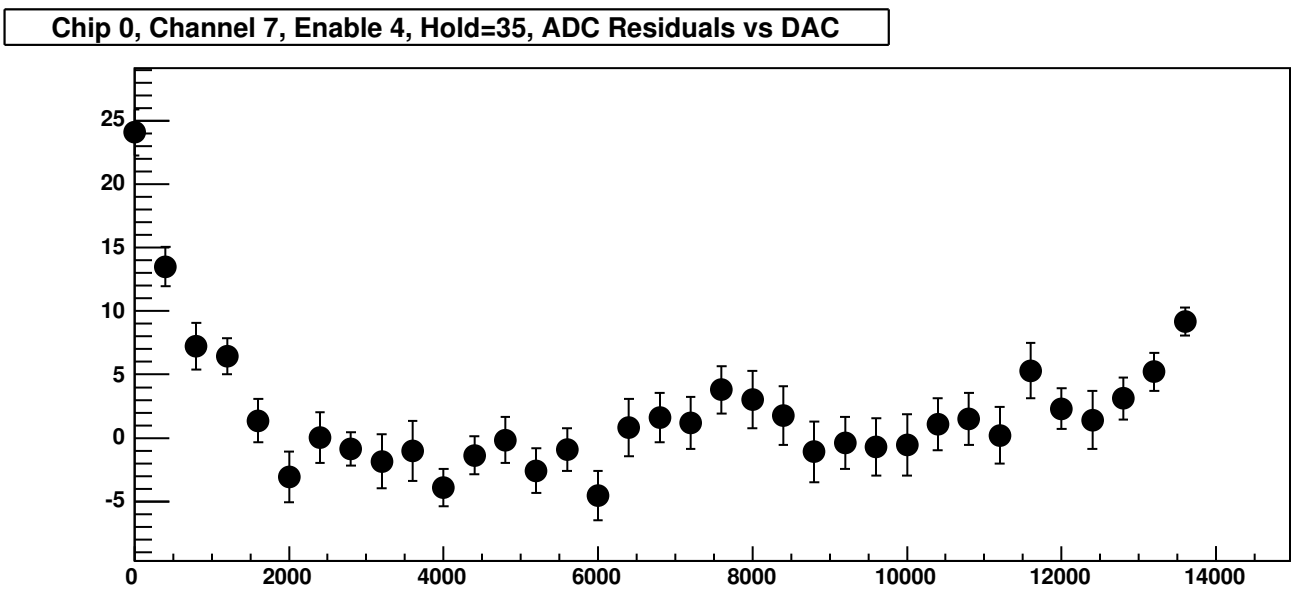


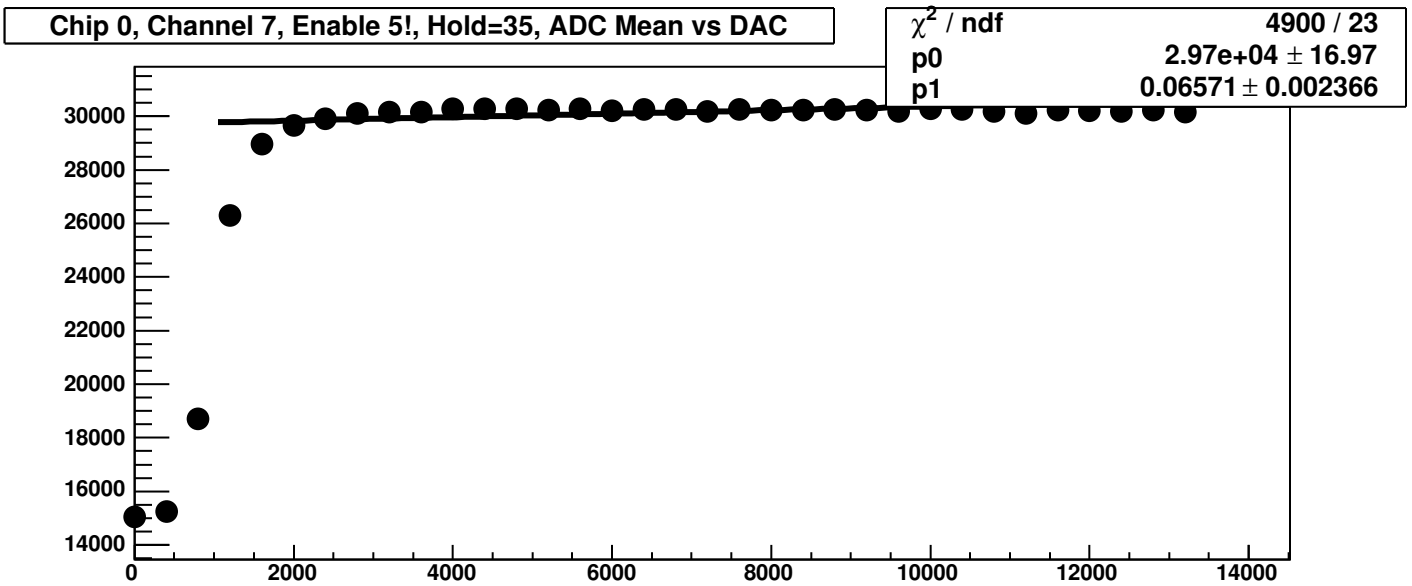
Chip 0, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

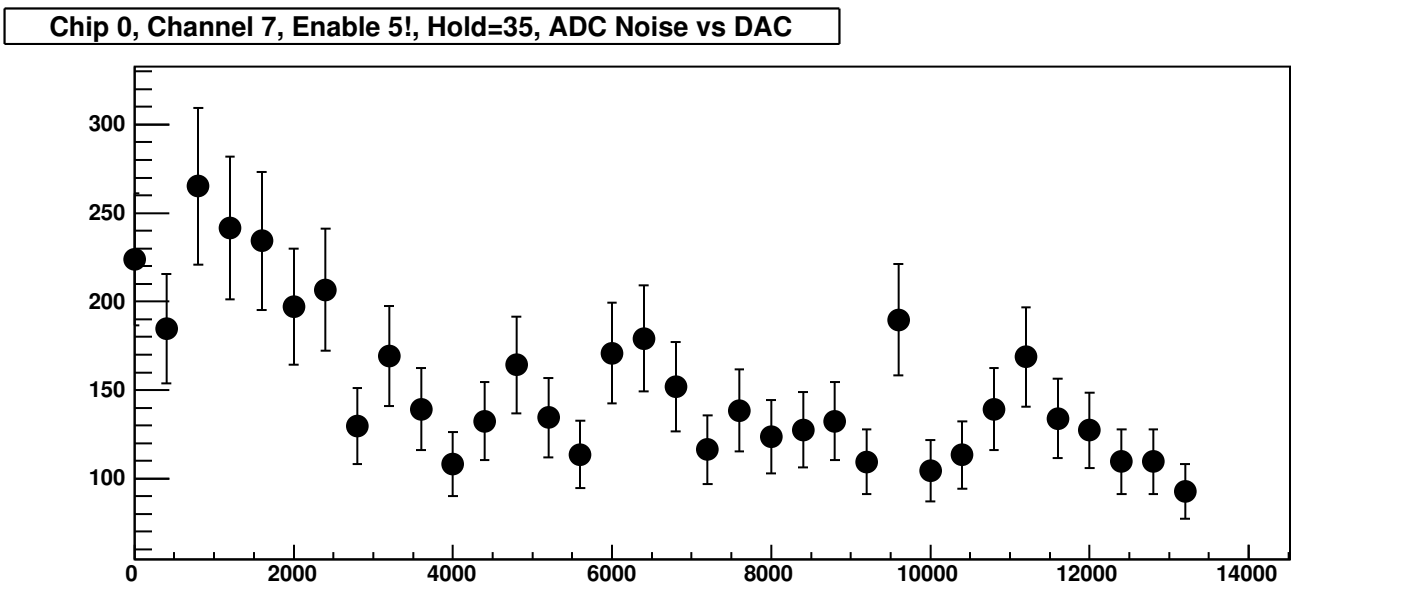


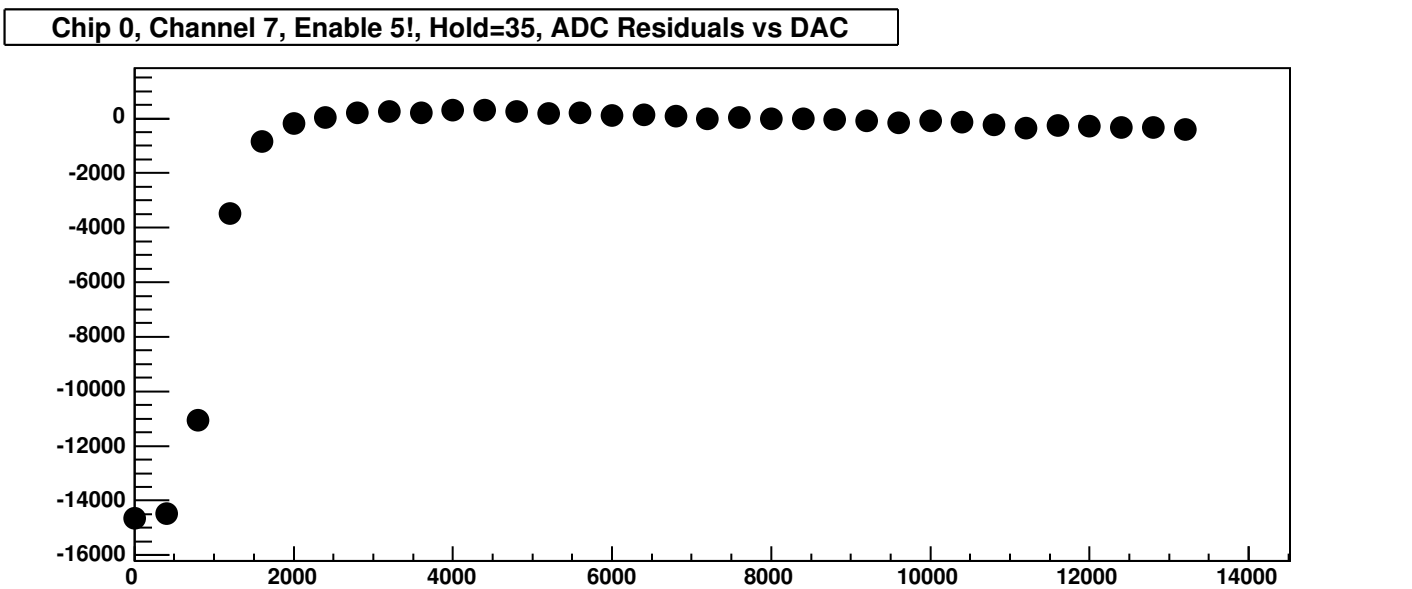

Chip 0, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 49.57 / 23
p0 96.55 ± 0.8035
p1 0.02578 ± 0.000132
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

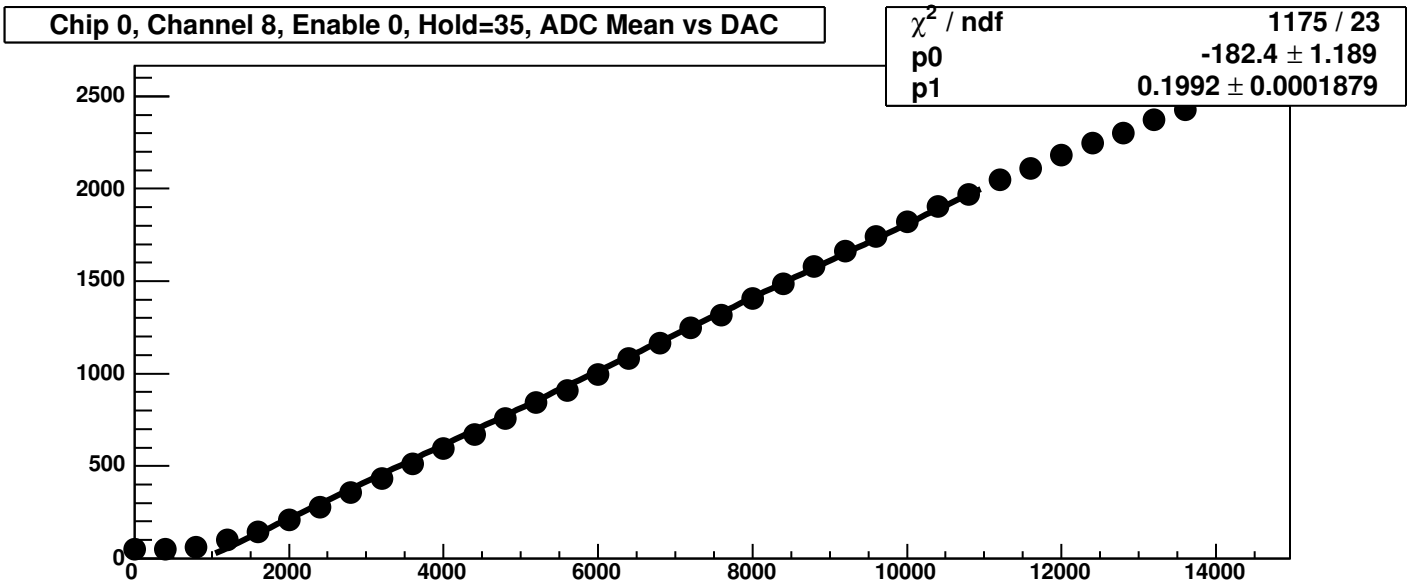

Chip 0, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

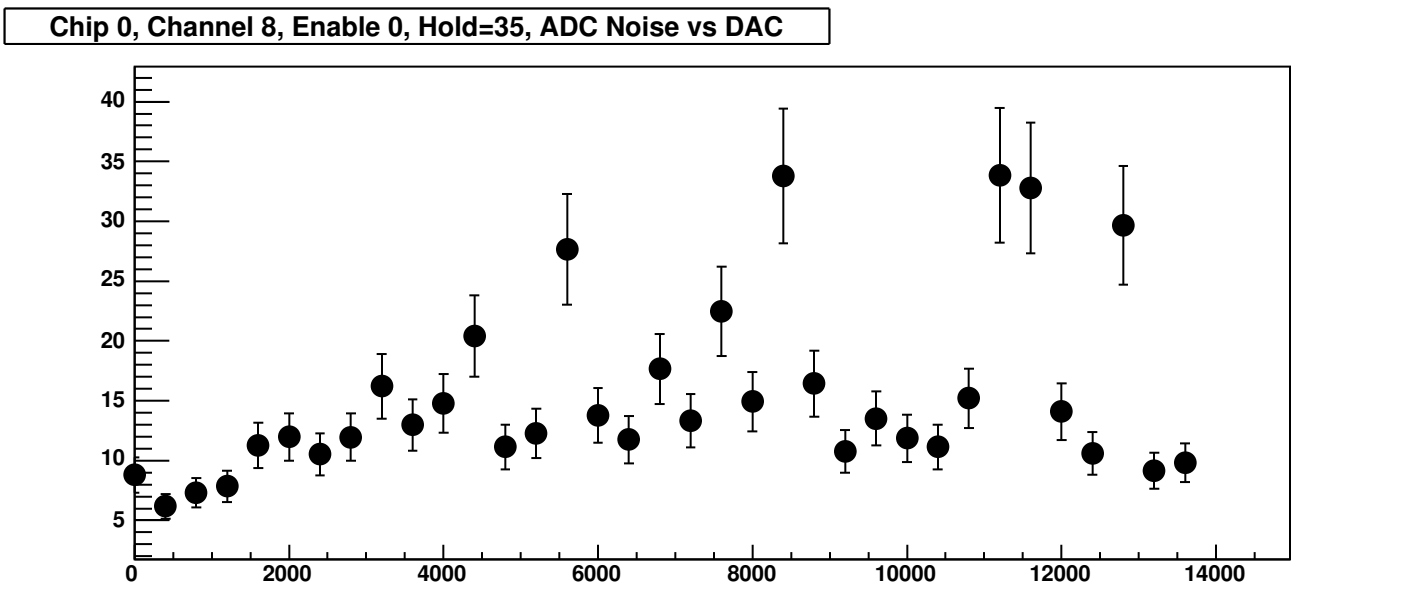

Chip 0, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

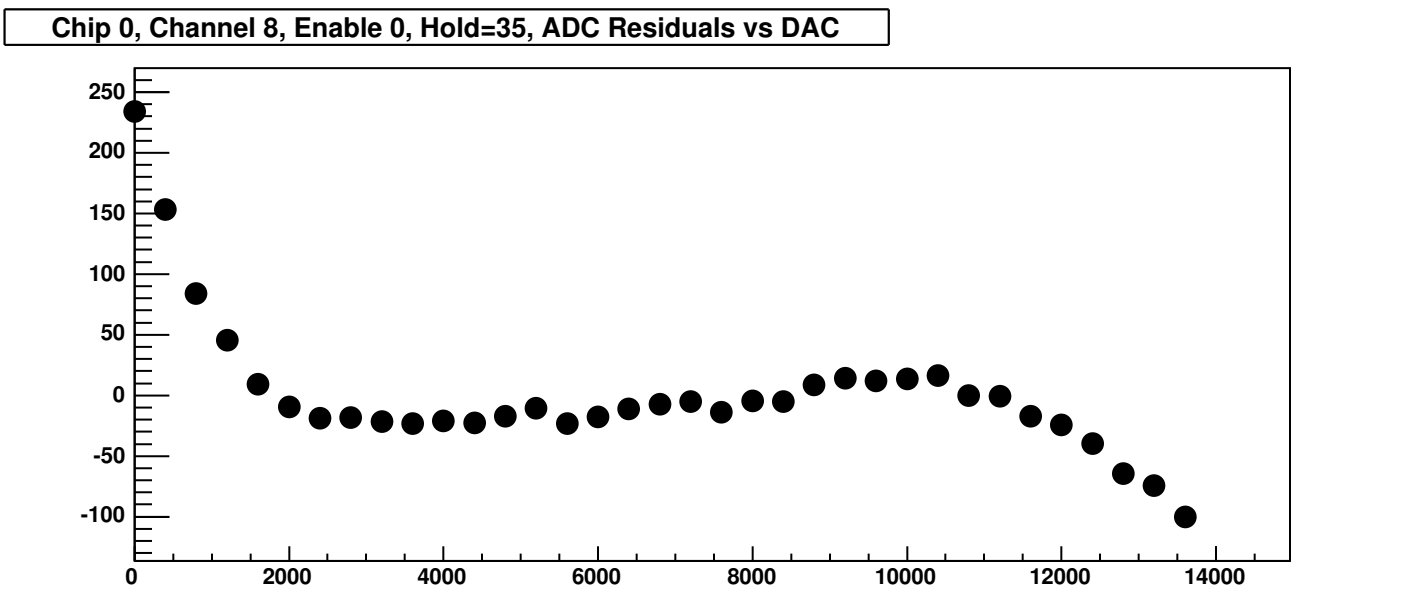

Chip 0, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4900 / 23
p0 2.97e+04 ± 16.97
p1 0.06571 ± 0.002366
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

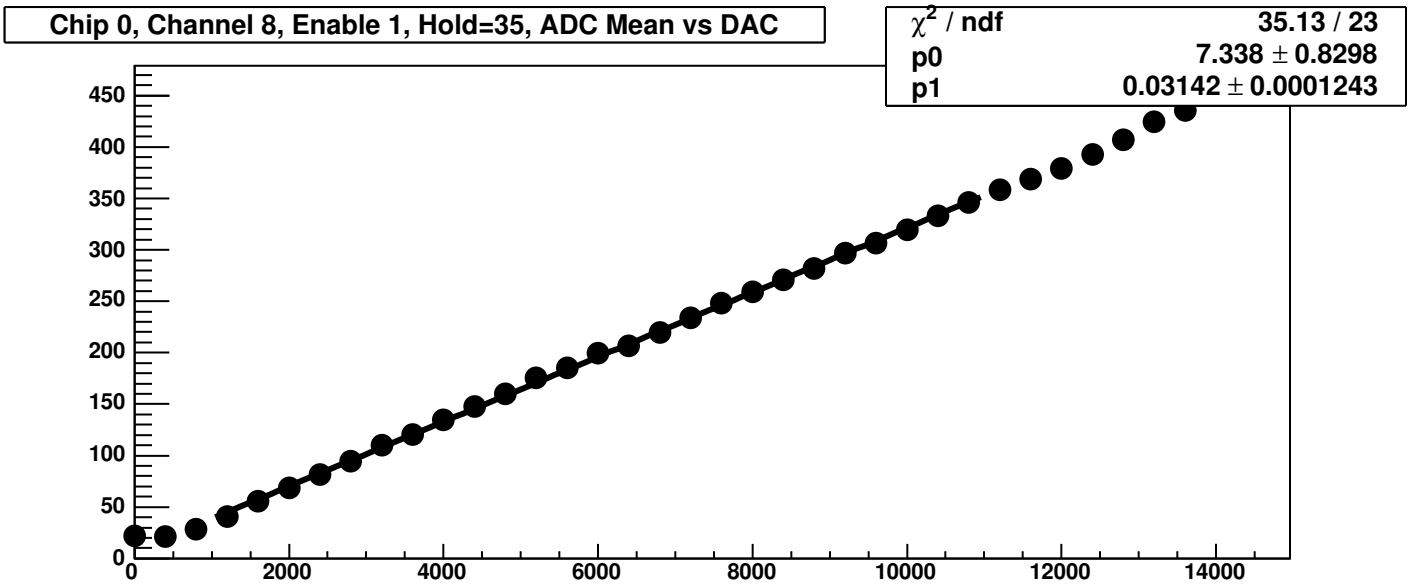

Chip 0, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

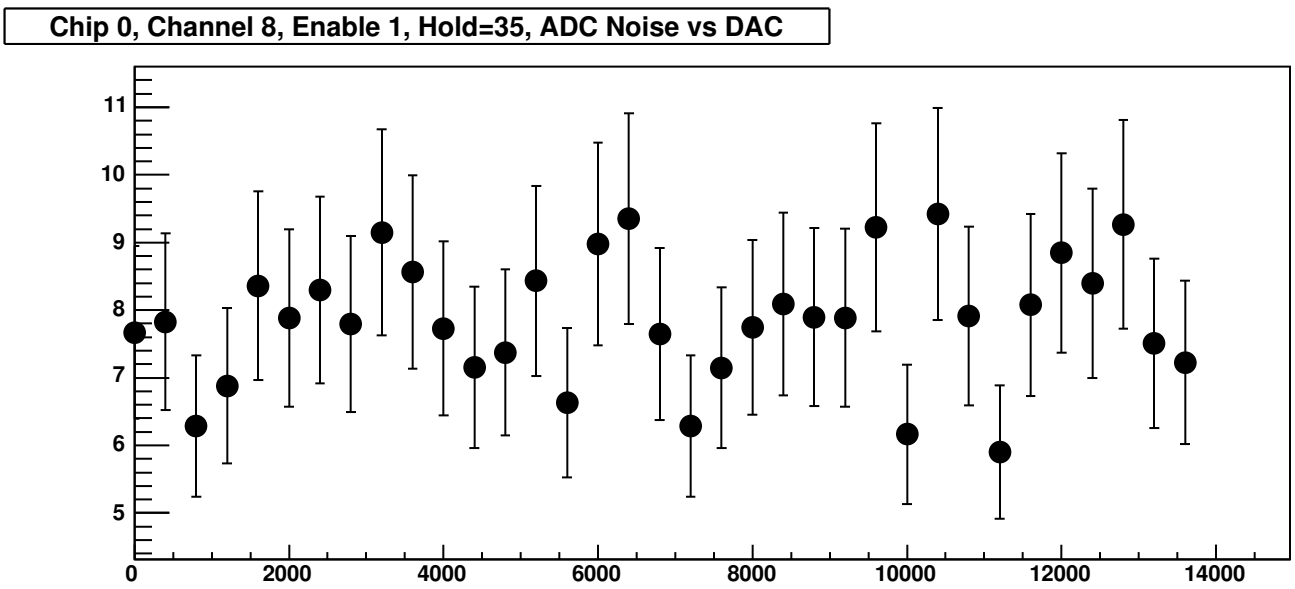

Chip 0, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

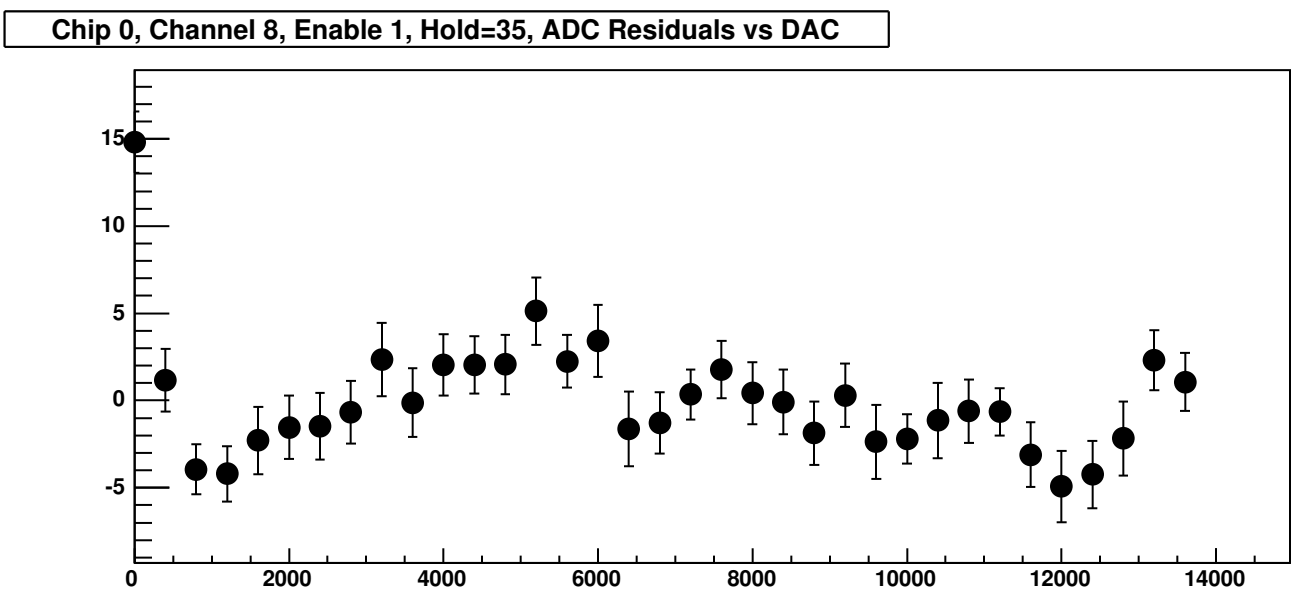

Chip 0, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1175 / 23
p0 -182.4 ± 1.189
p1 0.1992 ± 0.0001879
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

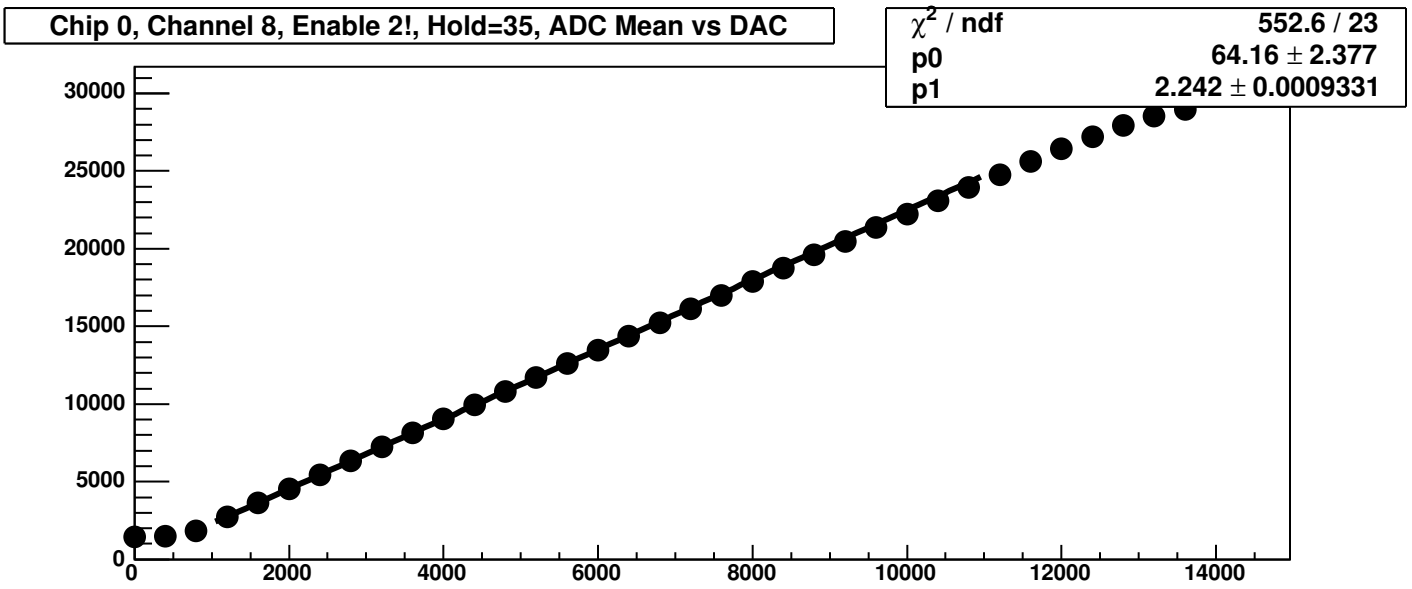

Chip 0, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

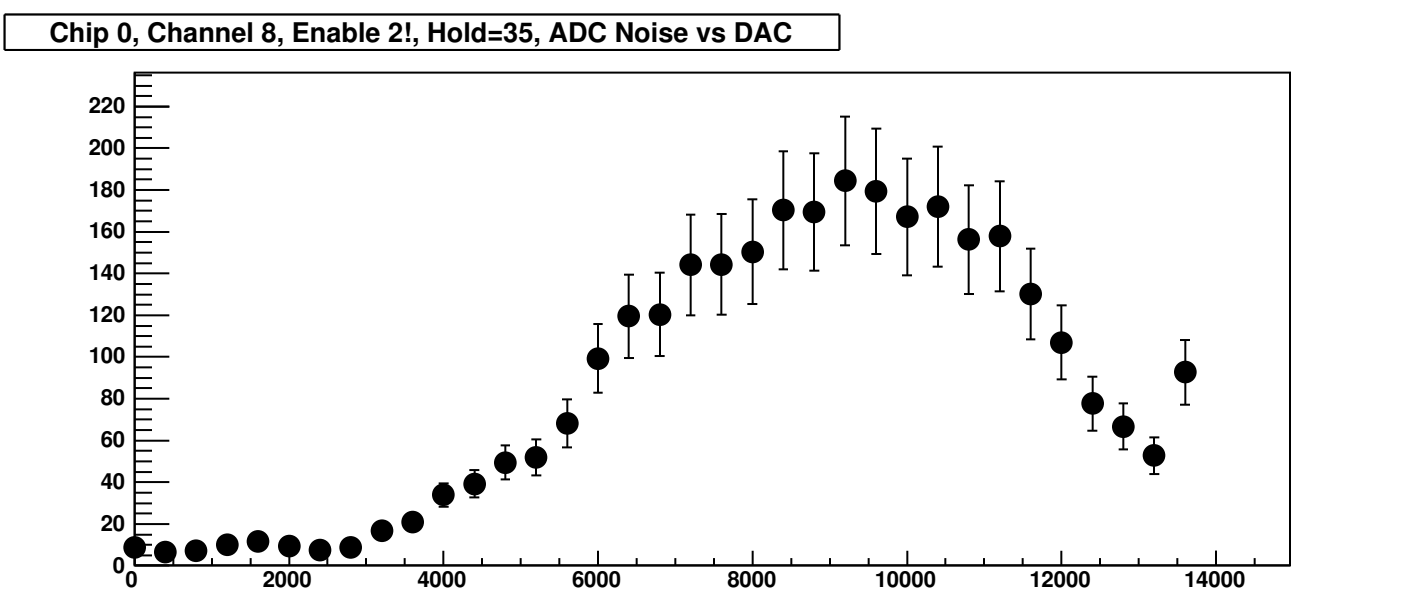

Chip 0, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

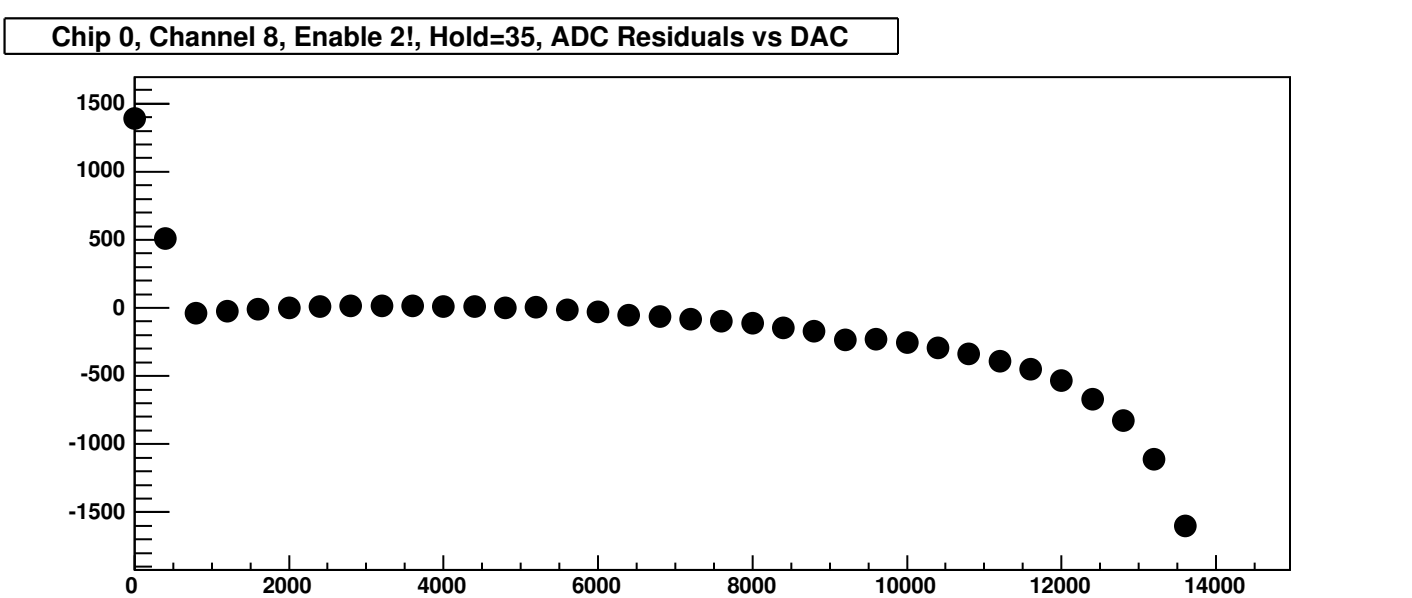

Chip 0, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 35.13 / 23
p0 7.338 ± 0.8298
p1 0.03142 ± 0.0001243
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

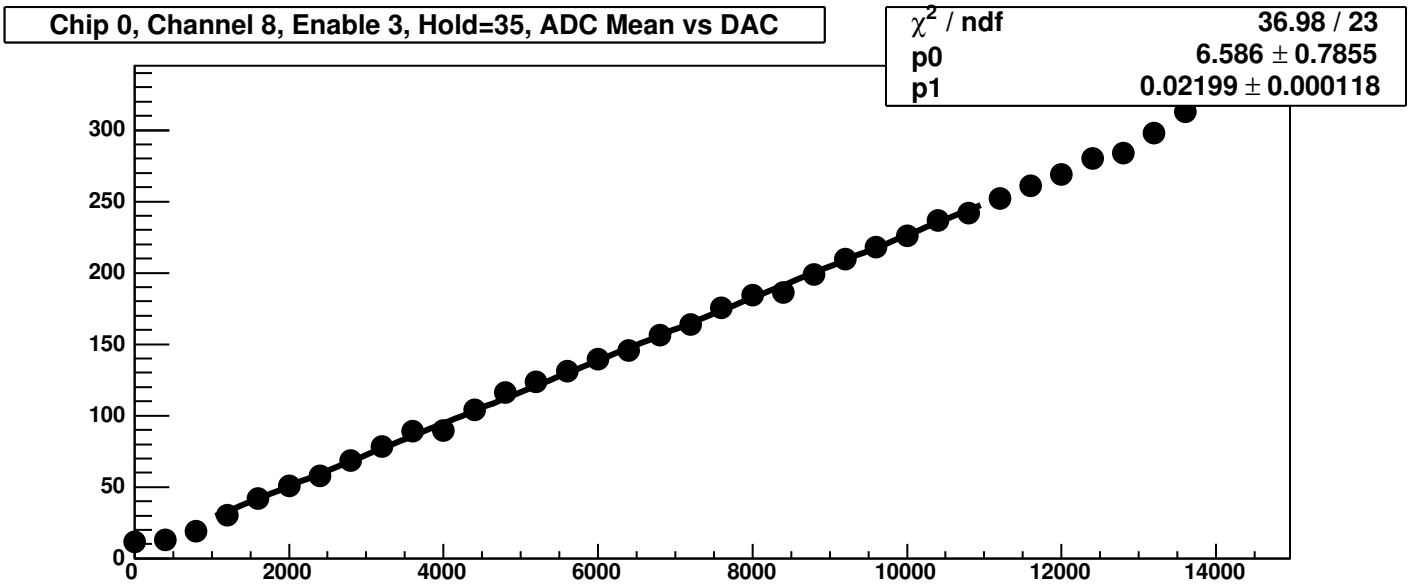

Chip 0, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

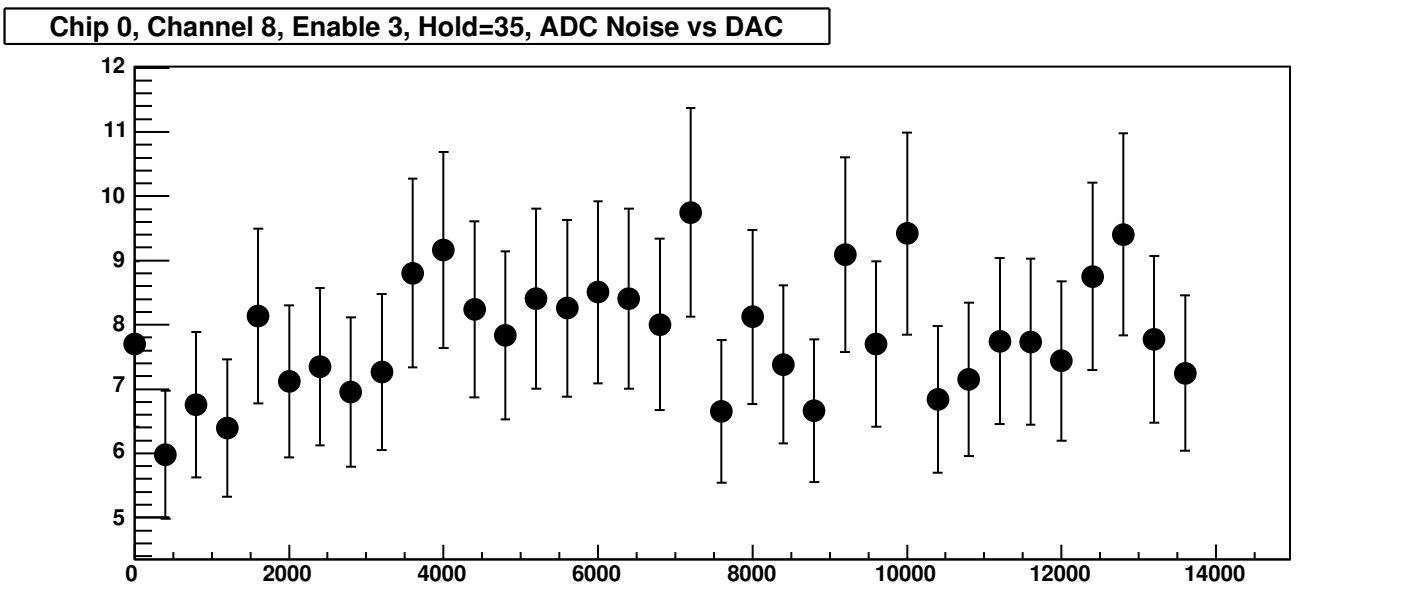

Chip 0, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

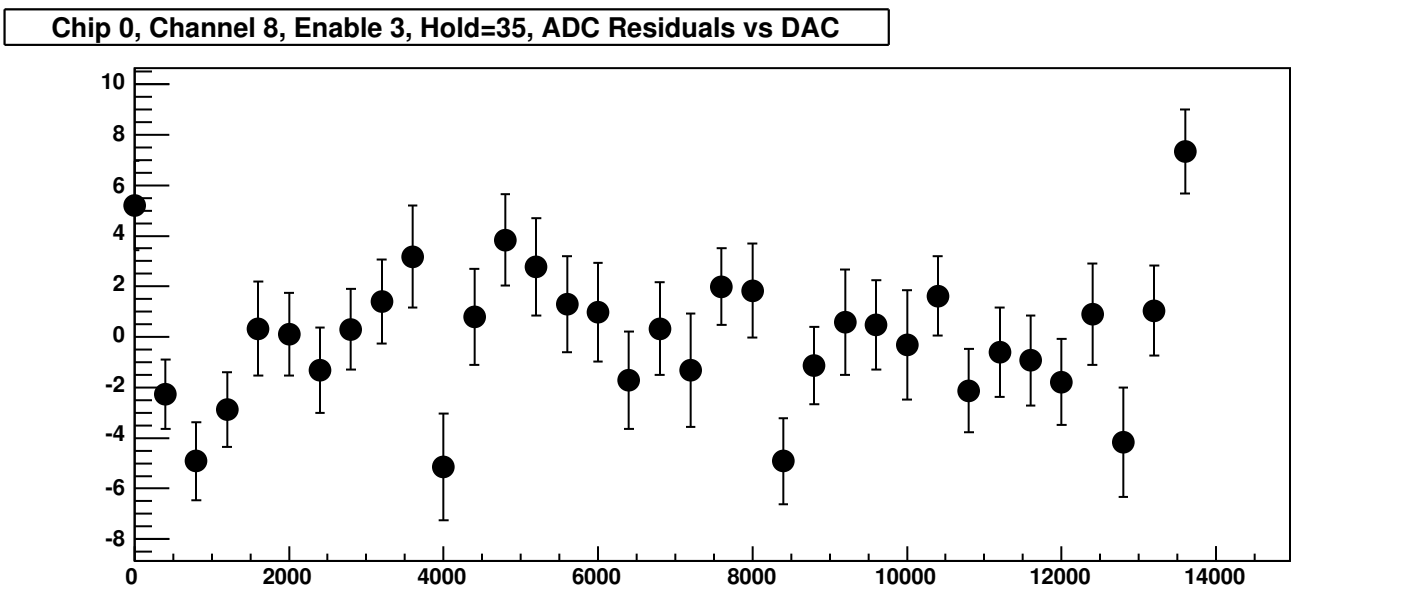

Chip 0, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 552.6 / 23
p0 64.16 ± 2.377
p1 2.242 ± 0.0009331
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

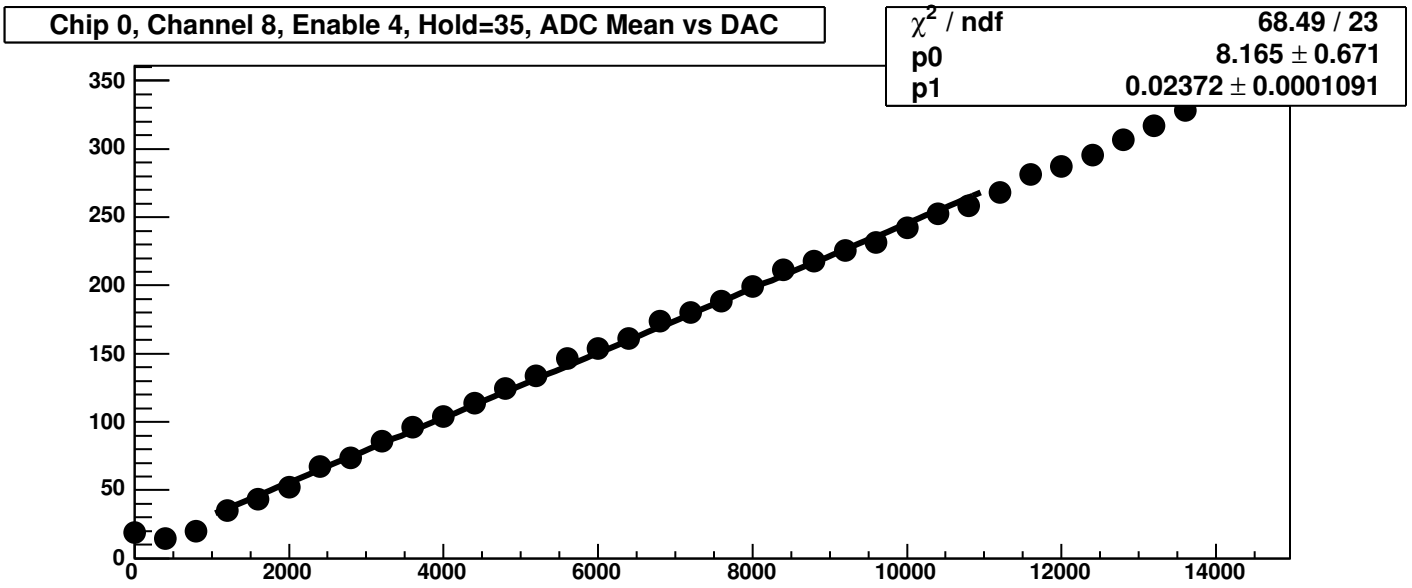

Chip 0, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

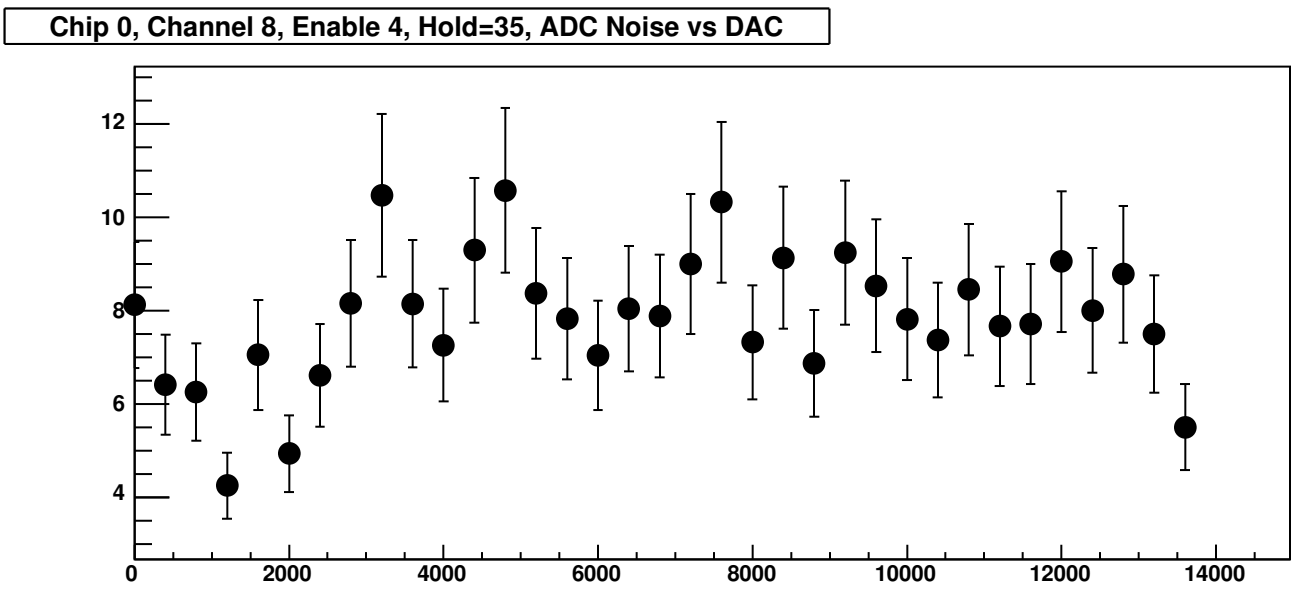

Chip 0, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

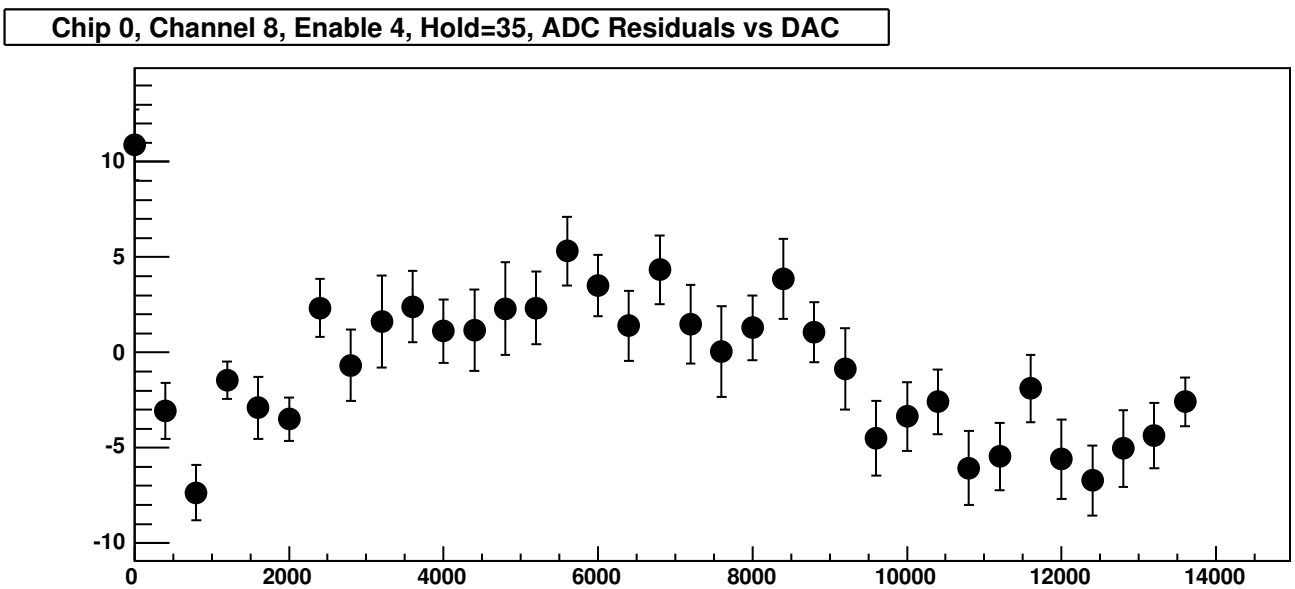

Chip 0, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.98 / 23
p0 6.586 ± 0.7855
p1 0.02199 ± 0.000118
0 50 100 150 200 250 300
0 2000 4000 6000 8000 10000 12000 14000

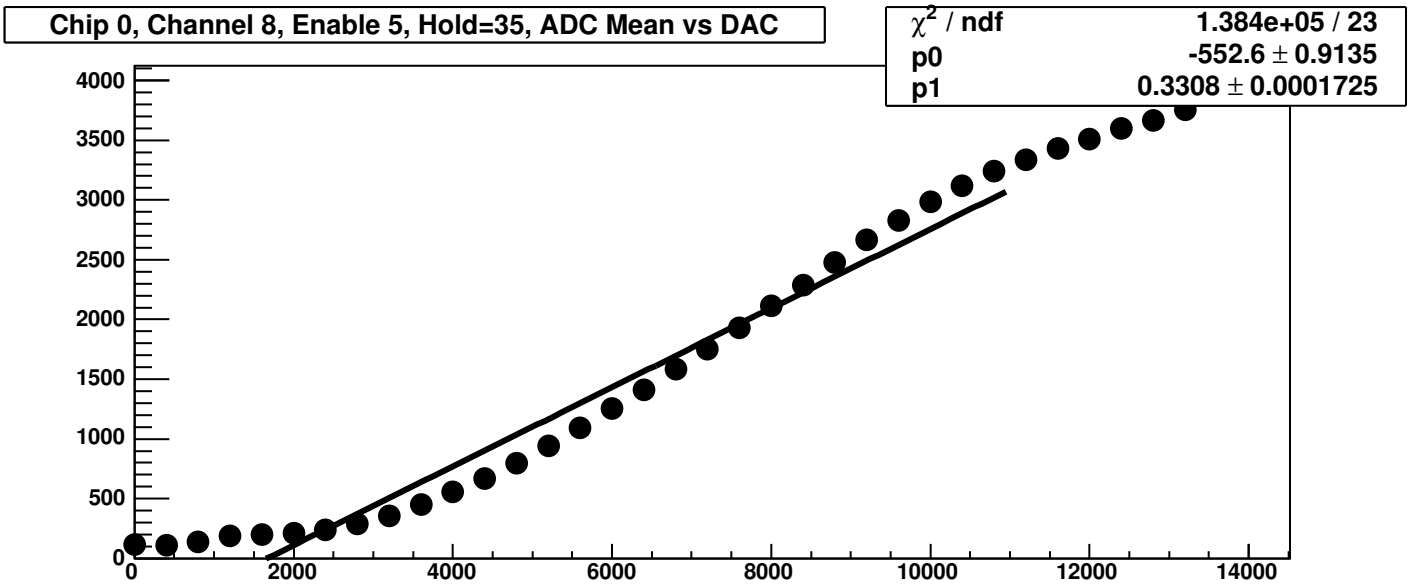

Chip 0, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
5 6 7 8 9 10 11 12
0 2000 4000 6000 8000 10000 12000 14000

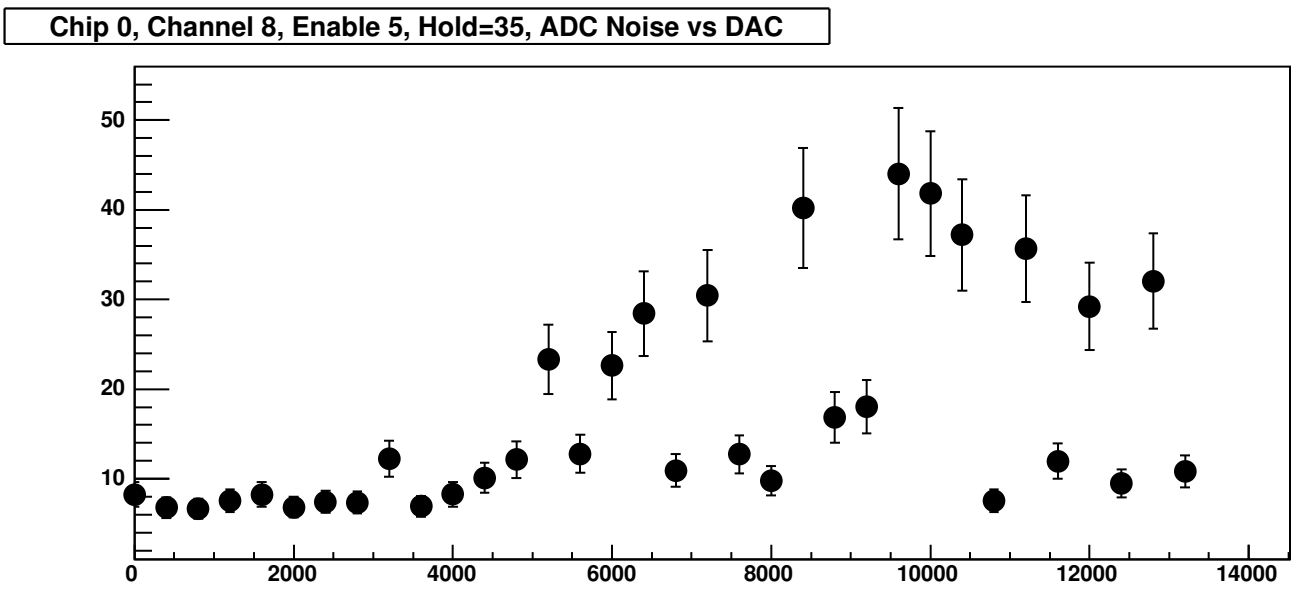

Chip 0, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
-8 -6 -4 -2 0 2 4 6 8 10
0 2000 4000 6000 8000 10000 12000 14000

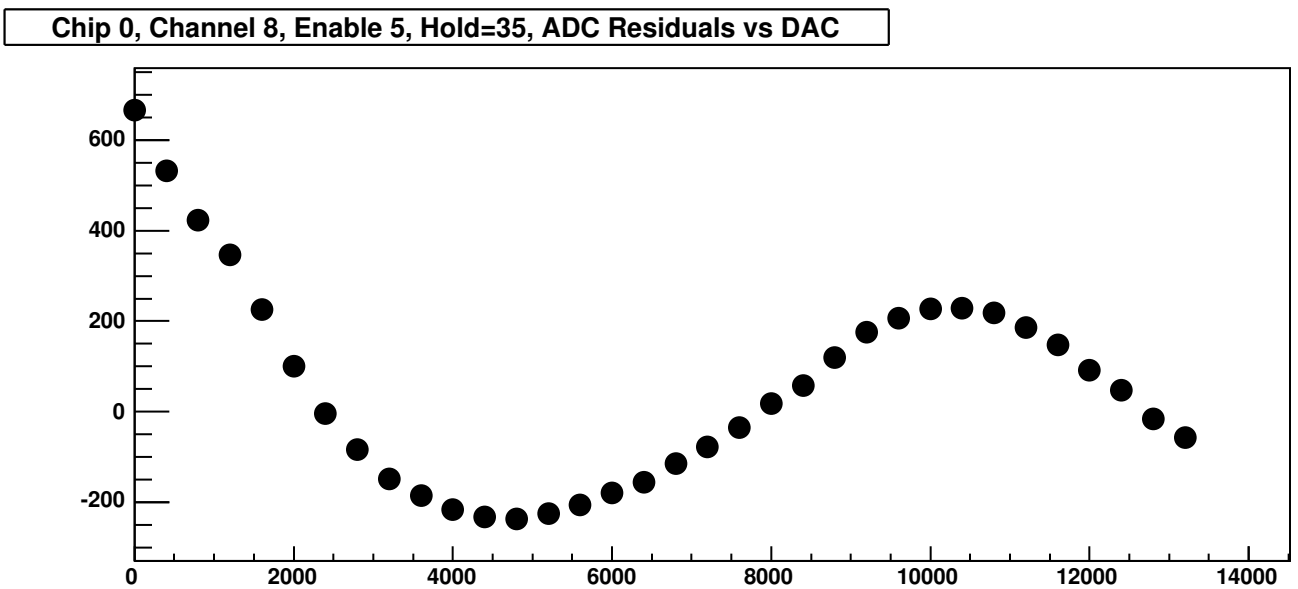

Chip 0, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 68.49 / 23
p0 8.165 ± 0.671
p1 0.02372 ± 0.0001091
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

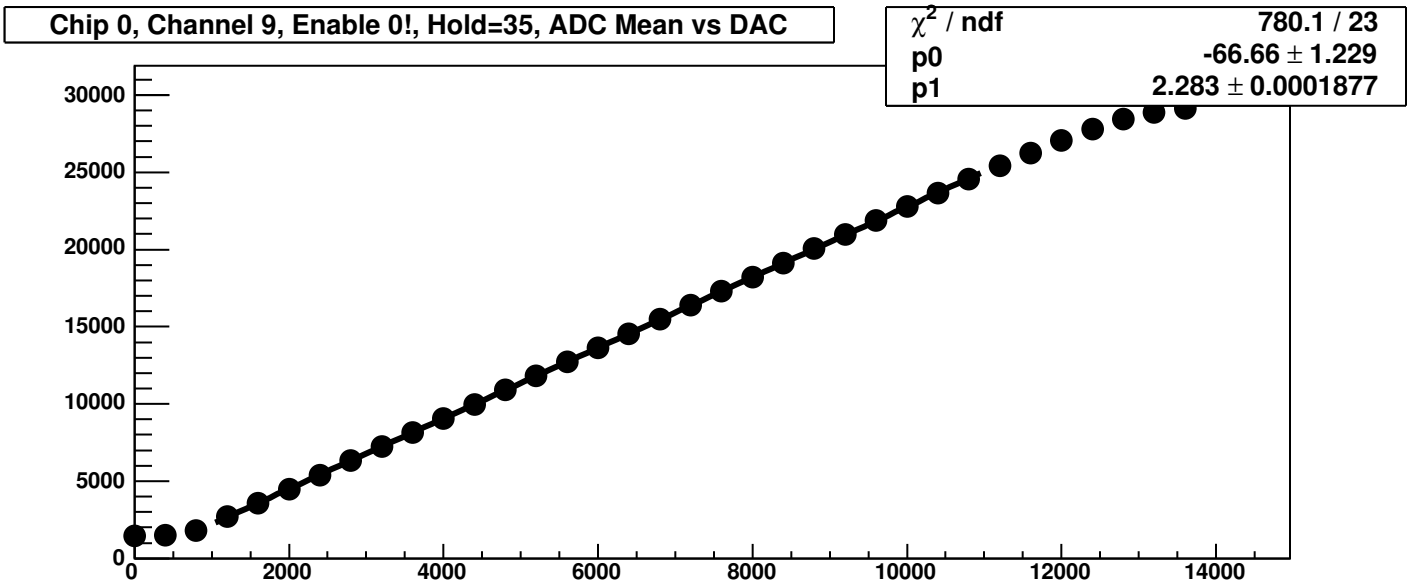

Chip 0, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

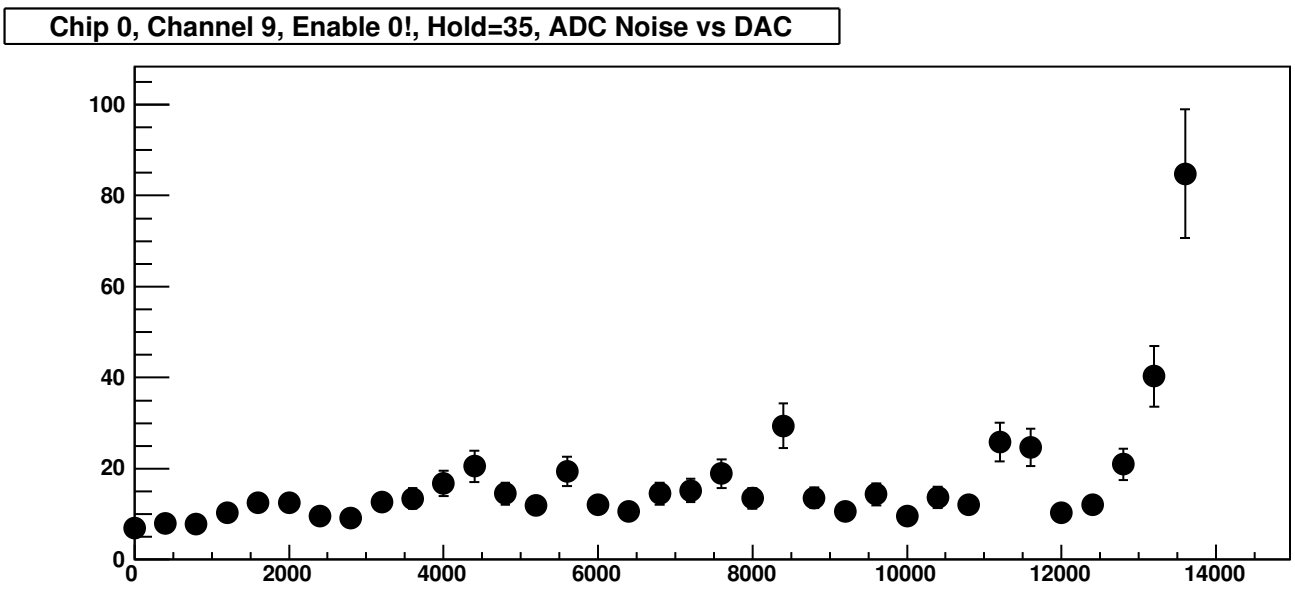

Chip 0, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

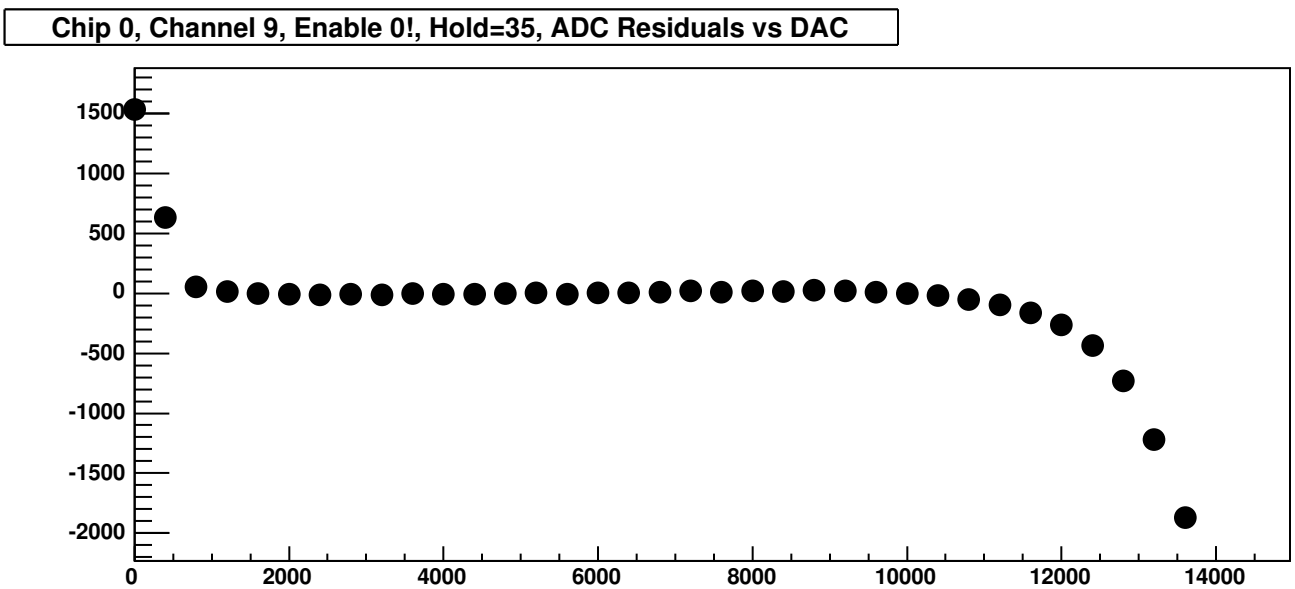

Chip 0, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.384e+05 / 23
p0 -552.6 ± 0.9135
p1 0.3308 ± 0.0001725
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

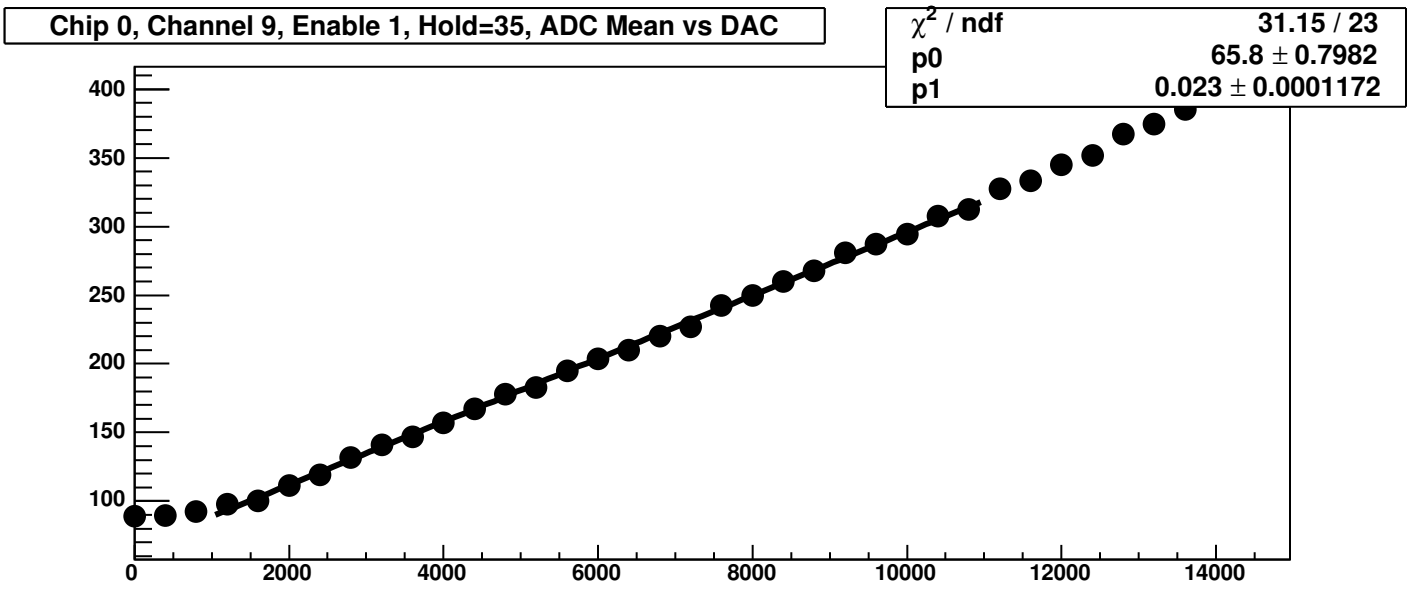

Chip 0, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

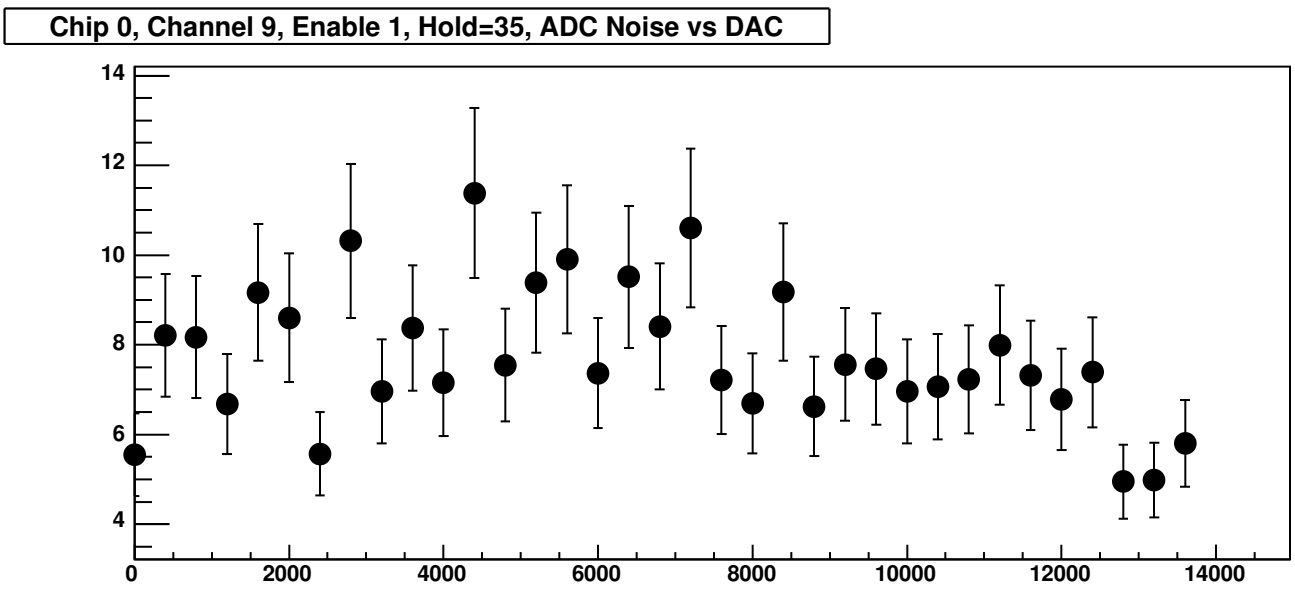

Chip 0, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

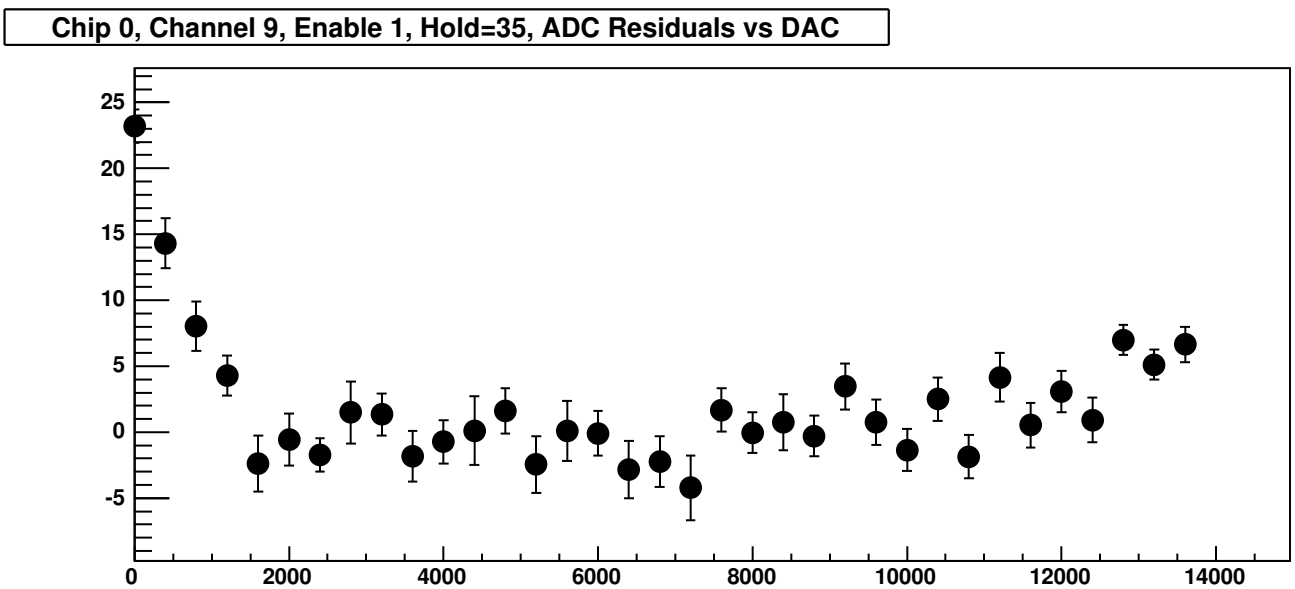

Chip 0, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 780.1 / 23
p0 -66.66 ± 1.229
p1 2.283 ± 0.0001877
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

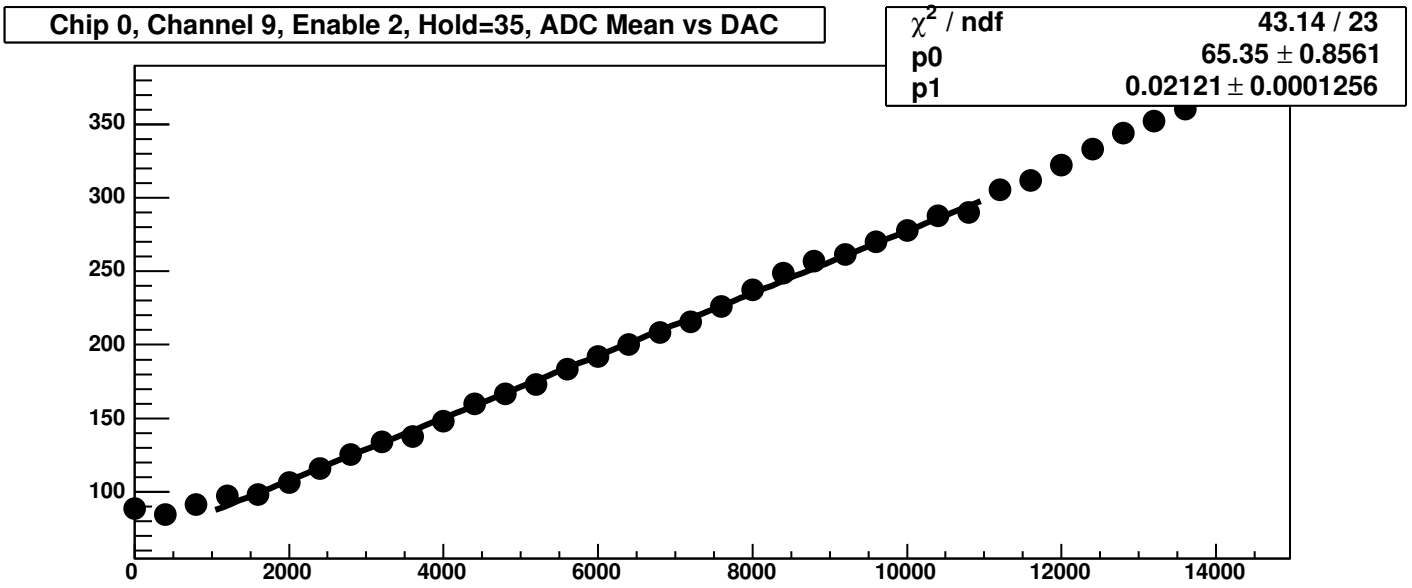

Chip 0, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

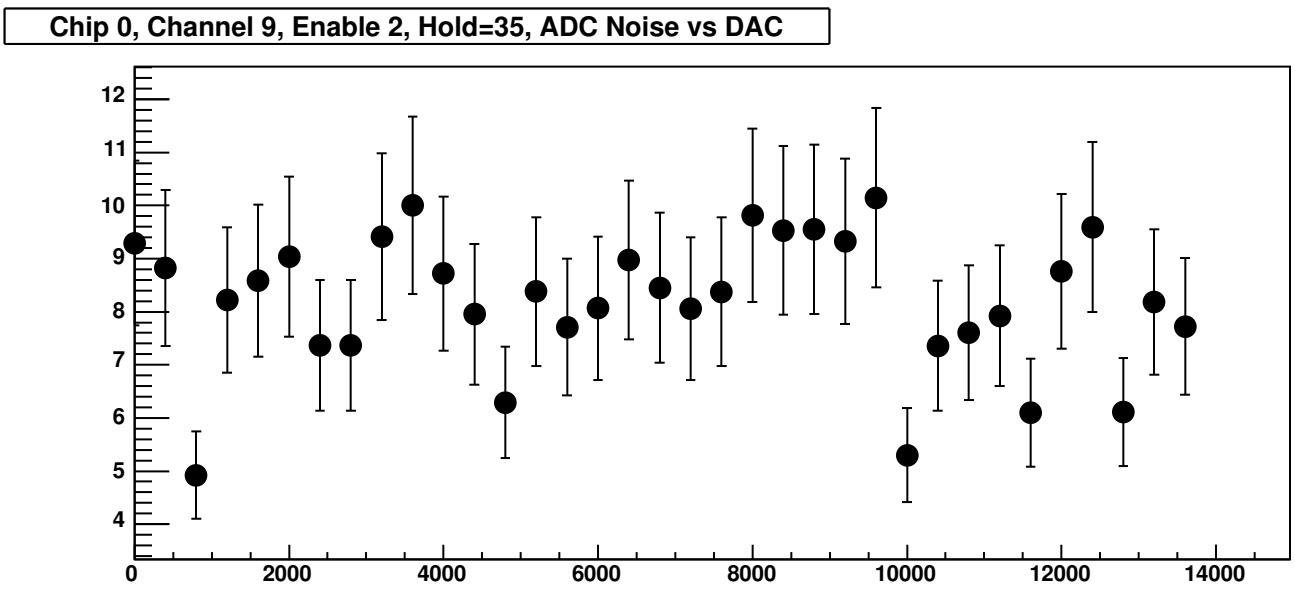

Chip 0, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

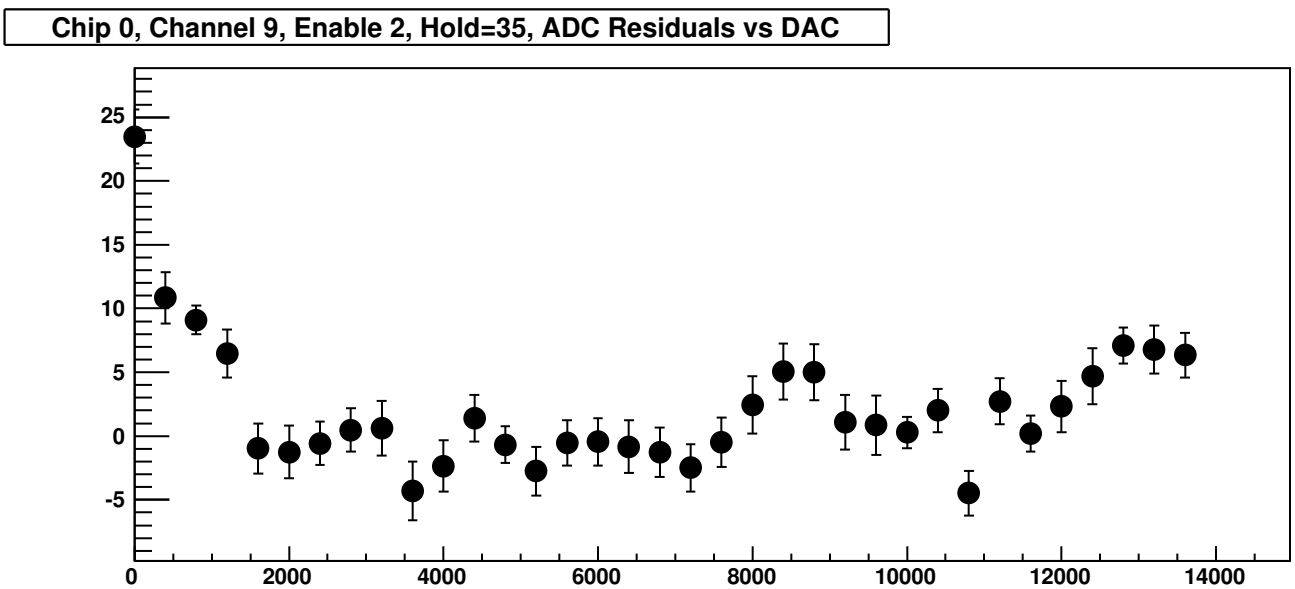

Chip 0, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.15 / 23
p0 65.8 ± 0.7982
p1 0.023 ± 0.0001172
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

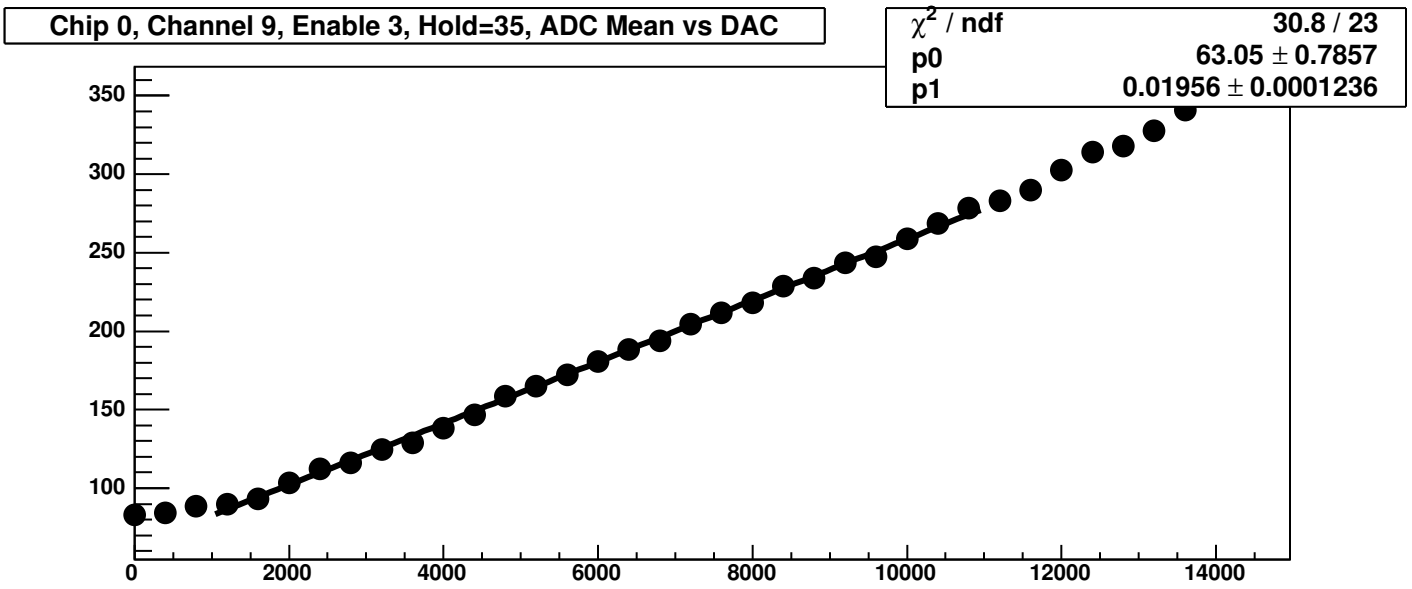

Chip 0, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

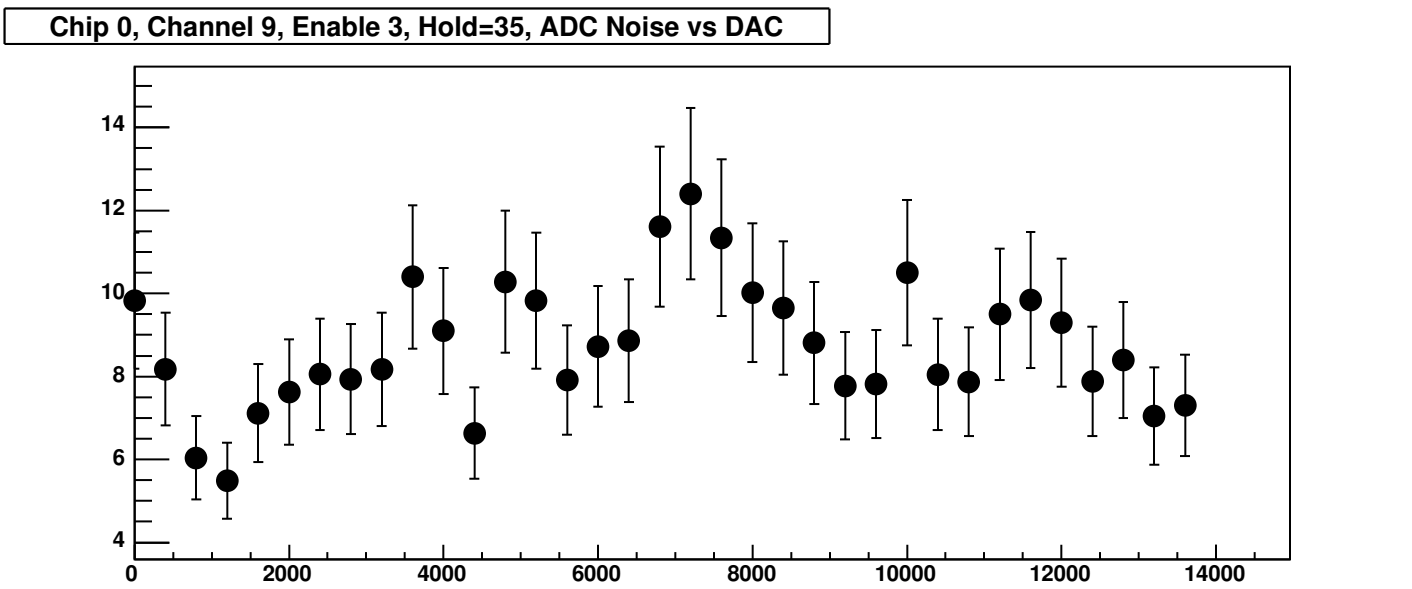

Chip 0, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

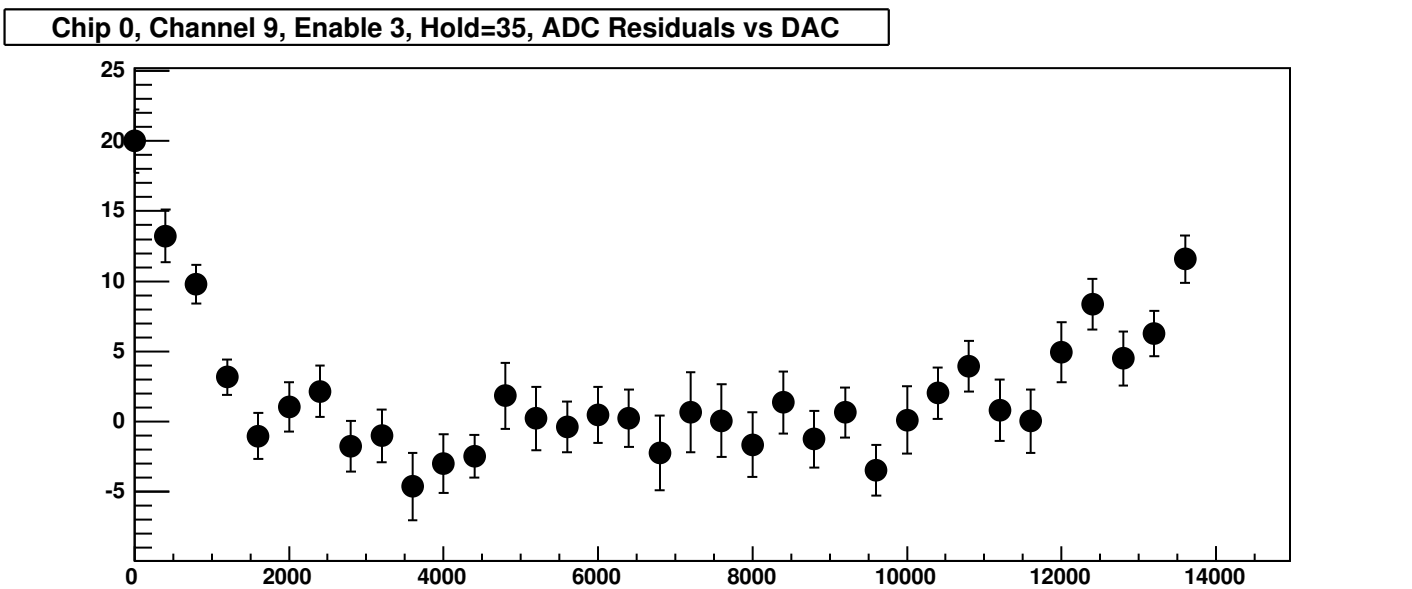

Chip 0, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 43.14 / 23
p0 65.35 ± 0.8561
p1 0.02121 ± 0.0001256
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

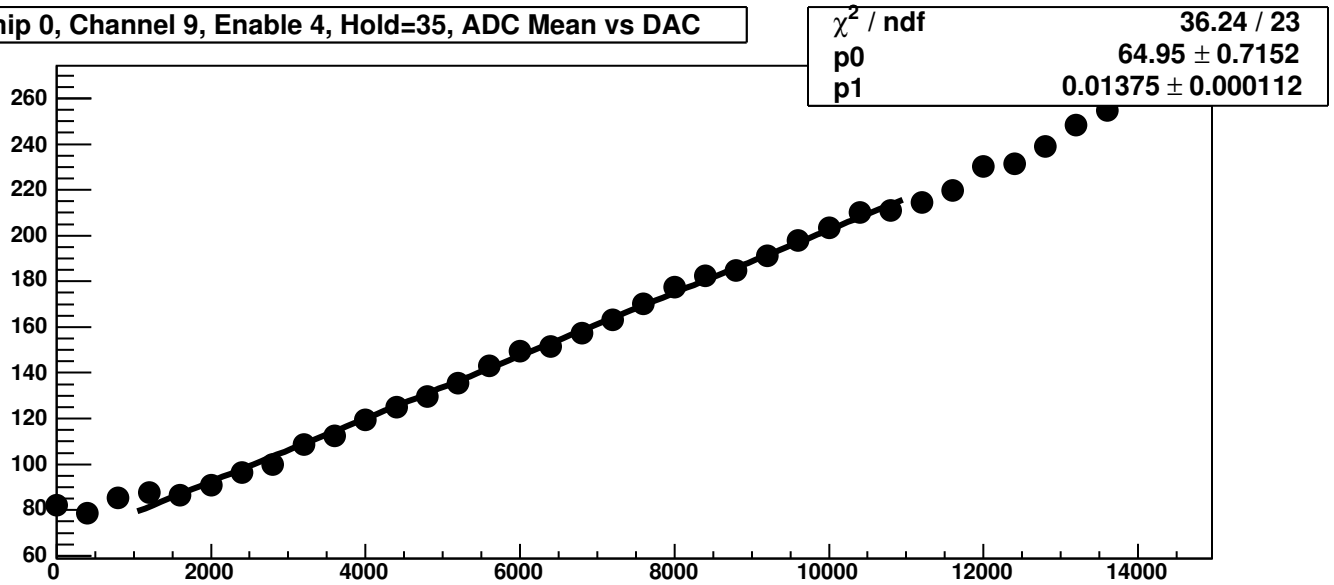

Chip 0, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 30.8 / 23
p0 63.05 ± 0.7857
p1 0.01956 ± 0.0001236
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

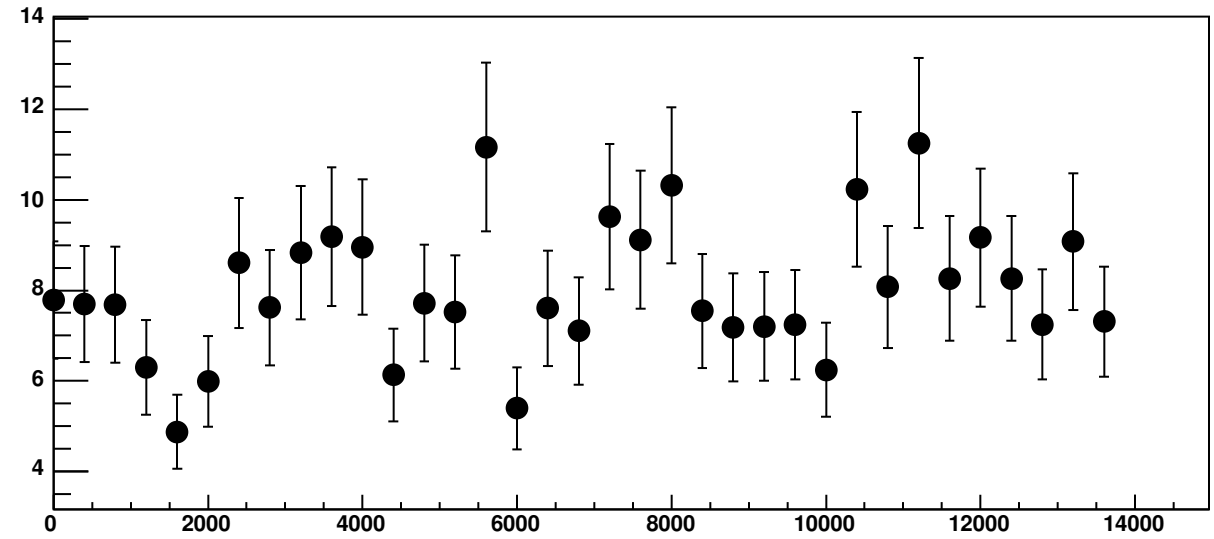

Chip 0, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

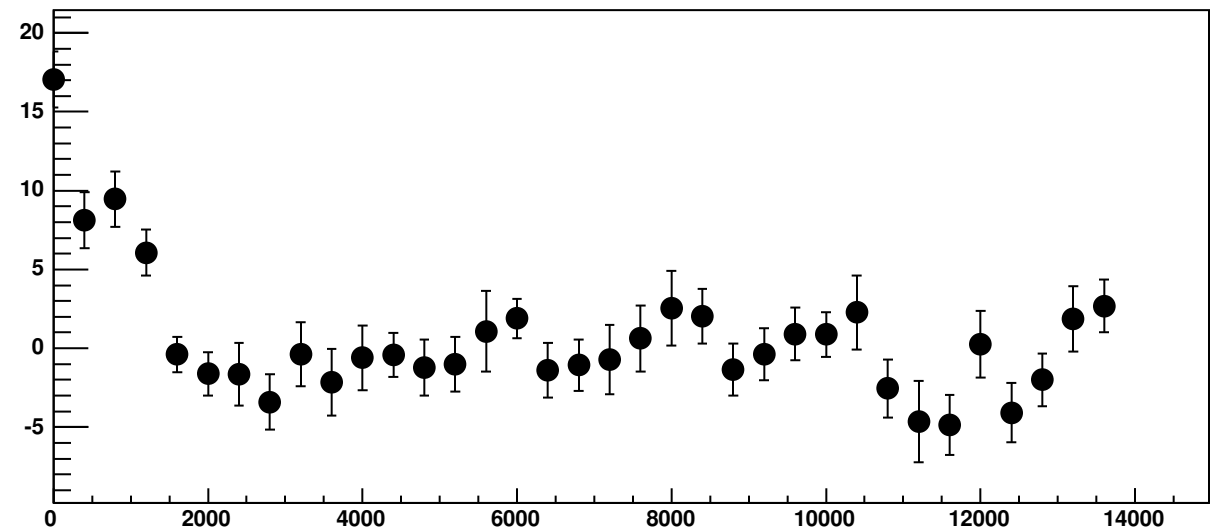
Chip 0, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



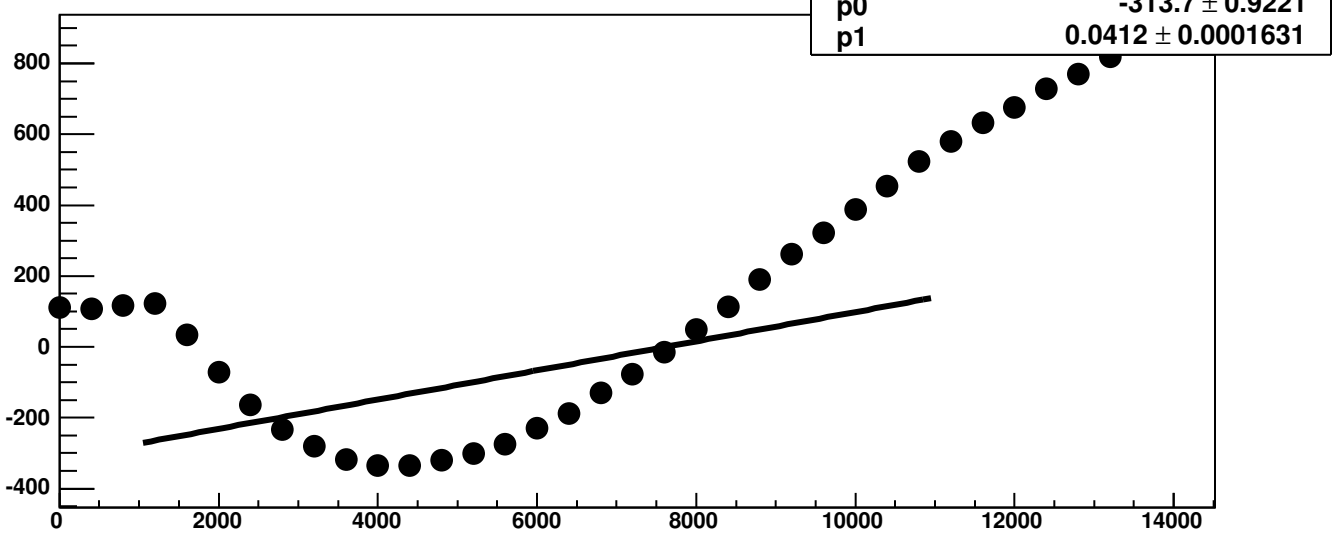
Chip 0, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



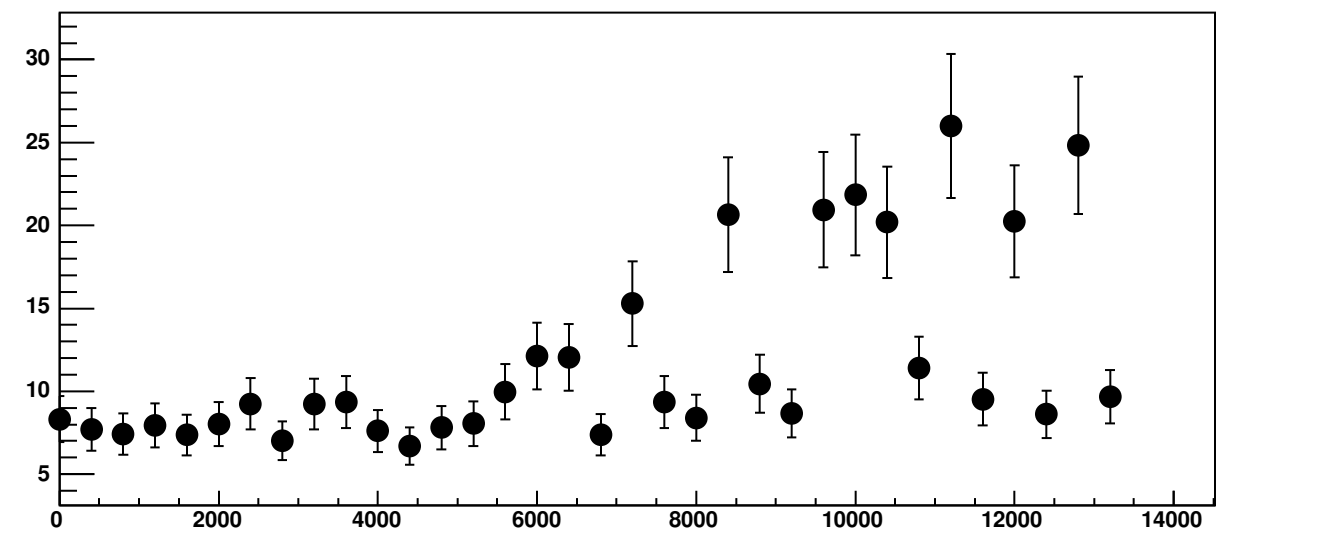
Chip 0, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



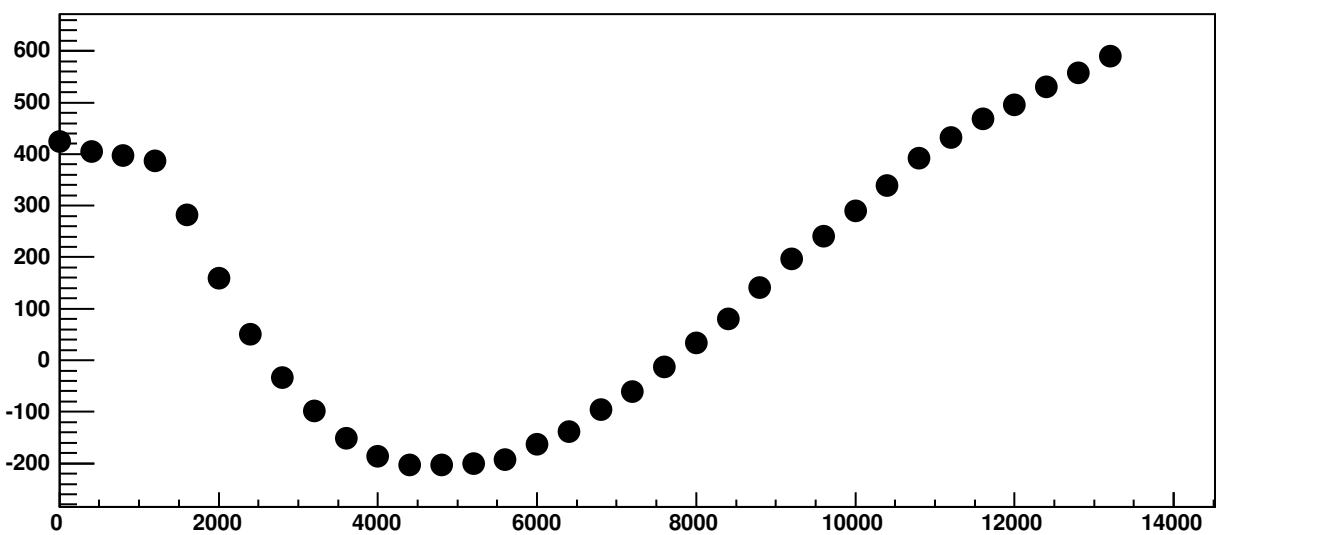
Chip 0, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

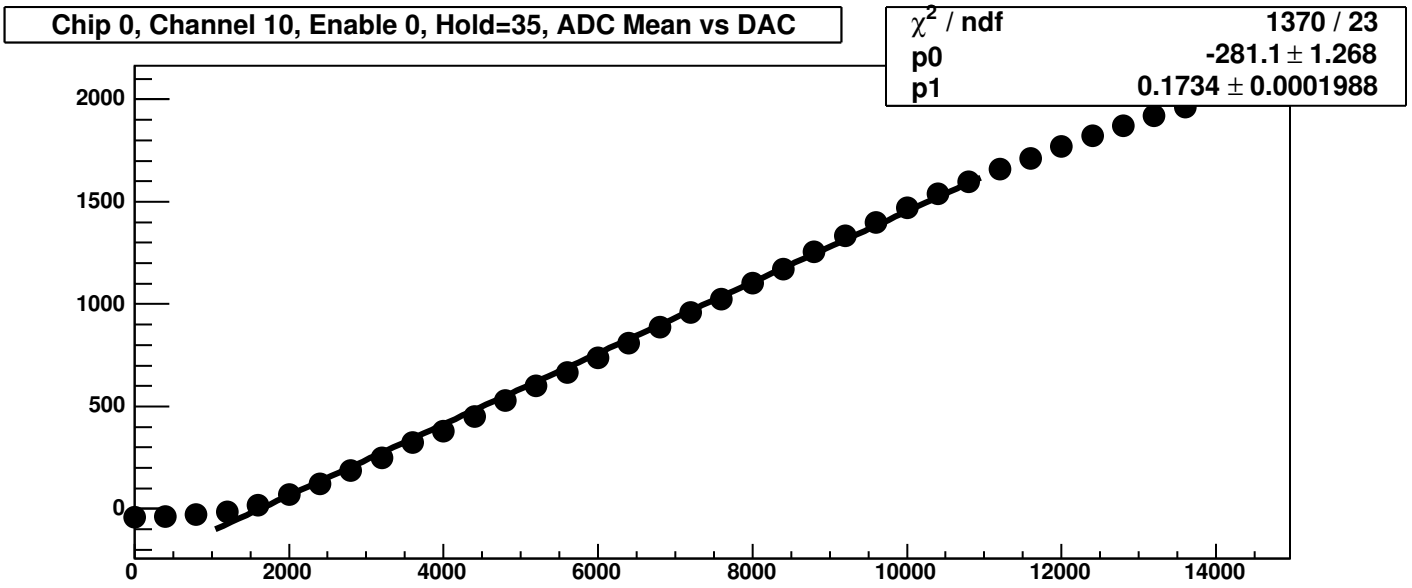


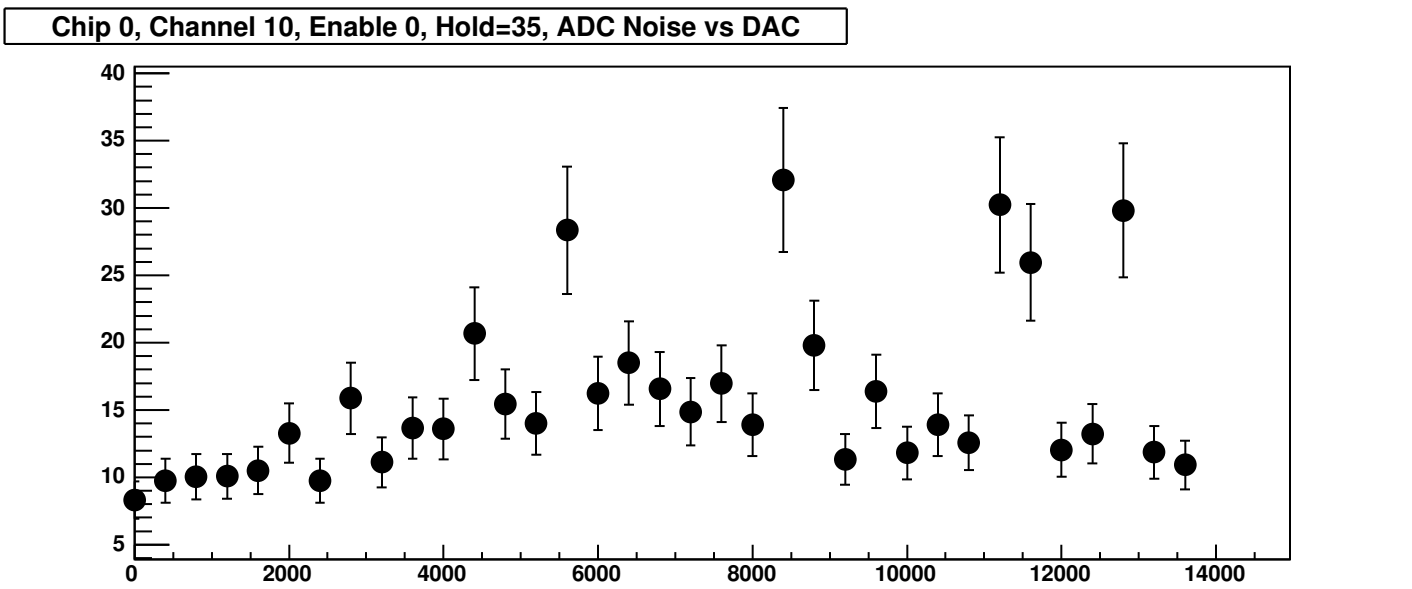
Chip 0, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

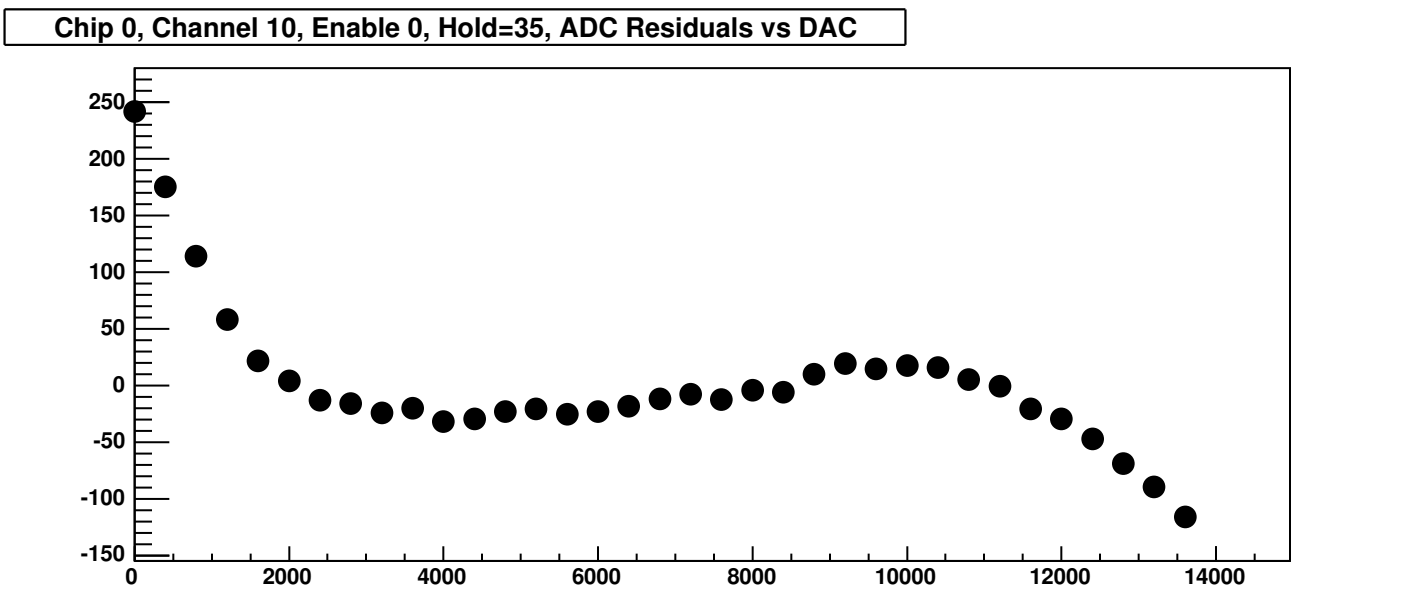


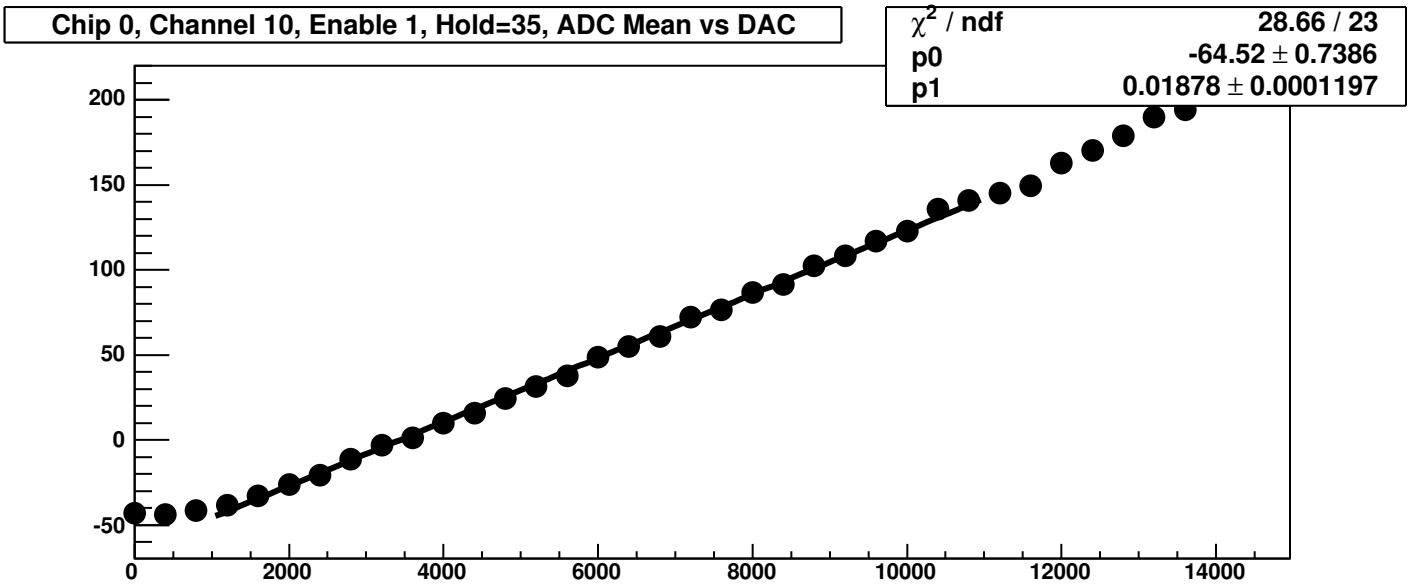
Chip 0, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

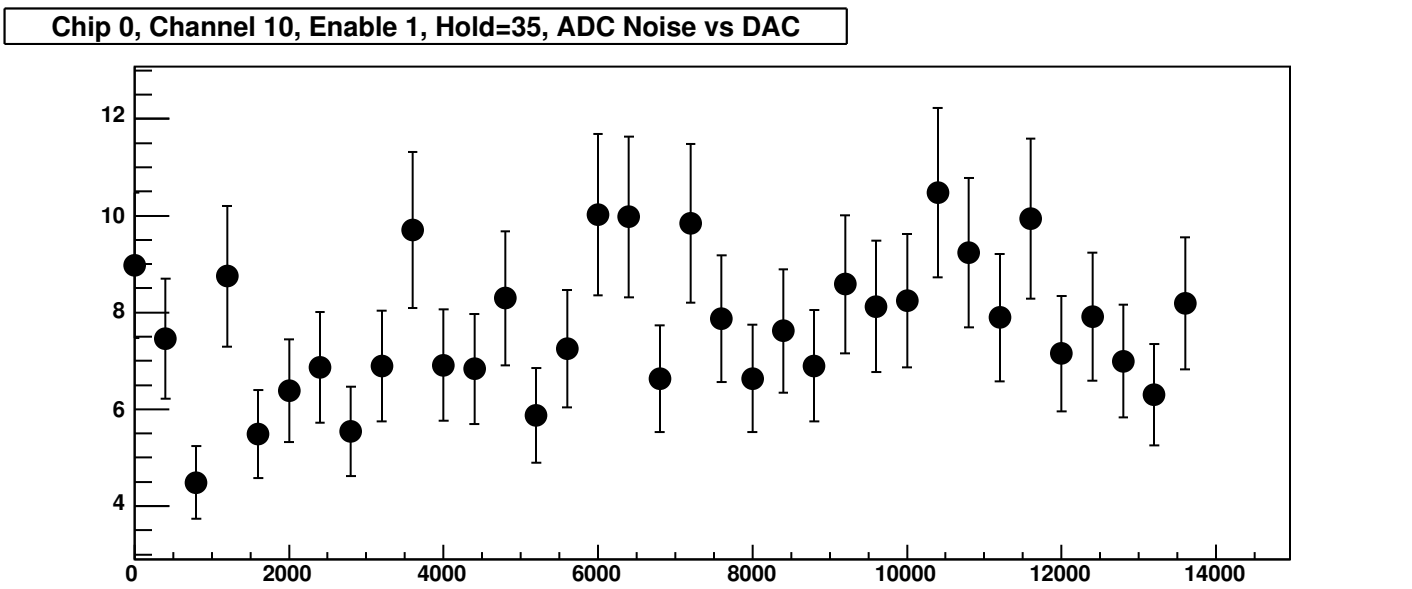


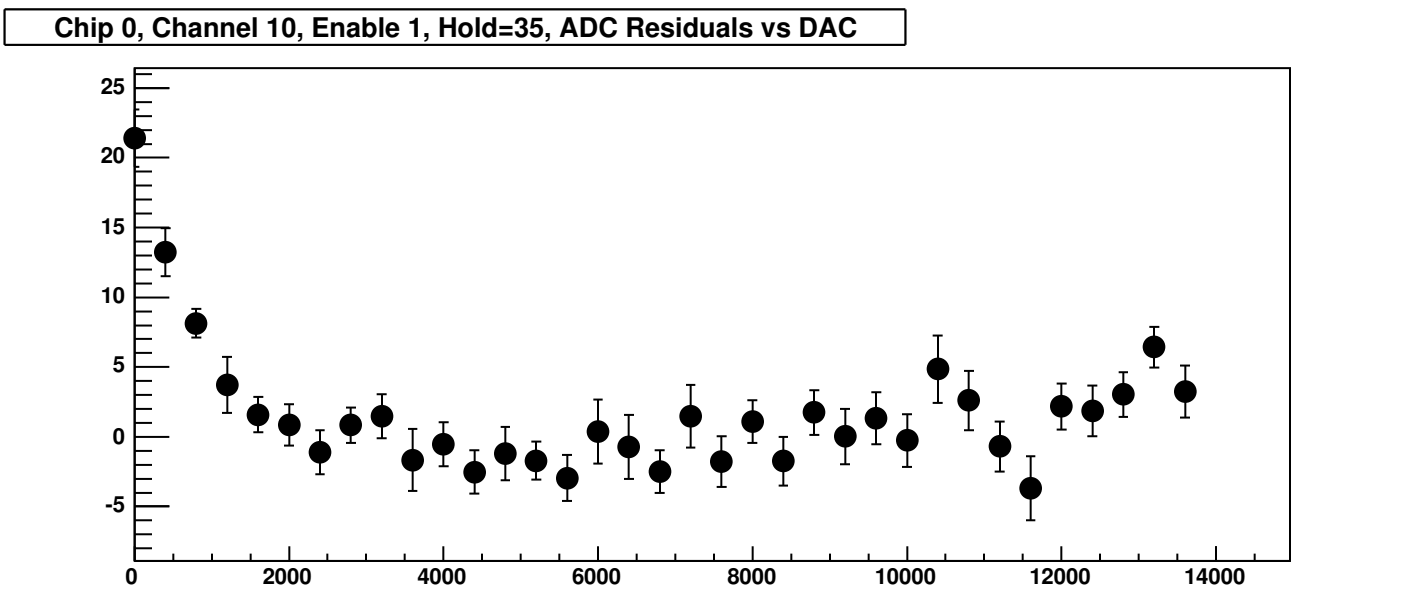

Chip 0, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1370 / 23
p0 -281.1 ± 1.268
p1 0.1734 ± 0.0001988
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

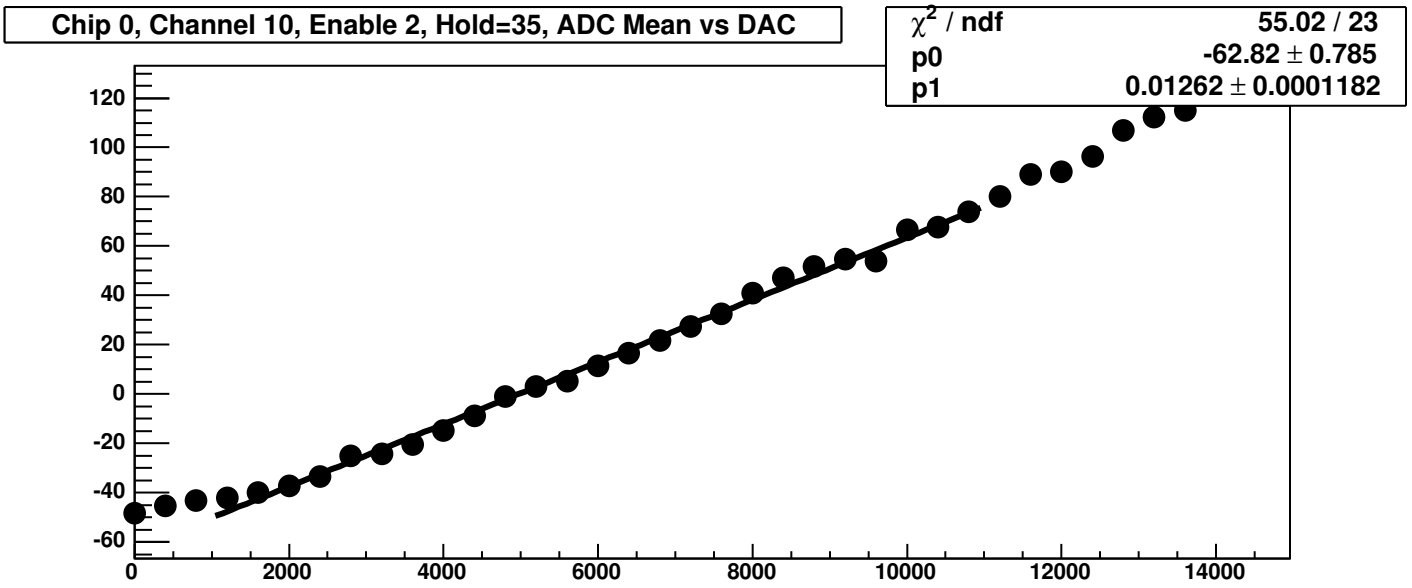

Chip 0, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

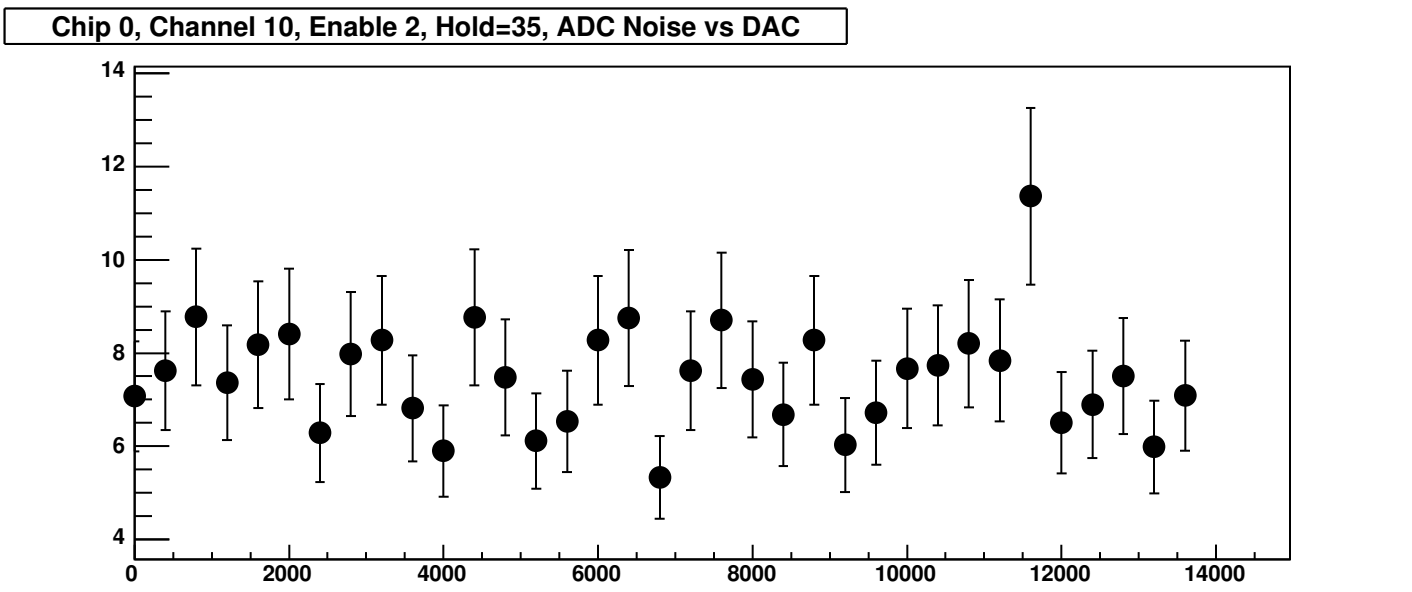

Chip 0, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

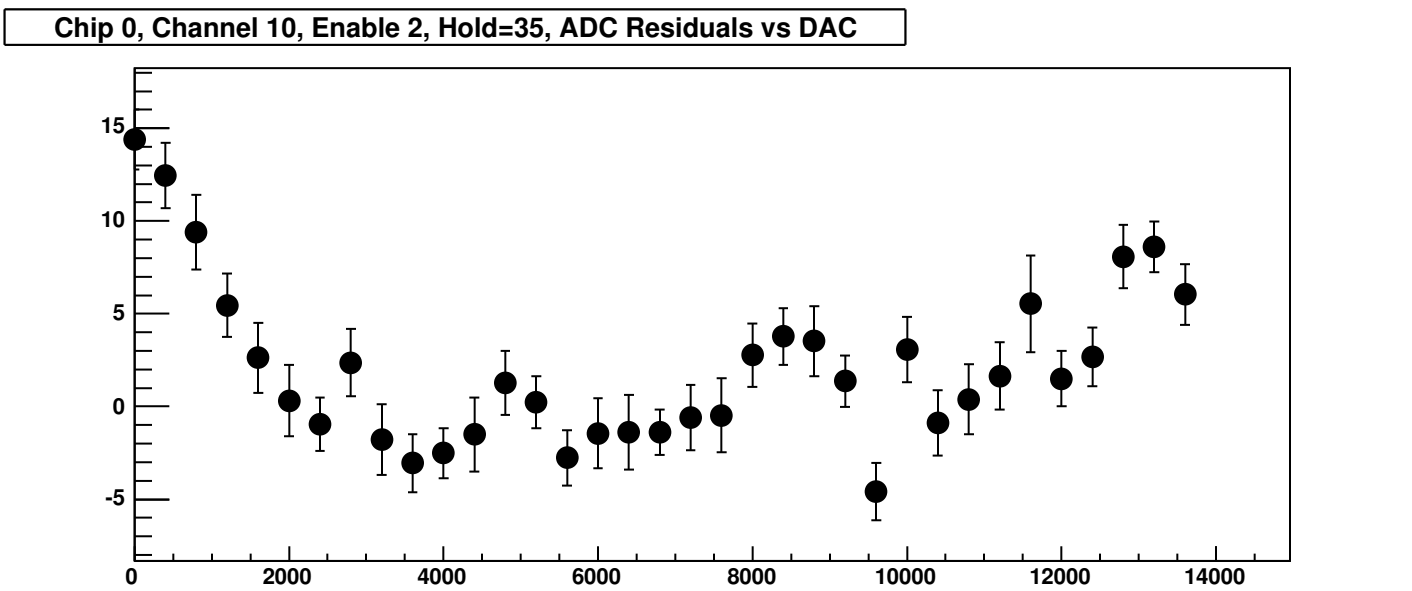

Chip 0, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 28.66 / 23
p0 -64.52 ± 0.7386
p1 0.01878 ± 0.0001197
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

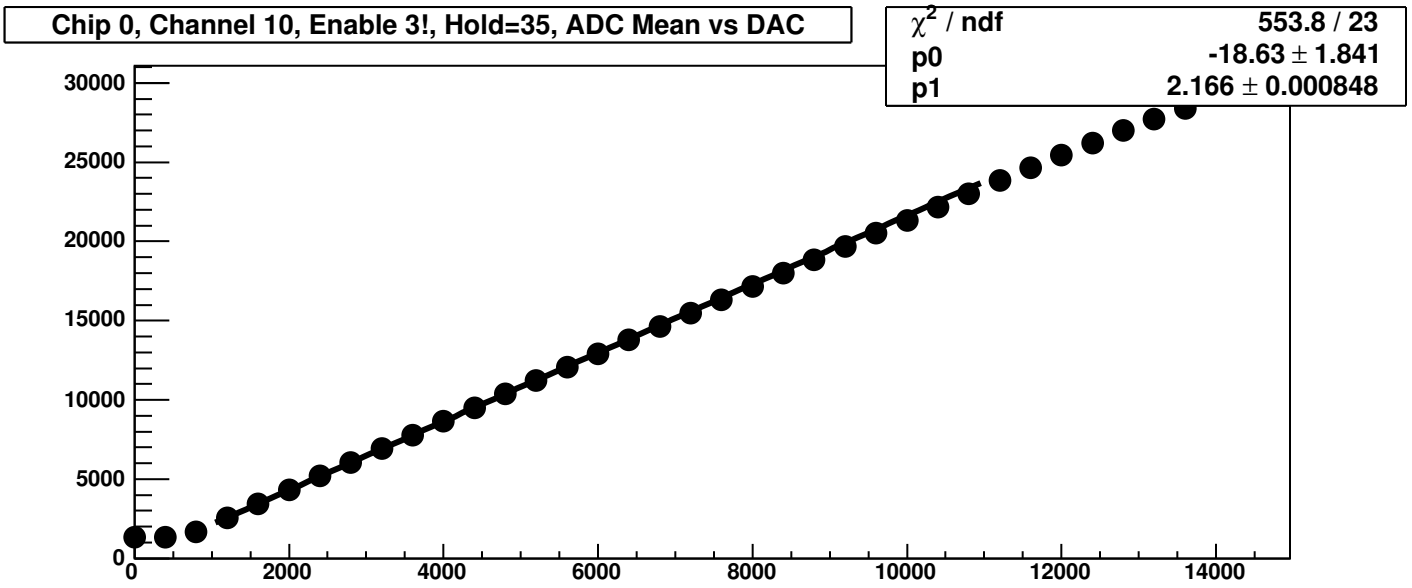

Chip 0, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

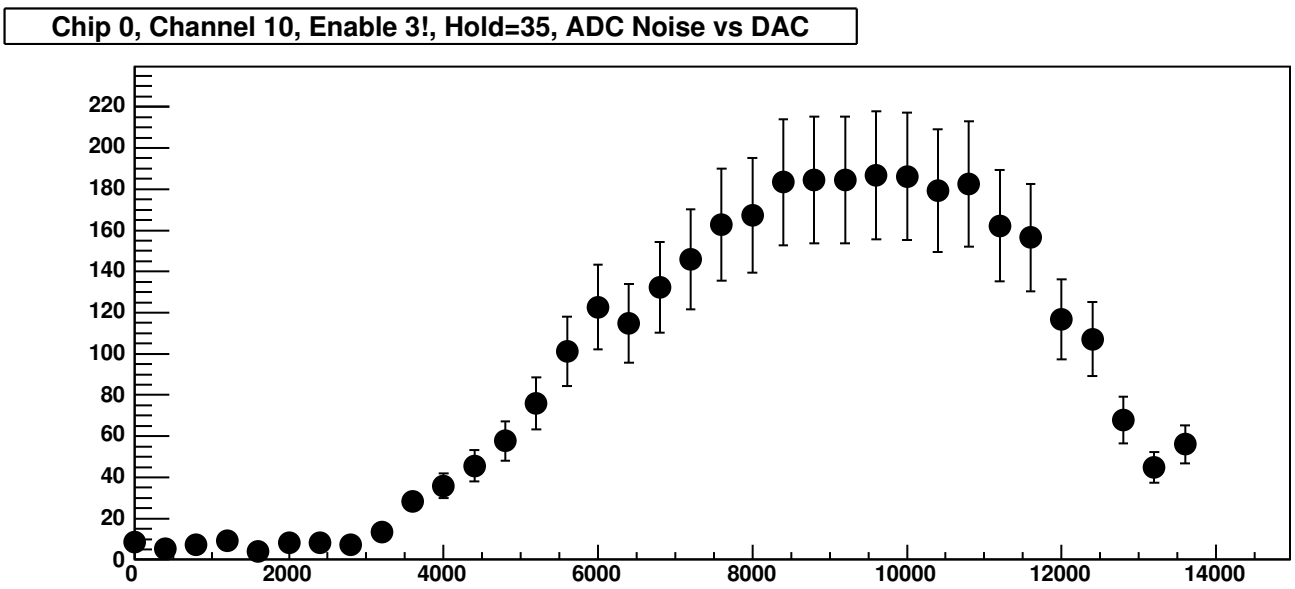

Chip 0, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

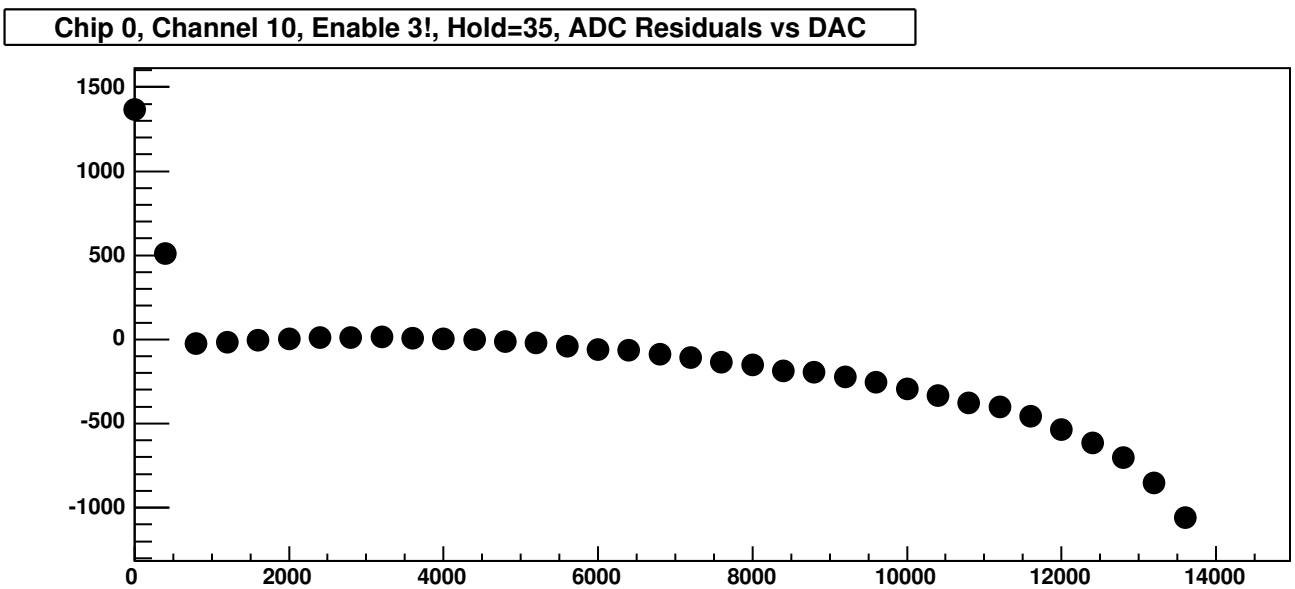

Chip 0, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 55.02 / 23
p0 -62.82 ± 0.785
p1 0.01262 ± 0.0001182
120
100
80
60
40
20
0
-20
-40
-60
0
2000
4000
6000
8000
10000
12000
14000

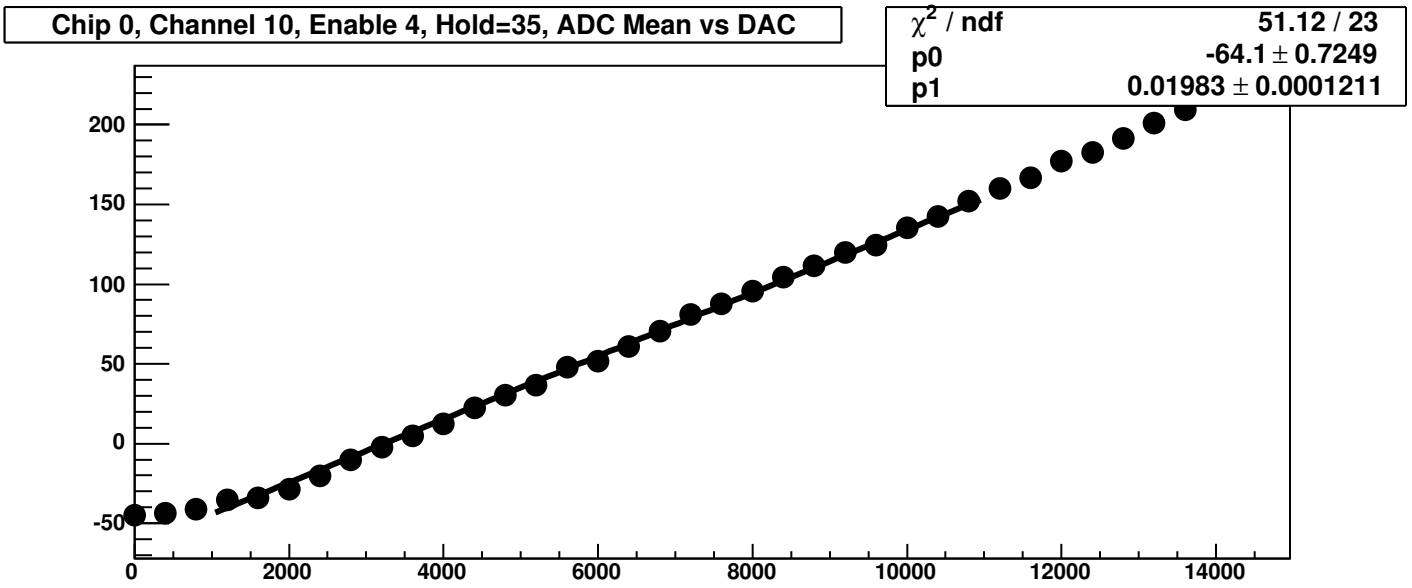

Chip 0, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

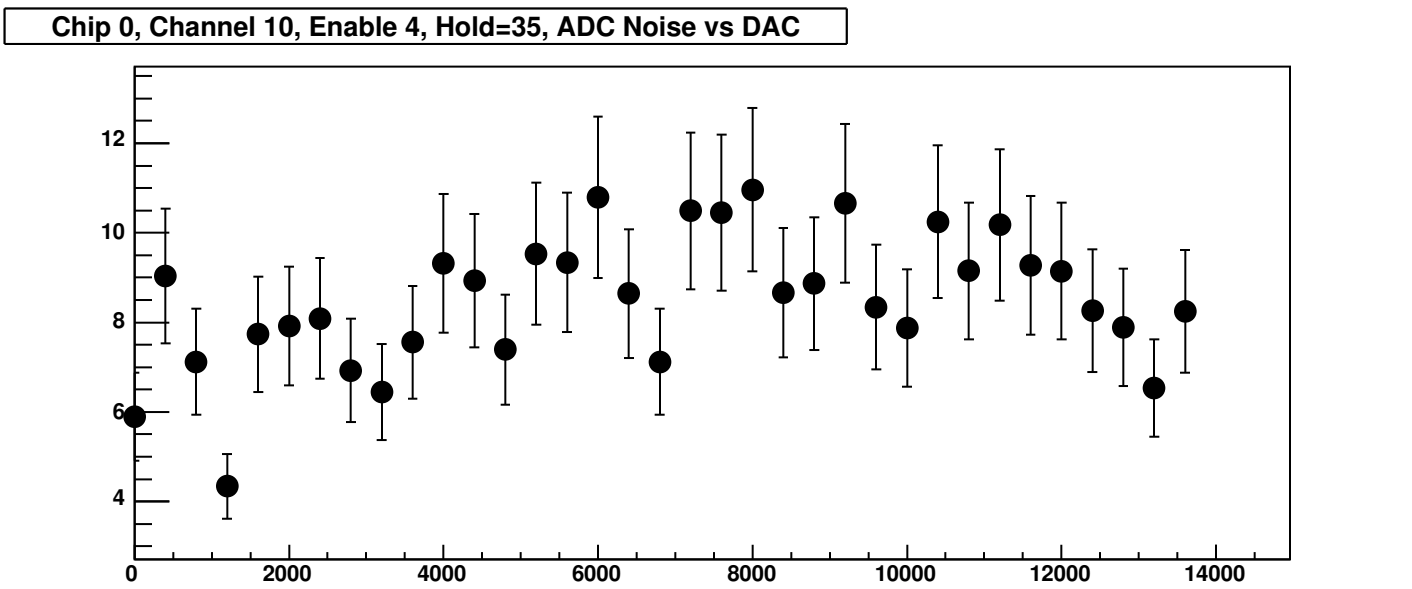

Chip 0, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

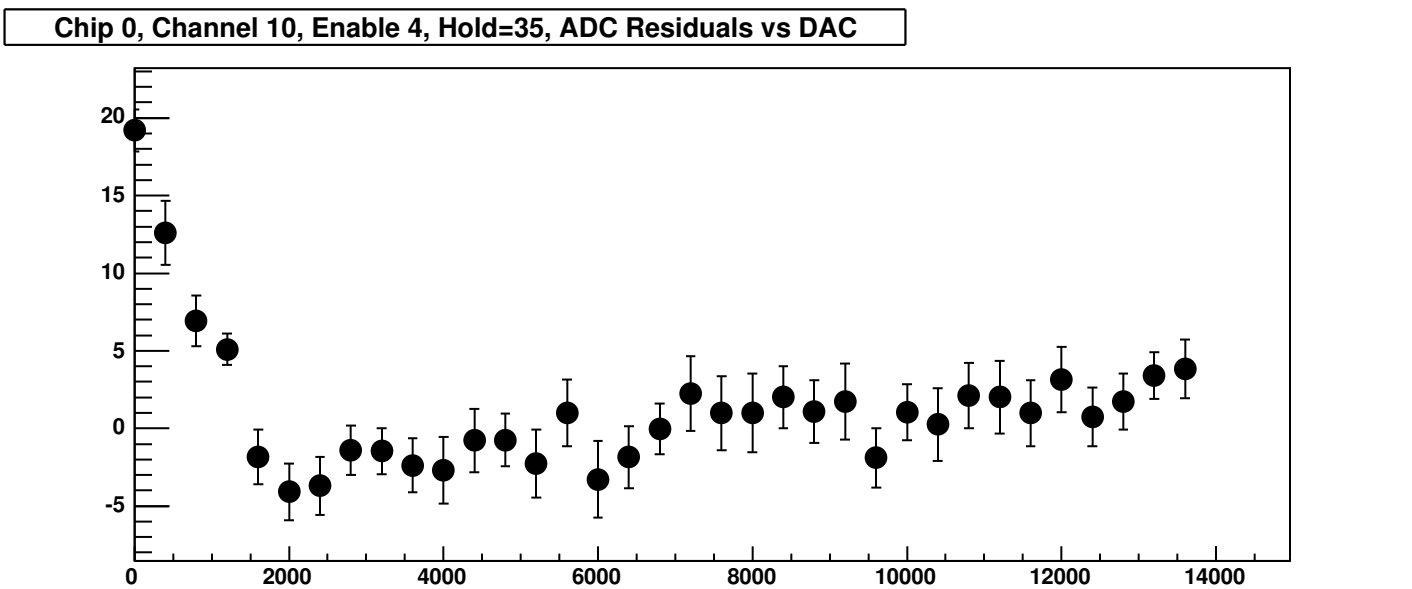

Chip 0, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 553.8 / 23
p0 -18.63 ± 1.841
p1 2.166 ± 0.000848
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

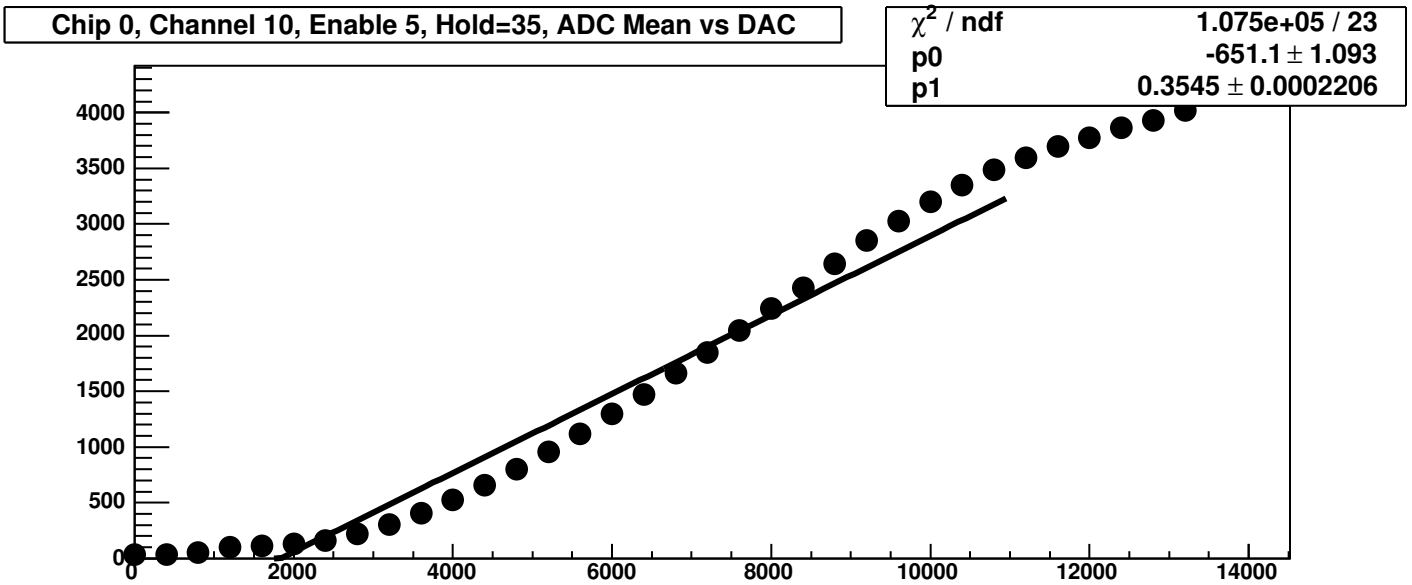

Chip 0, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

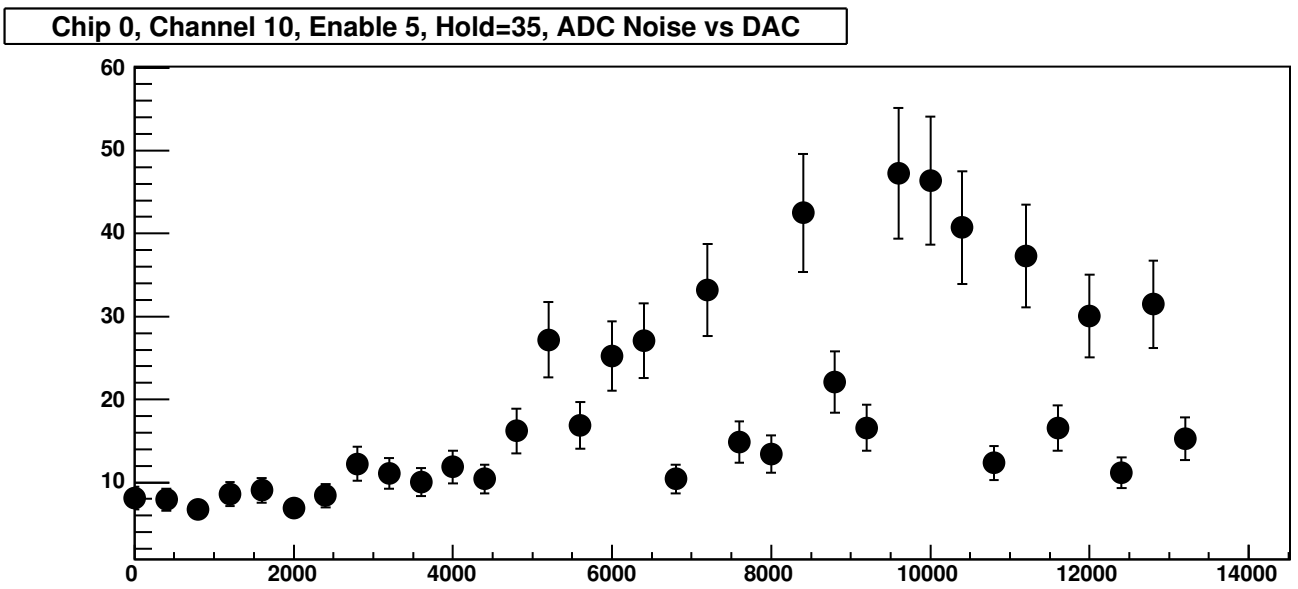

Chip 0, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

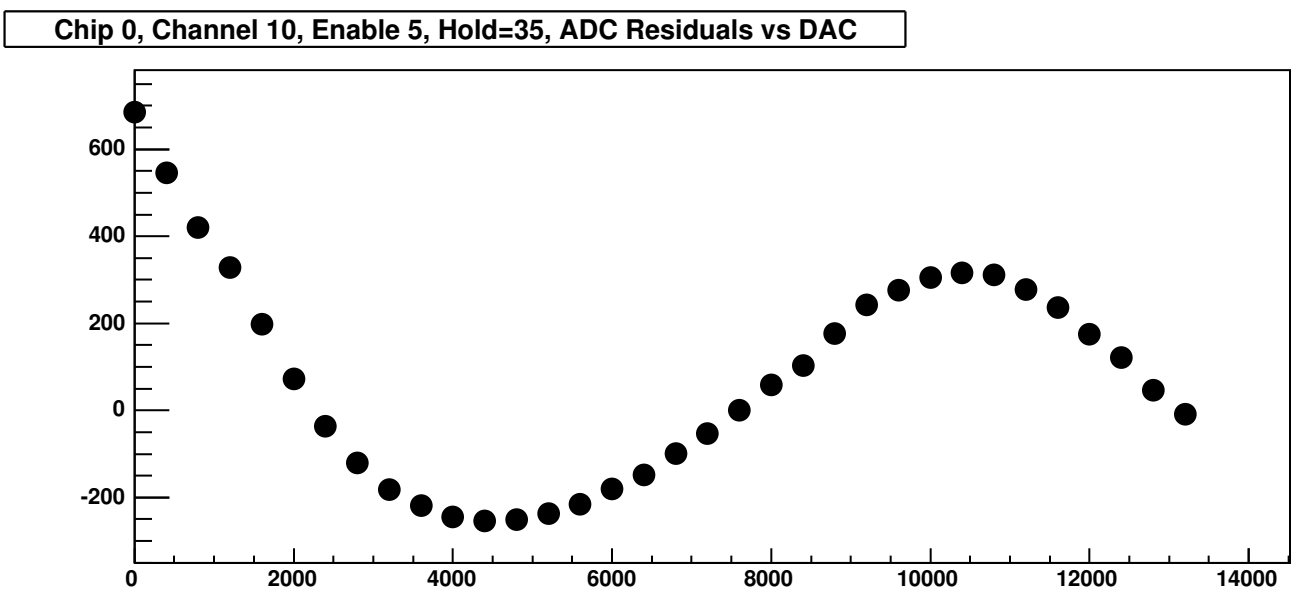

Chip 0, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 51.12 / 23
p0 -64.1 ± 0.7249
p1 0.01983 ± 0.0001211
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

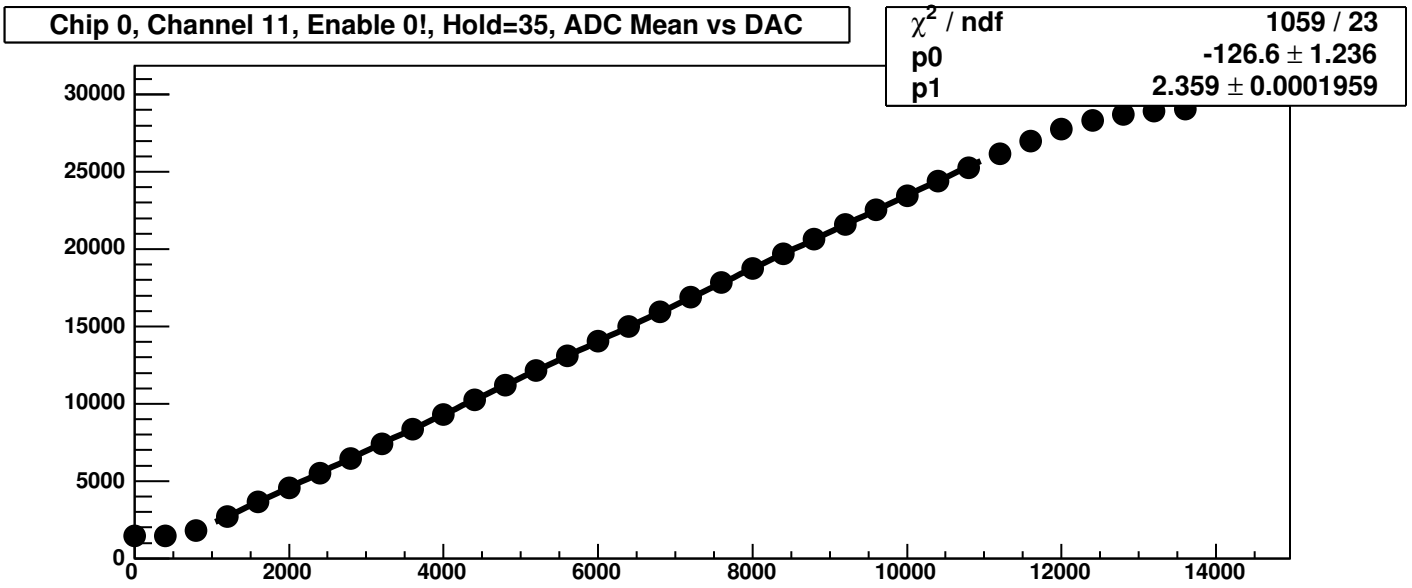

Chip 0, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

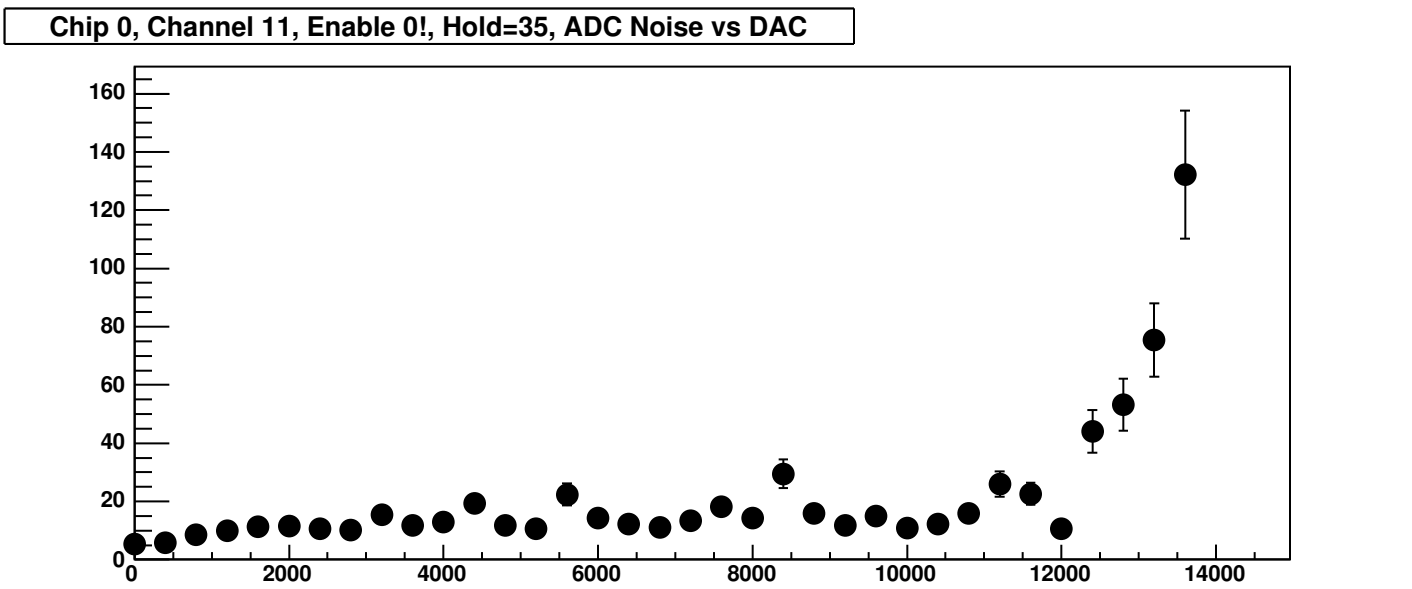

Chip 0, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

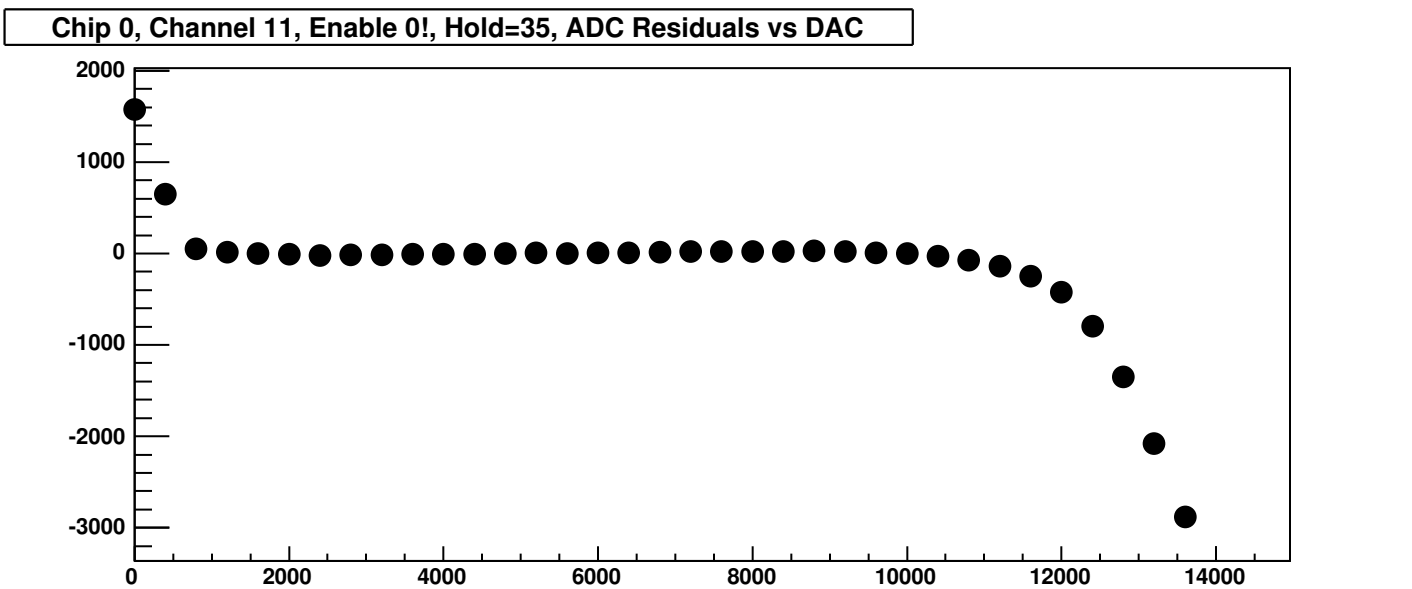

Chip 0, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.075e+05 / 23
p0 -651.1 ± 1.093
p1 0.3545 ± 0.0002206
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

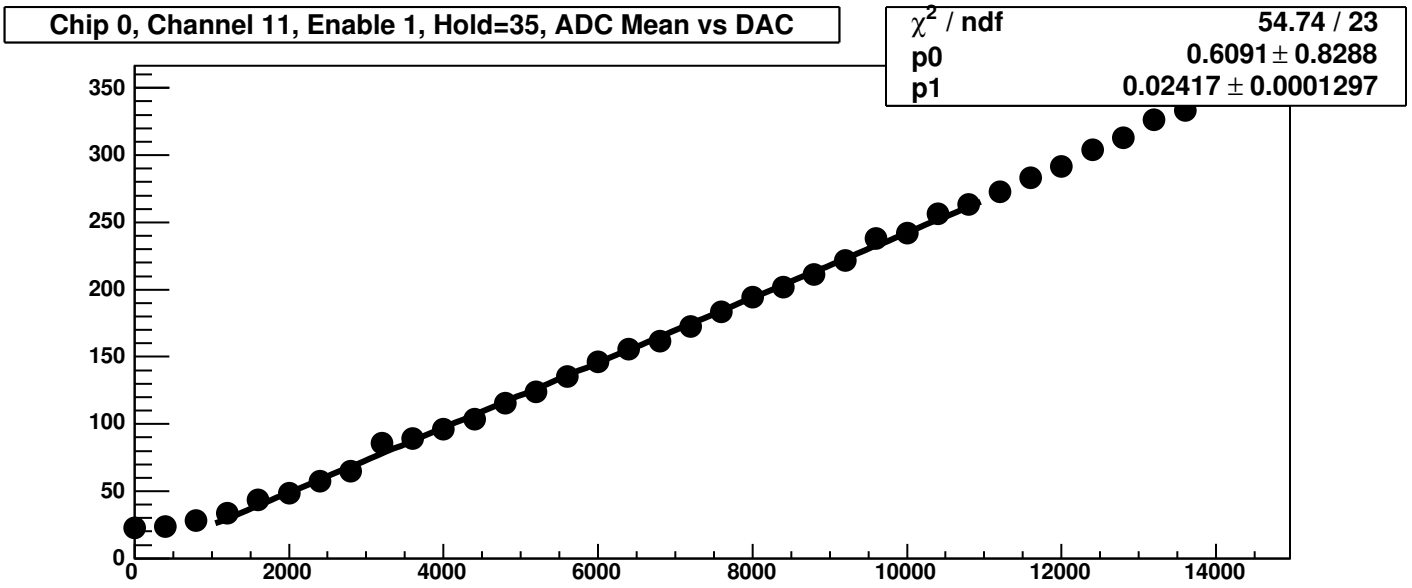

Chip 0, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

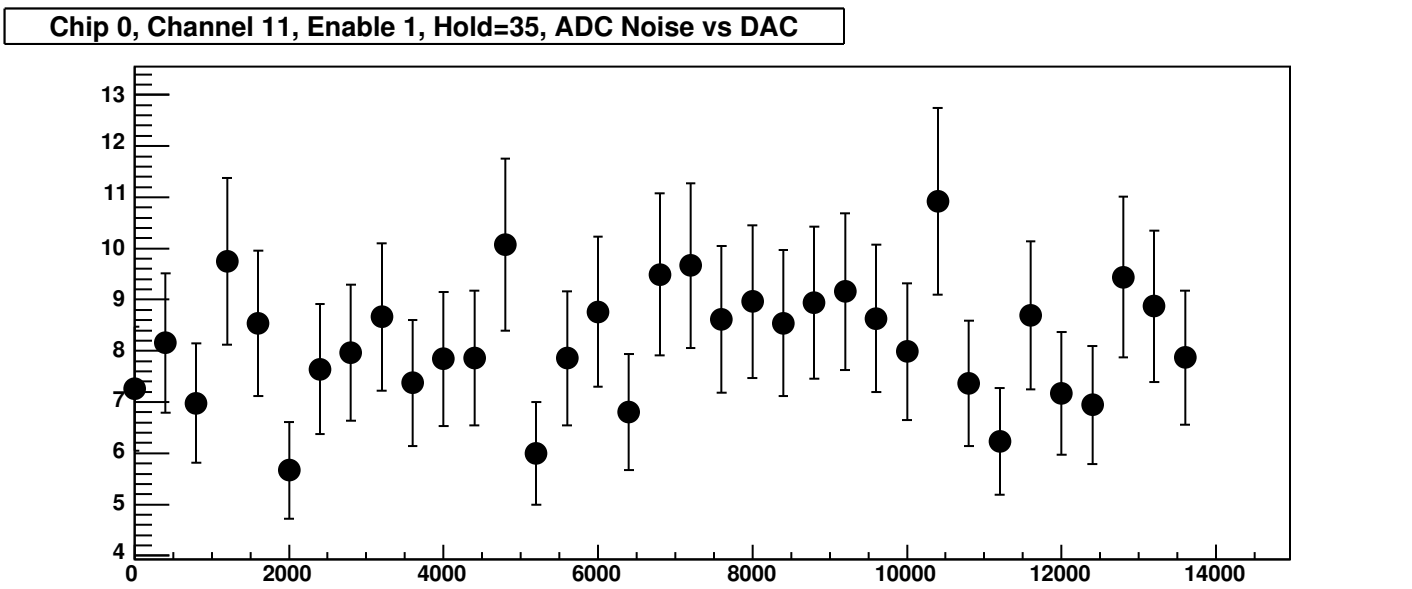

Chip 0, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

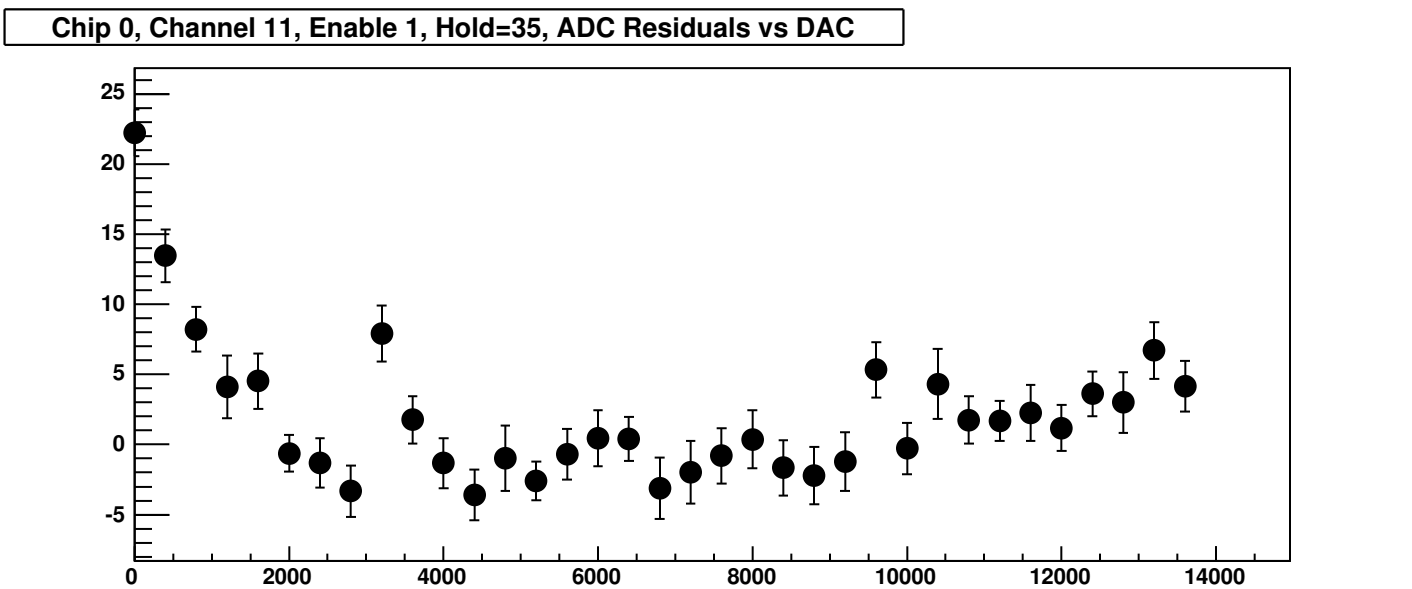

Chip 0, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1059 / 23
p0 -126.6 ± 1.236
p1 2.359 ± 0.0001959
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

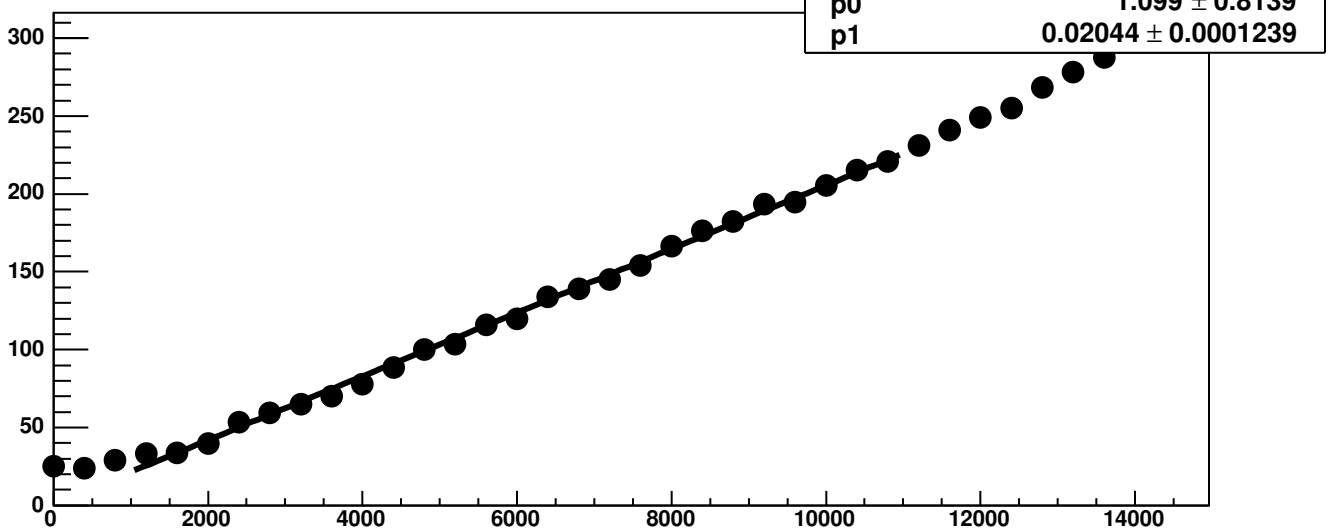

Chip 0, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 54.74 / 23
p0 0.6091 ± 0.8288
p1 0.02417 ± 0.0001297
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

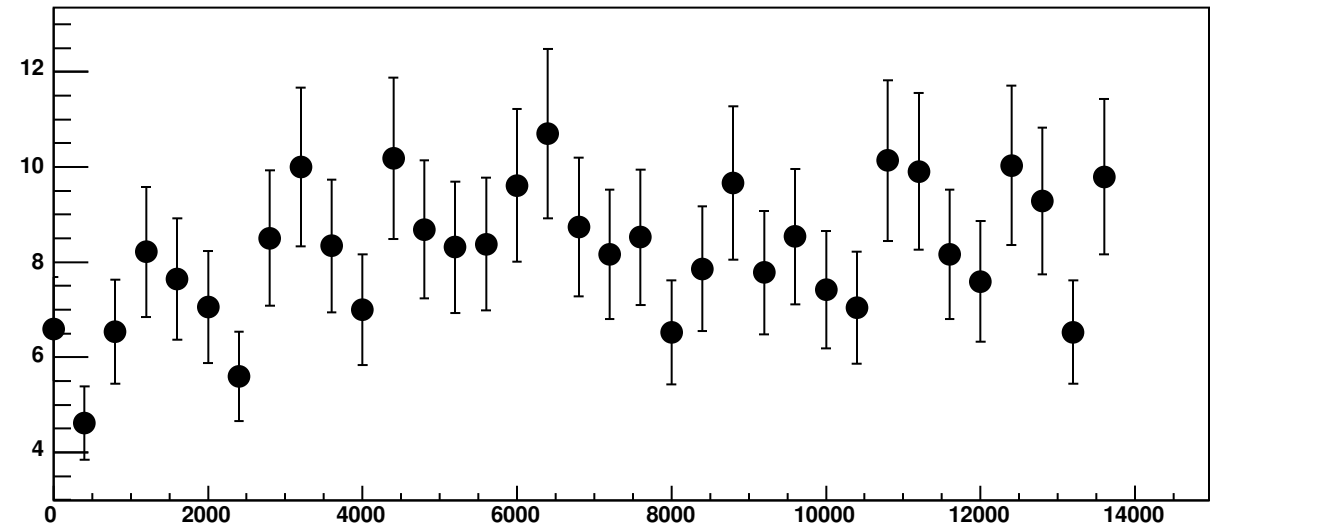

Chip 0, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

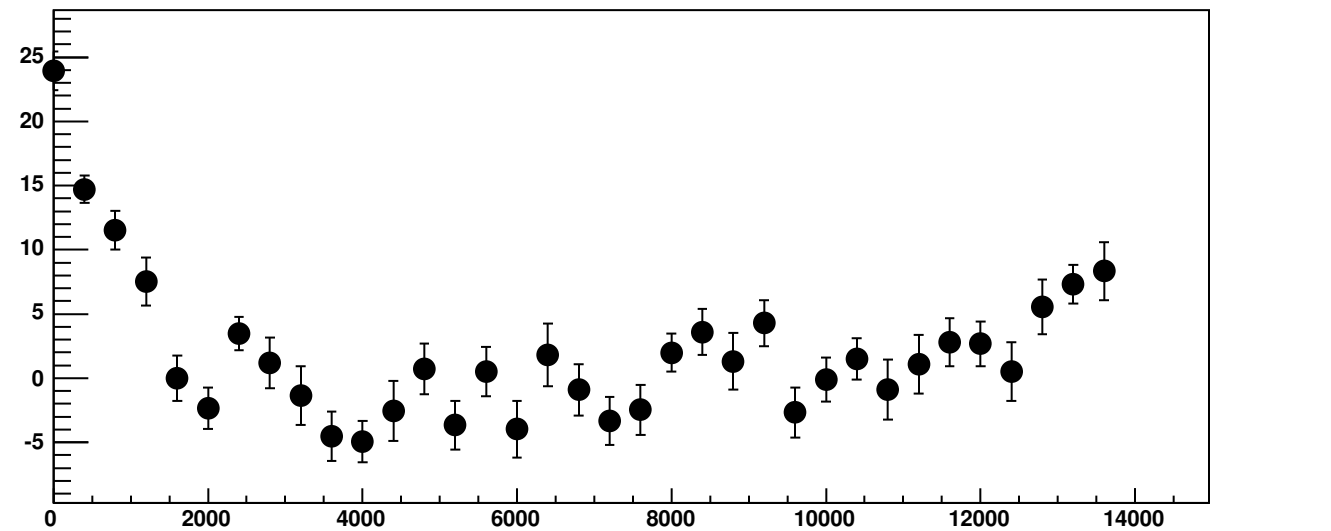
Chip 0, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC



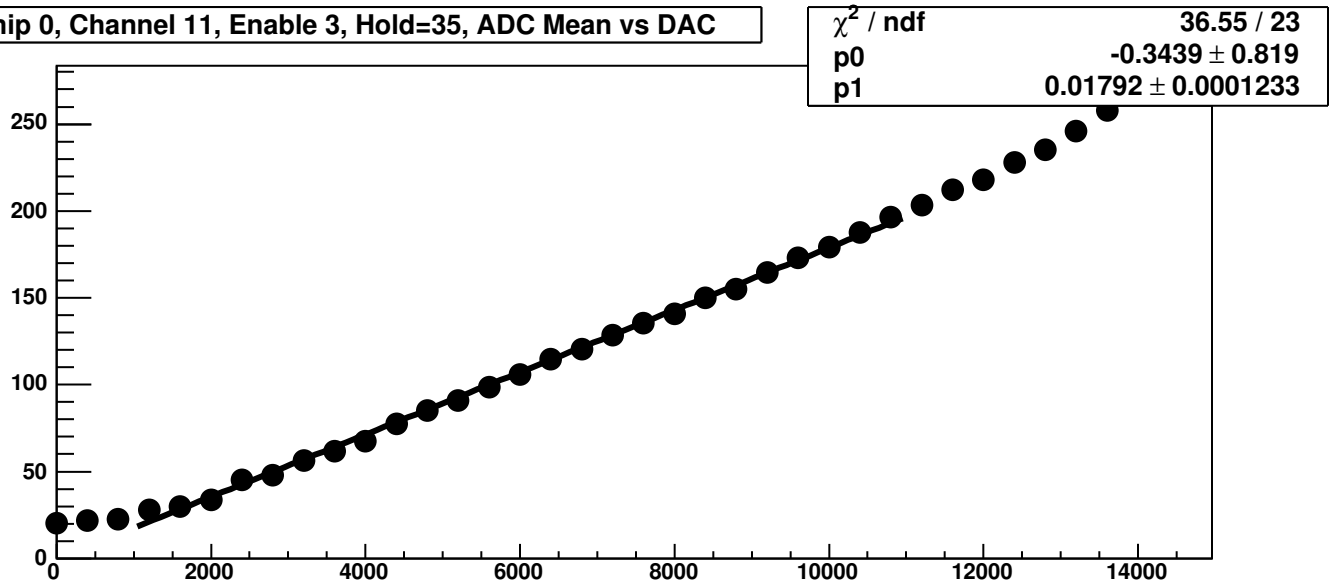
Chip 0, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC



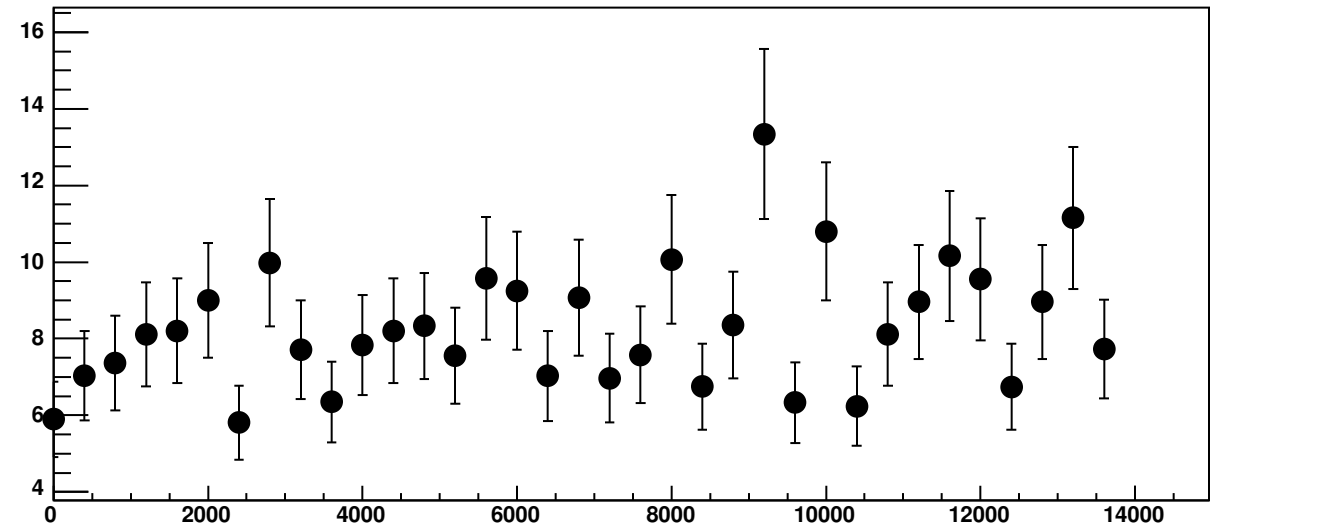
Chip 0, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC



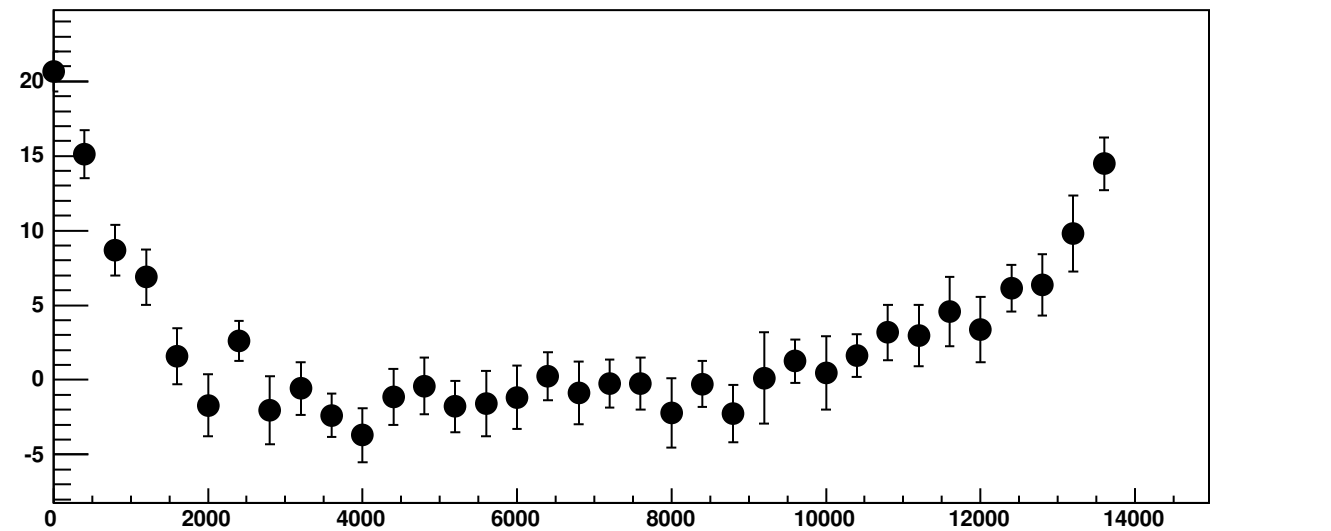
Chip 0, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

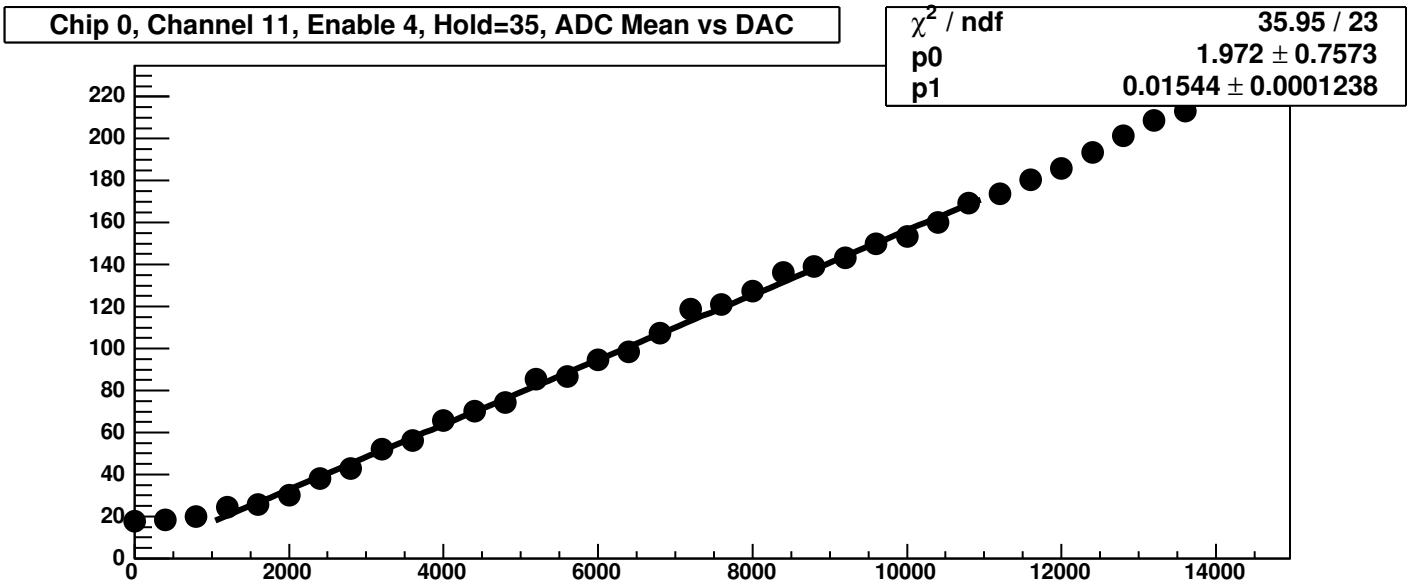


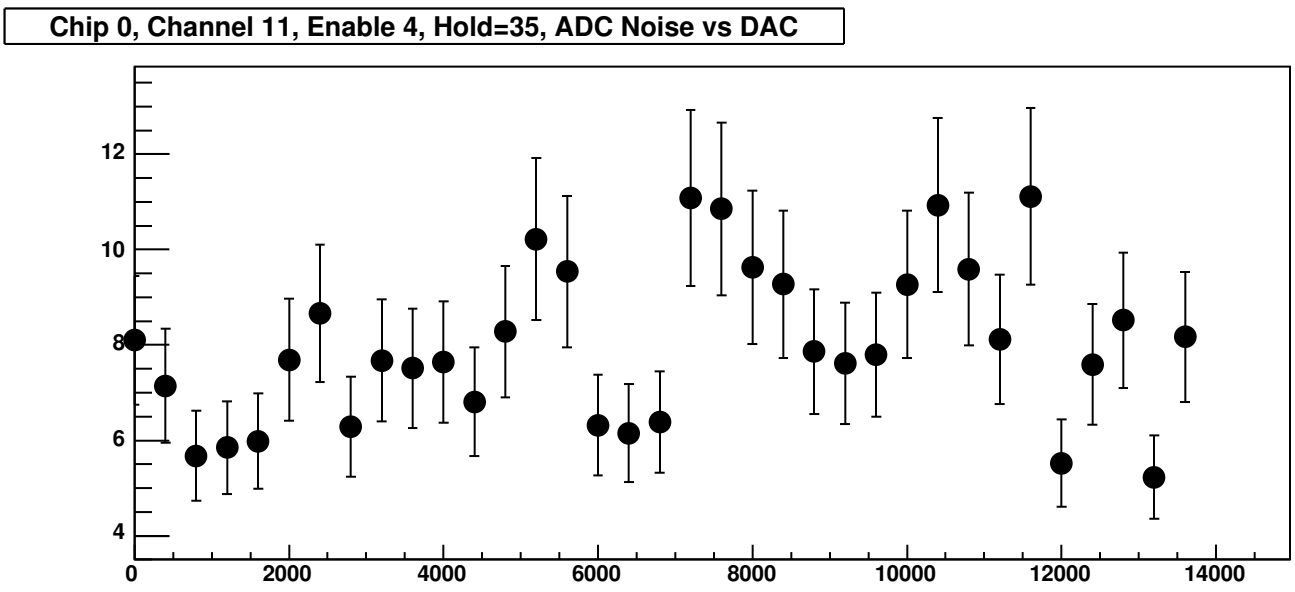
Chip 0, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC

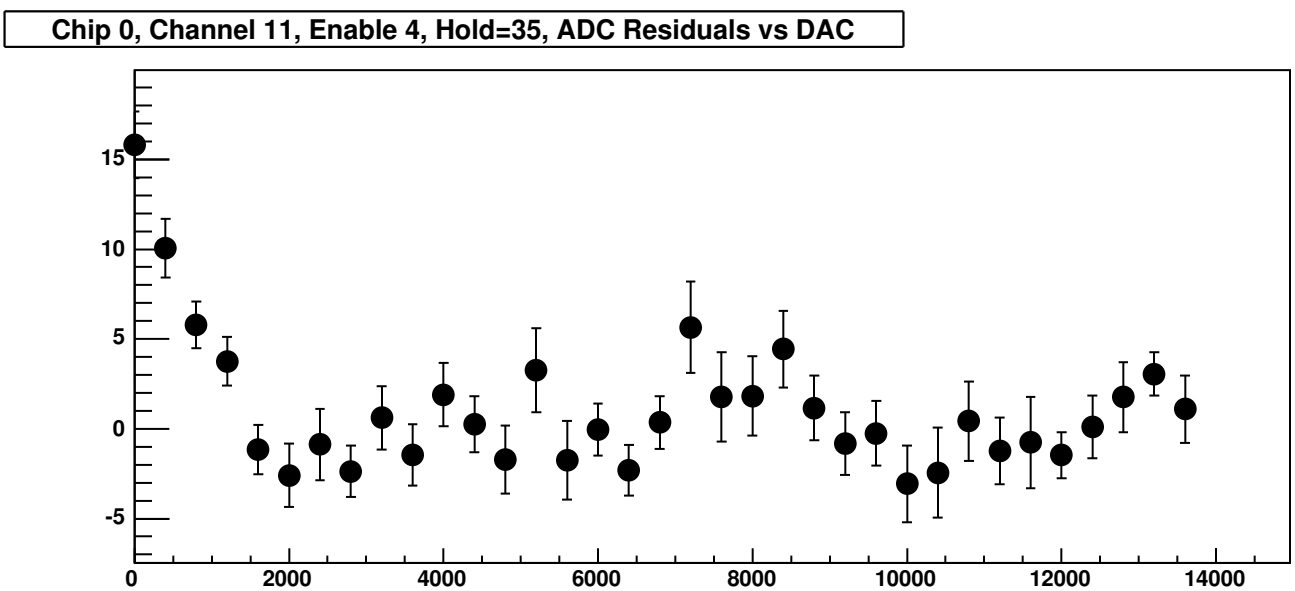


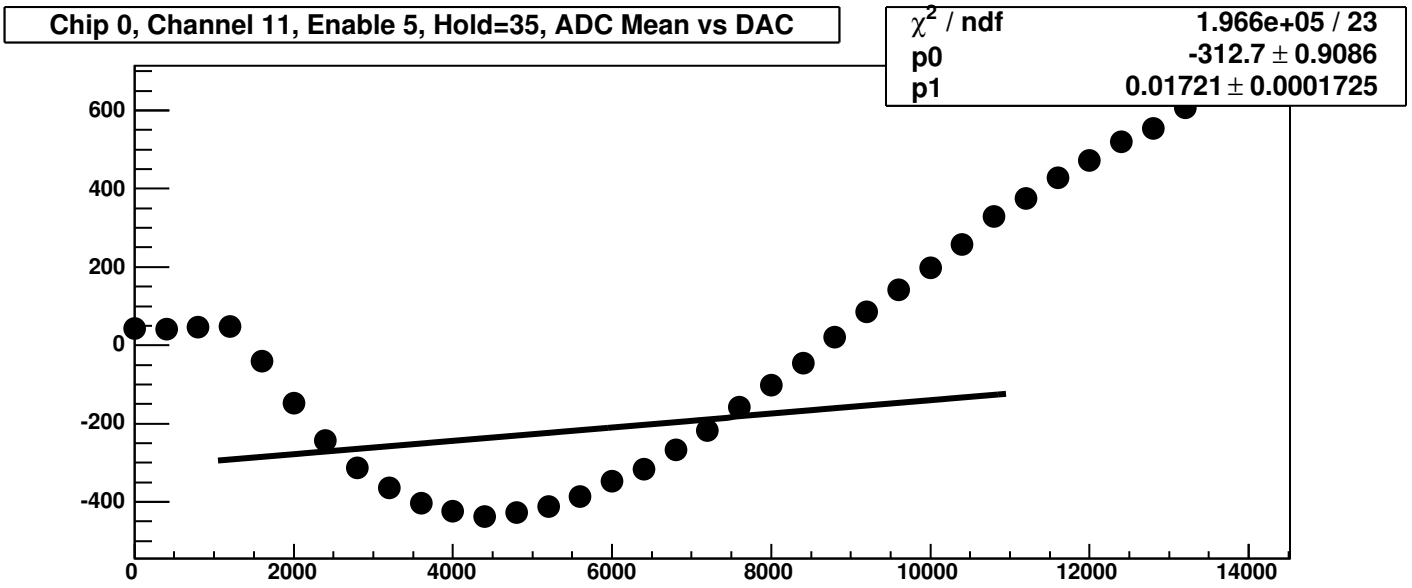
Chip 0, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC

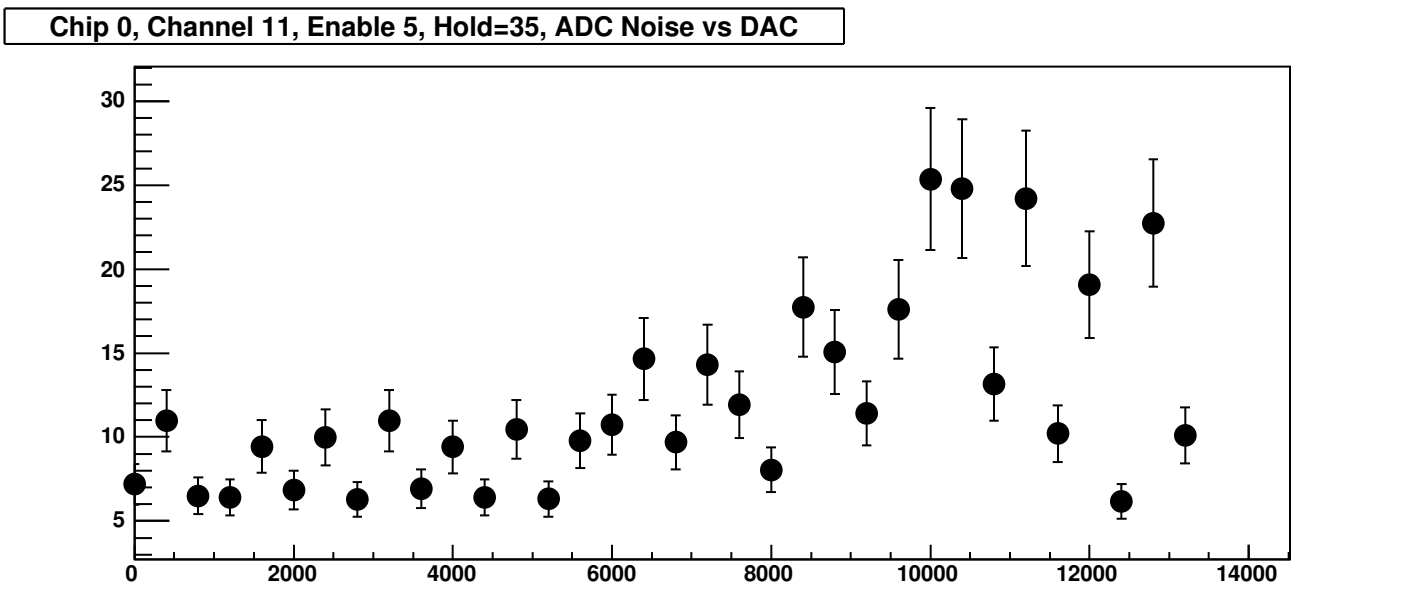


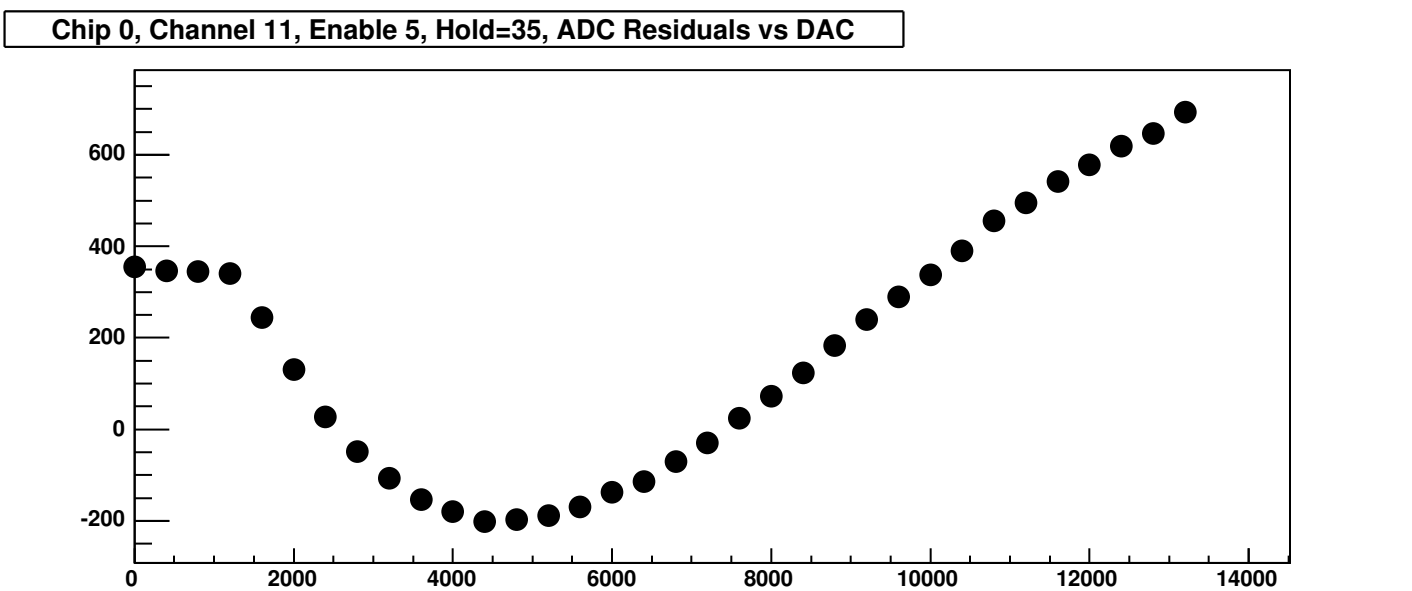

Chip 0, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 35.95 / 23
p0 1.972 ± 0.7573
p1 0.01544 ± 0.0001238
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

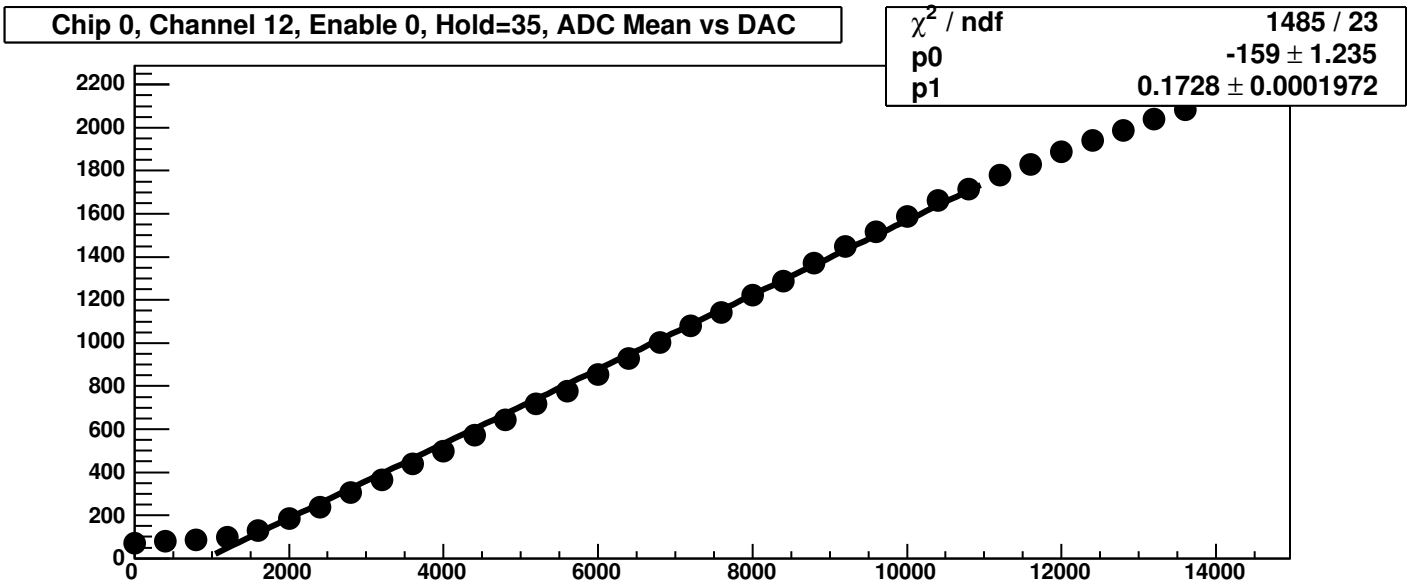

Chip 0, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

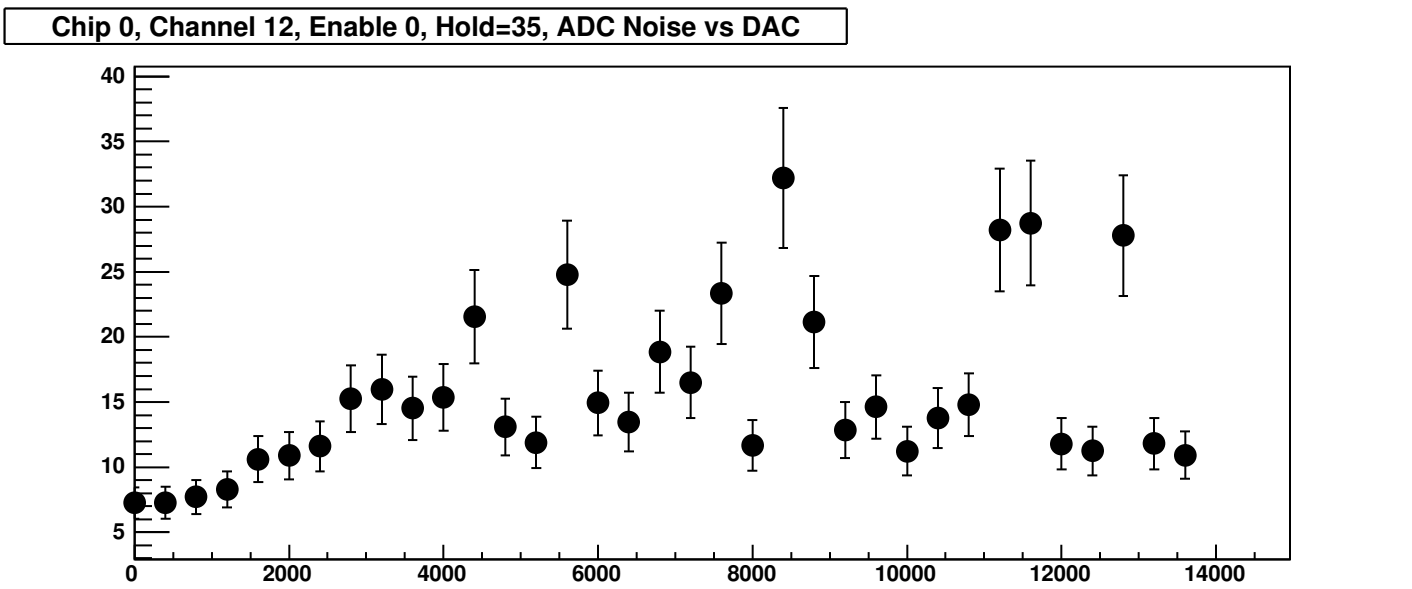

Chip 0, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

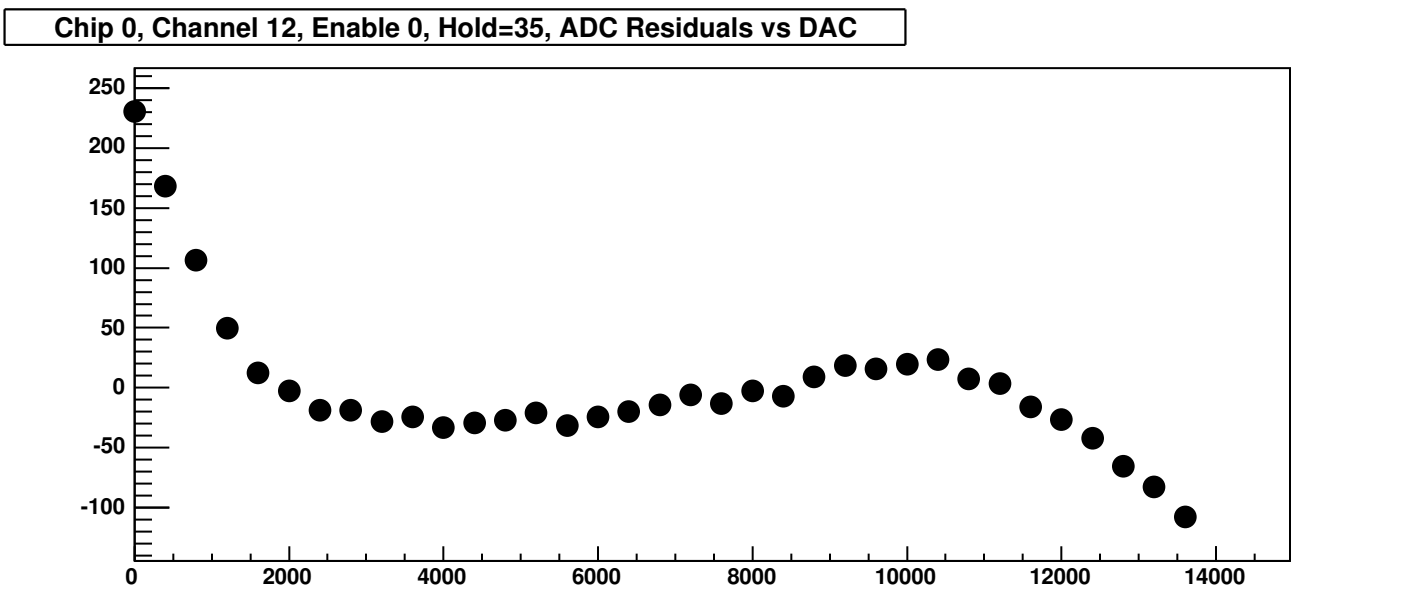

Chip 0, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.966e+05 / 23
p0 -312.7 ± 0.9086
p1 0.01721 ± 0.0001725
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

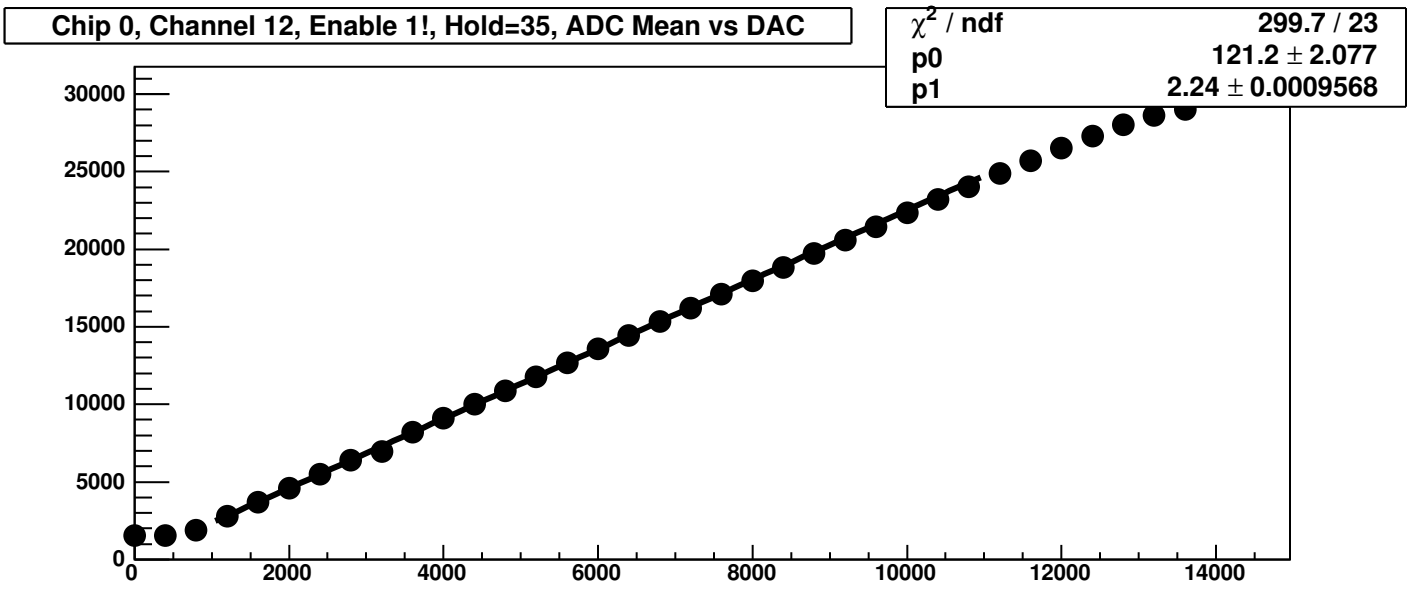

Chip 0, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

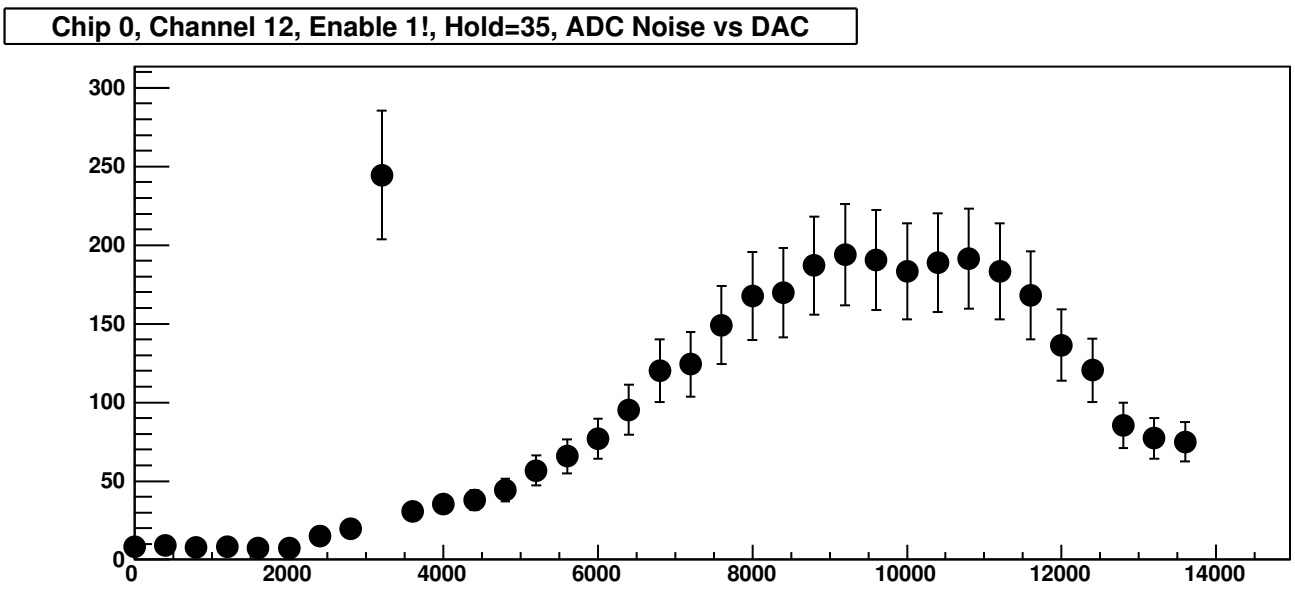

Chip 0, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

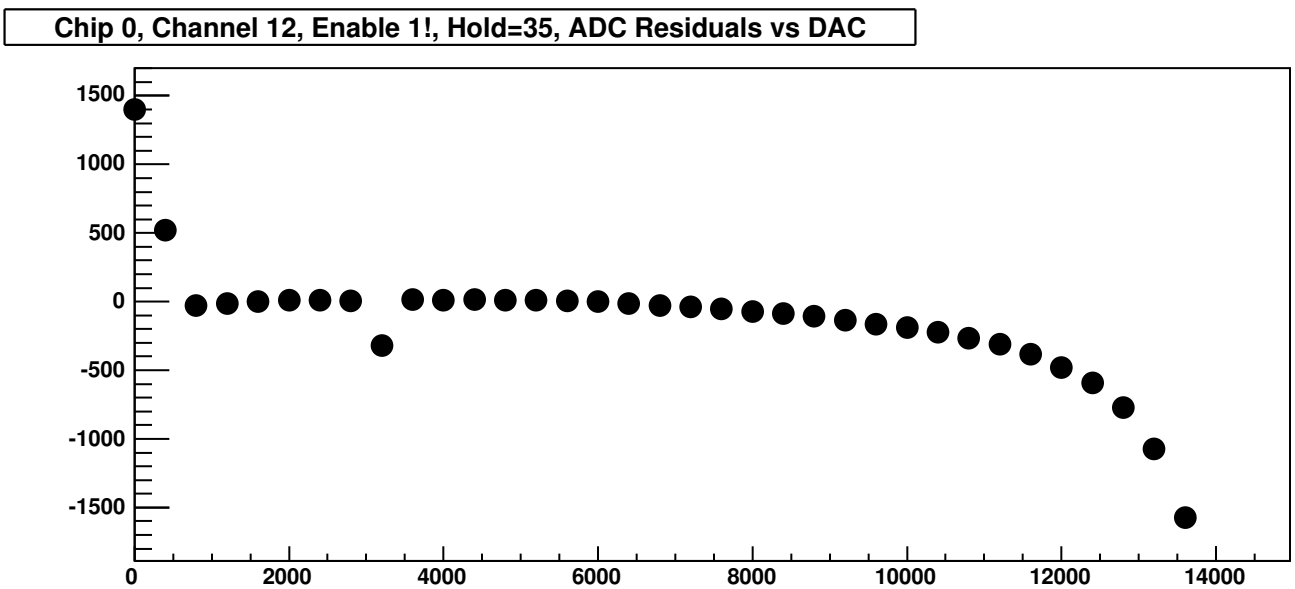

Chip 0, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1485 / 23
p0 -159 ± 1.235
p1 0.1728 ± 0.0001972
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

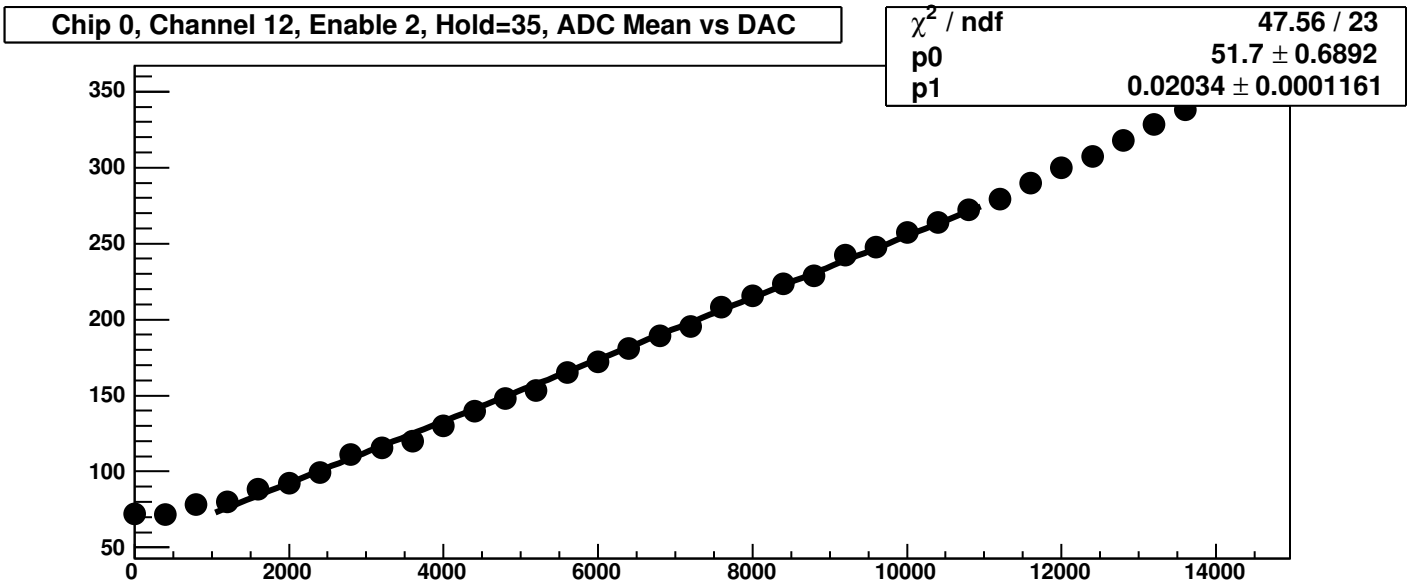

Chip 0, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

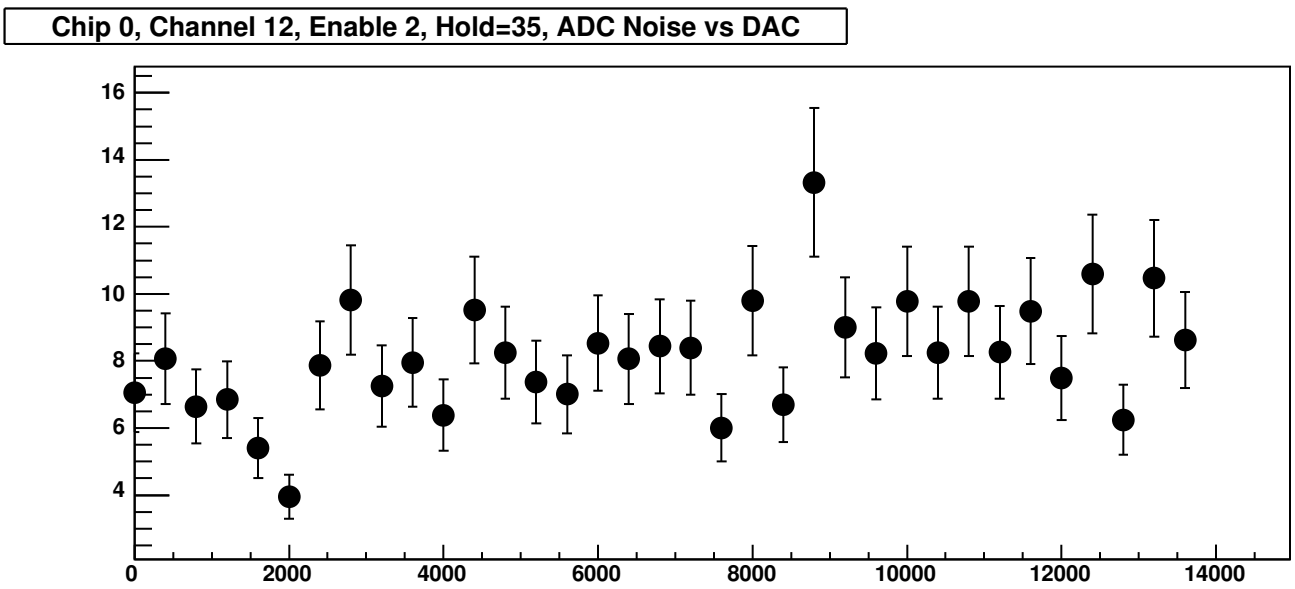

Chip 0, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

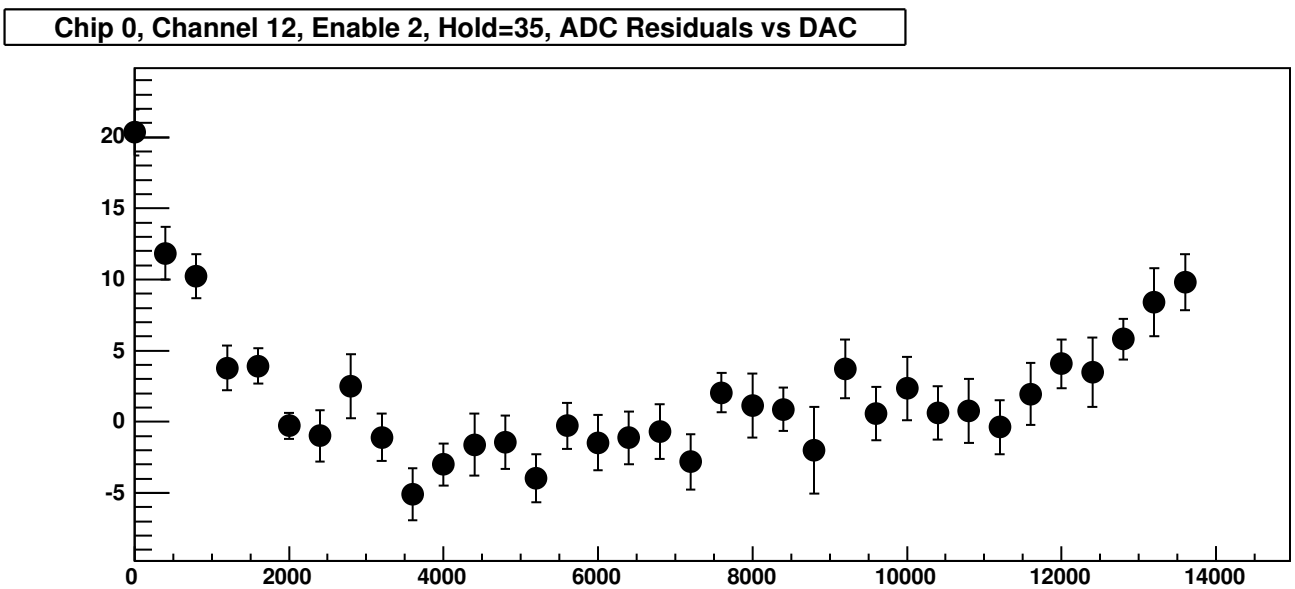

Chip 0, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 299.7 / 23
p0 121.2 ± 2.077
p1 2.24 ± 0.0009568
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

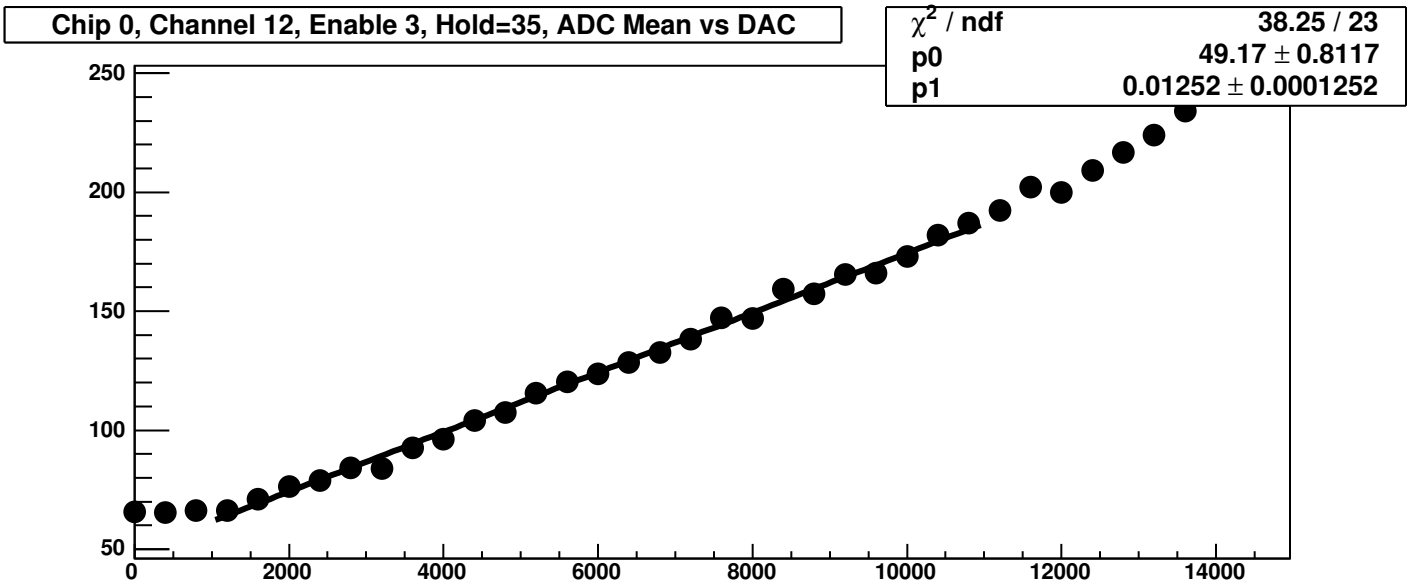

Chip 0, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

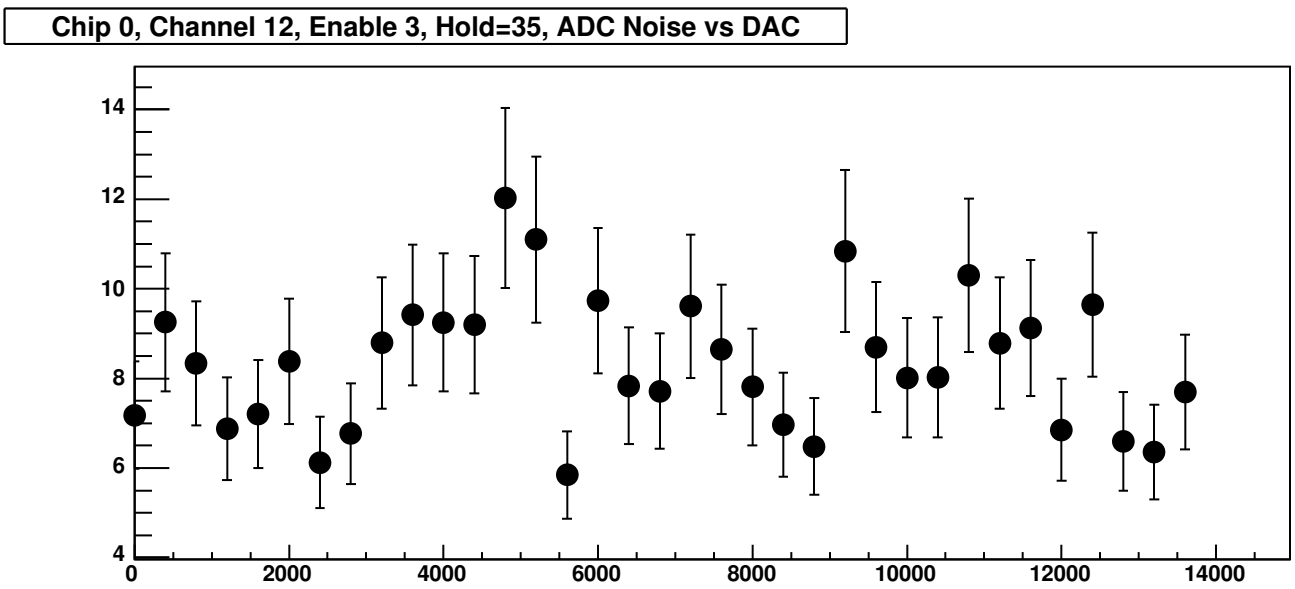

Chip 0, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

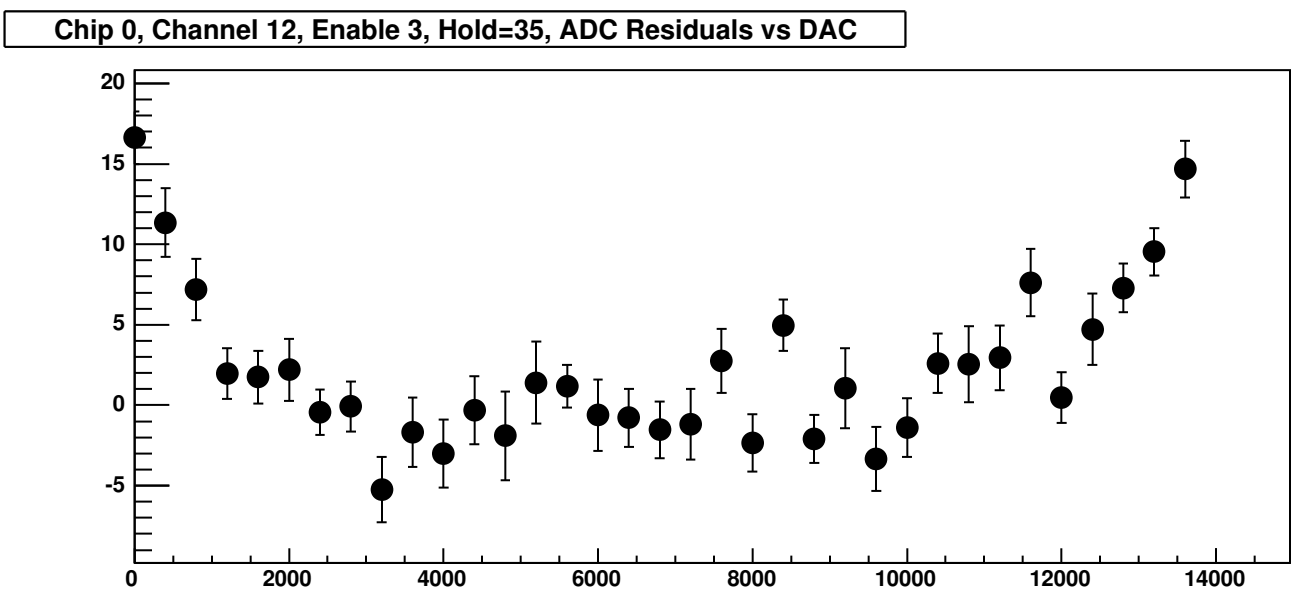

Chip 0, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 47.56 / 23
p0 51.7 ± 0.6892
p1 0.02034 ± 0.0001161
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

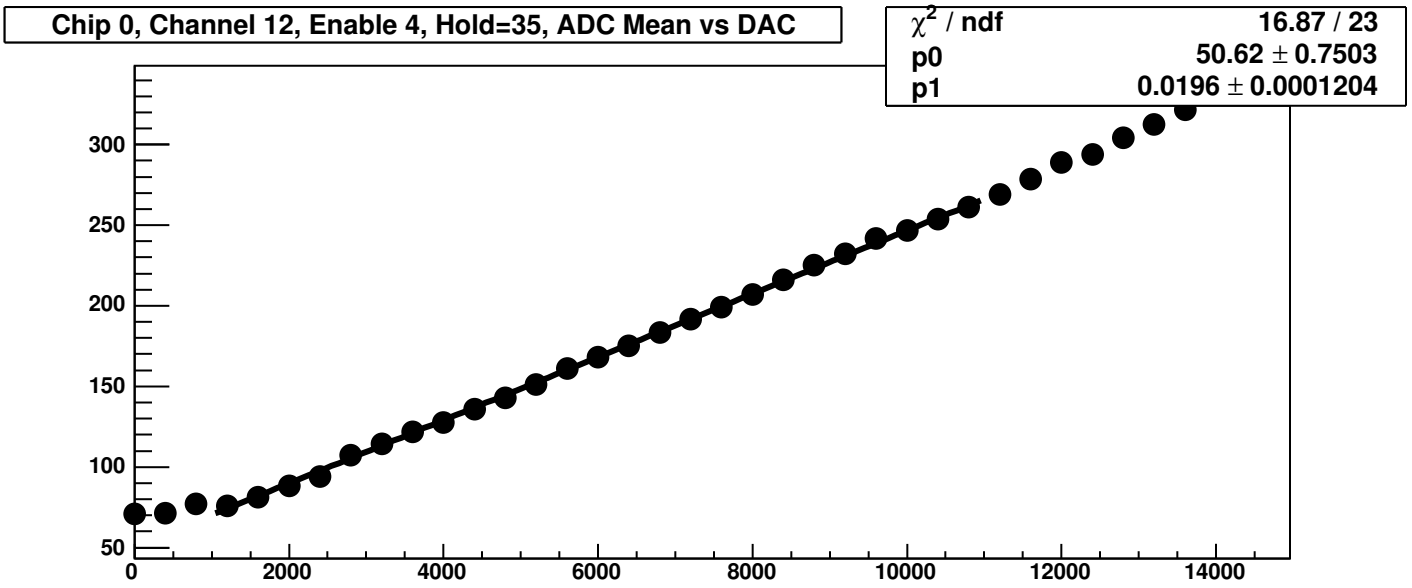

Chip 0, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

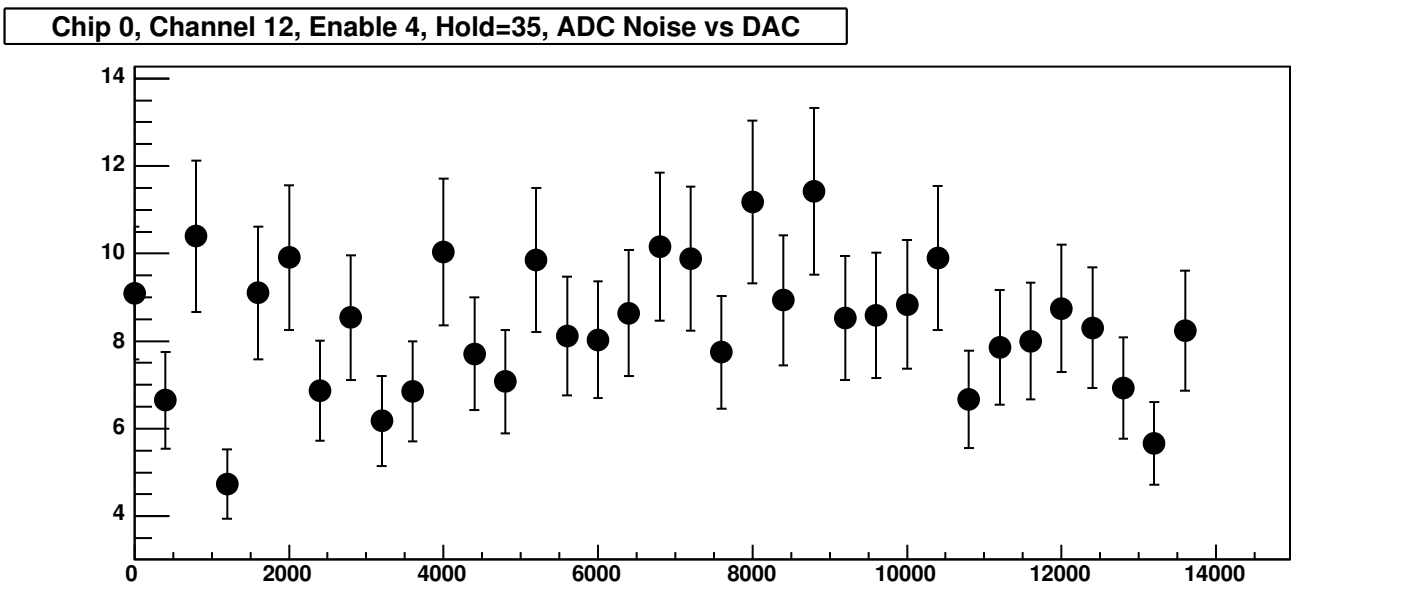

Chip 0, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

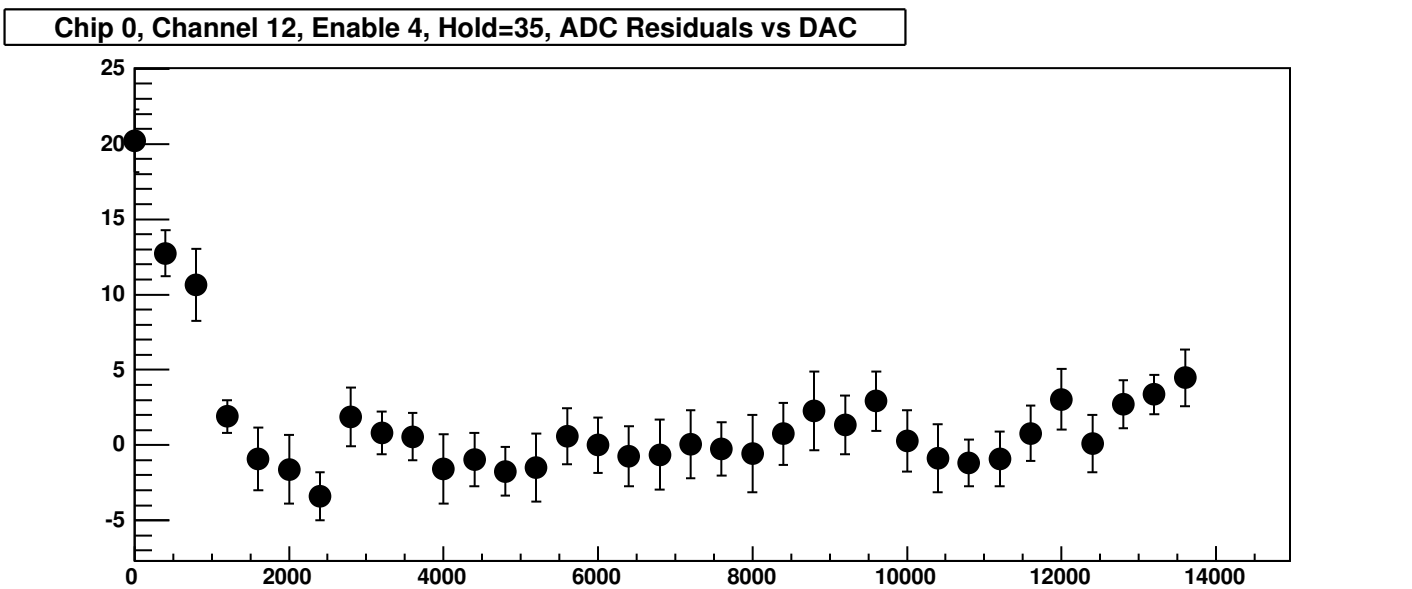

Chip 0, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.25 / 23
p0 49.17 ± 0.8117
p1 0.01252 ± 0.0001252
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

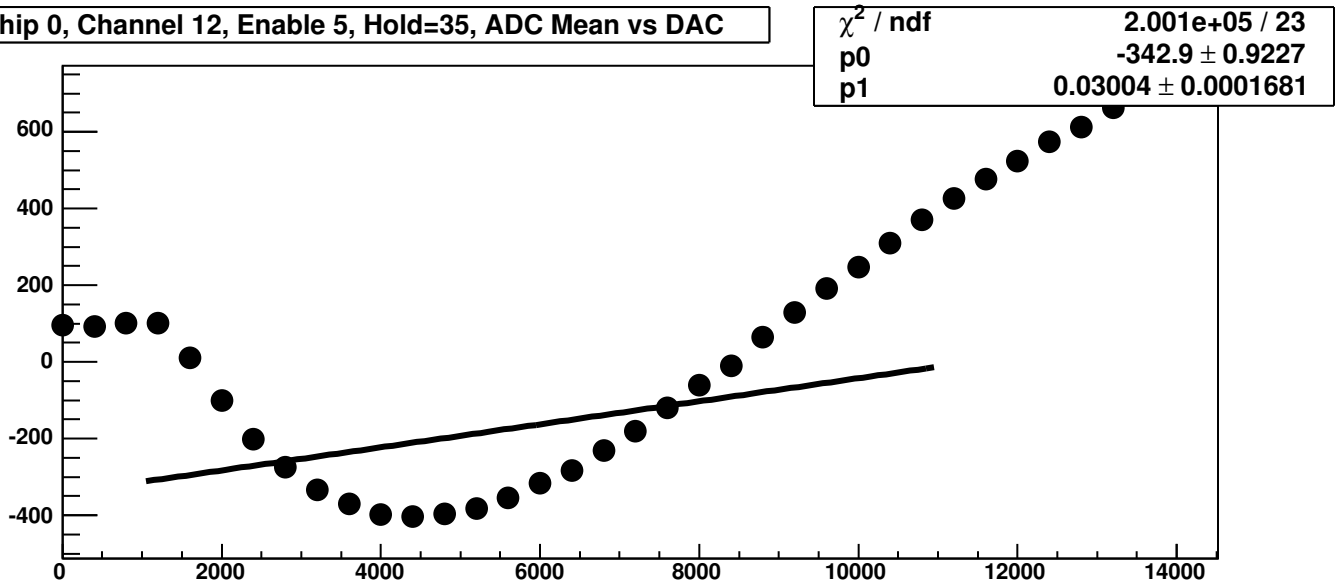

Chip 0, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 16.87 / 23
p0 50.62 ± 0.7503
p1 0.0196 ± 0.0001204
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

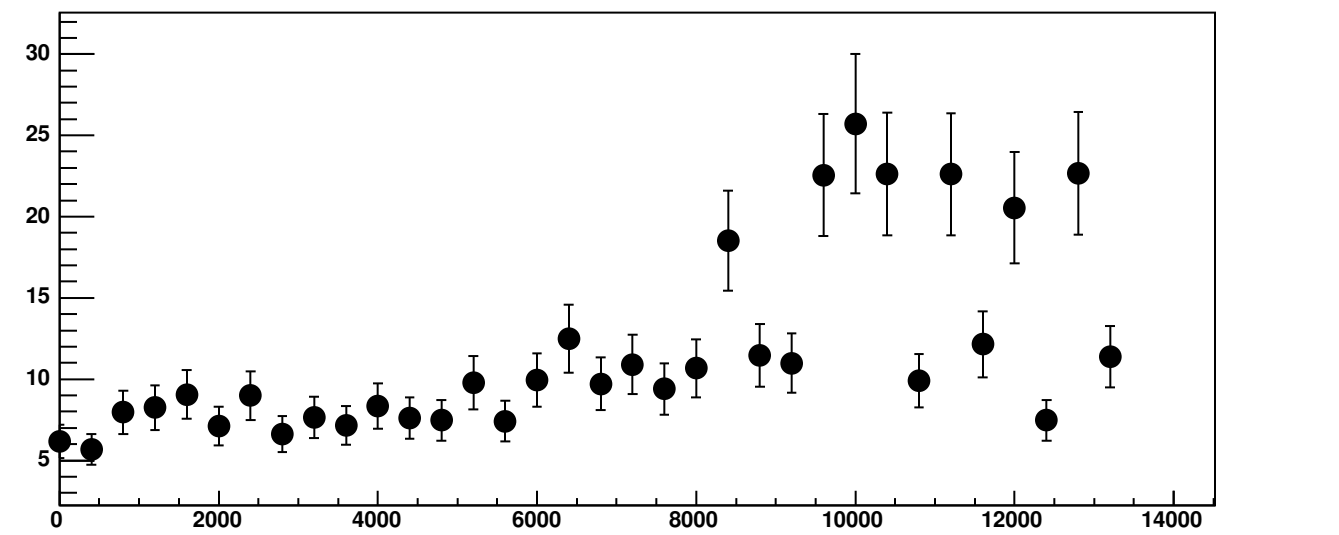

Chip 0, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

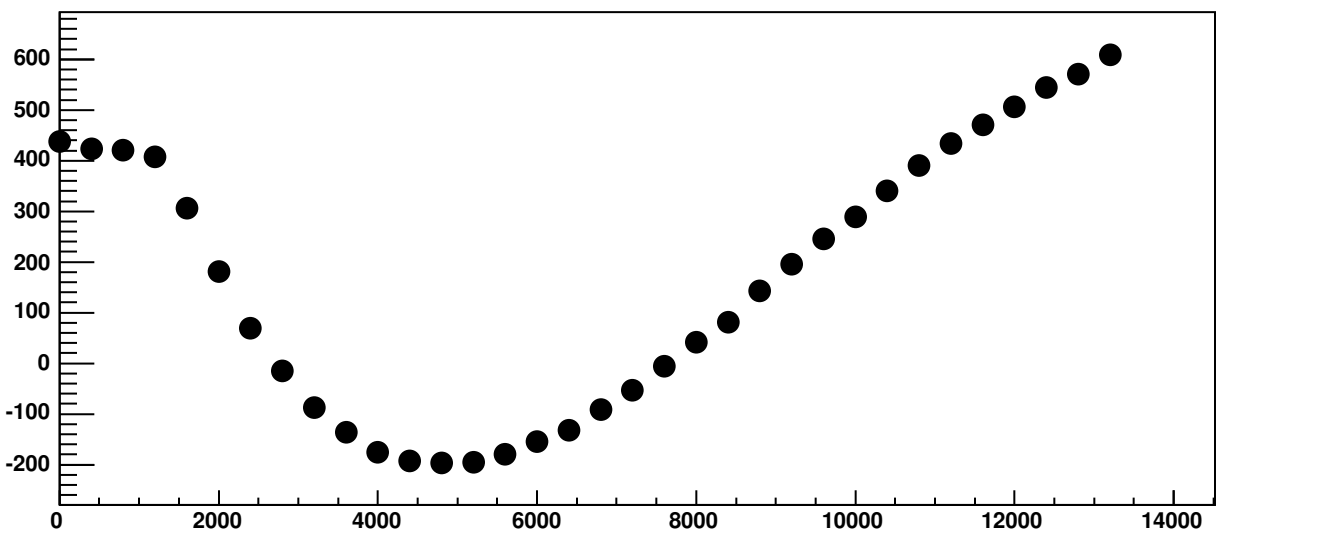
Chip 0, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

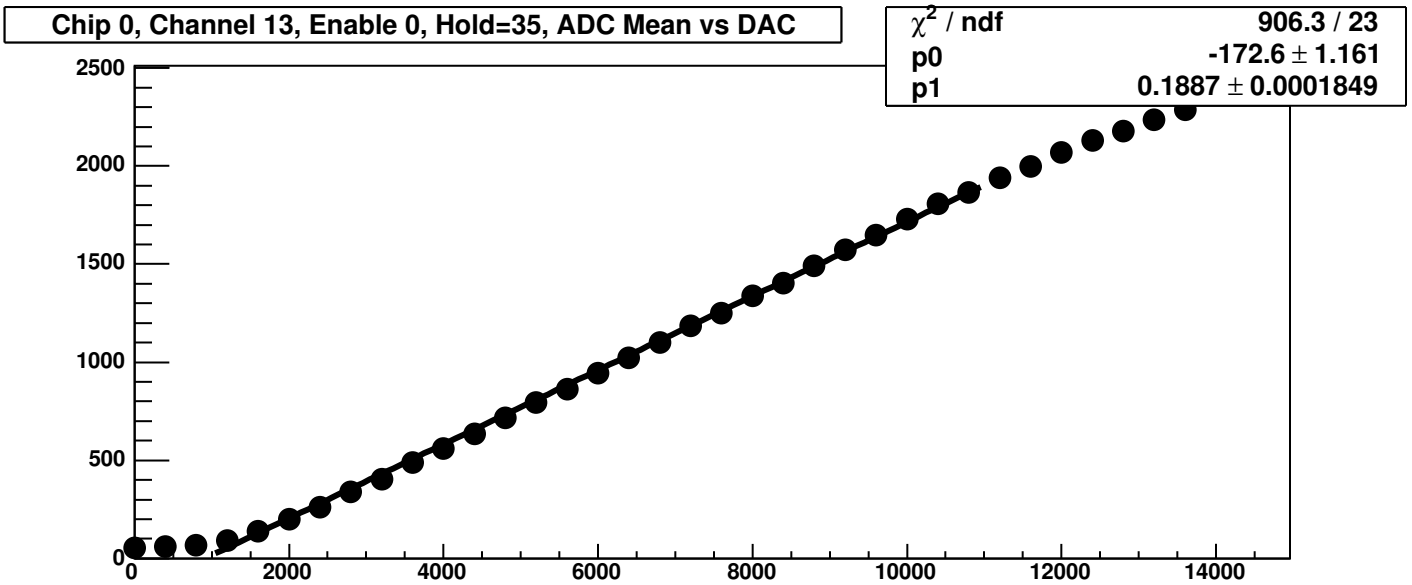


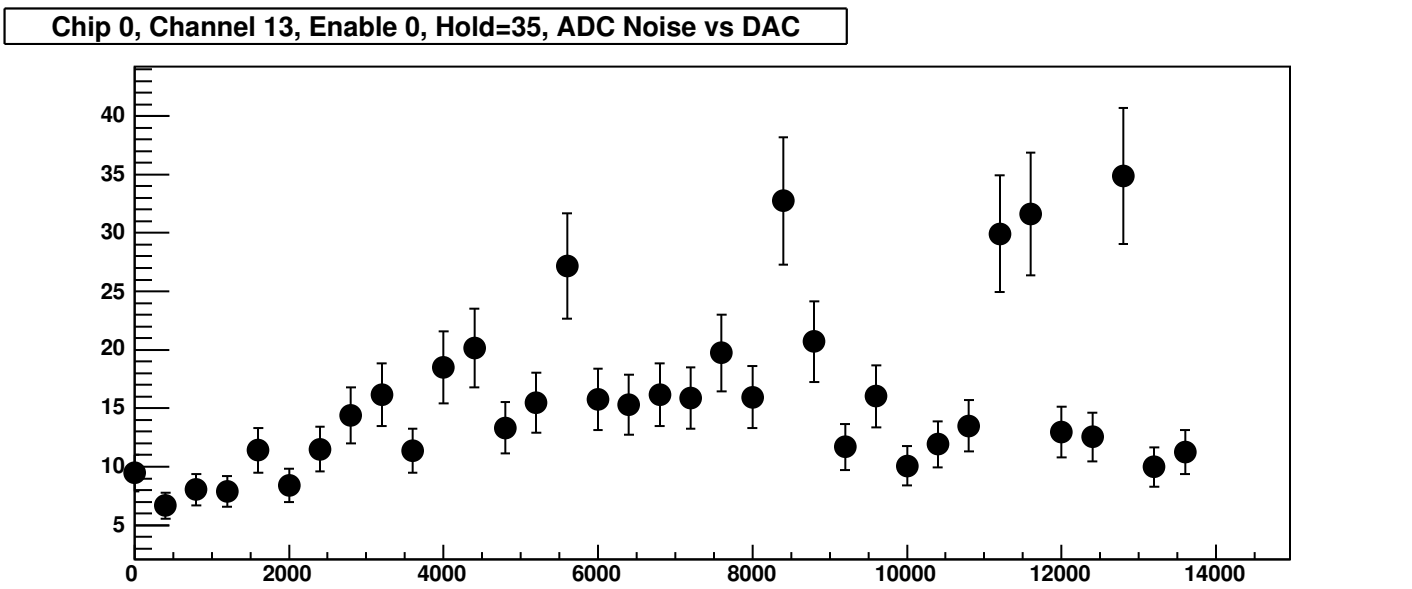
Chip 0, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

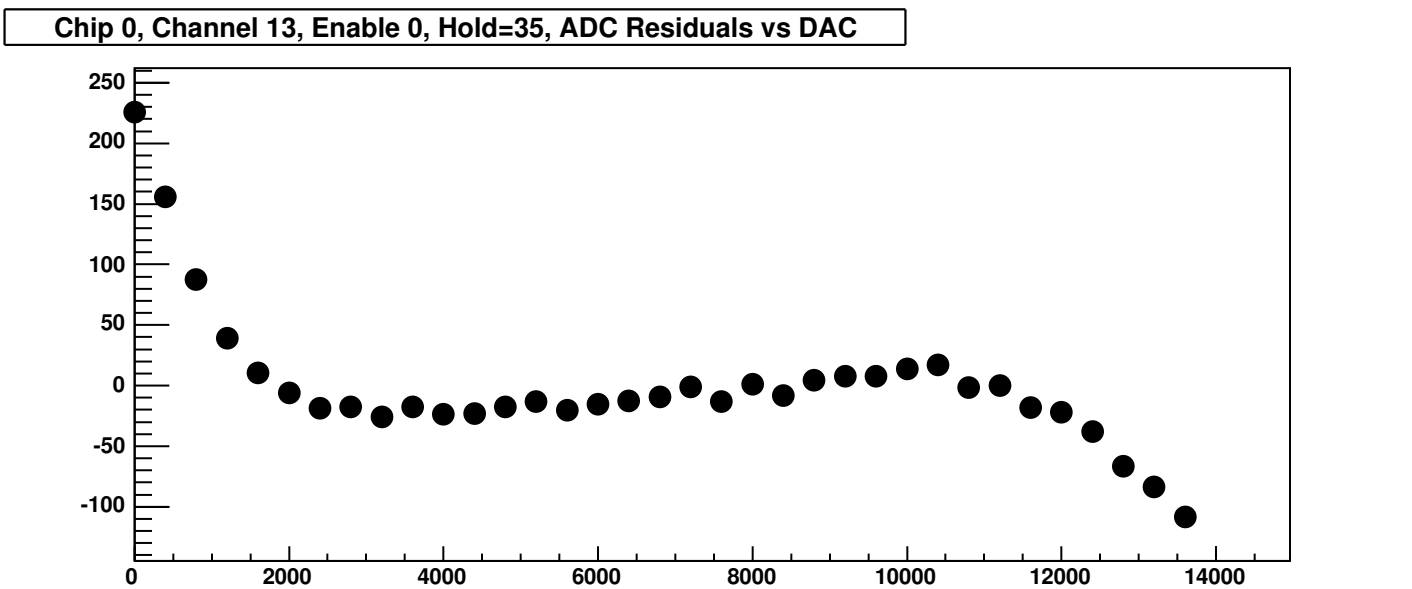


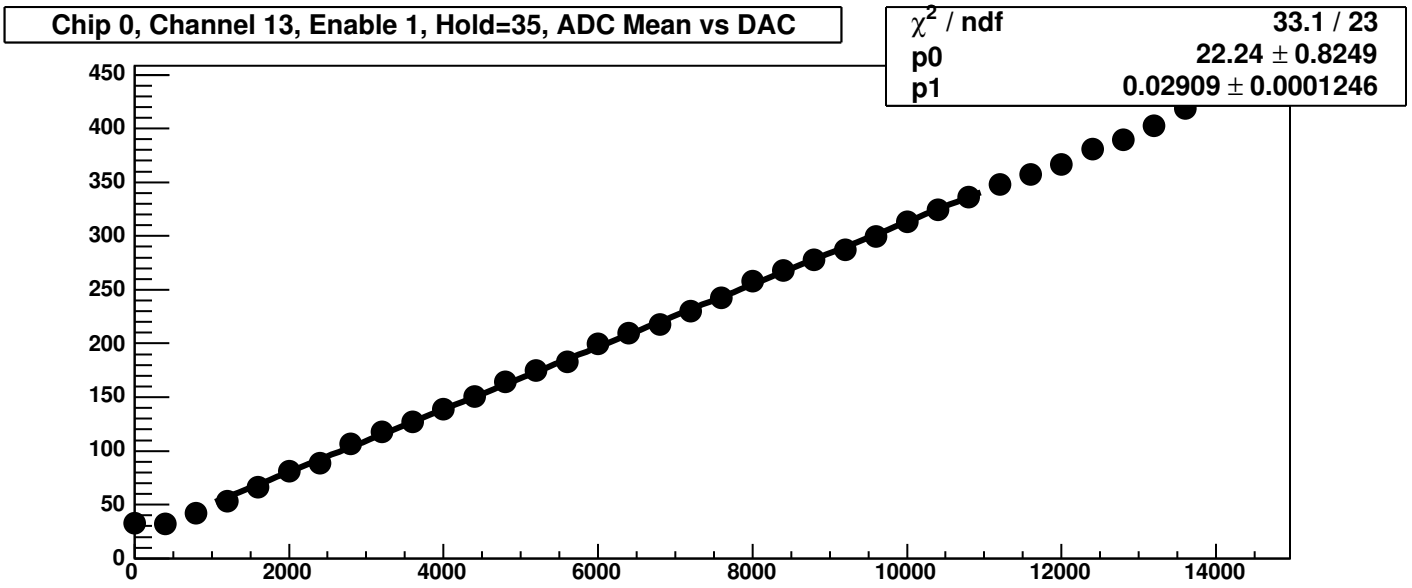
Chip 0, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

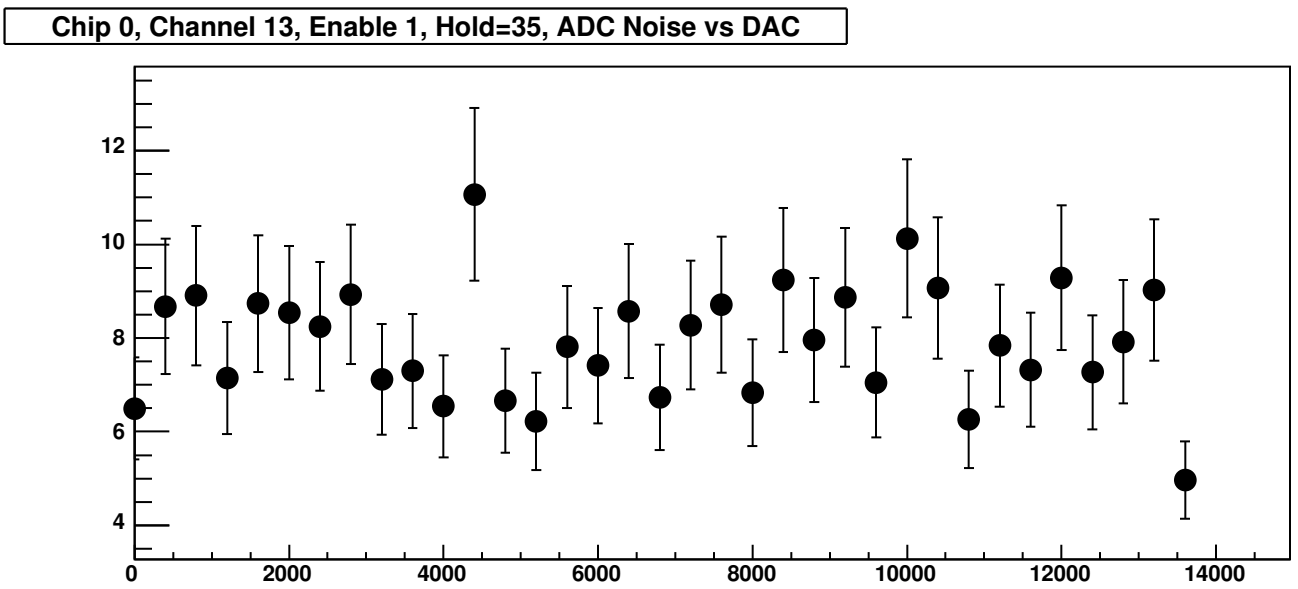


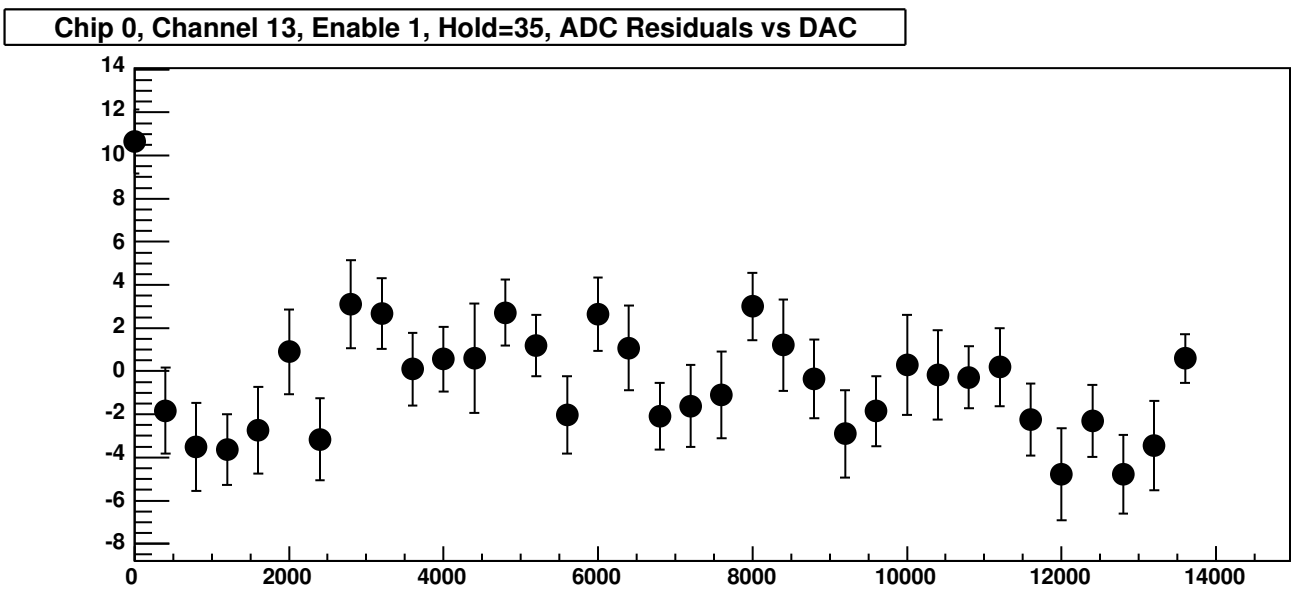

Chip 0, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 906.3 / 23
p0 -172.6 ± 1.161
p1 0.1887 ± 0.0001849
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

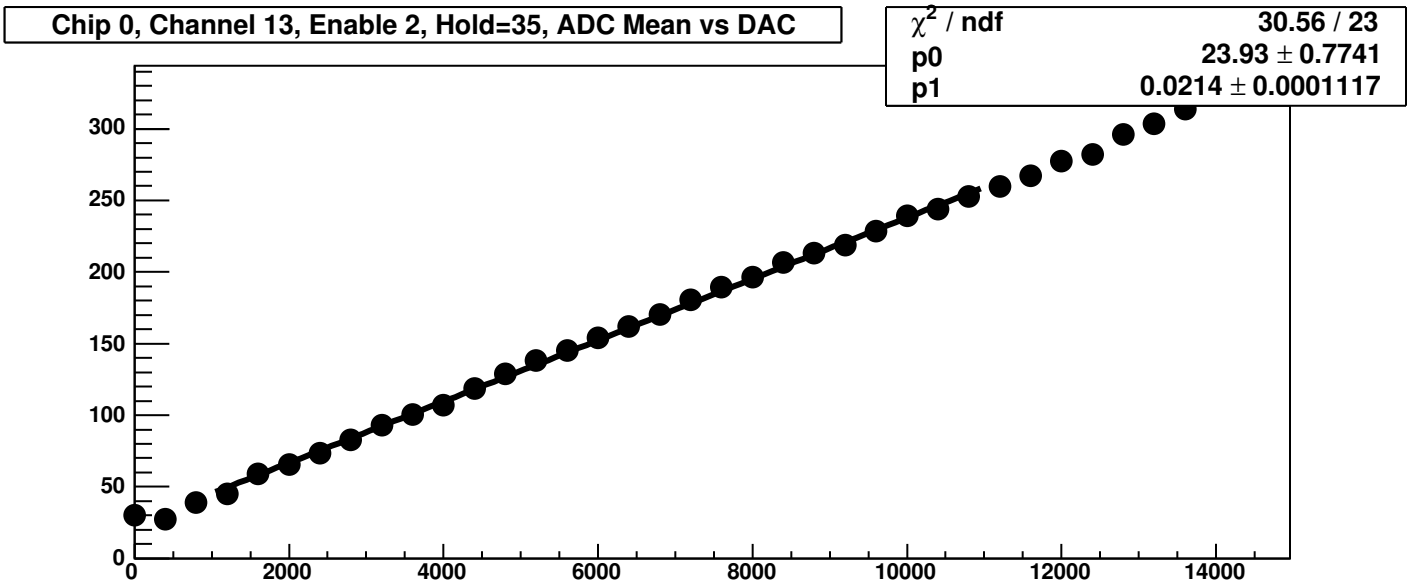

Chip 0, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

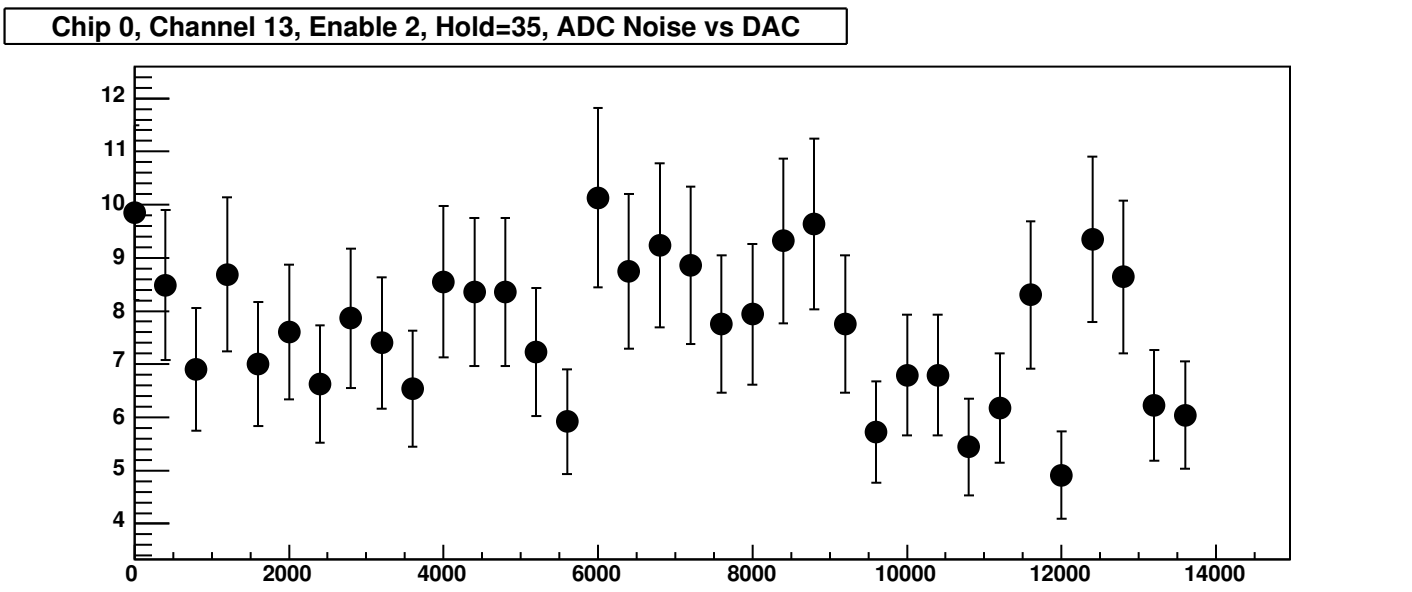

Chip 0, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

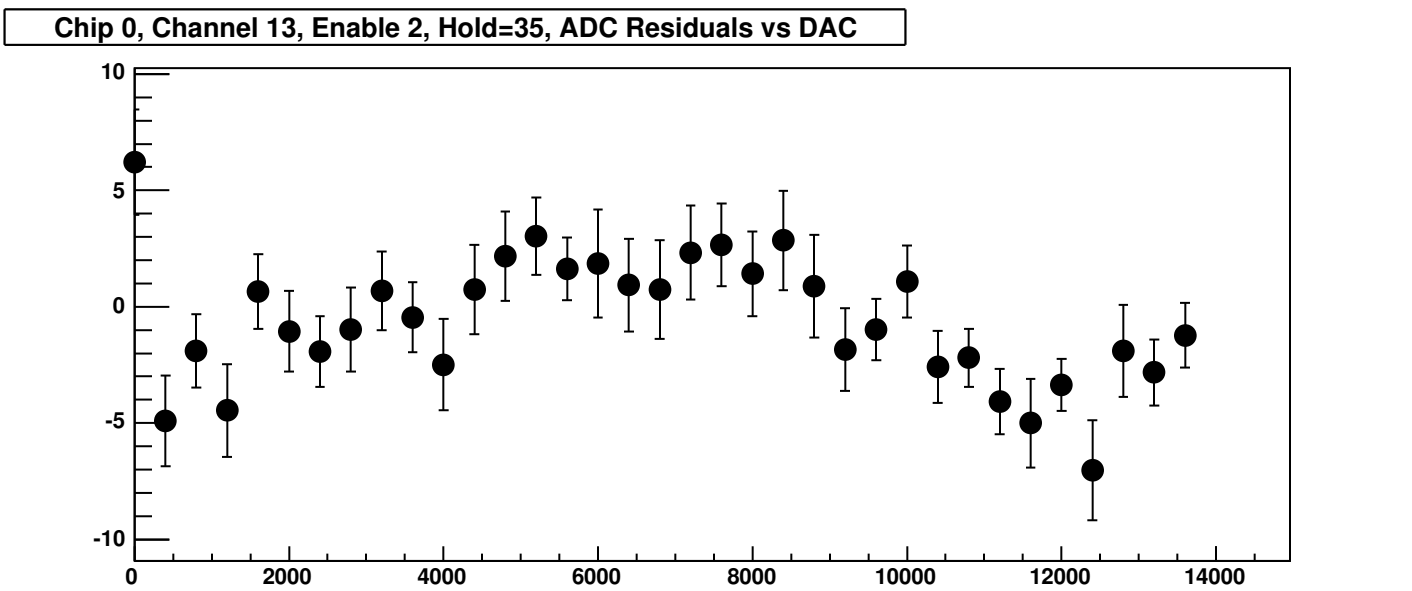

Chip 0, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.1 / 23
p0 22.24 ± 0.8249
p1 0.02909 ± 0.0001246
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

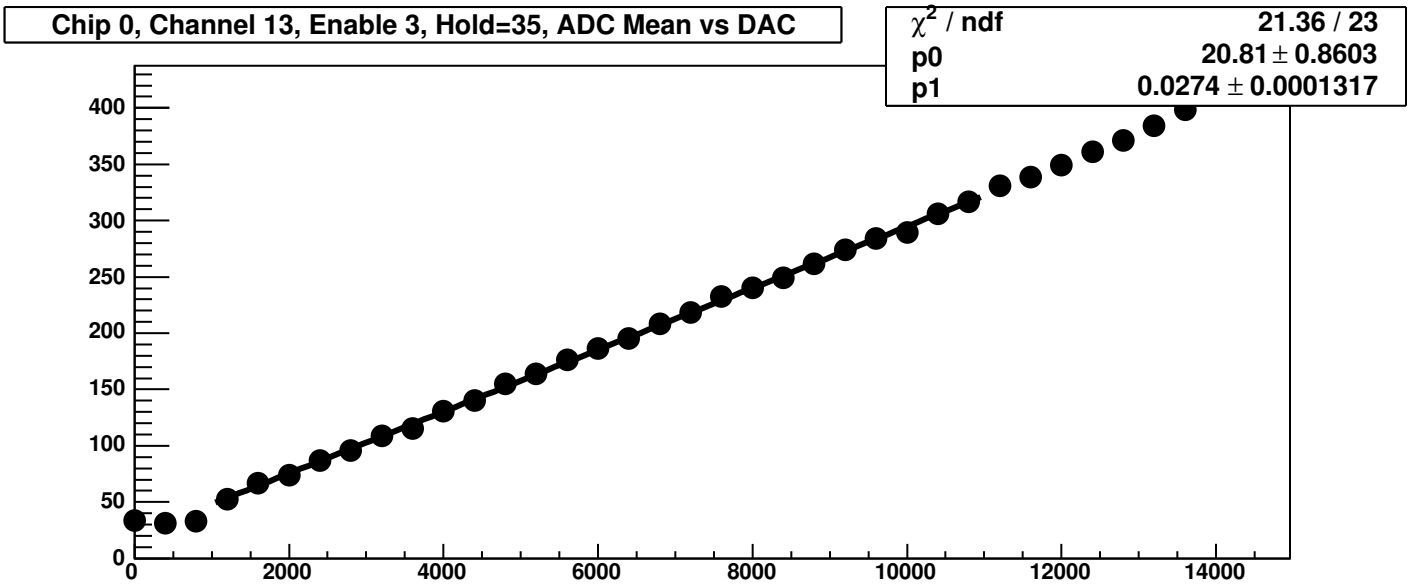

Chip 0, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

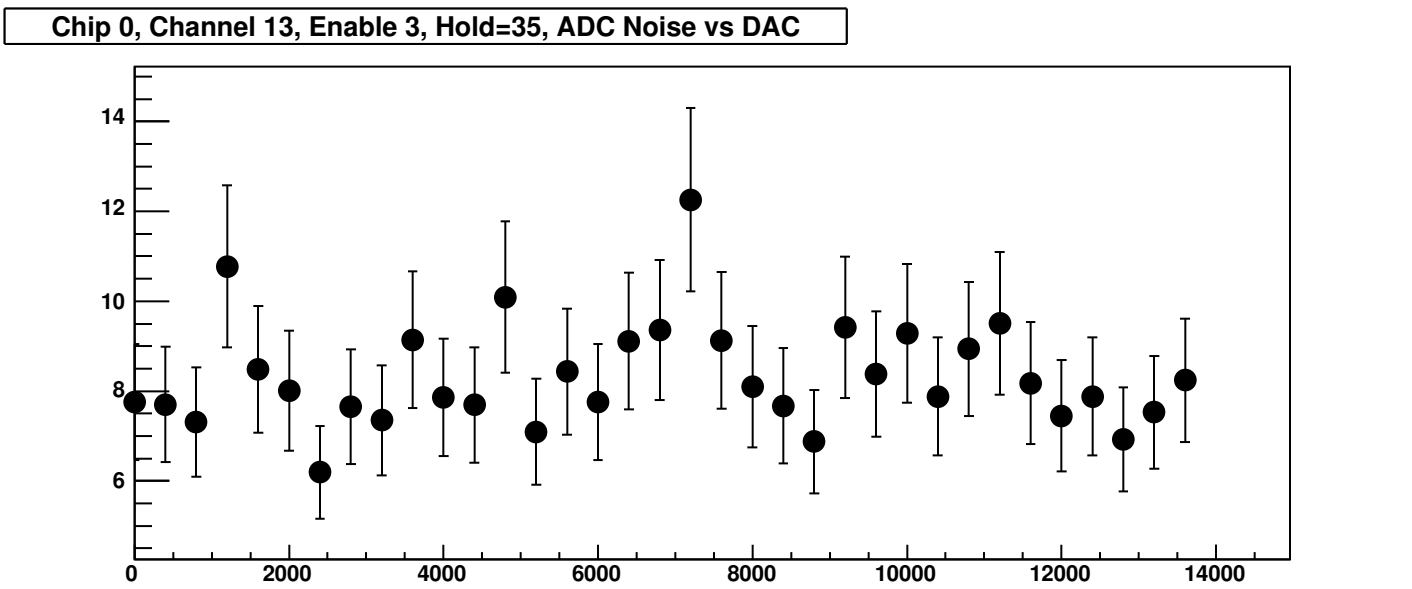

Chip 0, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

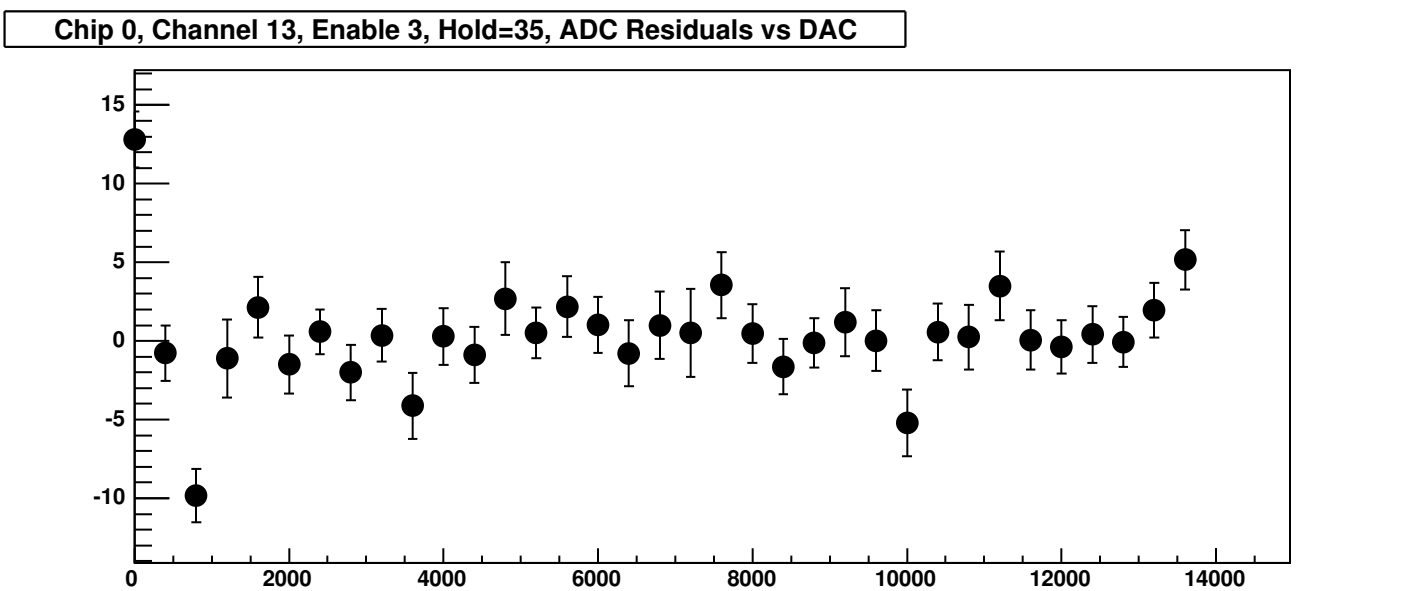

Chip 0, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 30.56 / 23
p0 23.93 ± 0.7741
p1 0.0214 ± 0.0001117
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

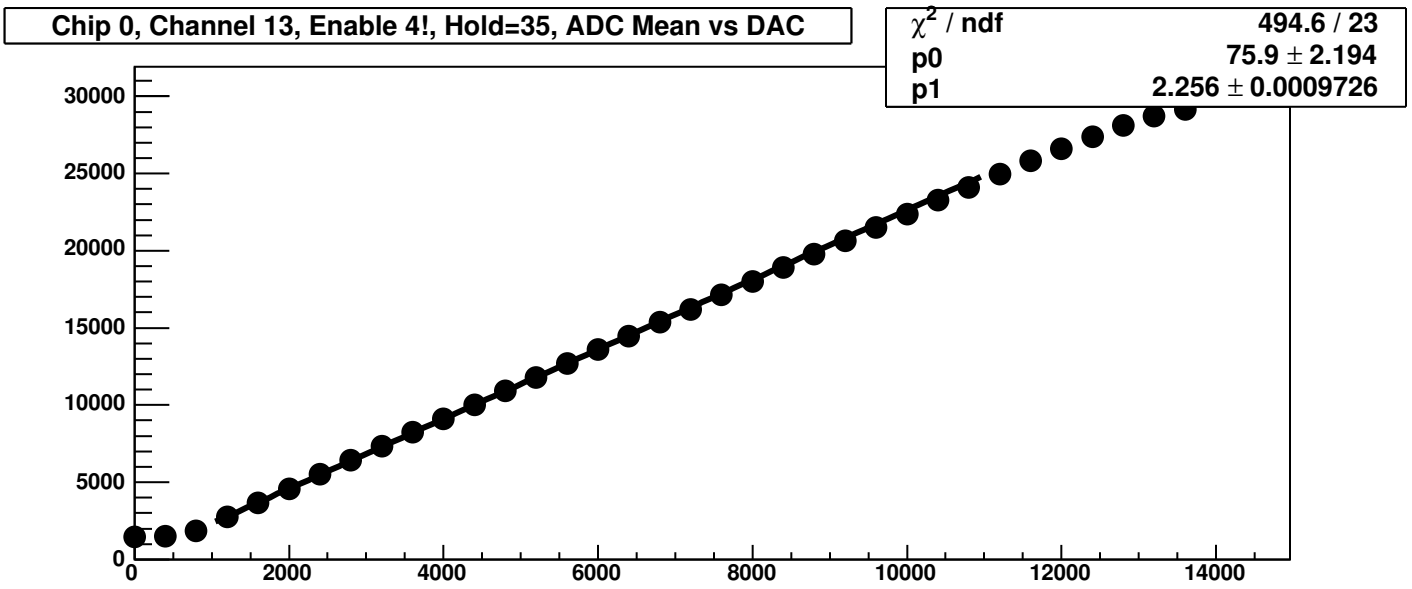

Chip 0, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

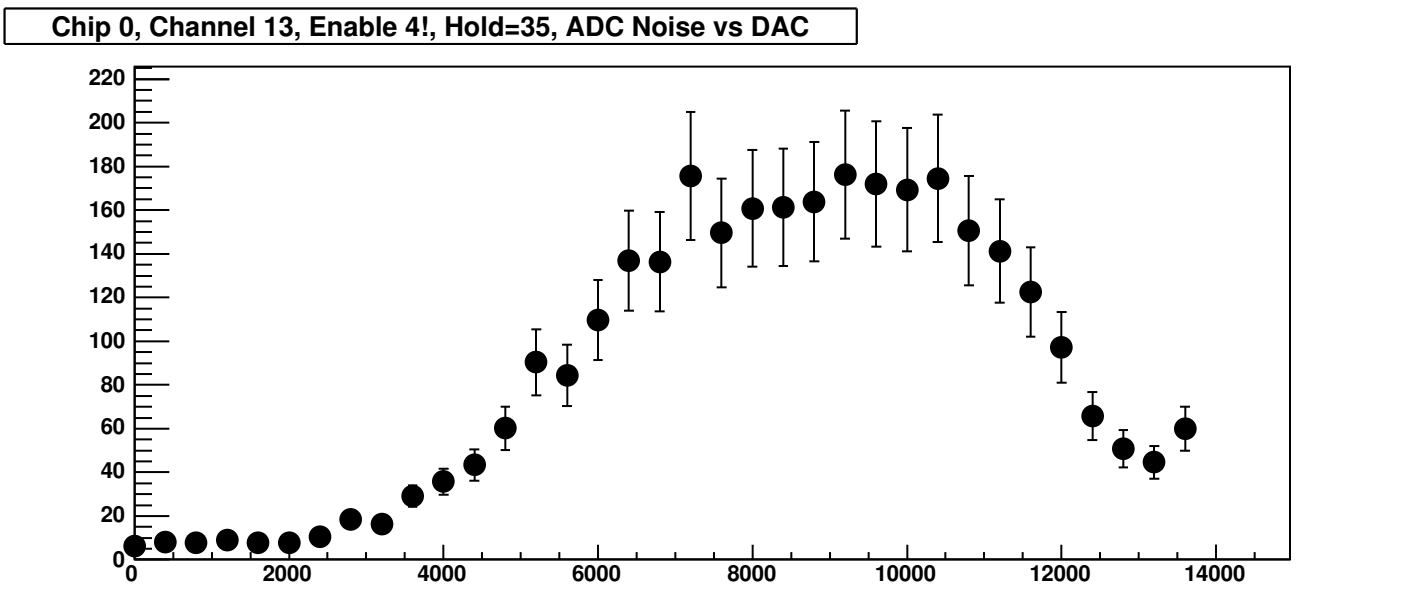

Chip 0, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

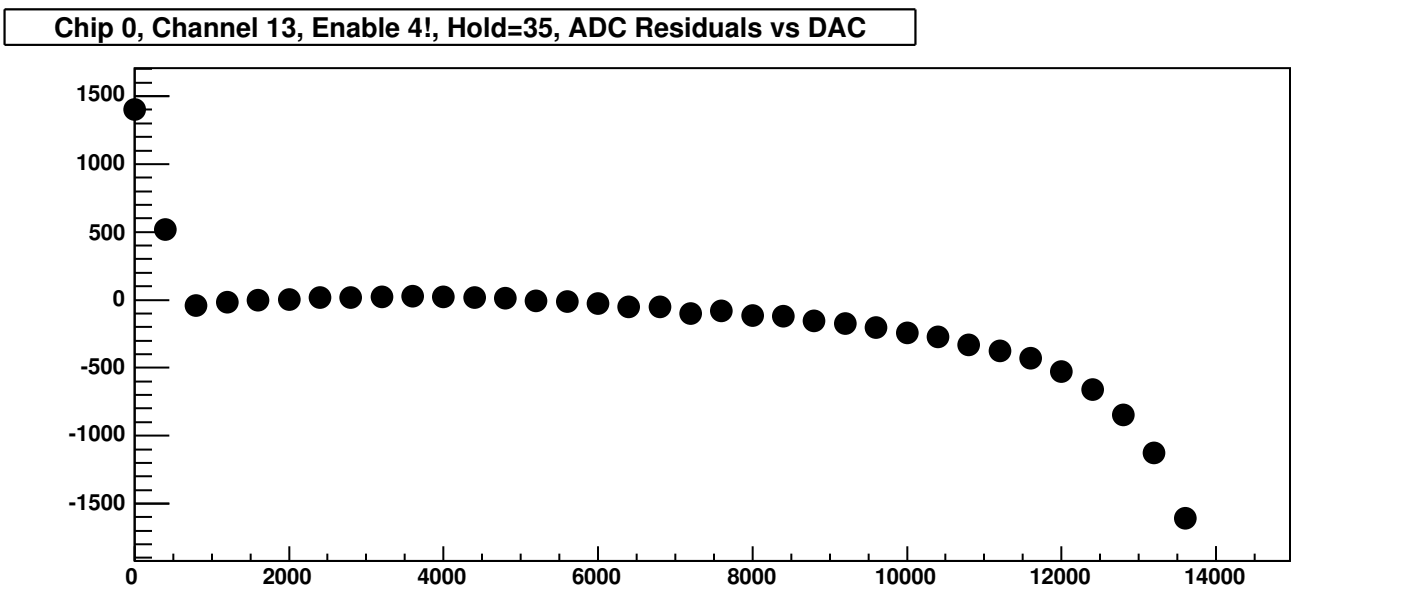

Chip 0, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.36 / 23
p0 20.81 ± 0.8603
p1 0.0274 ± 0.0001317
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

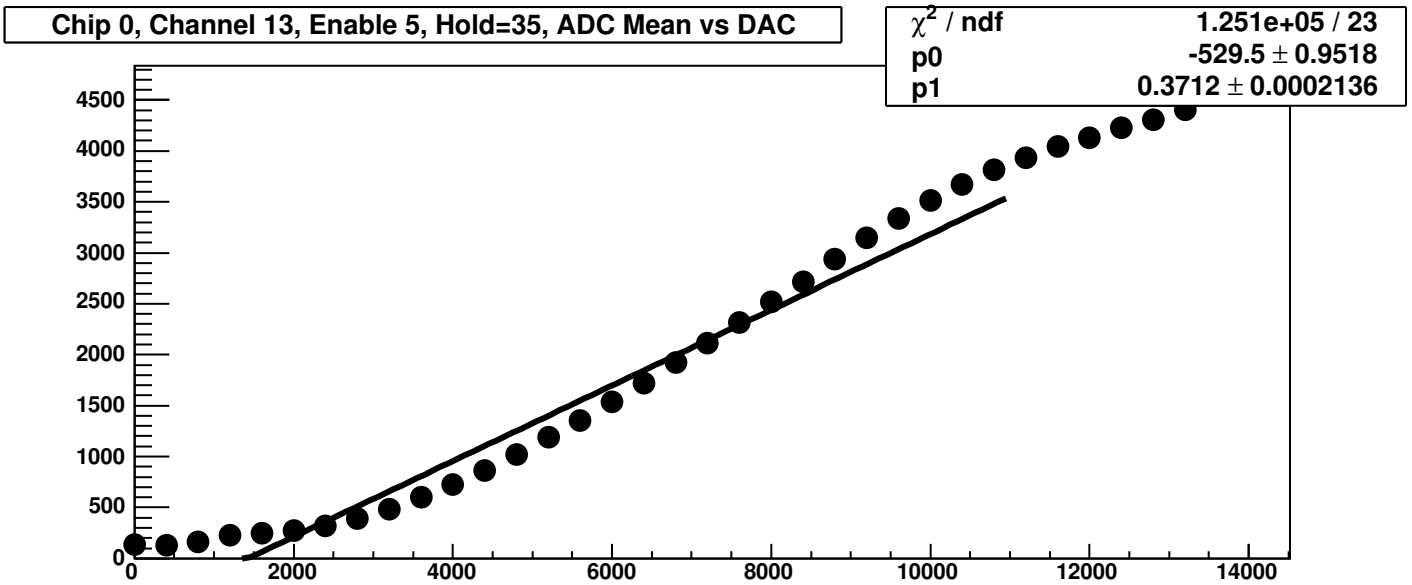

Chip 0, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

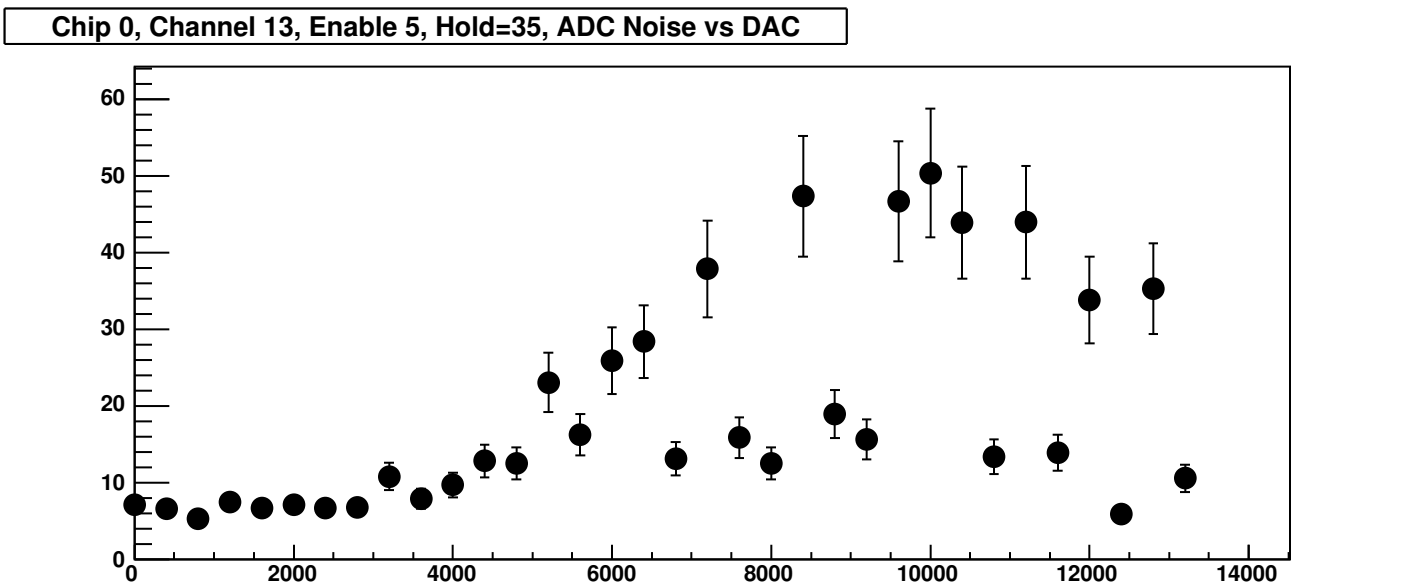

Chip 0, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

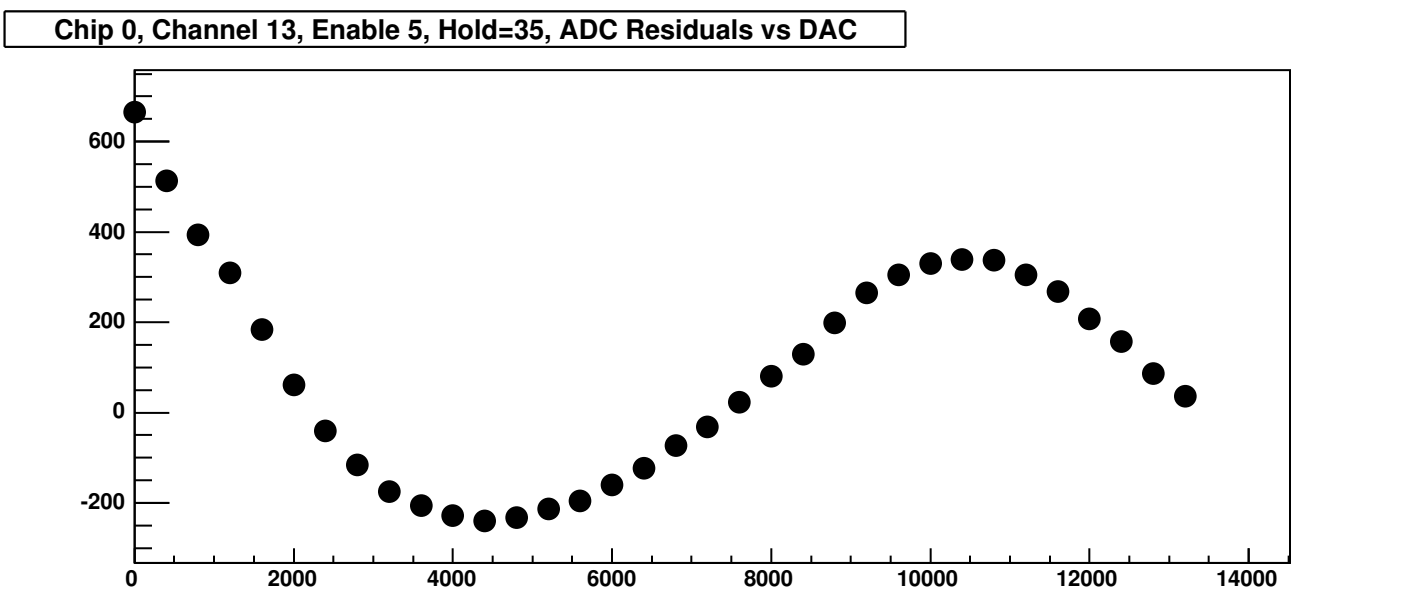

Chip 0, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 494.6 / 23
p0 75.9 ± 2.194
p1 2.256 ± 0.0009726
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

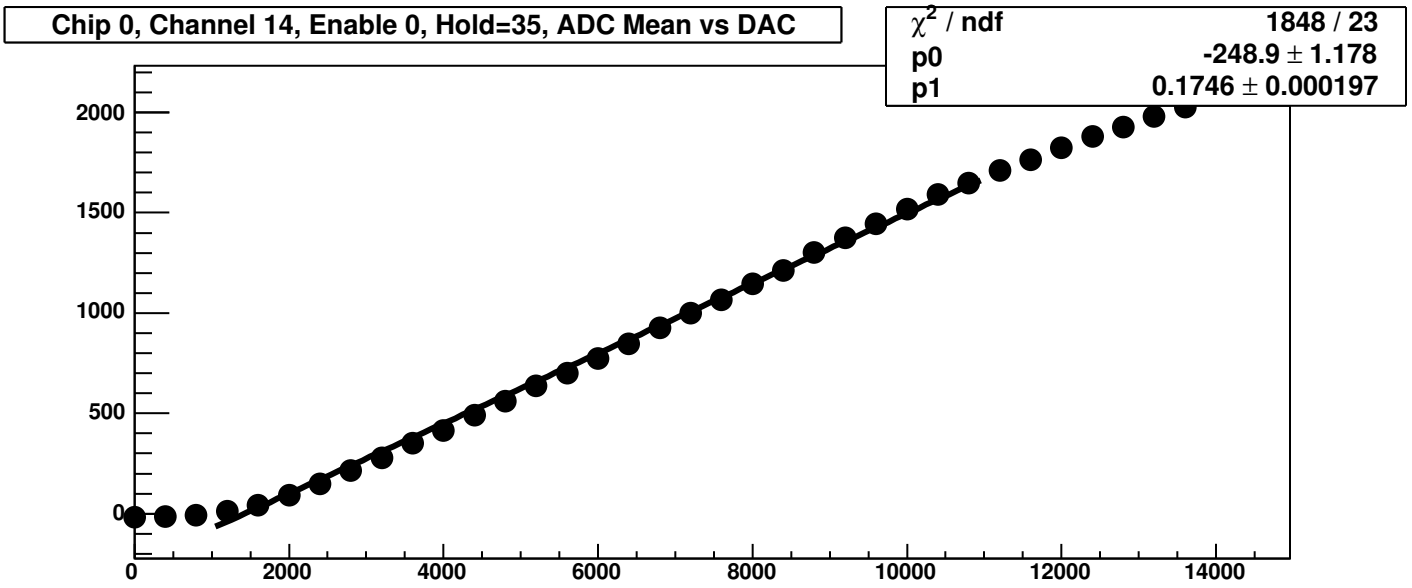

Chip 0, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

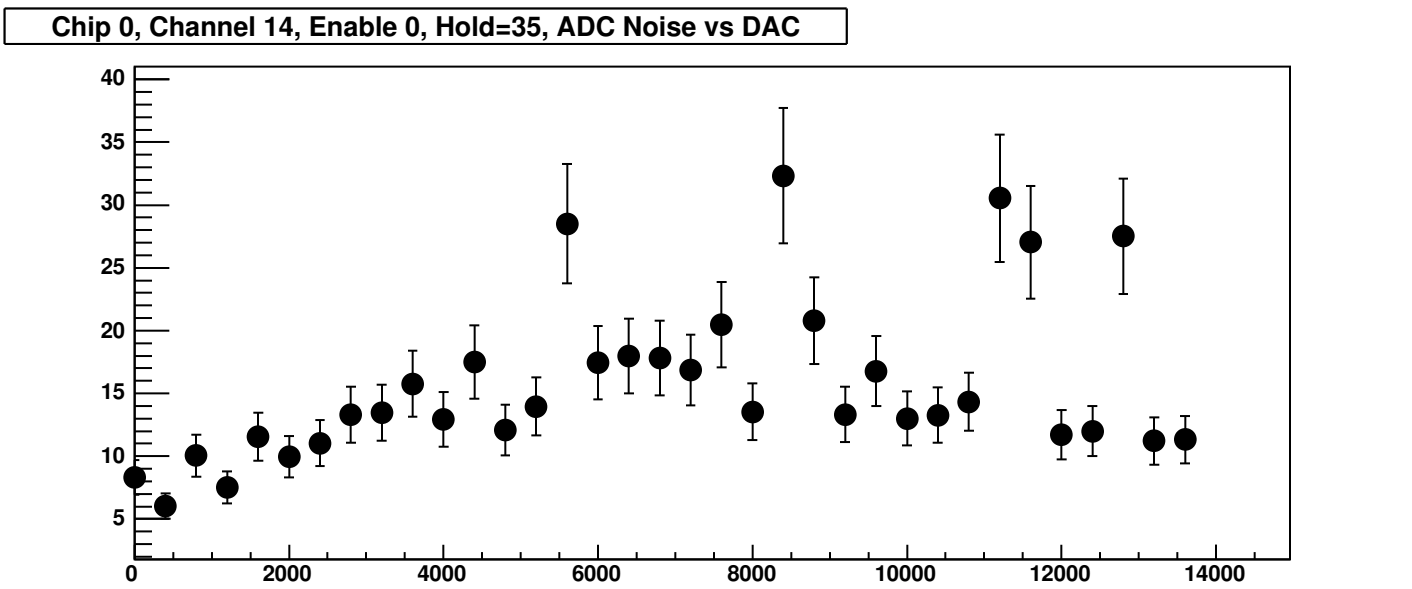

Chip 0, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

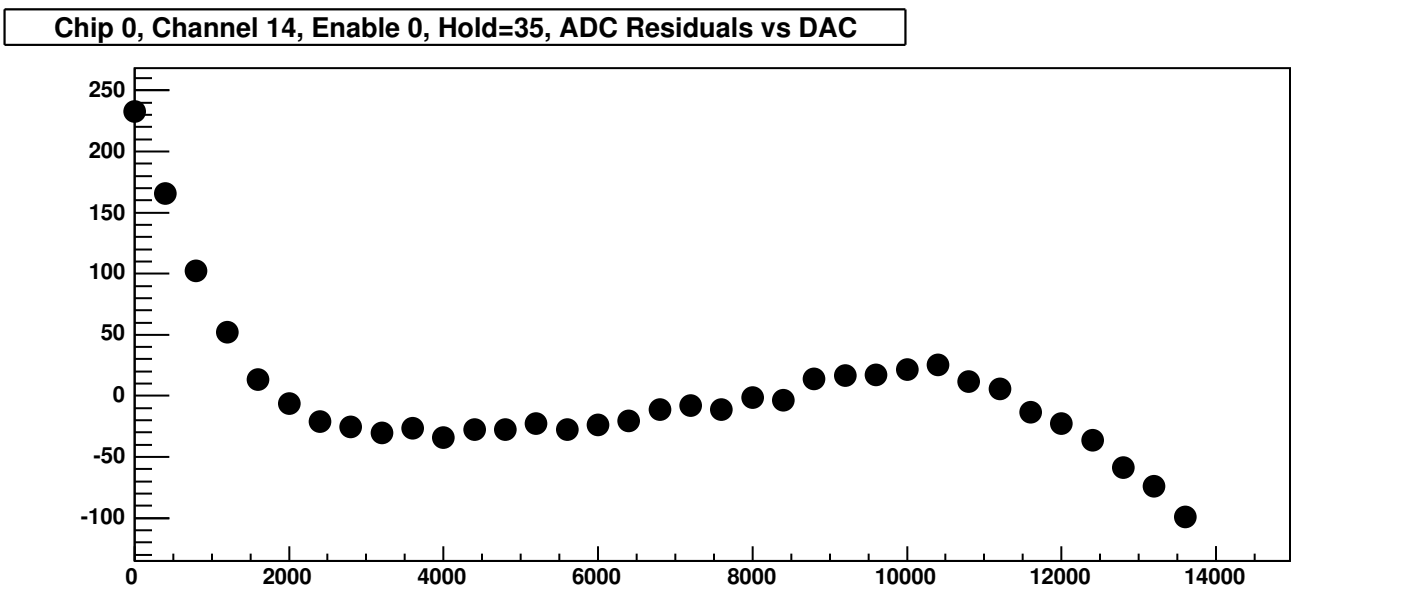

Chip 0, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.251e+05 / 23
p0 -529.5 ± 0.9518
p1 0.3712 ± 0.0002136
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

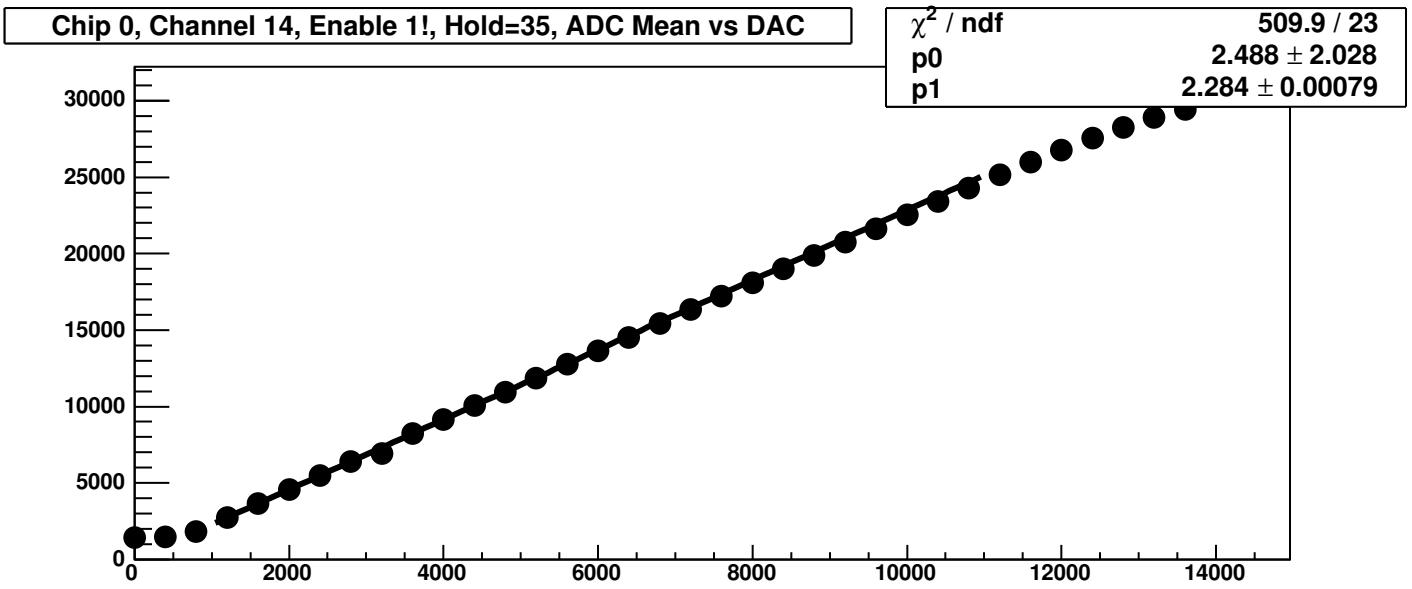

Chip 0, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

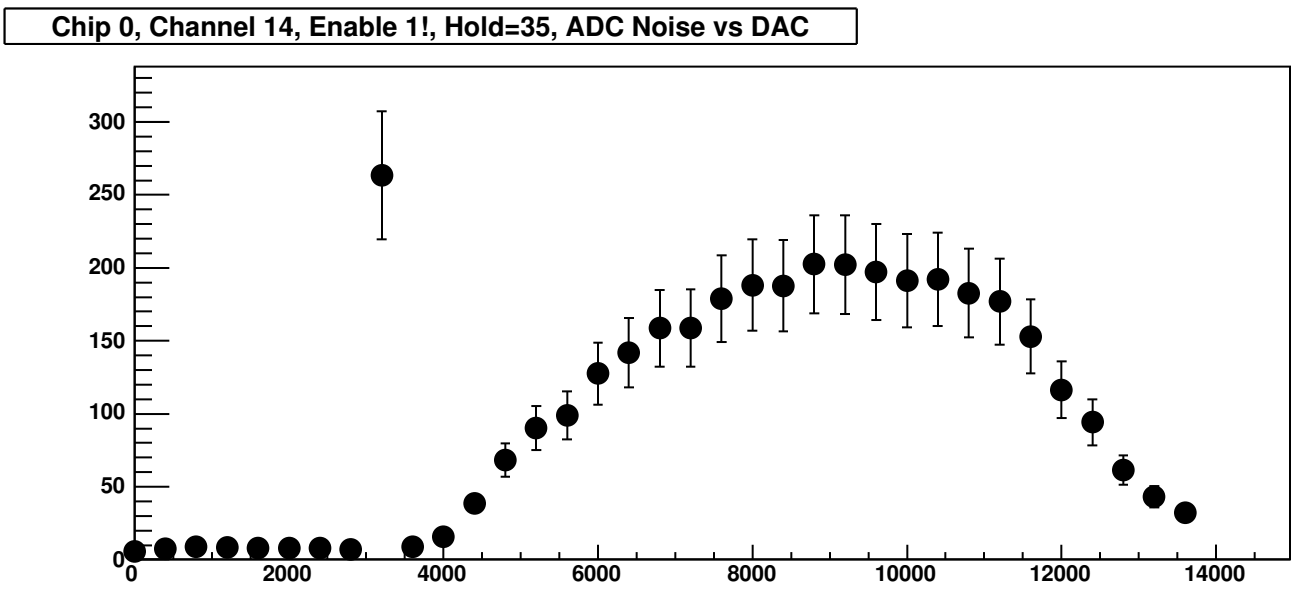

Chip 0, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

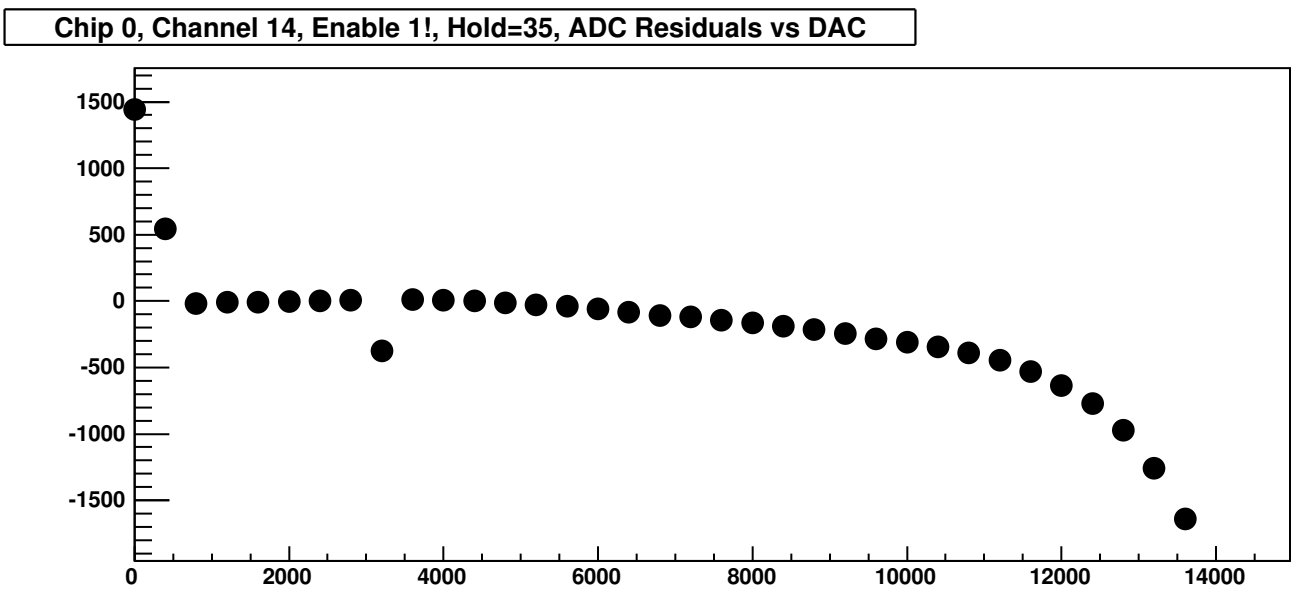

Chip 0, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1848 / 23
p0 -248.9 ± 1.178
p1 0.1746 ± 0.000197
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

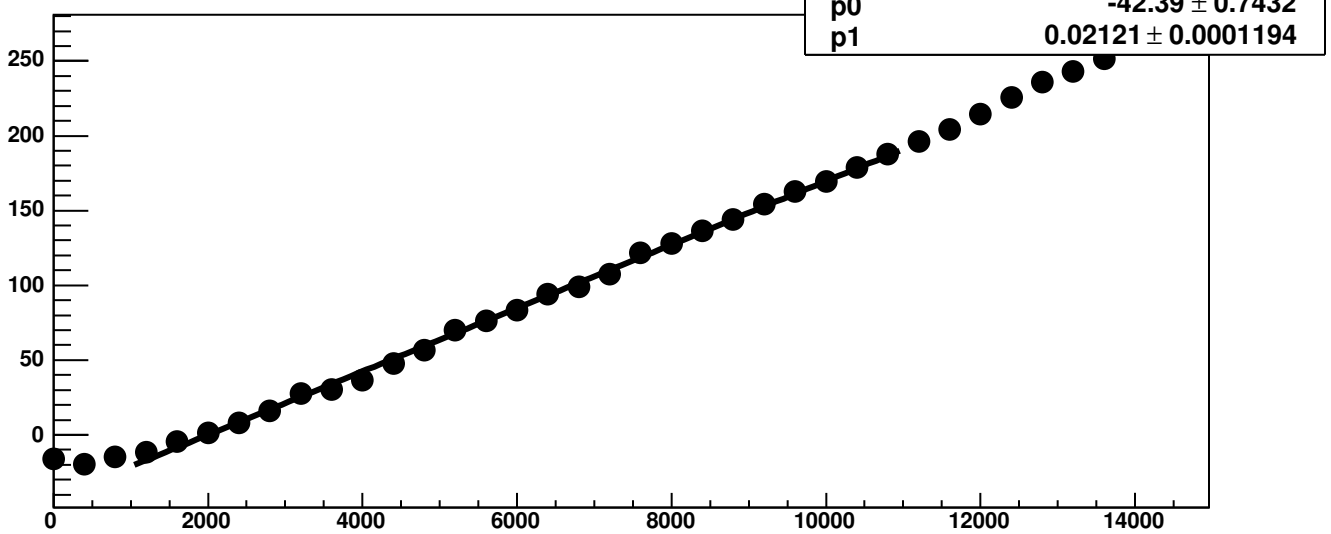

Chip 0, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 509.9 / 23
p0 2.488 ± 2.028
p1 2.284 ± 0.00079
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

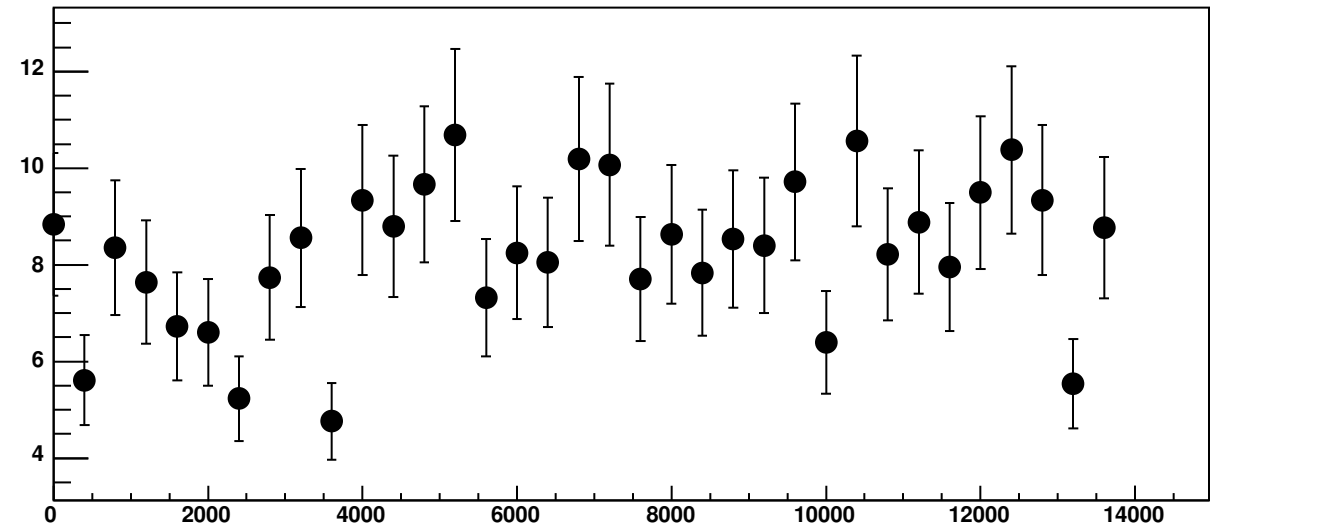

Chip 0, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

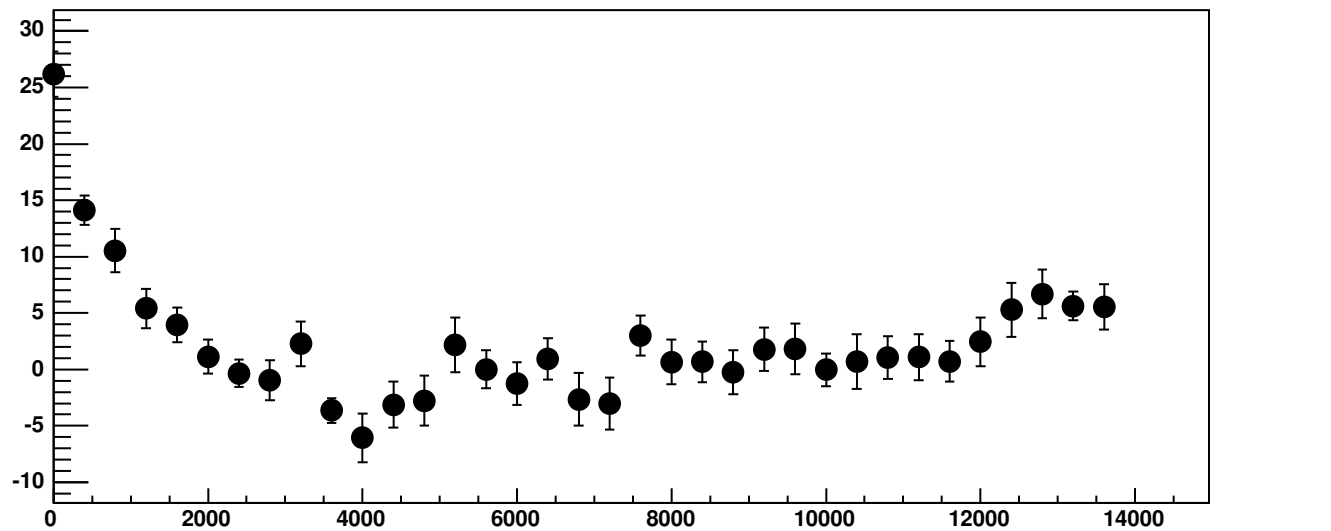
Chip 0, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

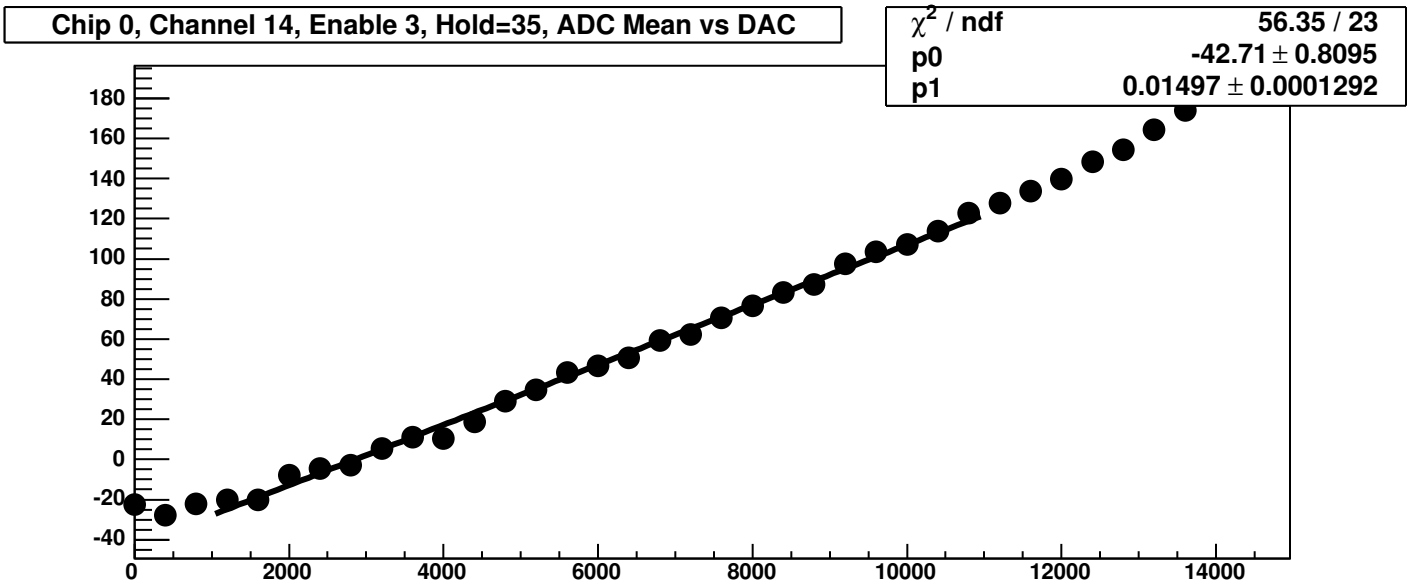


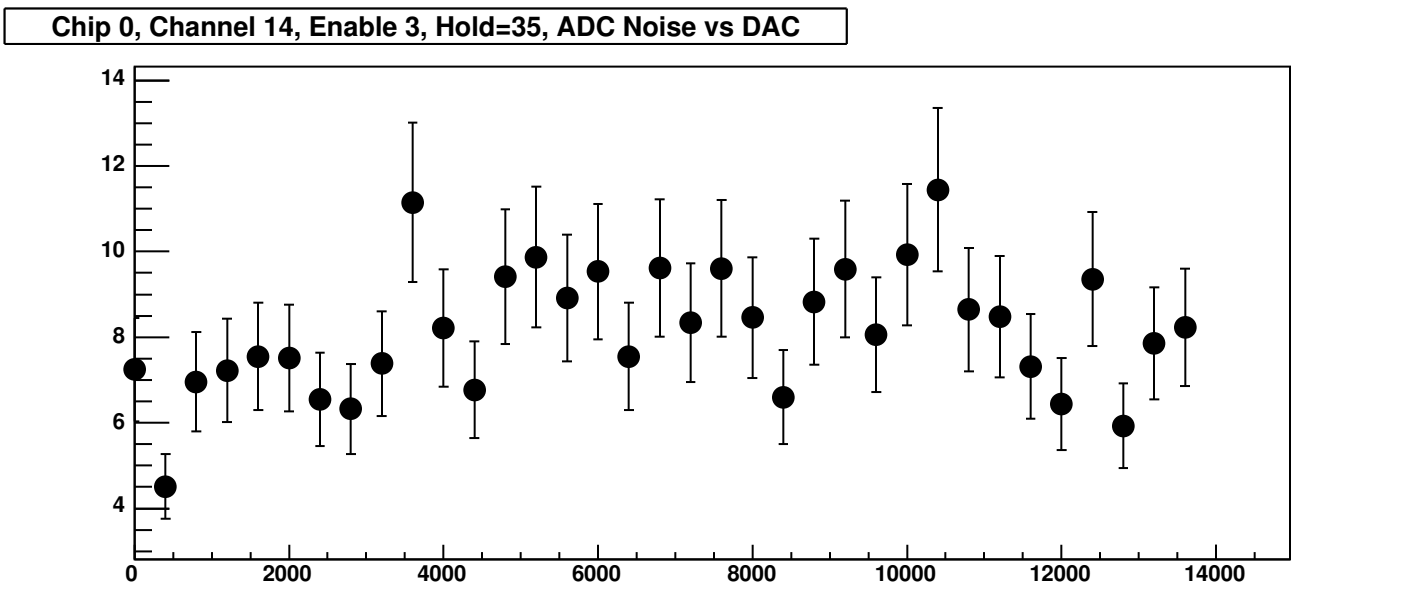
Chip 0, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC

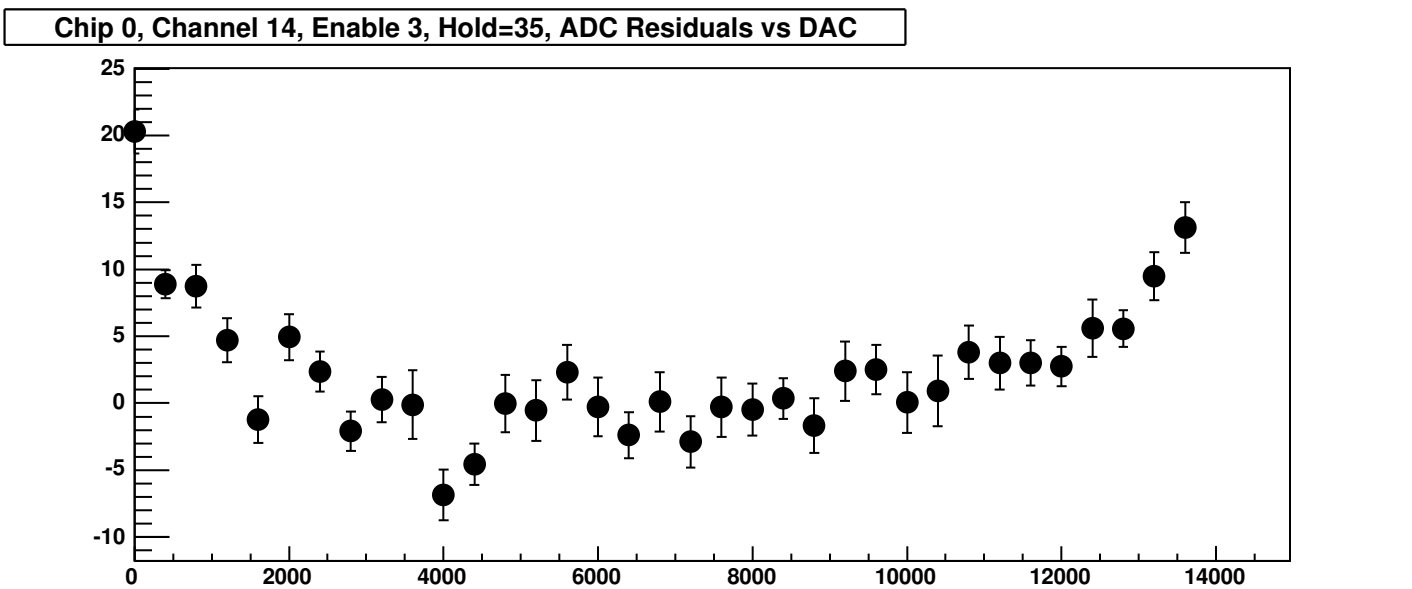


Chip 0, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

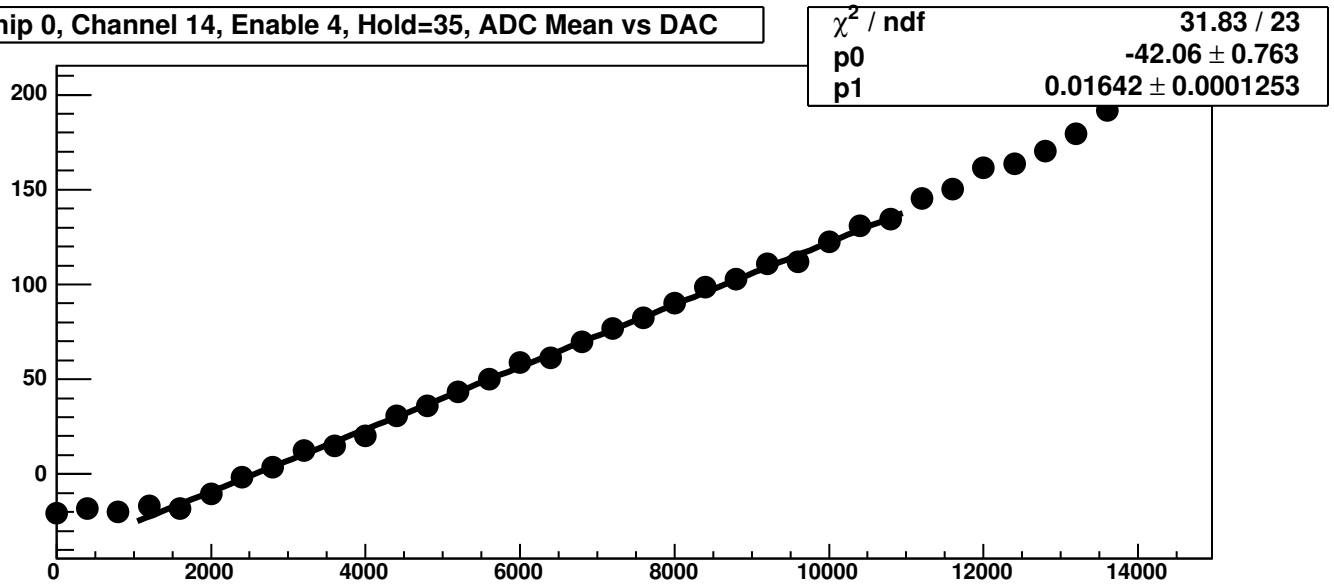



Chip 0, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 56.35 / 23
p0 -42.71 ± 0.8095
p1 0.01497 ± 0.0001292
180
160
140
120
100
80
60
40
20
0
-20
-40
0
2000
4000
6000
8000
10000
12000
14000

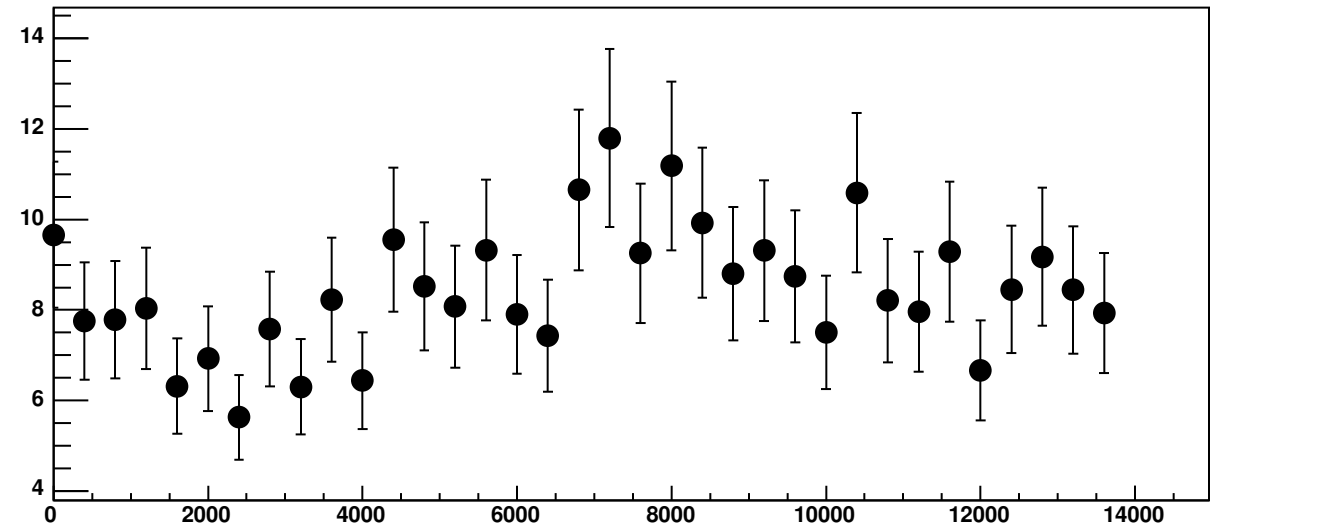

Chip 0, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

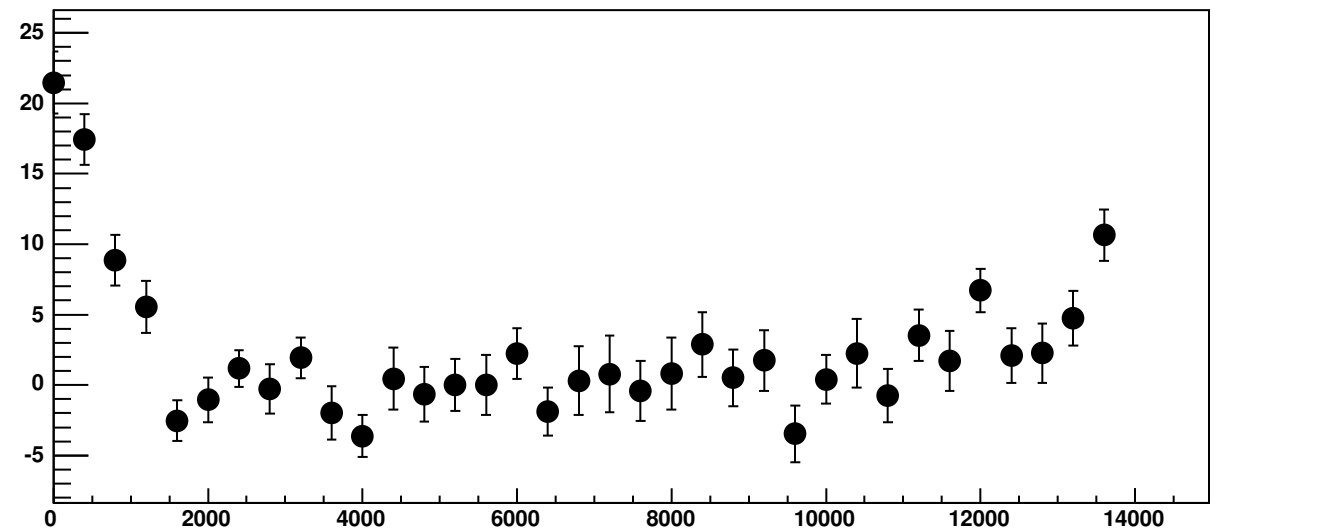
Chip 0, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC



Chip 0, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC

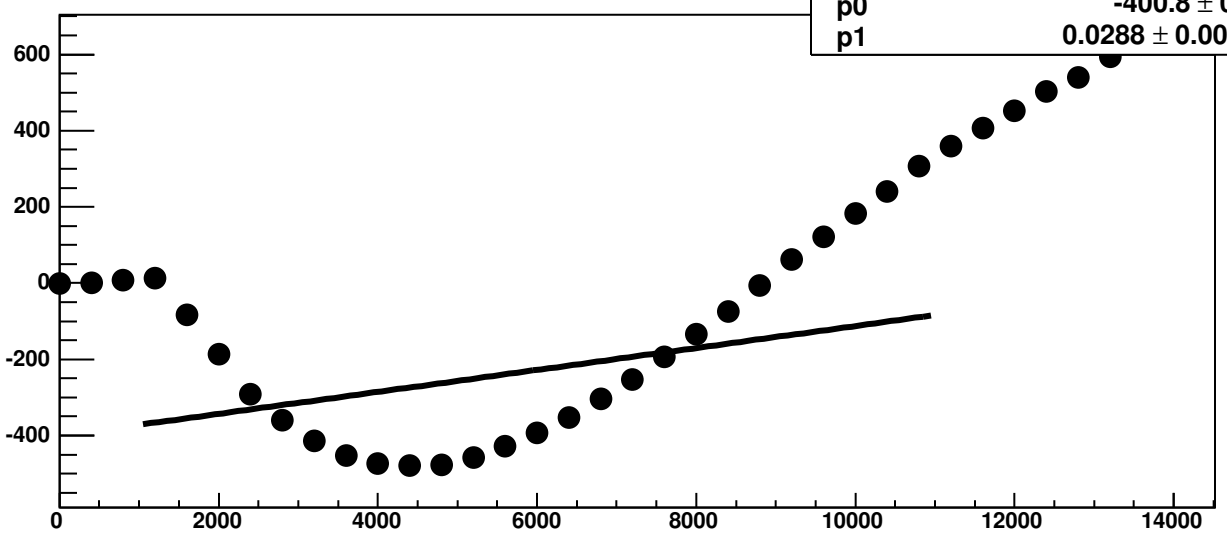


Chip 0, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC

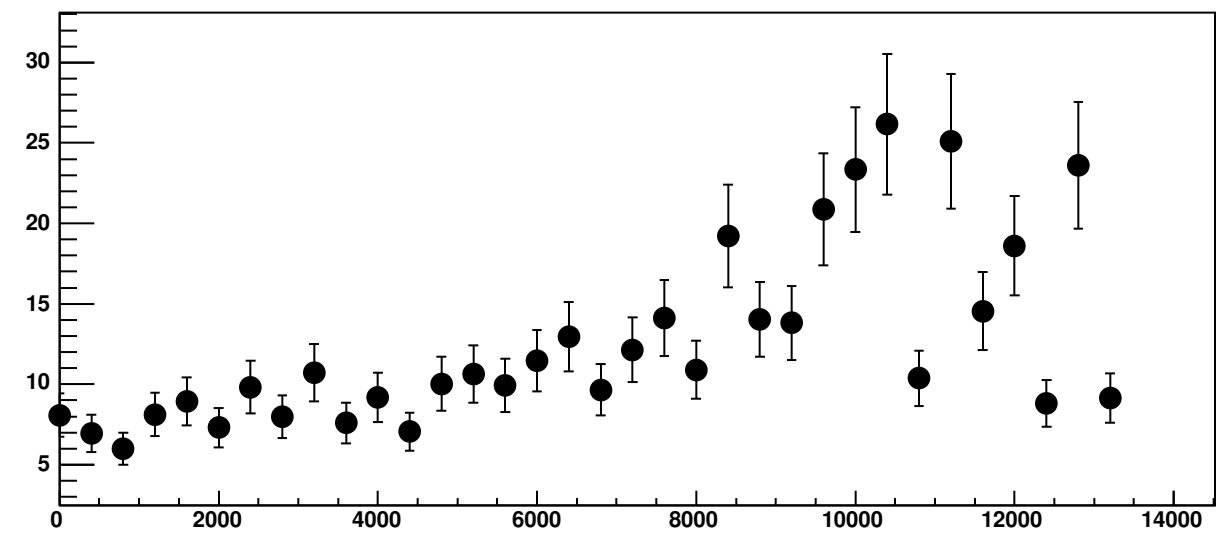


Chip 0, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

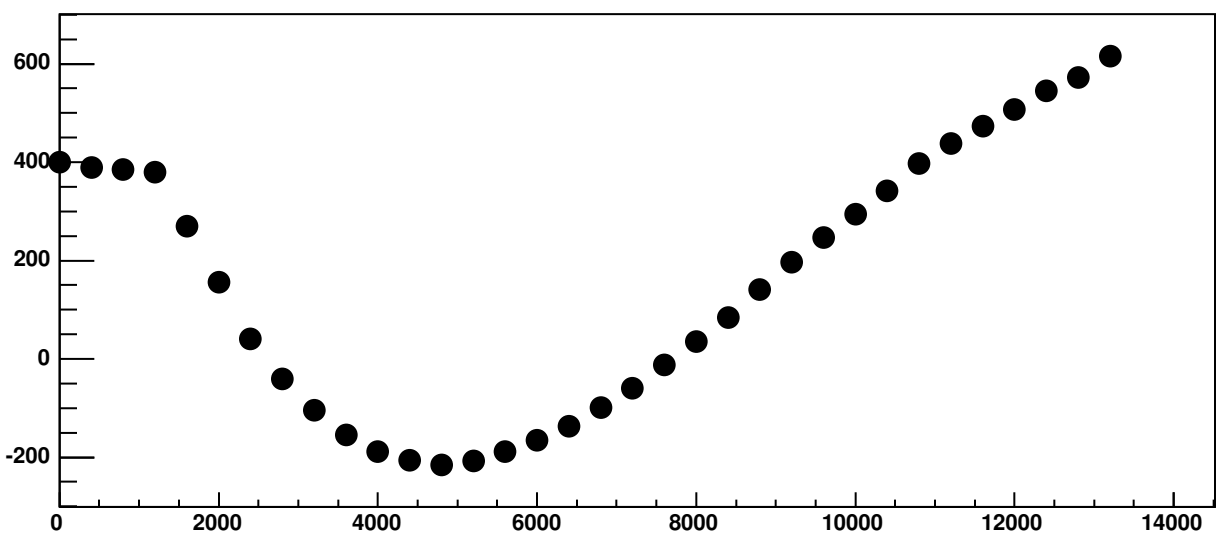
χ^2 / ndf	1.767e+05 / 23
p0	-400.8 ± 0.9876
p1	0.0288 ± 0.0001817

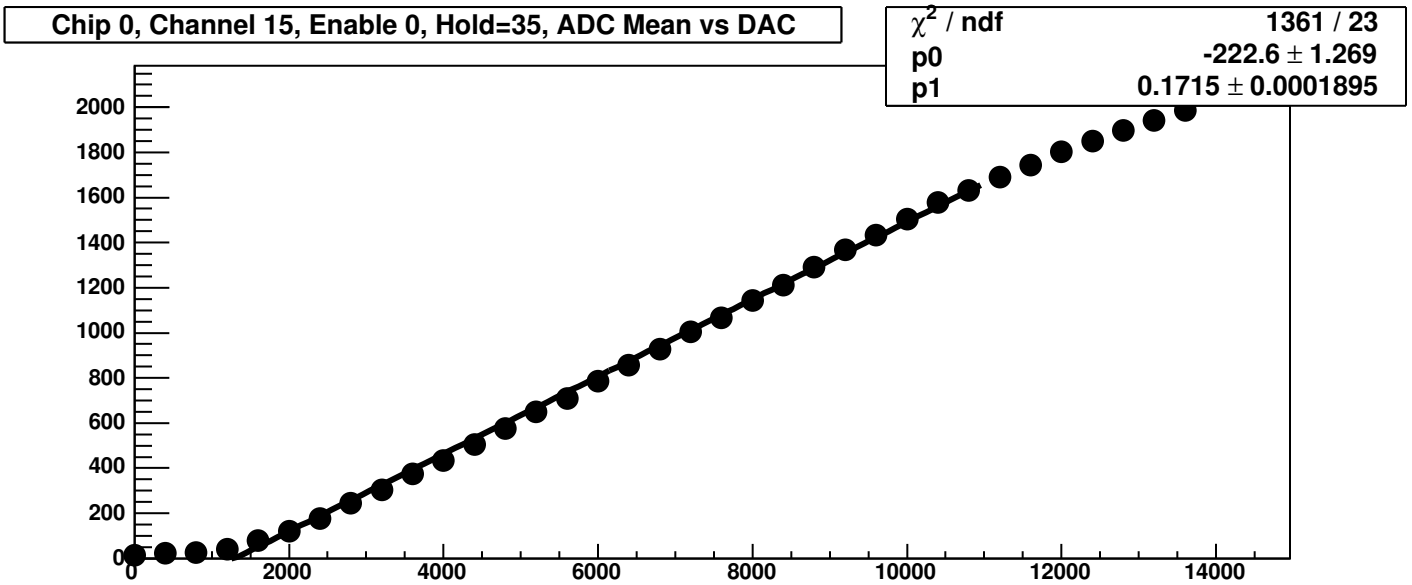


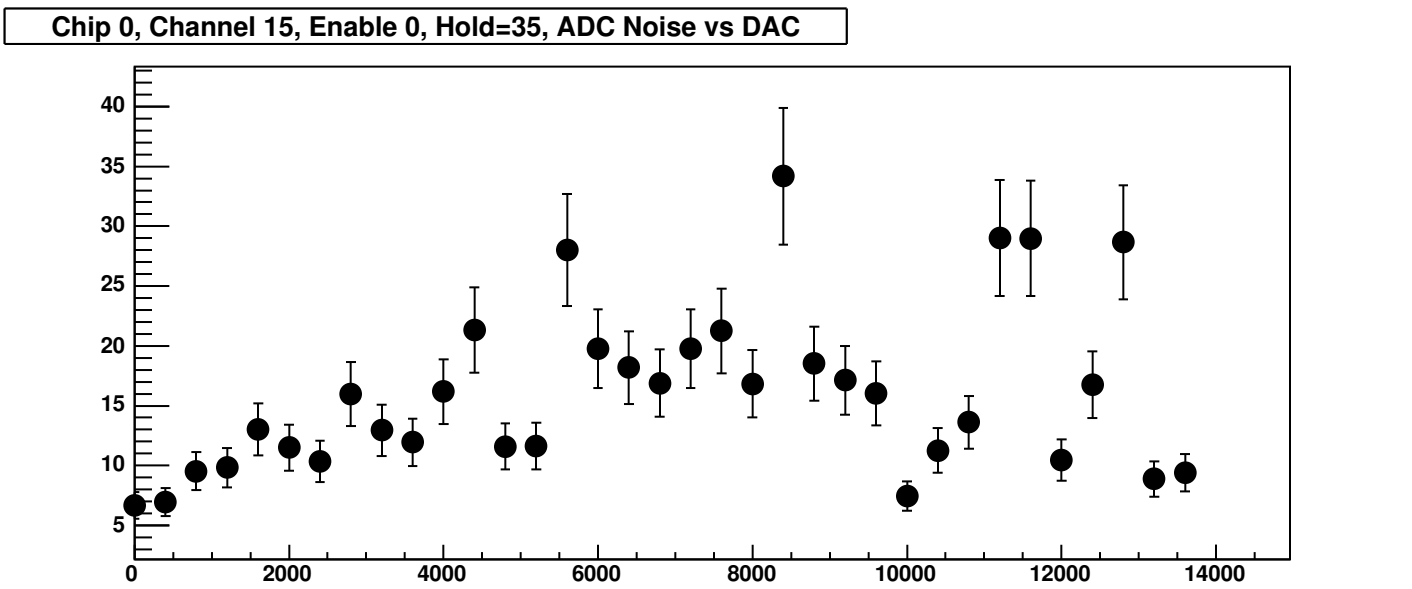
Chip 0, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC

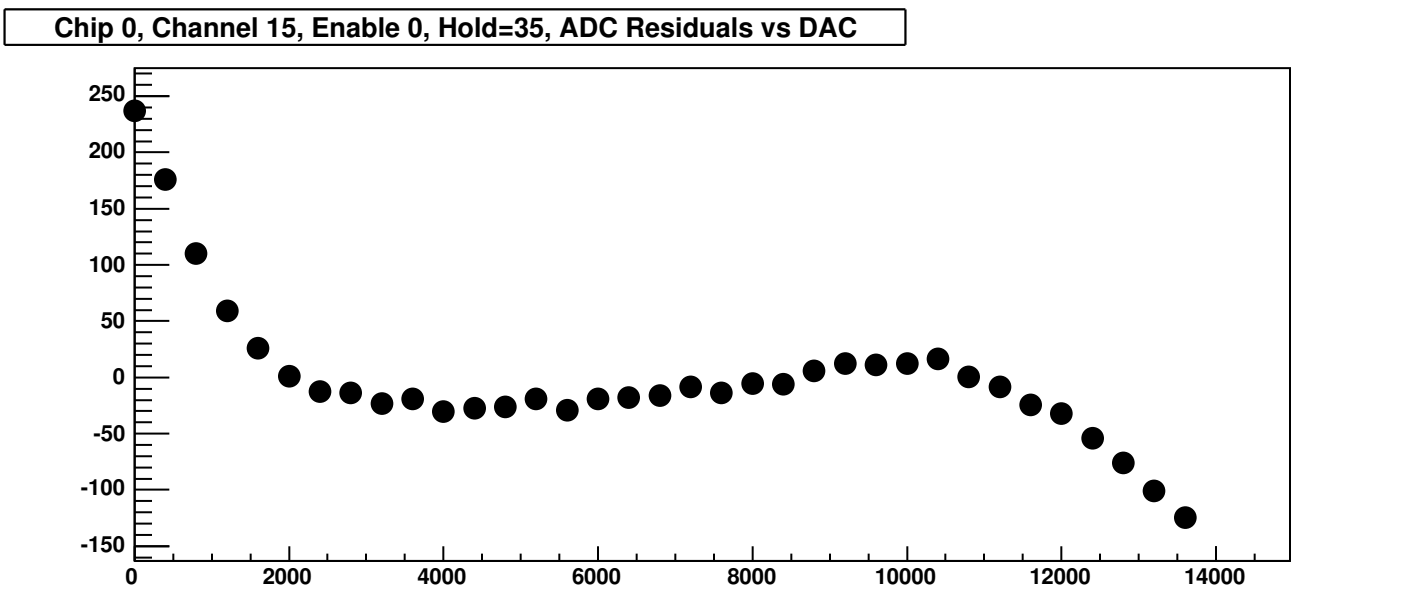


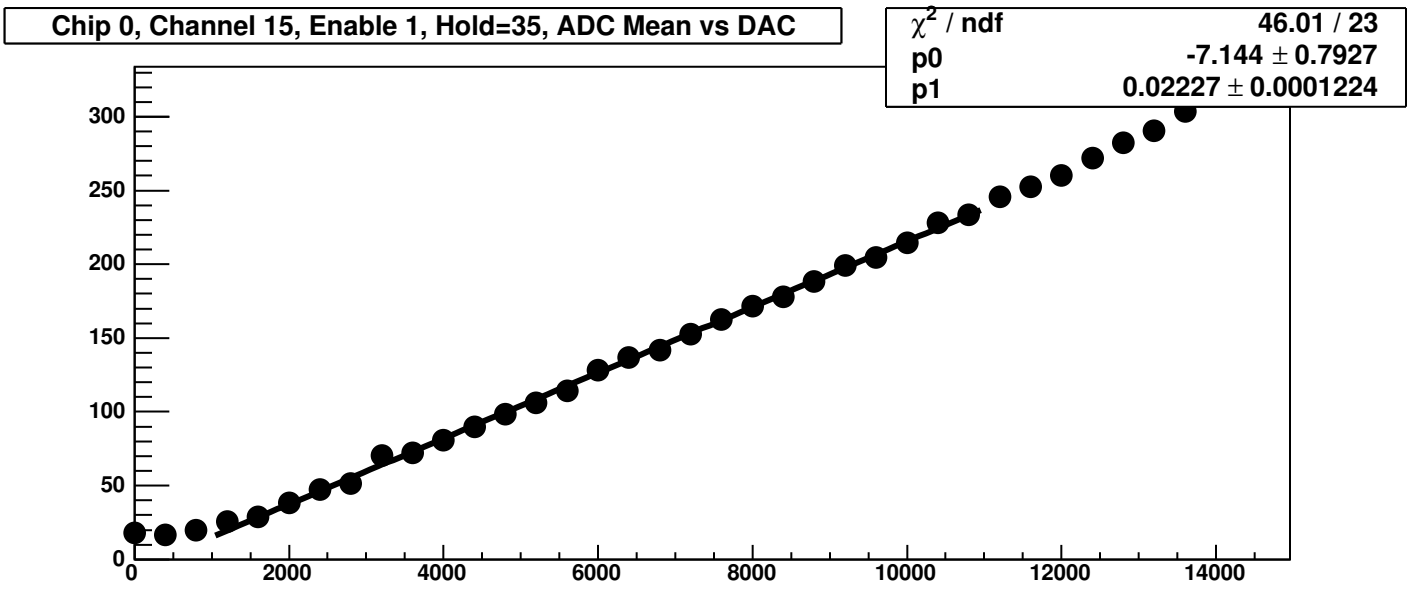
Chip 0, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

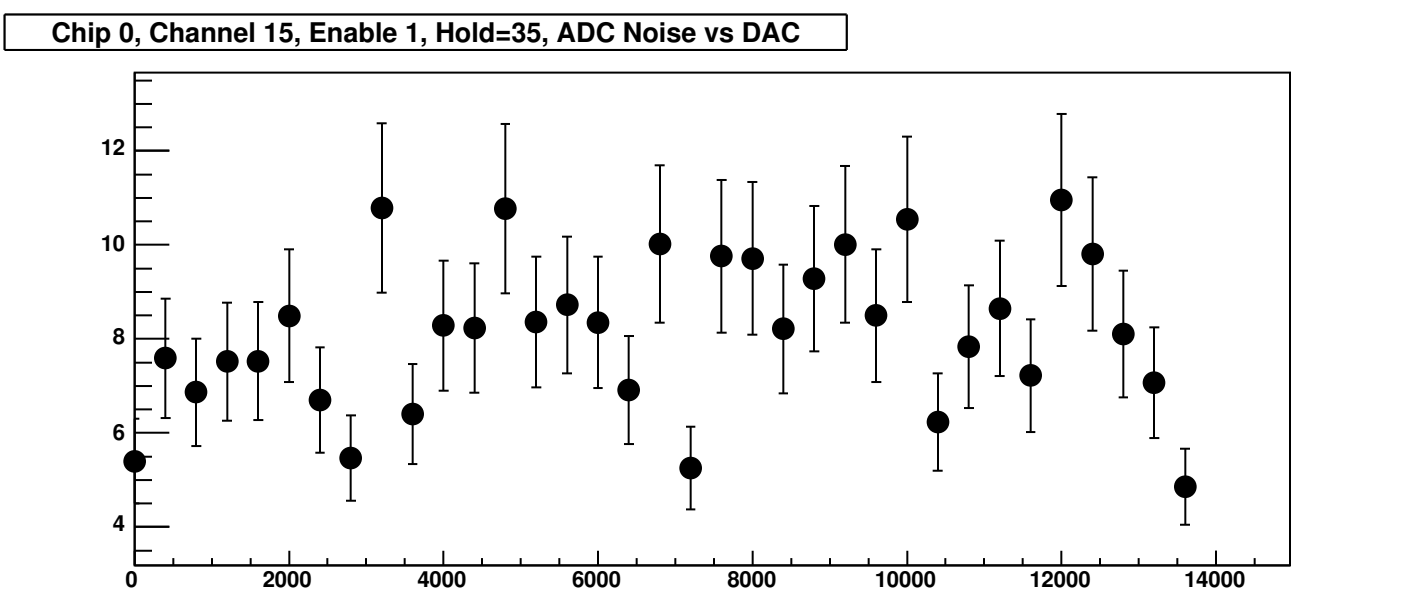


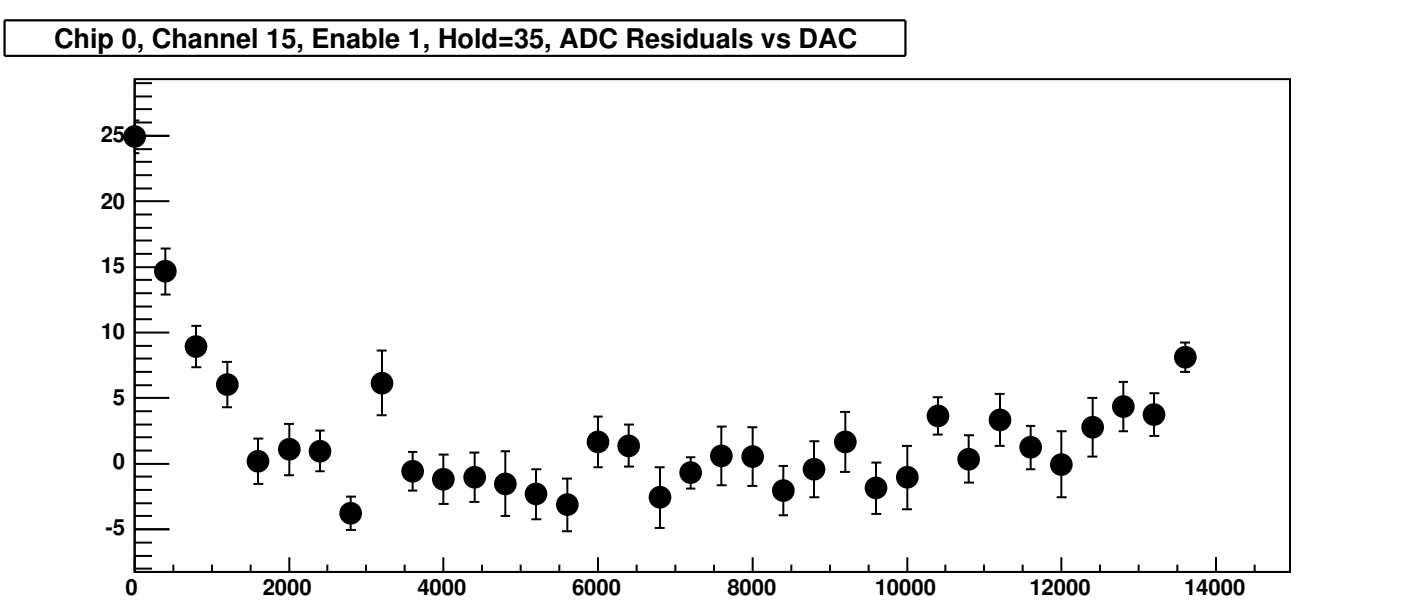

Chip 0, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1361 / 23
p0 -222.6 ± 1.269
p1 0.1715 ± 0.0001895
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

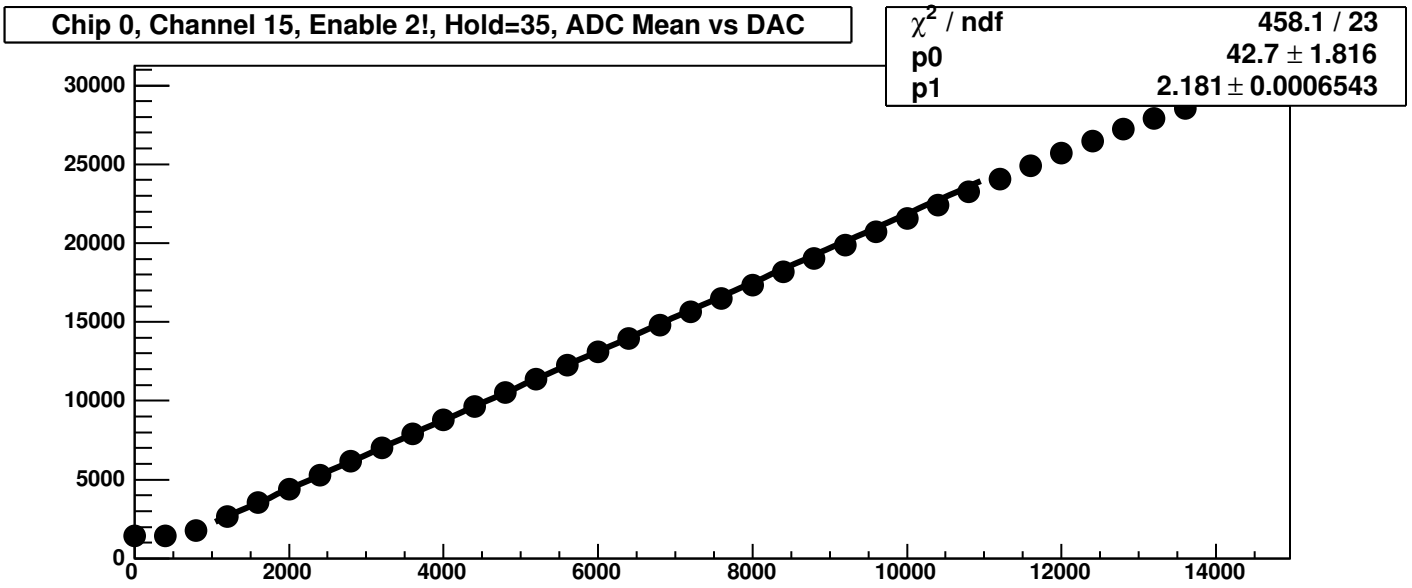

Chip 0, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

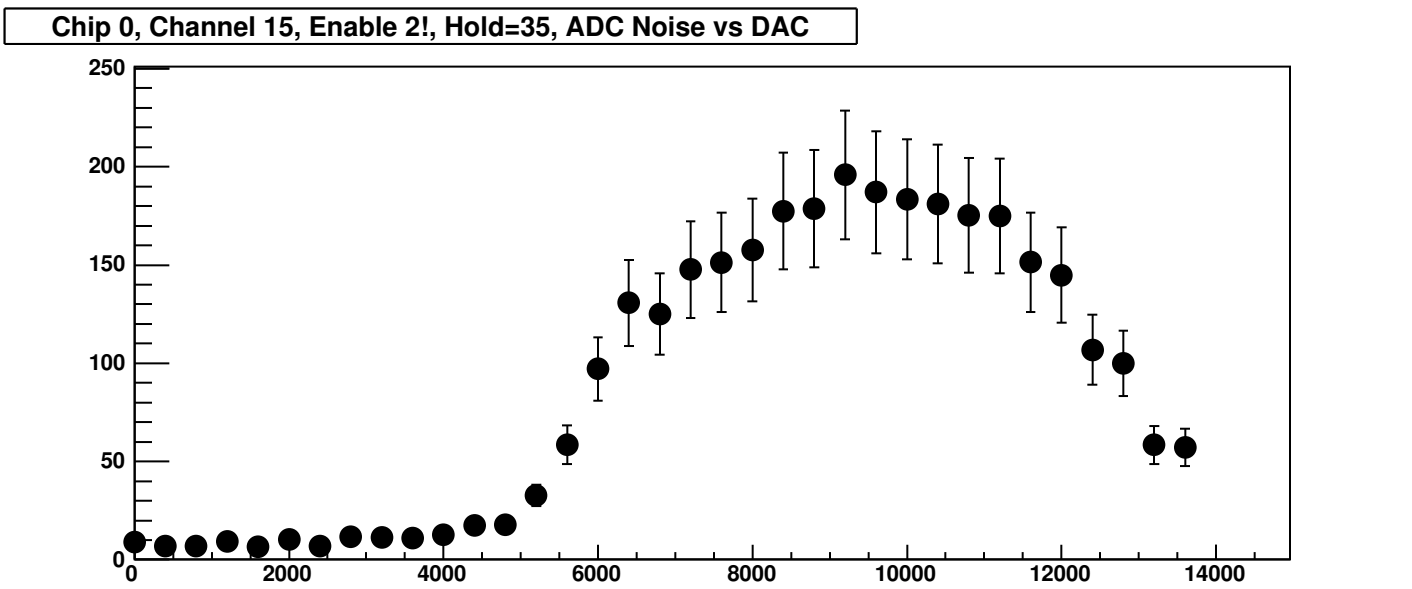

Chip 0, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

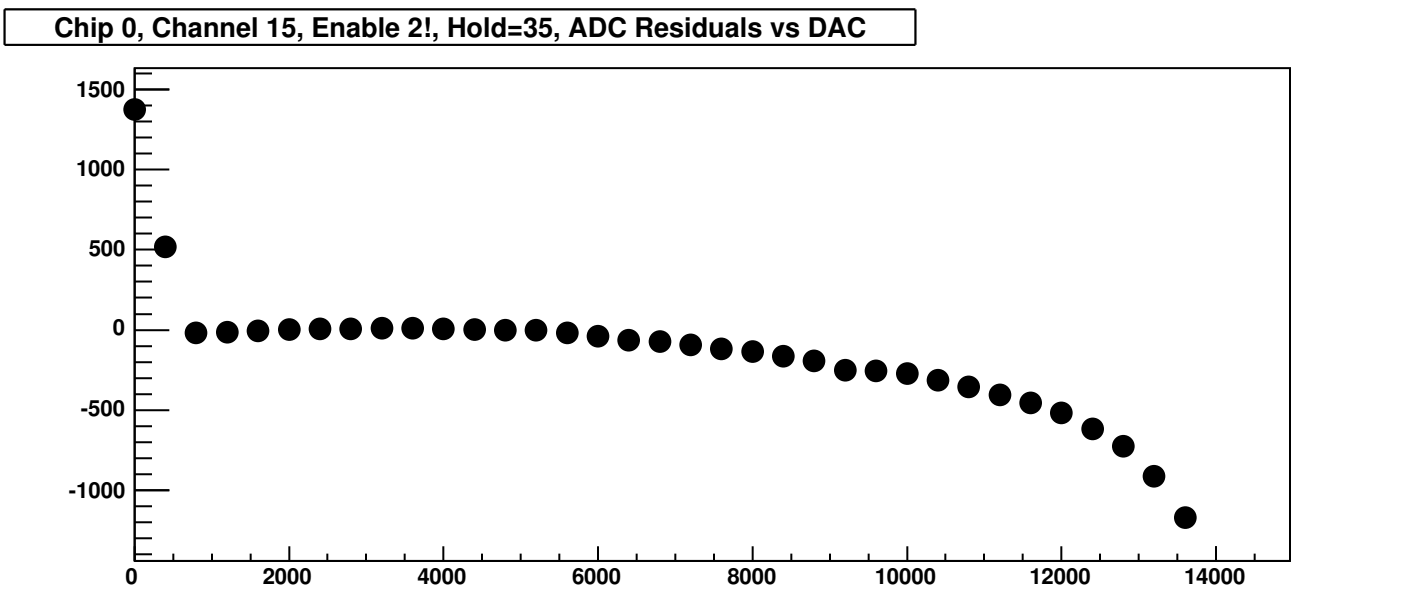

Chip 0, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 46.01 / 23
p0 -7.144 ± 0.7927
p1 0.02227 ± 0.0001224
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

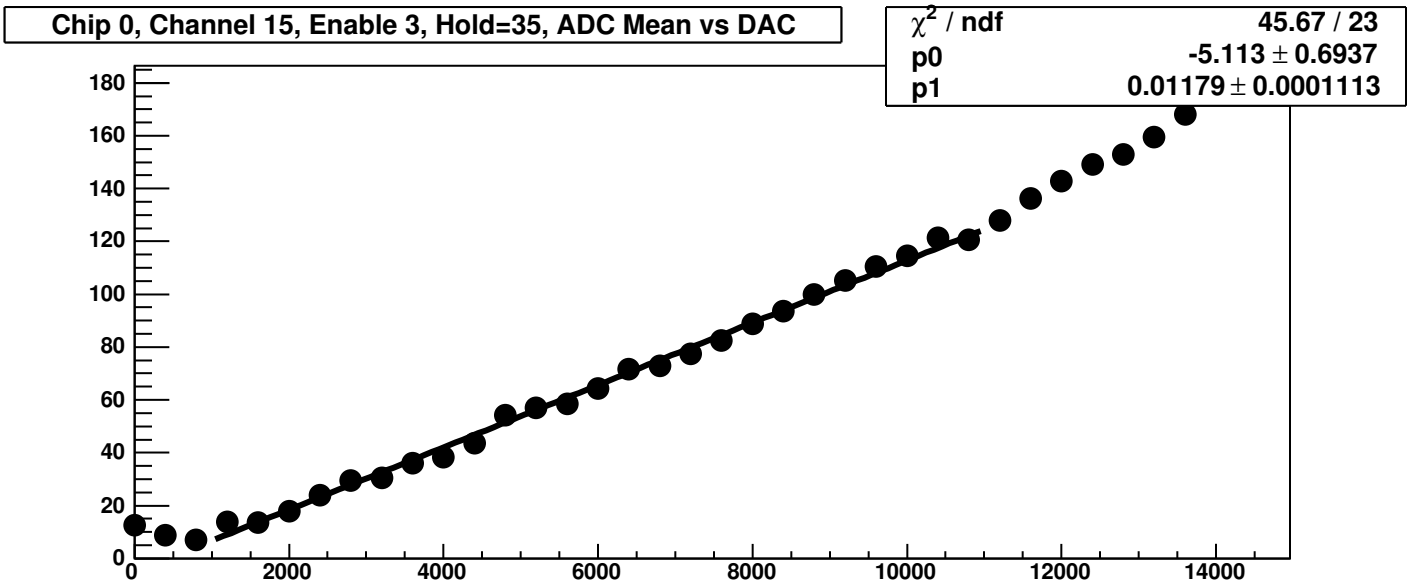

Chip 0, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

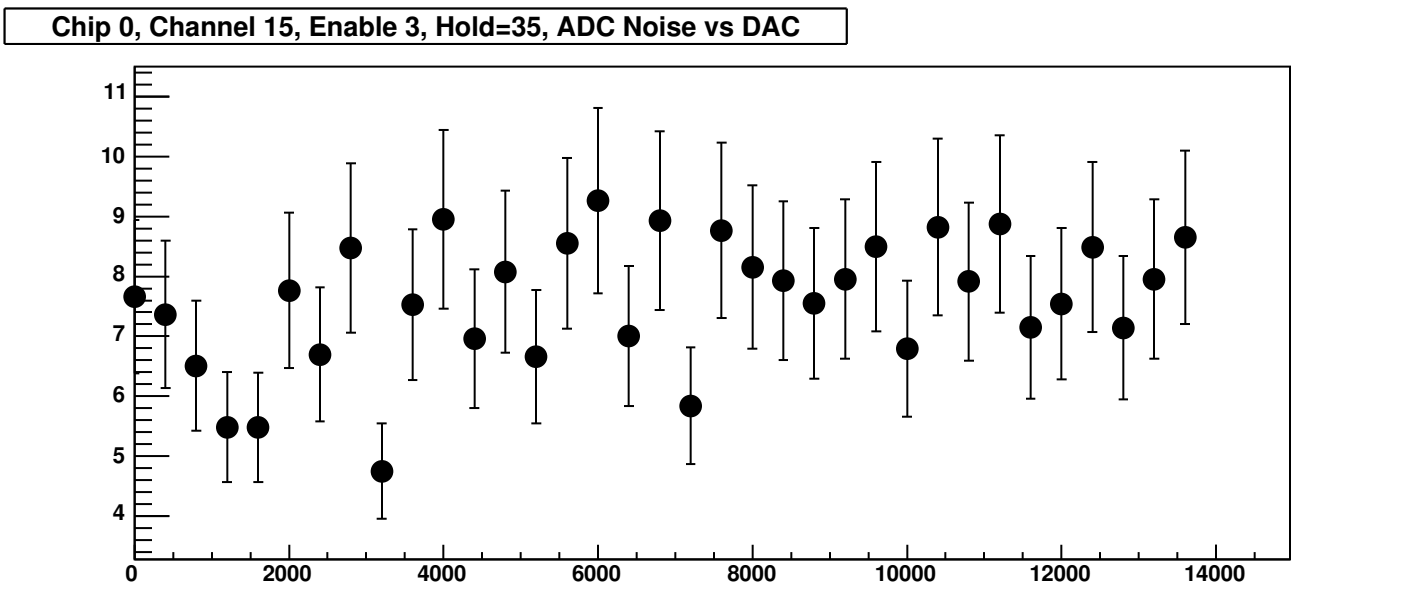

Chip 0, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

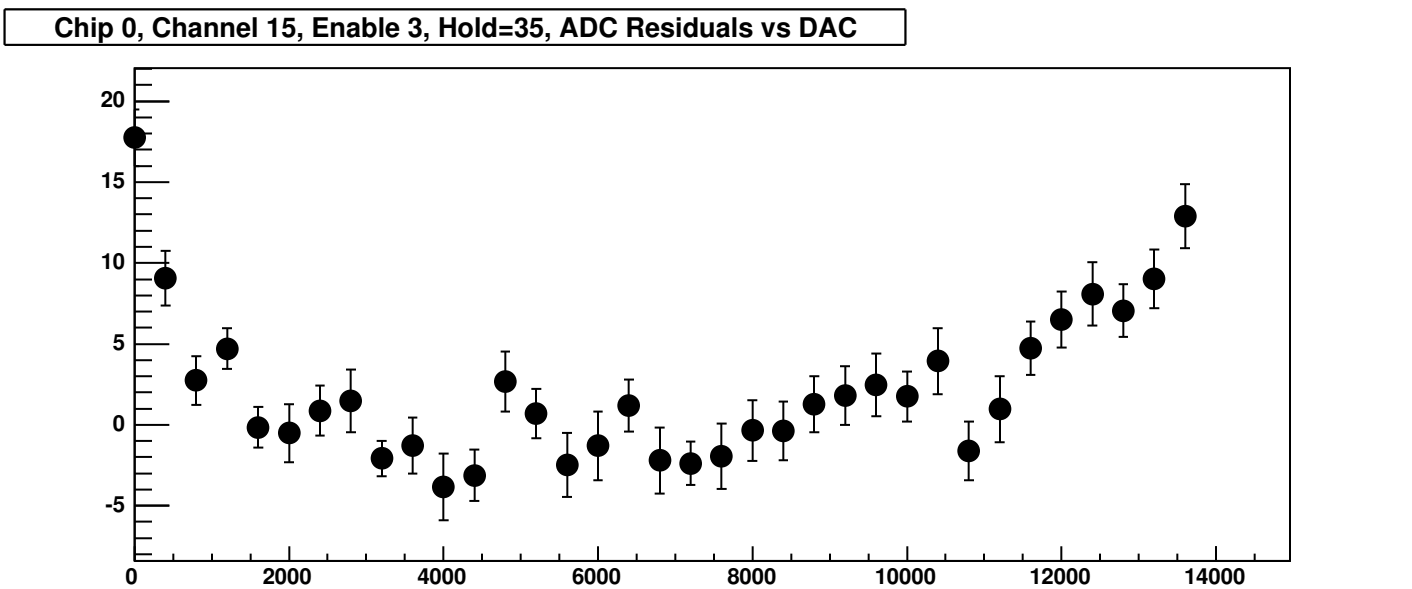

Chip 0, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 458.1 / 23
p0 42.7 ± 1.816
p1 2.181 ± 0.0006543
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

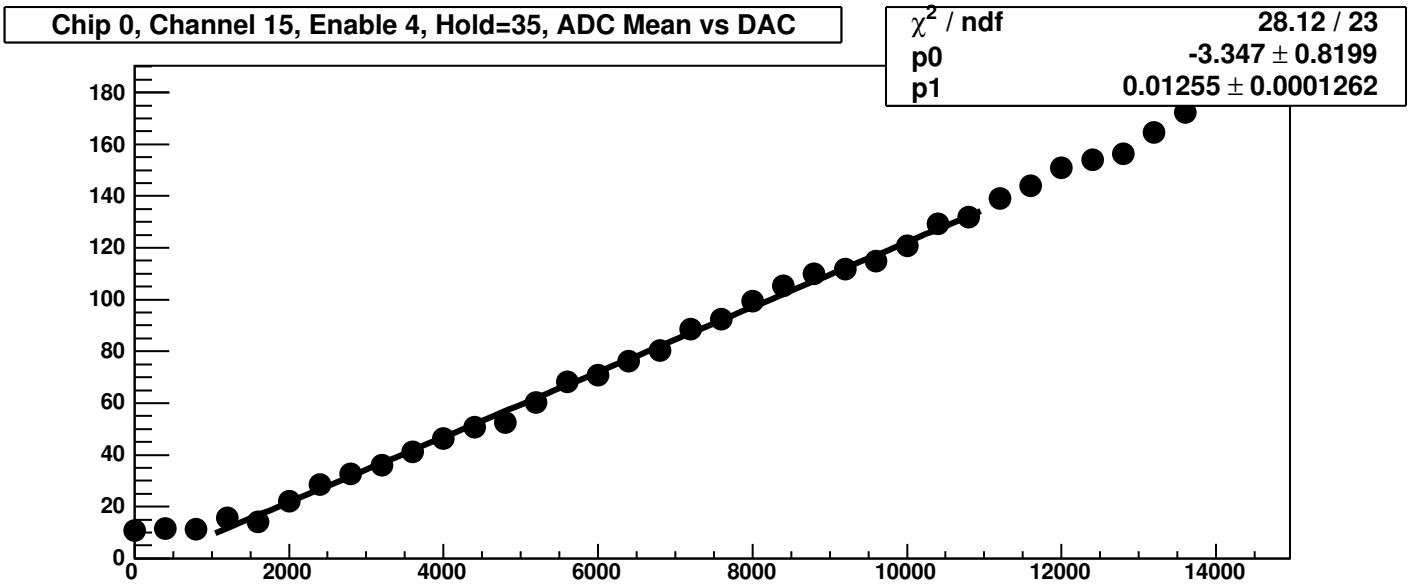

Chip 0, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

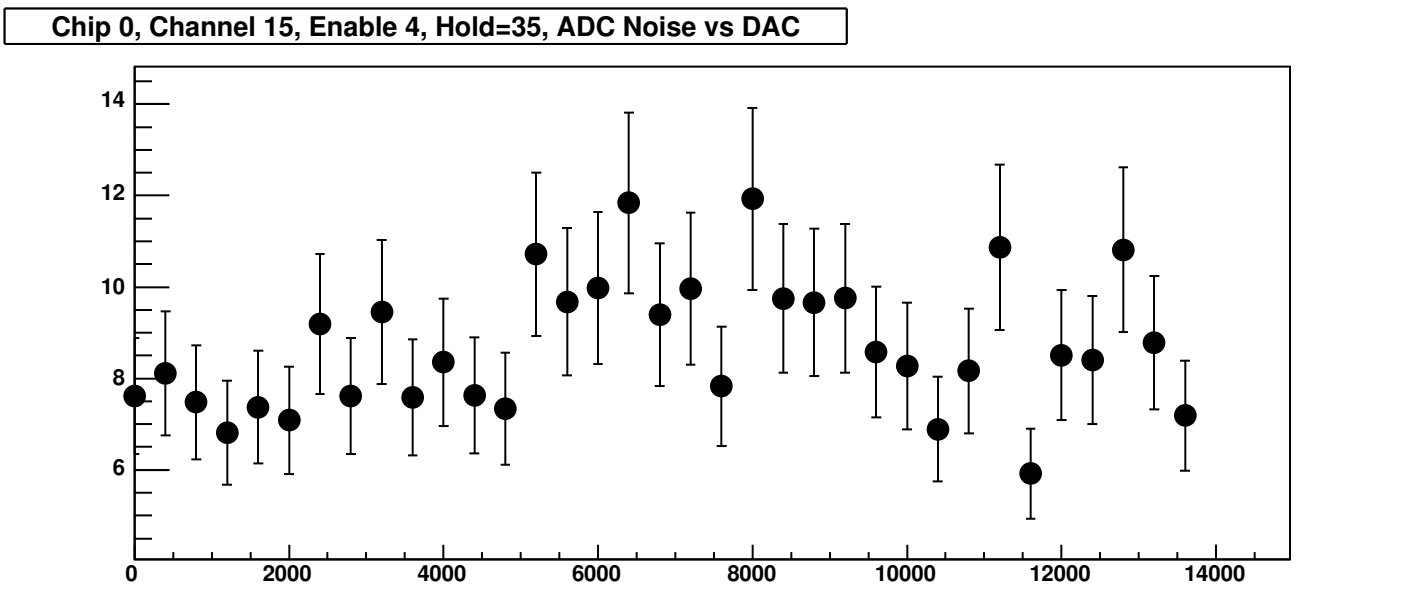

Chip 0, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

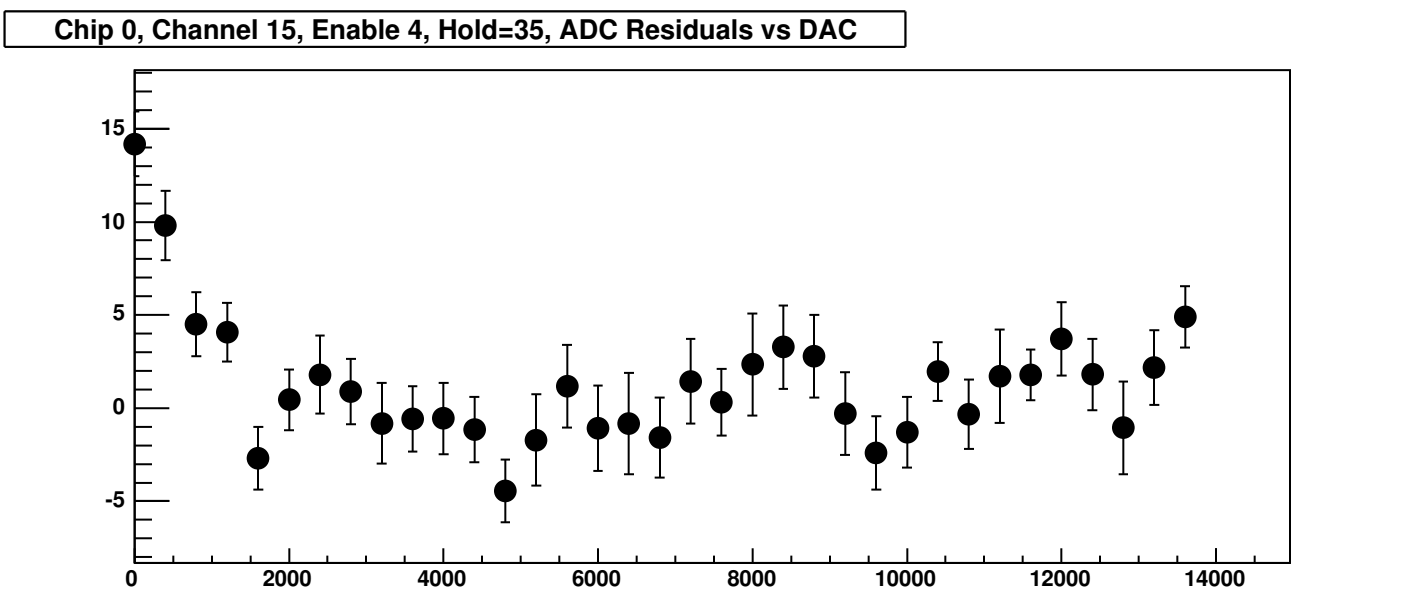

Chip 0, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 45.67 / 23
p0 -5.113 ± 0.6937
p1 0.01179 ± 0.0001113
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

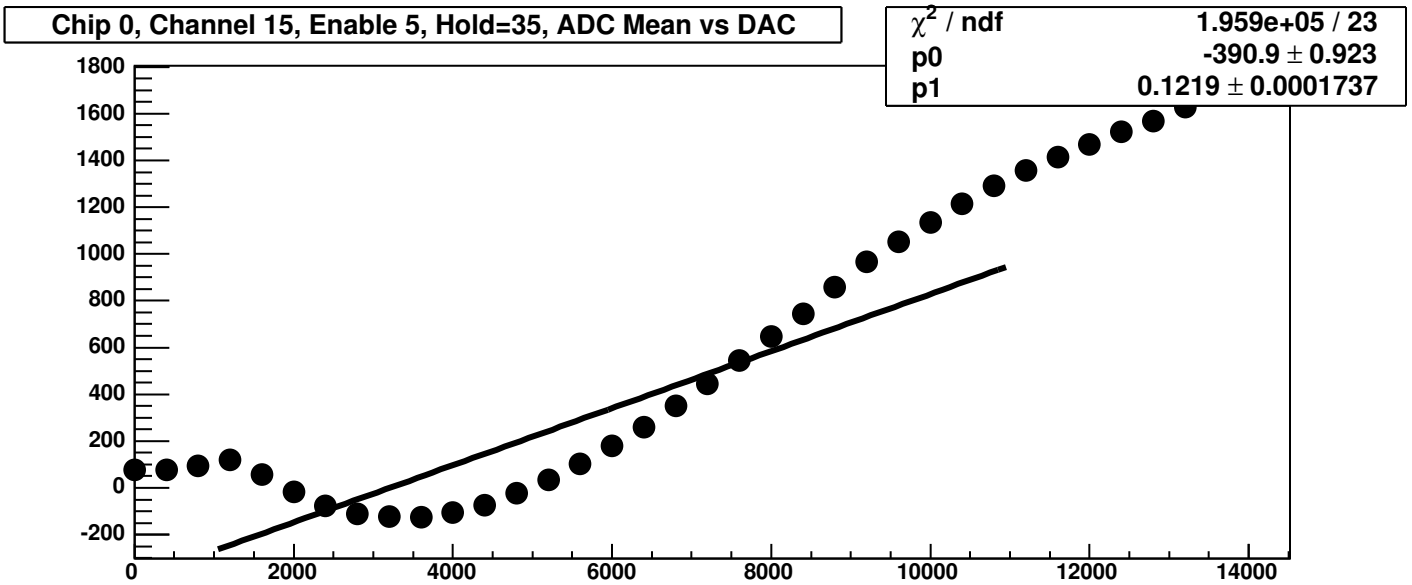

Chip 0, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

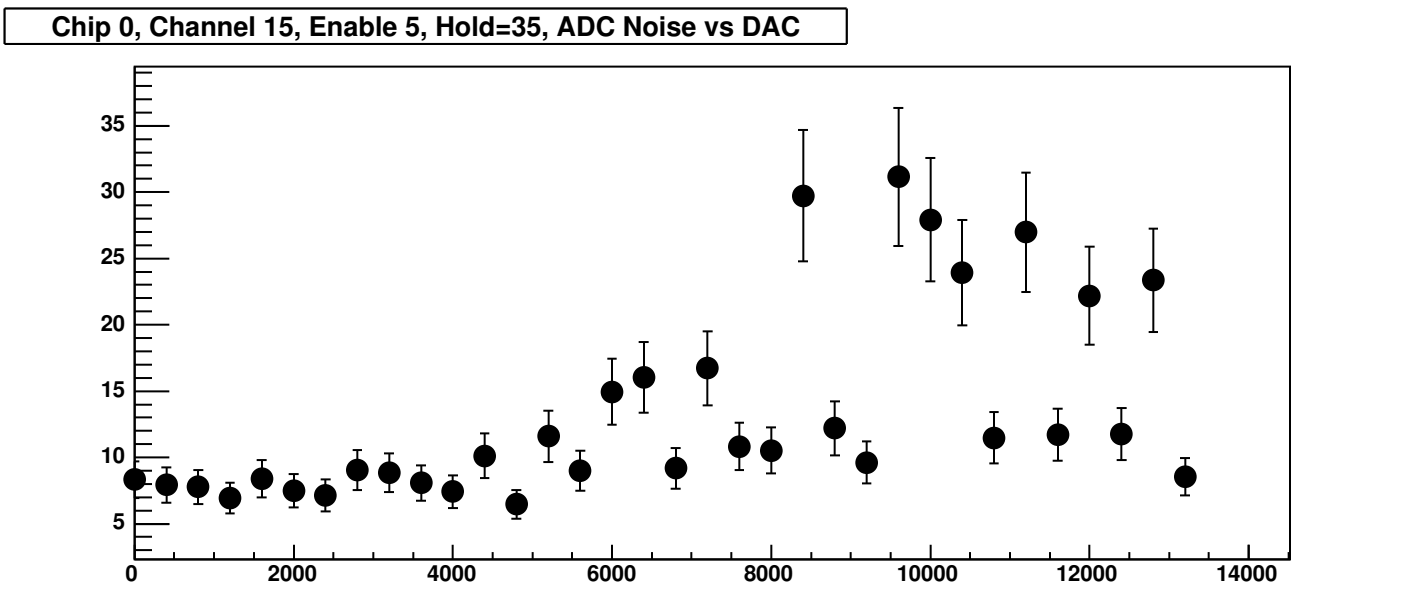

Chip 0, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

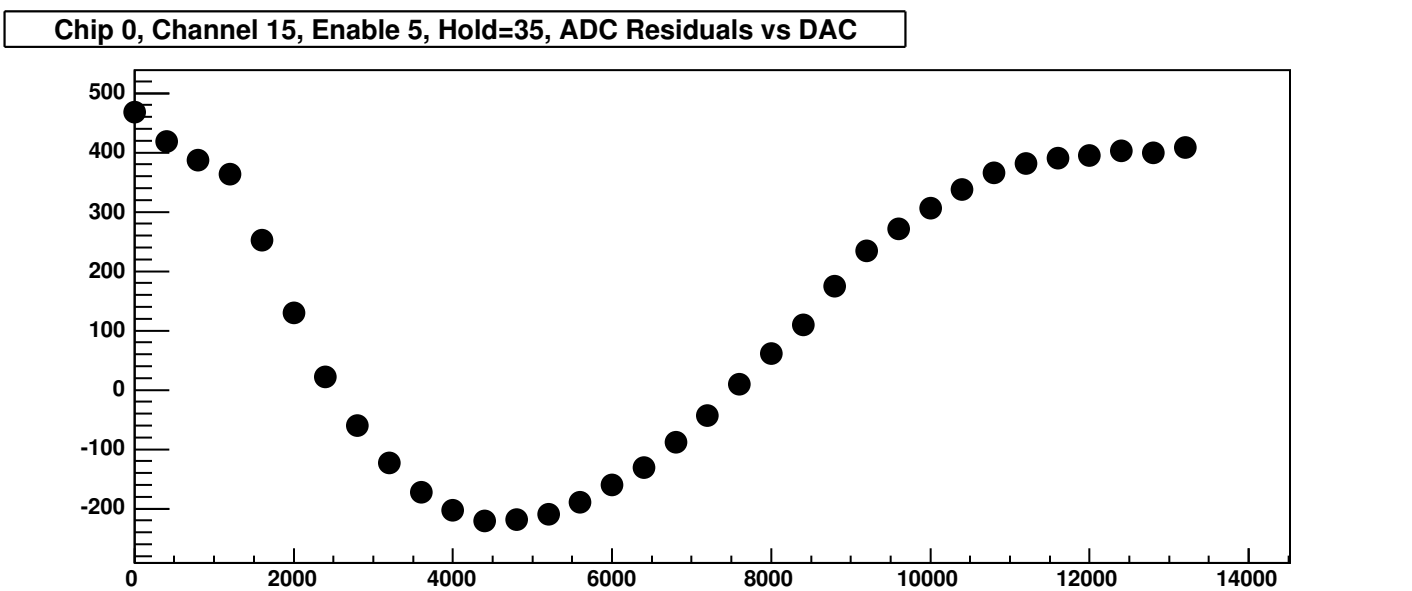

Chip 0, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 28.12 / 23
p0 -3.347 ± 0.8199
p1 0.01255 ± 0.0001262
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

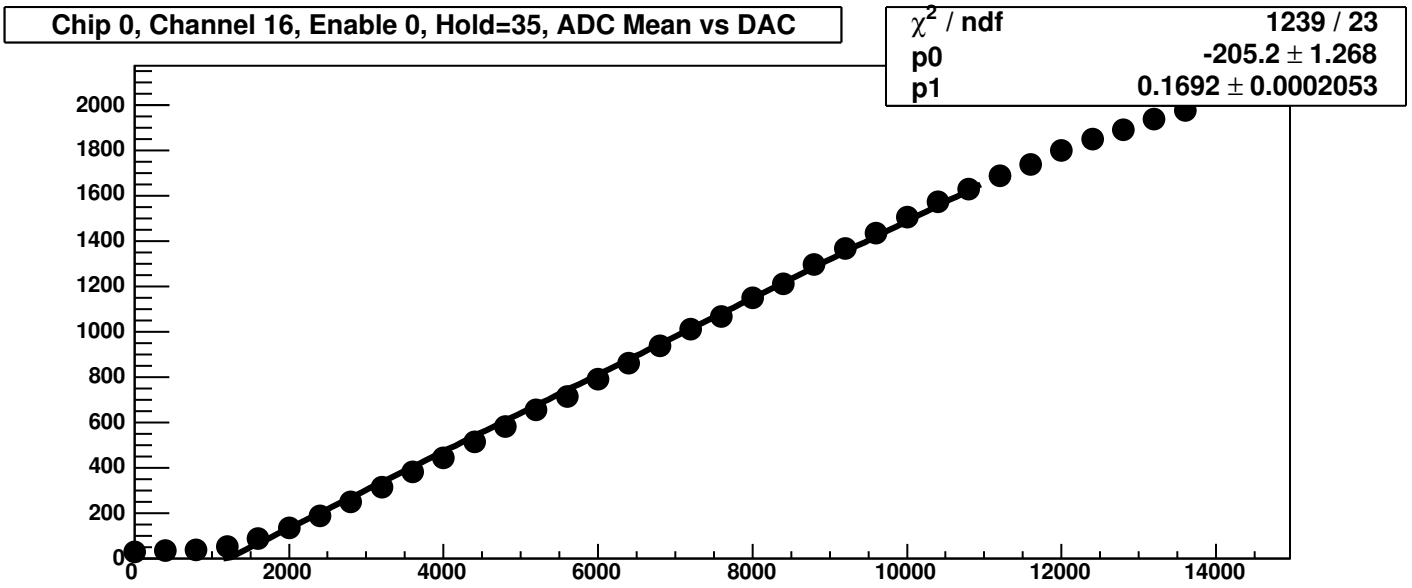

Chip 0, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

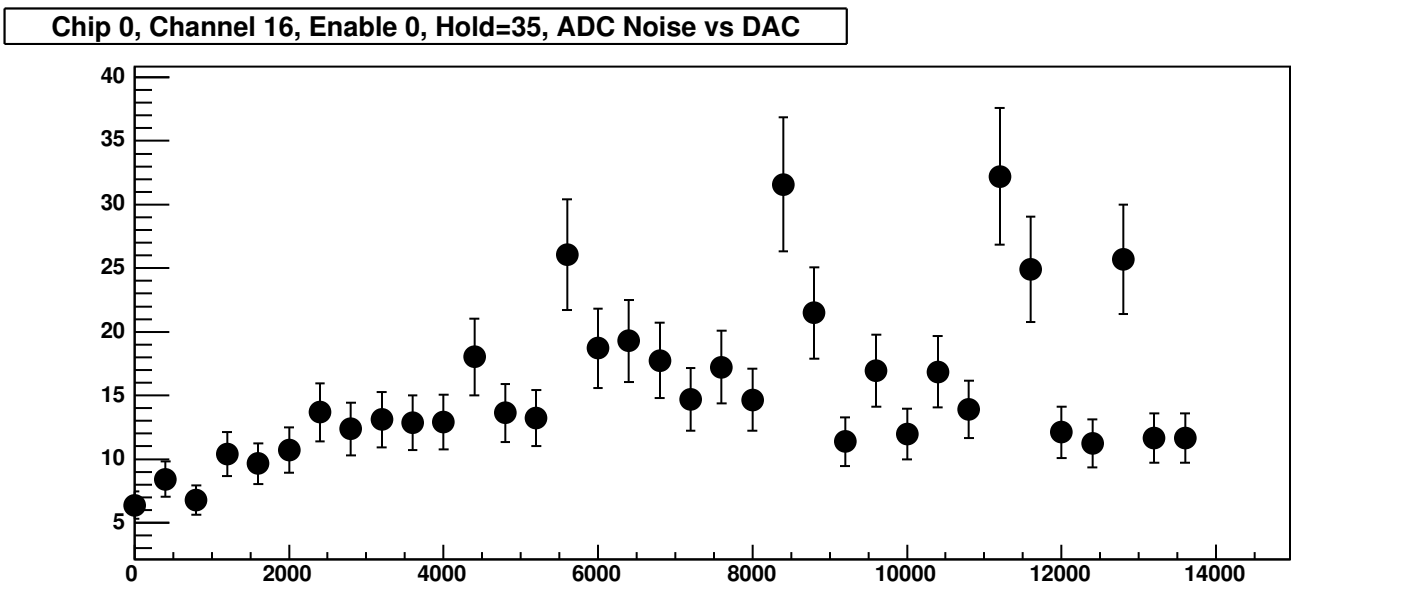

Chip 0, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

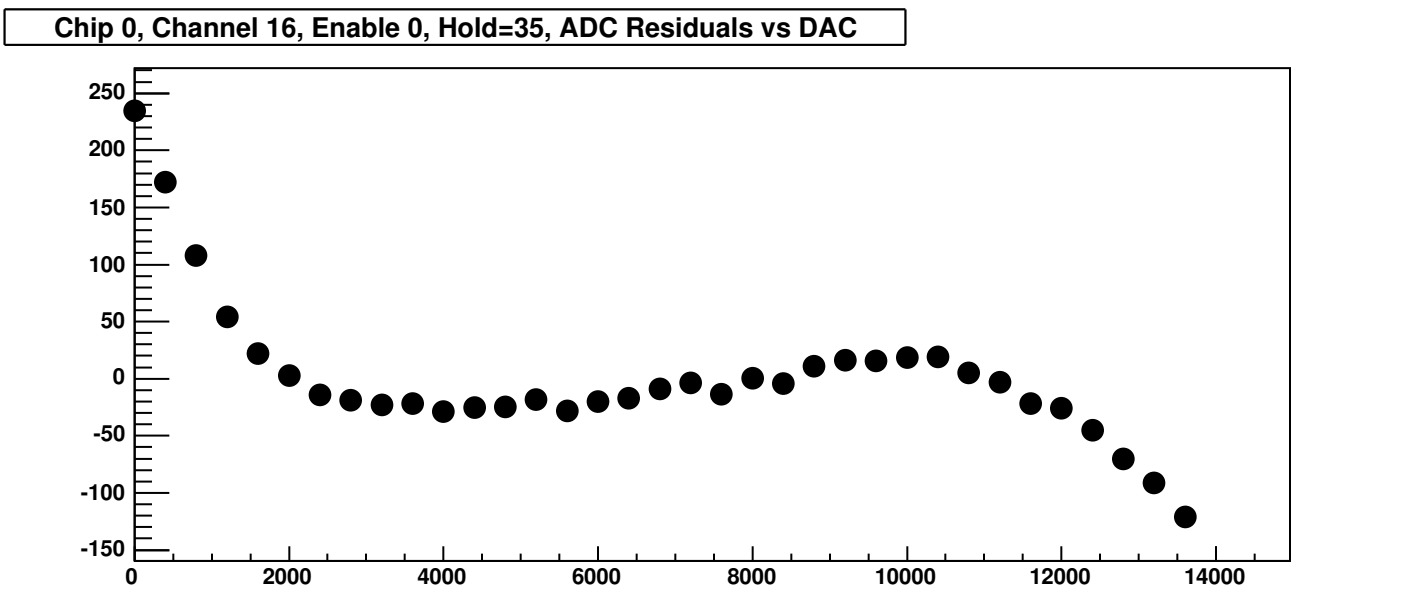

Chip 0, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.959e+05 / 23
p0 -390.9 ± 0.923
p1 0.1219 ± 0.0001737
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

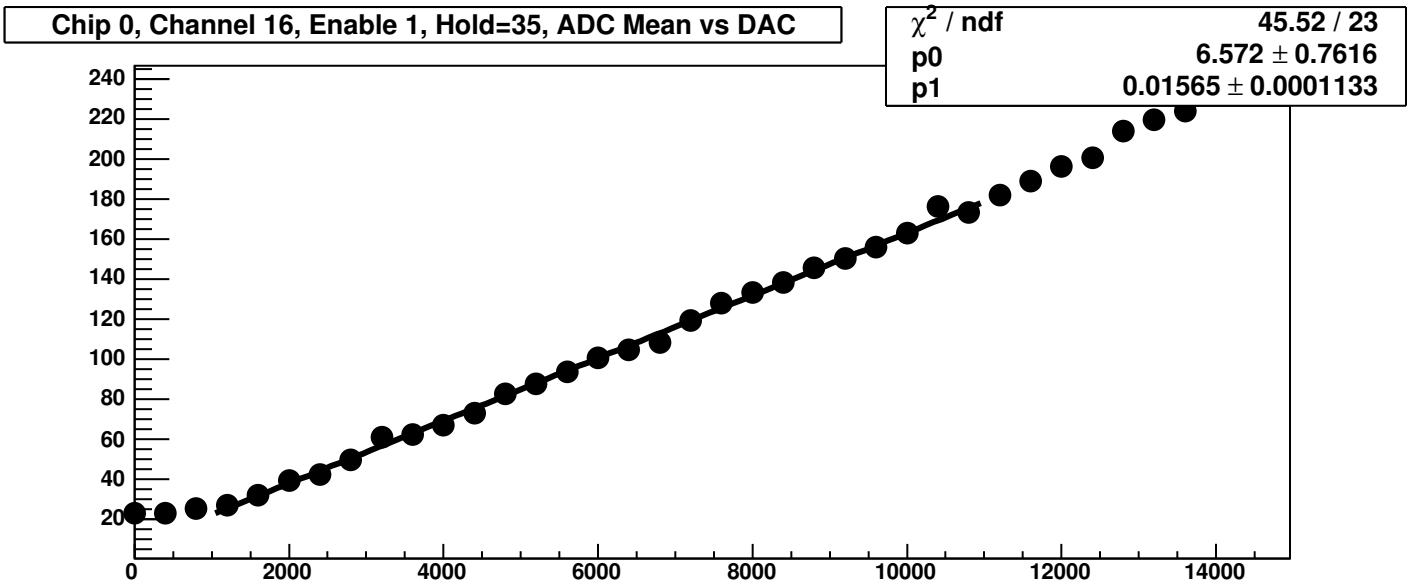

Chip 0, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

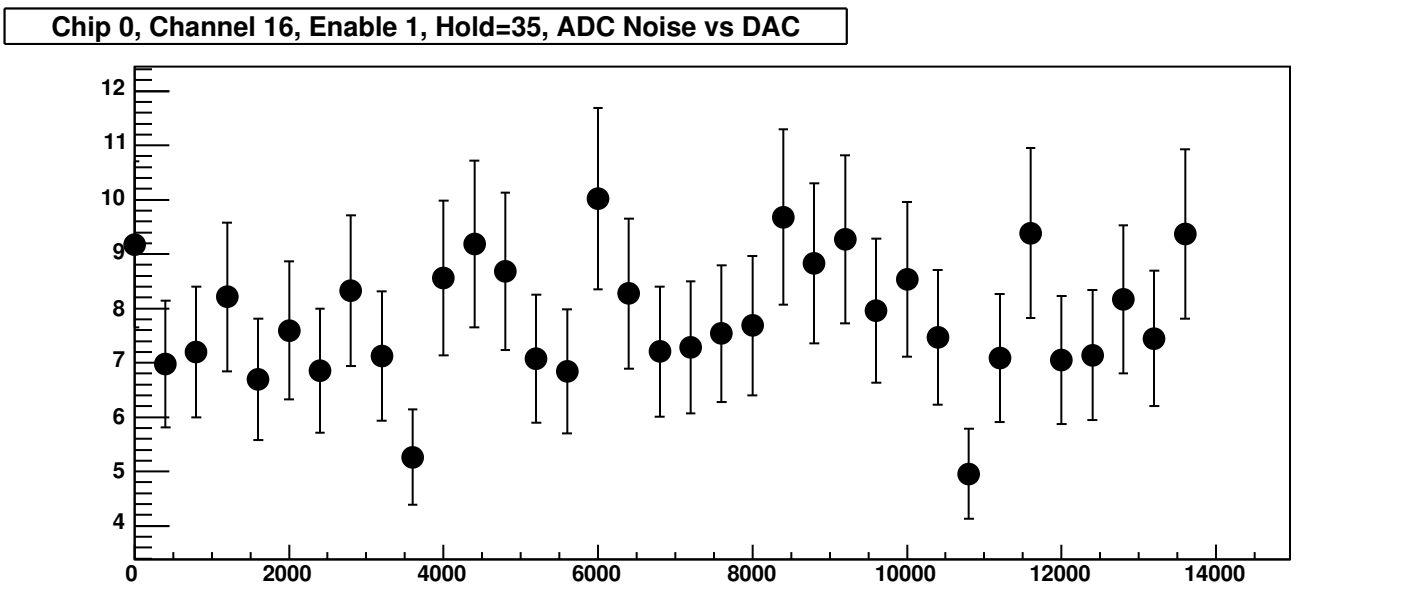

Chip 0, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

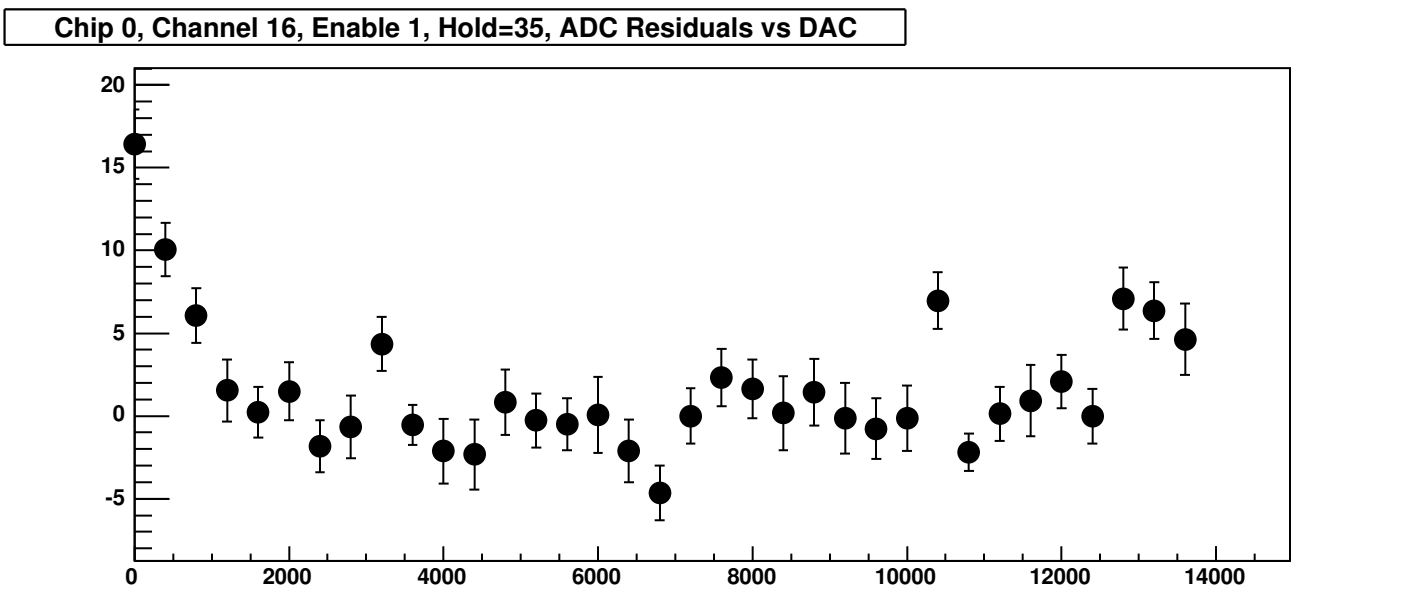

Chip 0, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1239 / 23
p0 -205.2 ± 1.268
p1 0.1692 ± 0.0002053
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

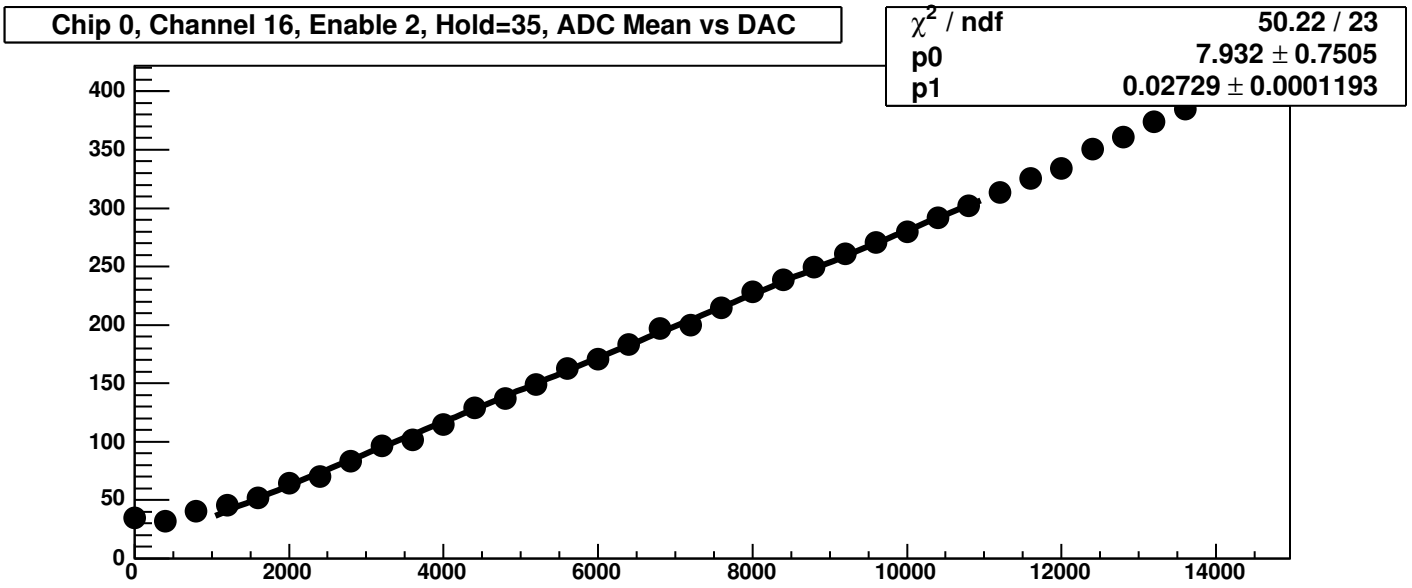

Chip 0, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

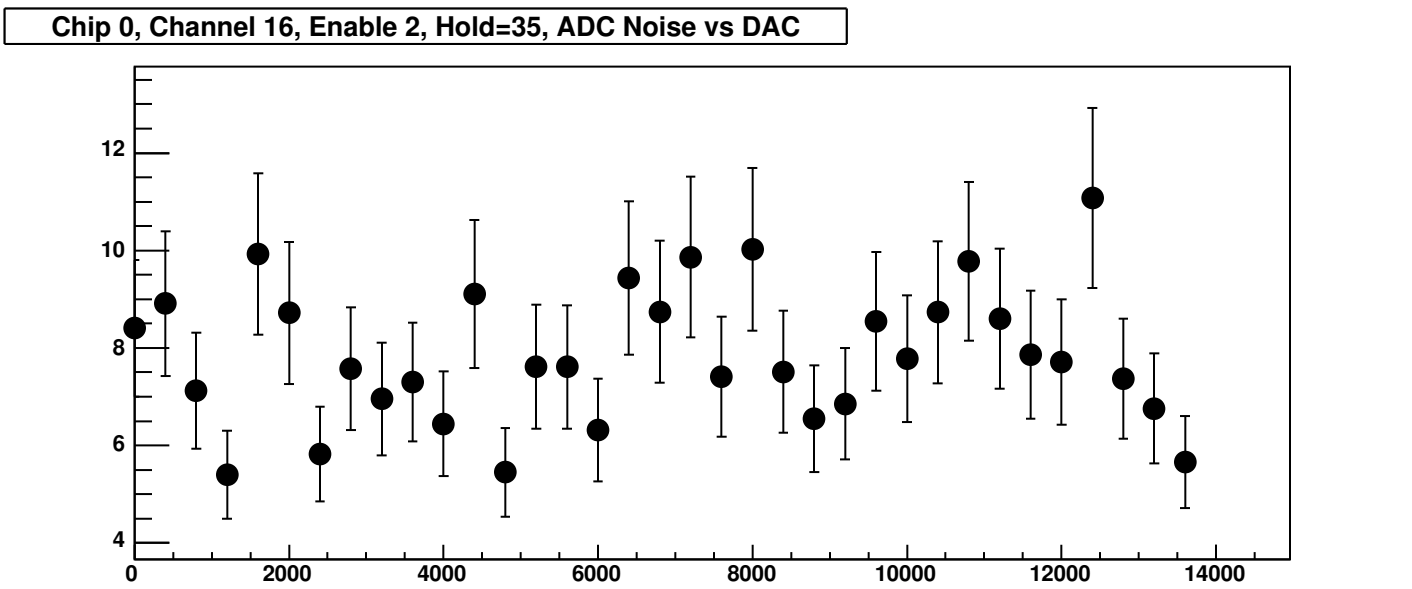

Chip 0, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

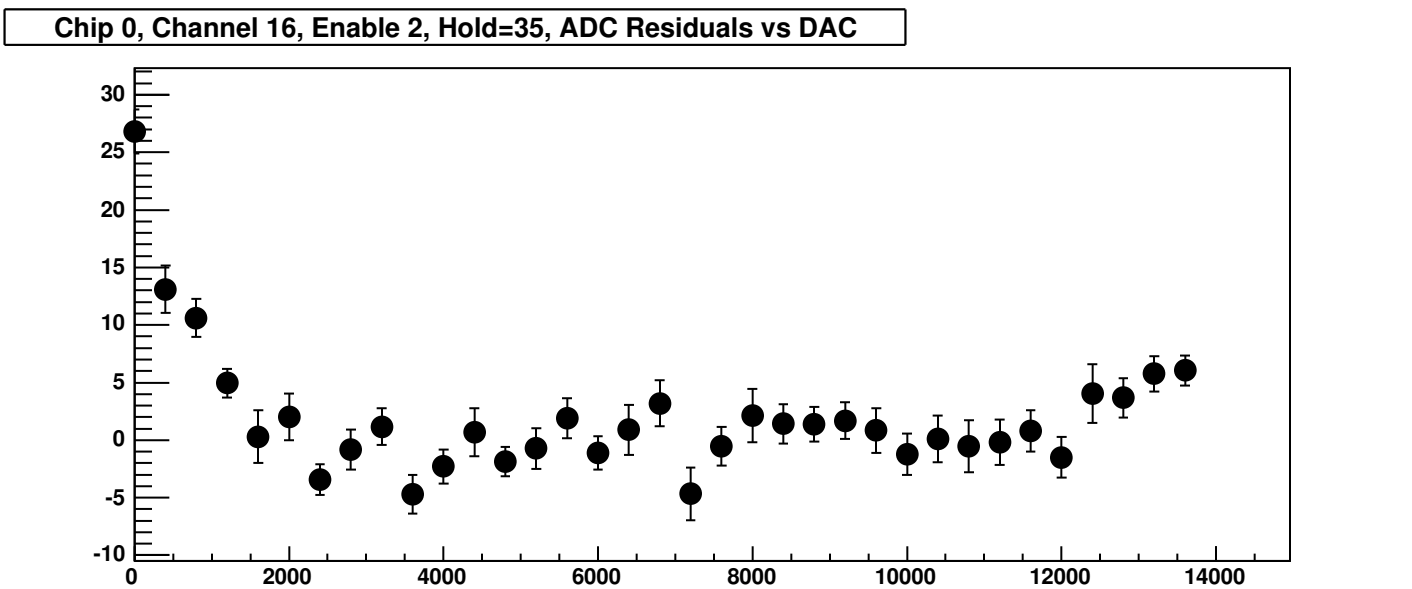

Chip 0, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 45.52 / 23
p0 6.572 ± 0.7616
p1 0.01565 ± 0.0001133
240
220
200
180
160
140
120
100
80
60
40
20
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

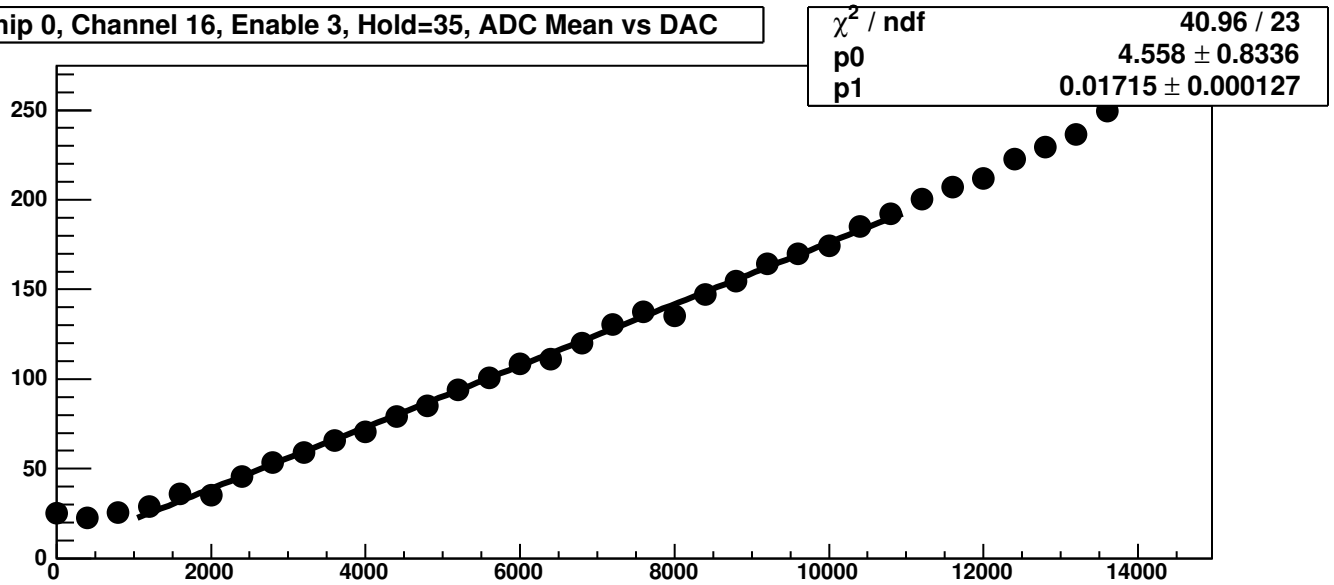

Chip 0, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 50.22 / 23
p0 7.932 ± 0.7505
p1 0.02729 ± 0.0001193
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

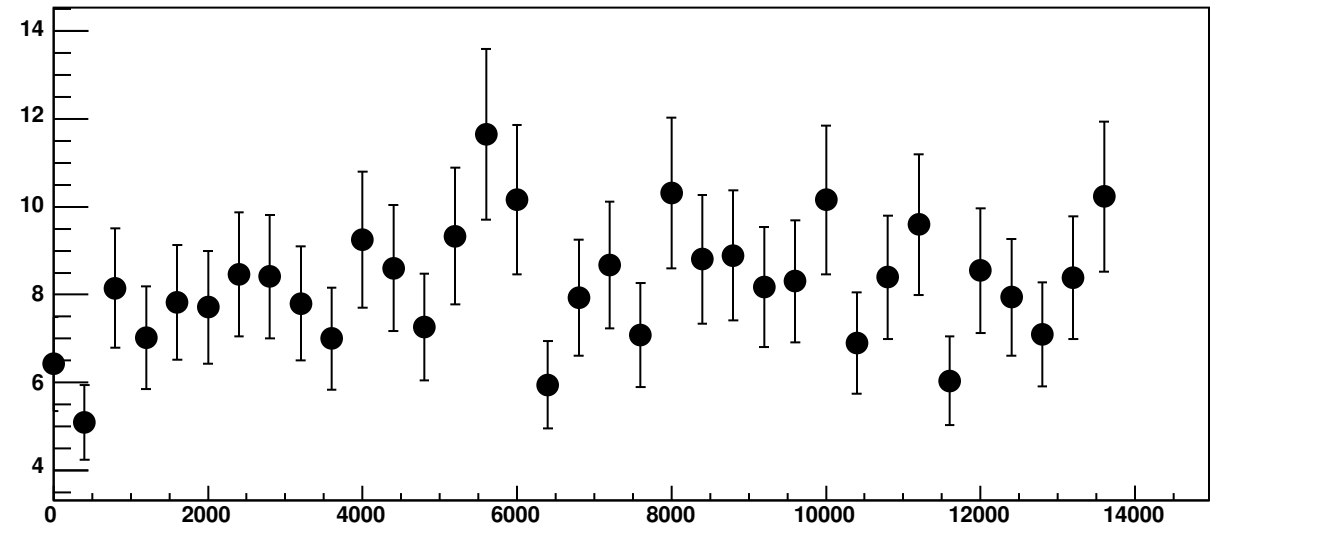

Chip 0, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

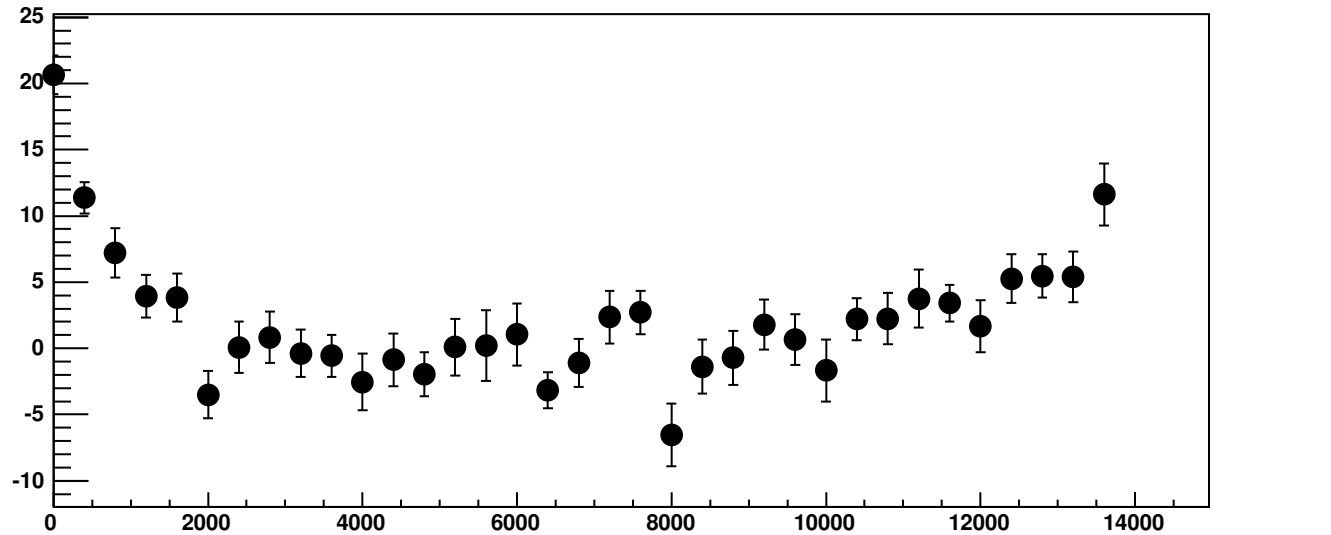
Chip 0, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

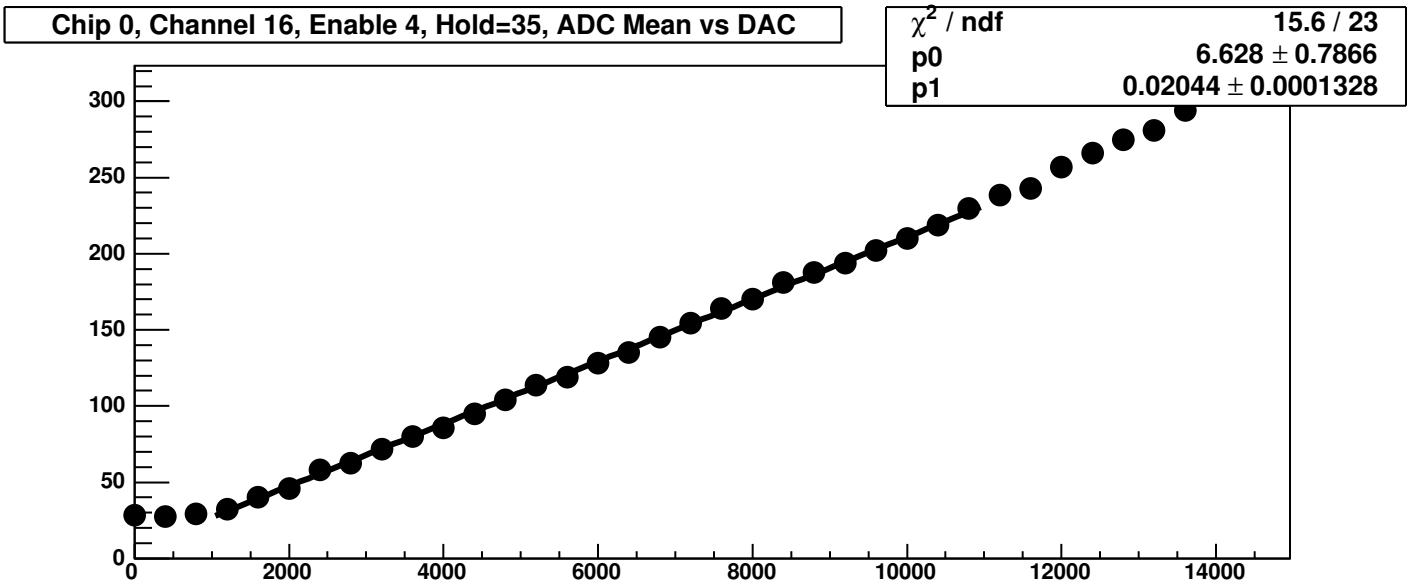


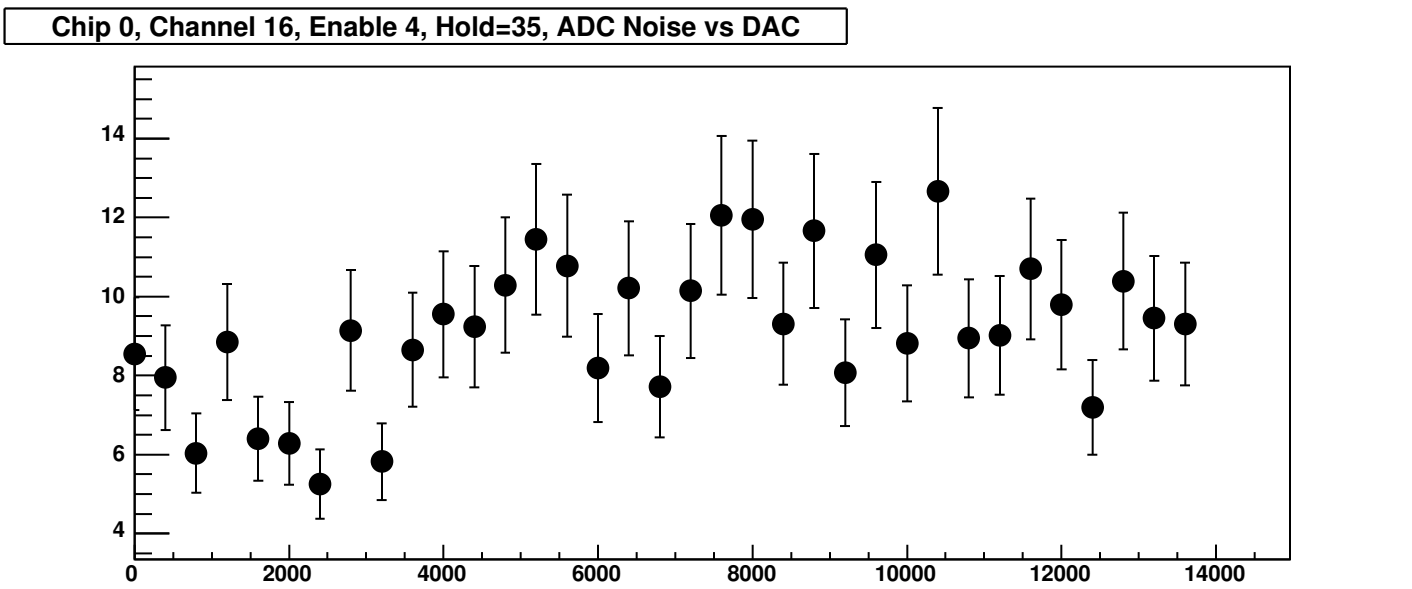
Chip 0, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC

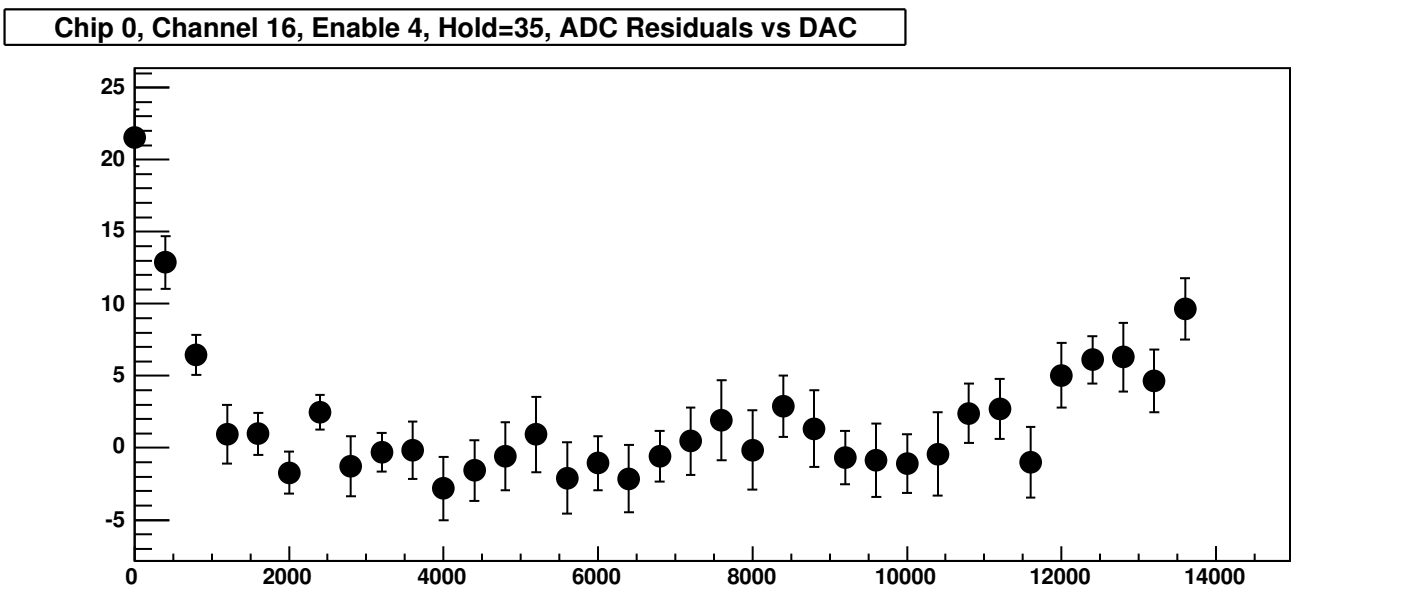


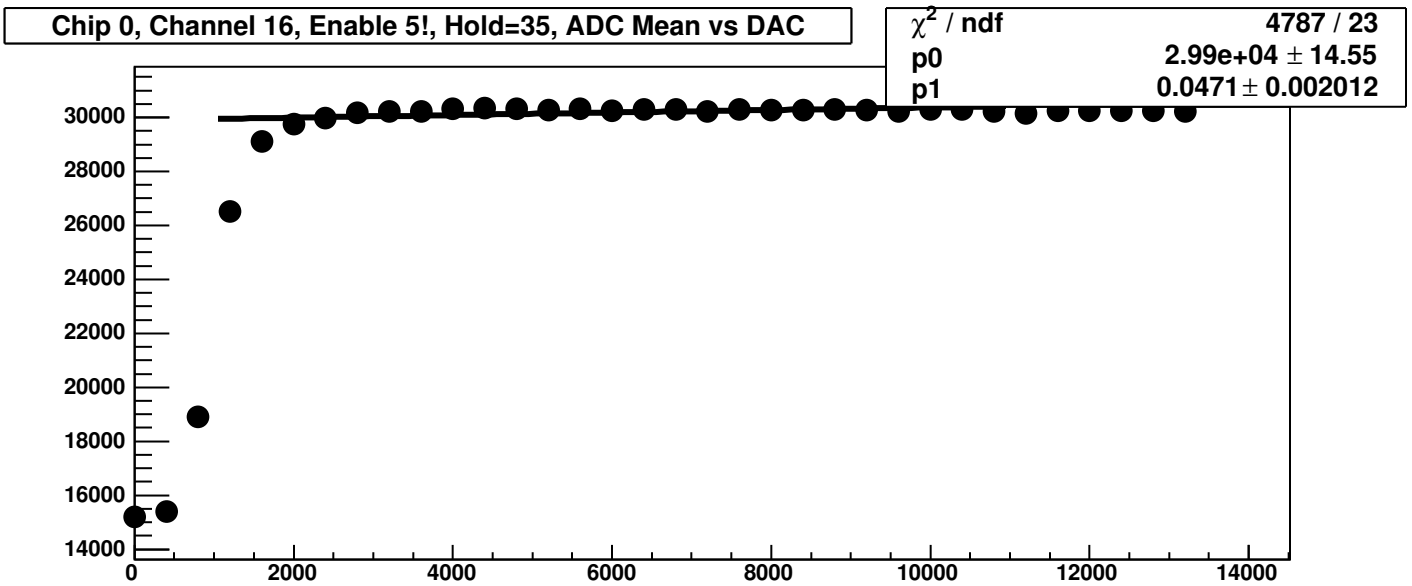
Chip 0, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

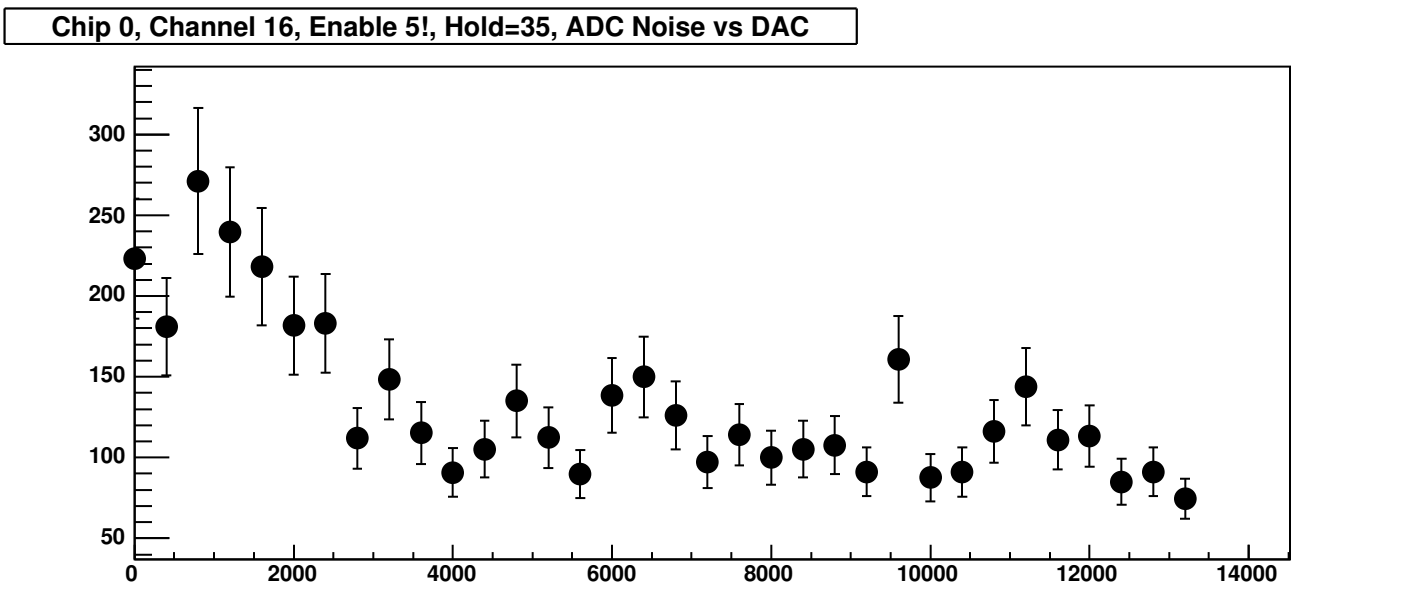


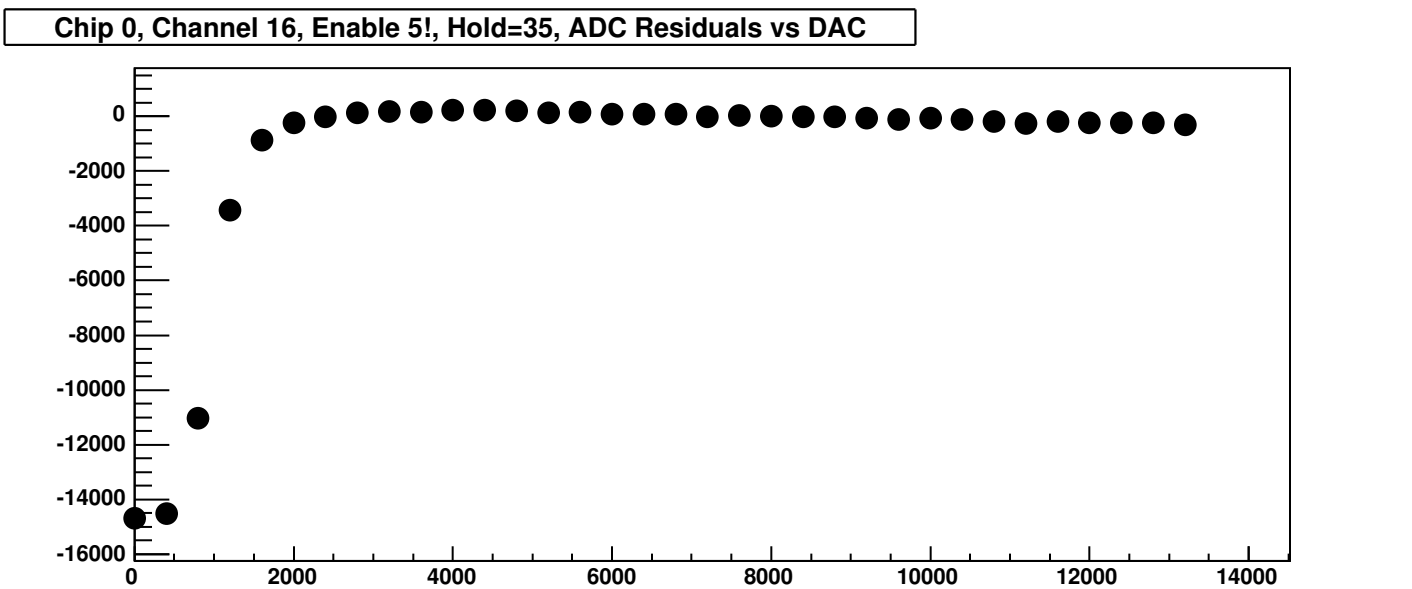

Chip 0, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 15.6 / 23
p0 6.628 ± 0.7866
p1 0.02044 ± 0.0001328
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

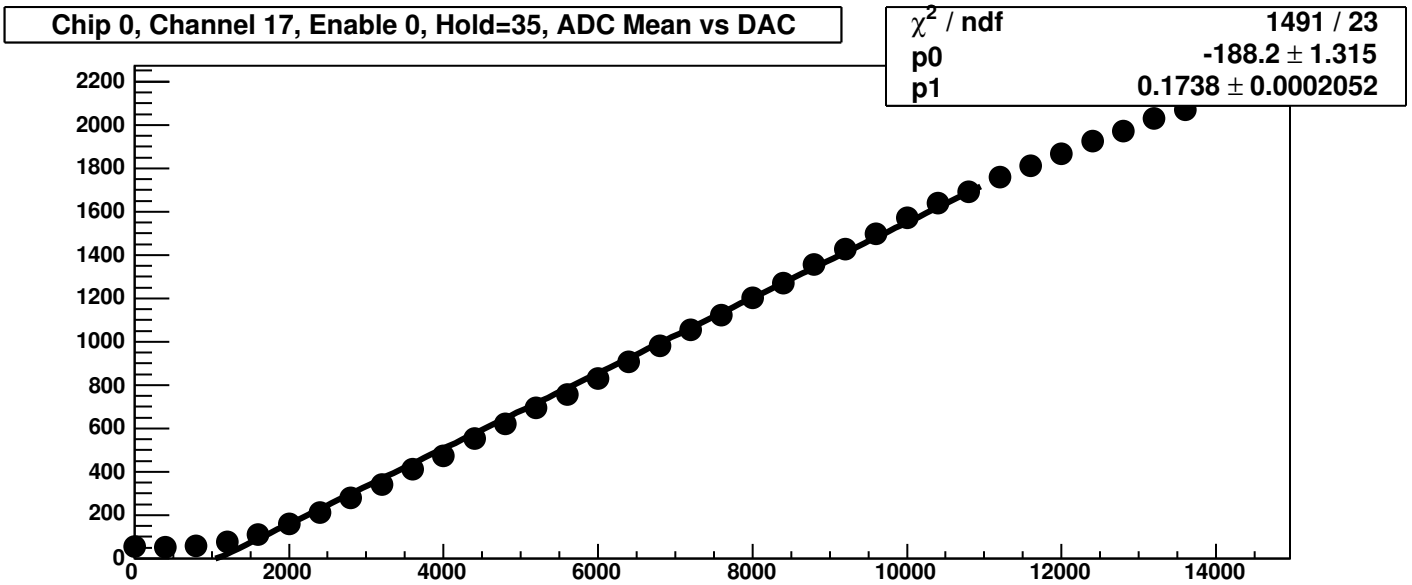

Chip 0, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

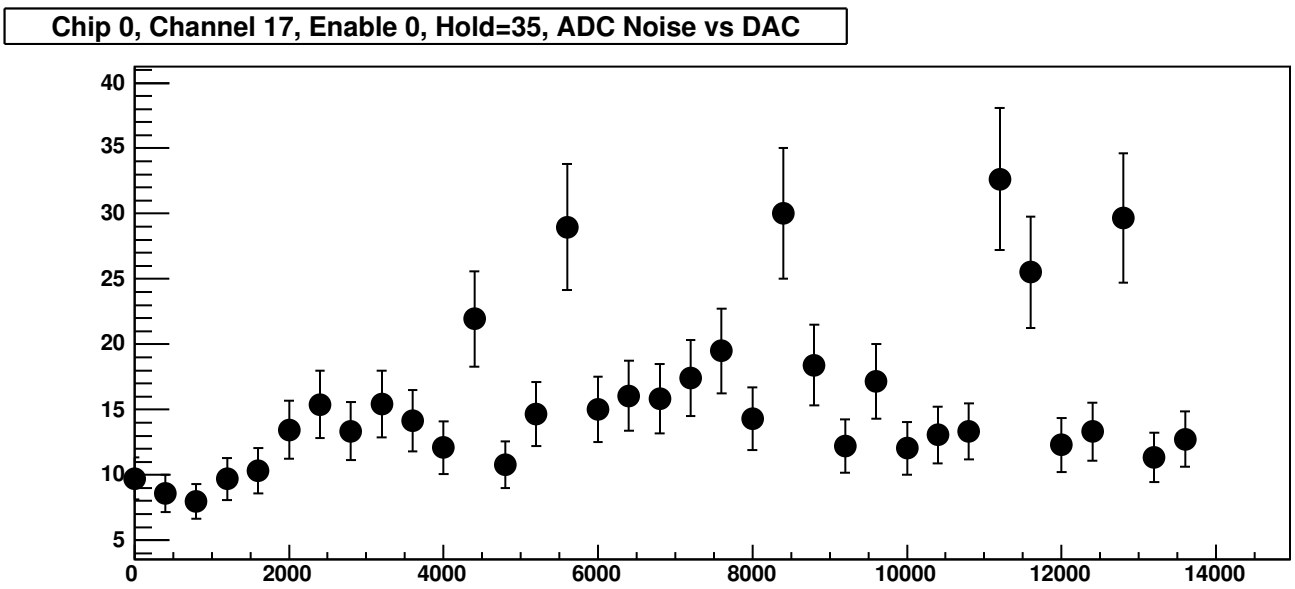

Chip 0, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

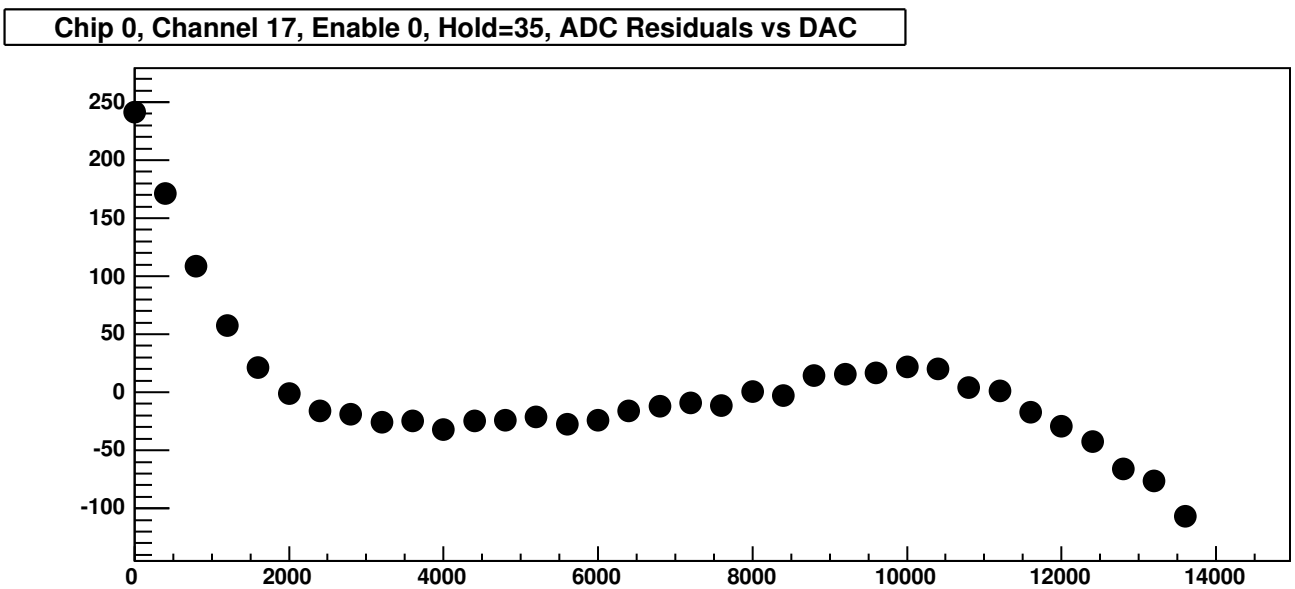

Chip 0, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4787 / 23
p0 2.99e+04 ± 14.55
p1 0.0471 ± 0.002012
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

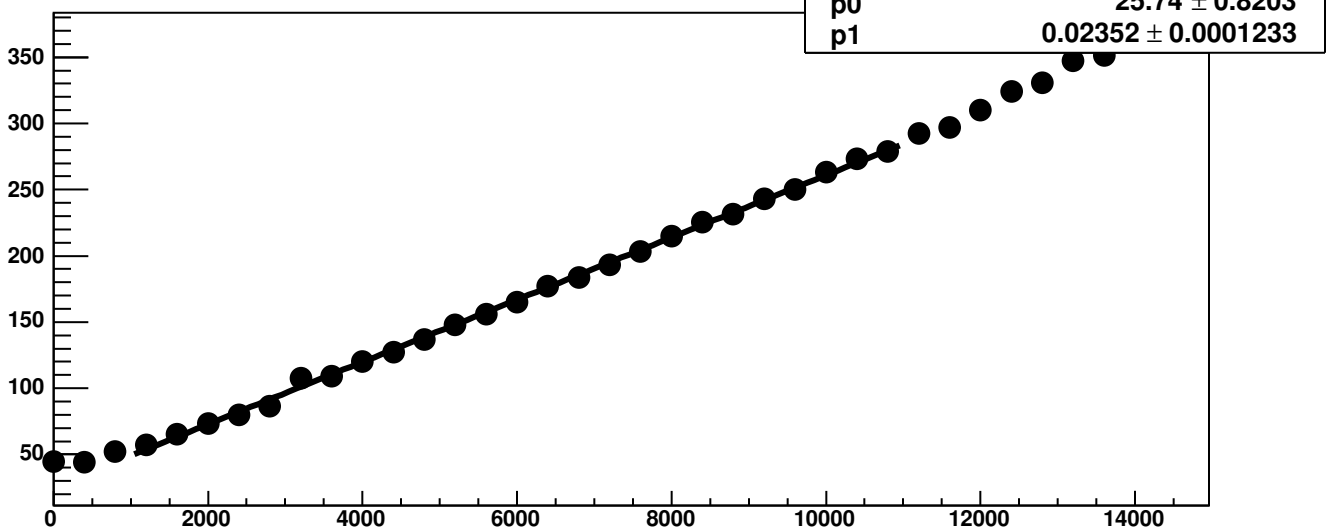

Chip 0, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1491 / 23
p0 -188.2 ± 1.315
p1 0.1738 ± 0.0002052
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

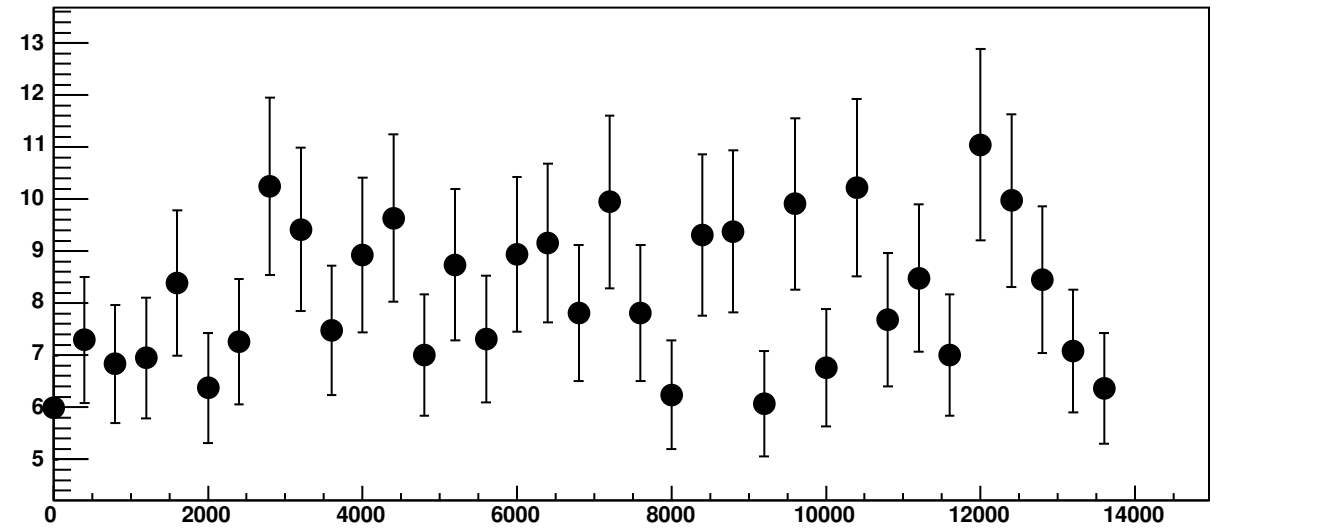

Chip 0, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

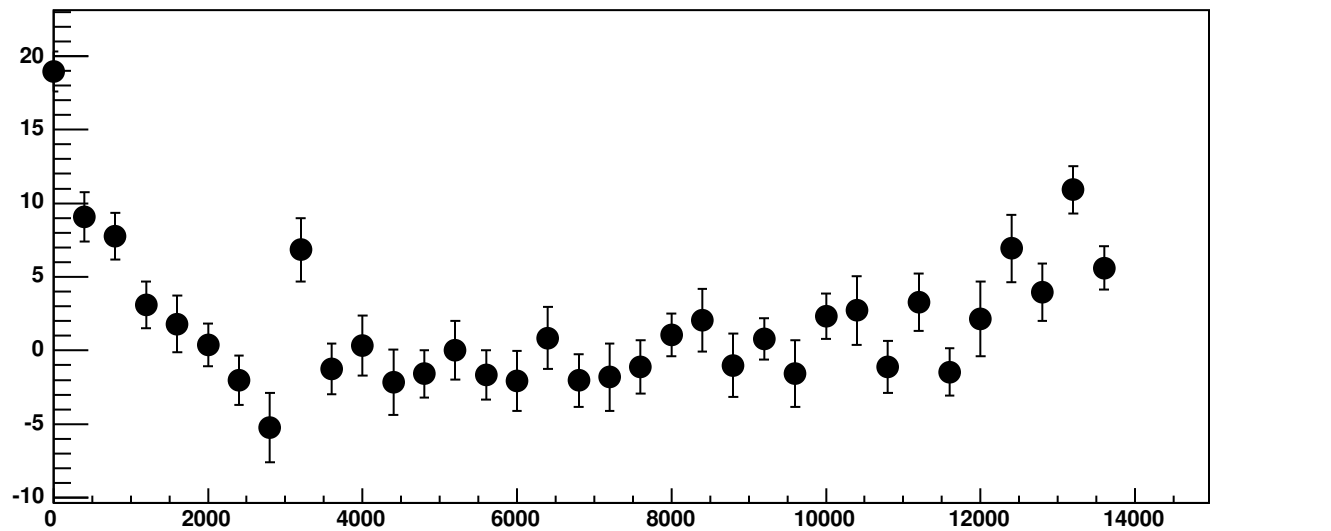
Chip 0, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

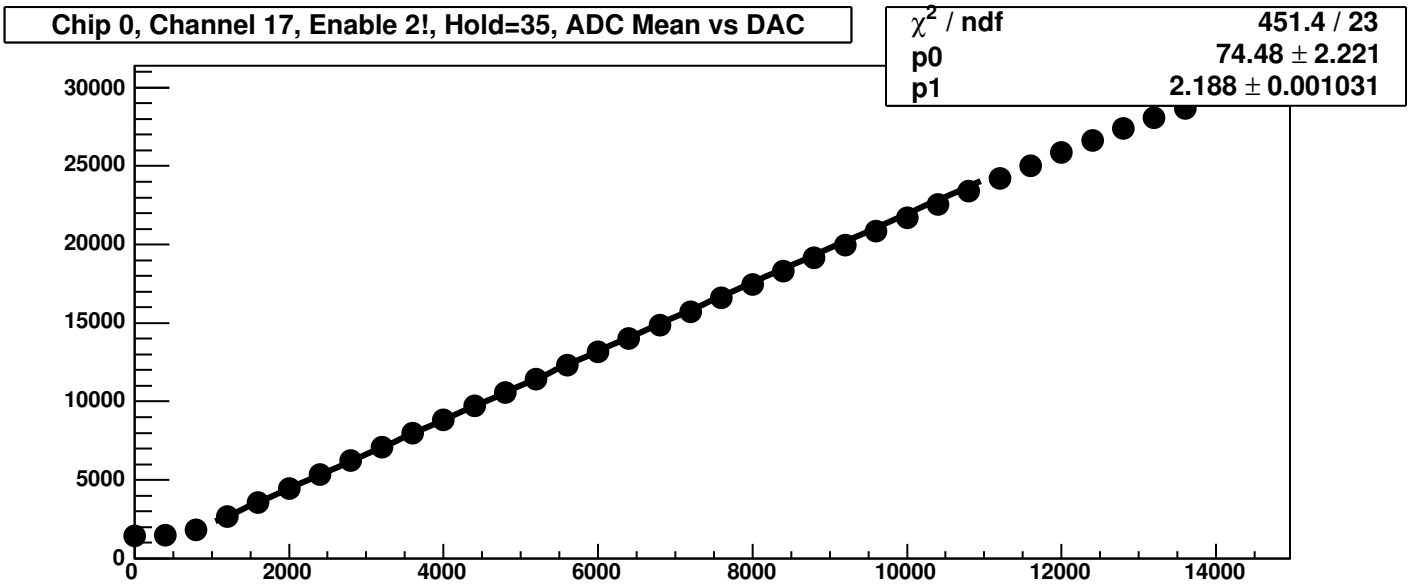


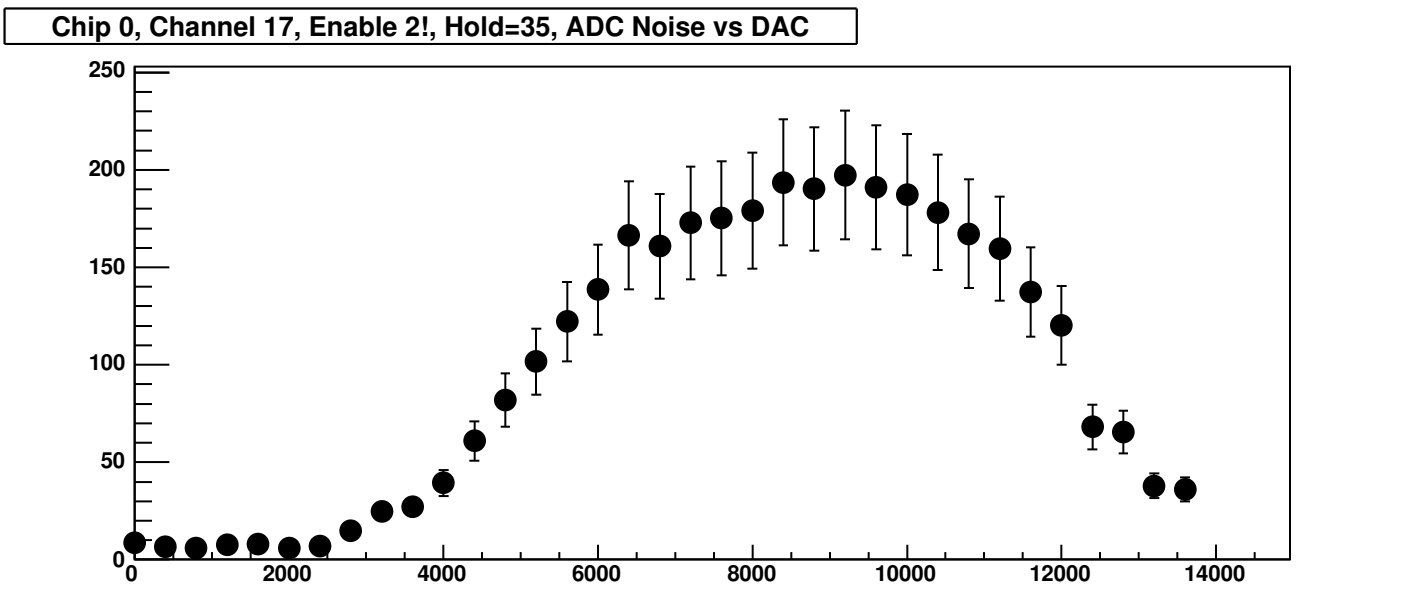
Chip 0, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

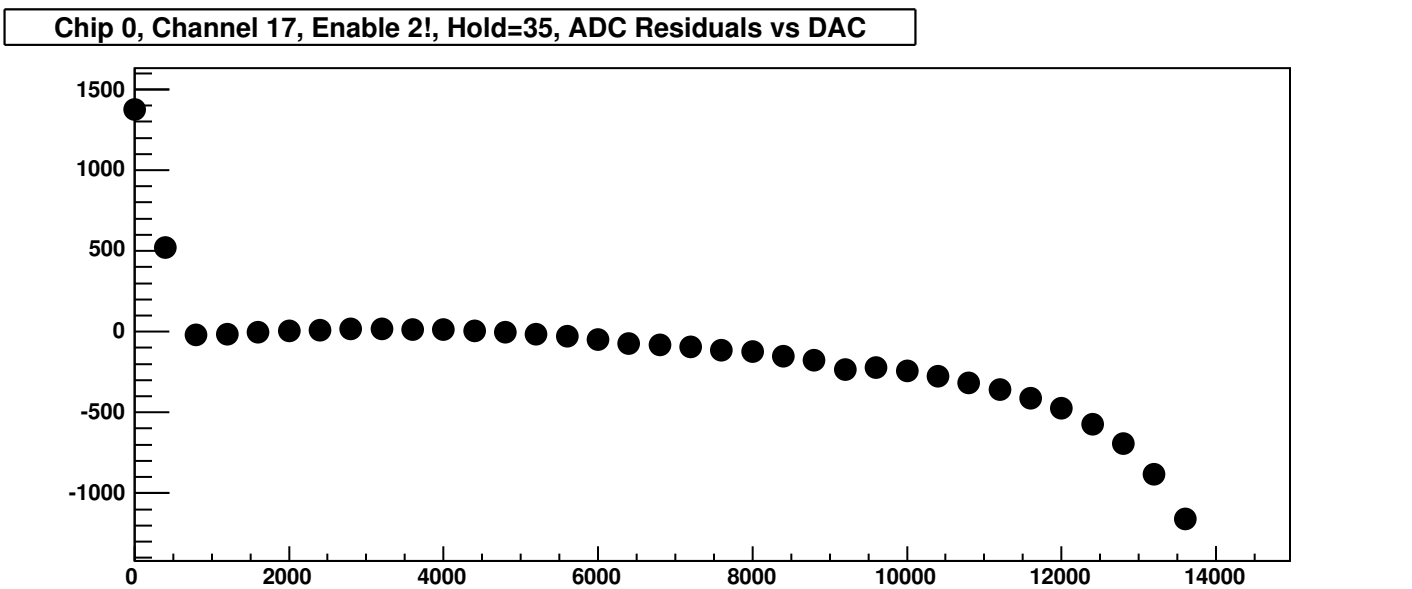


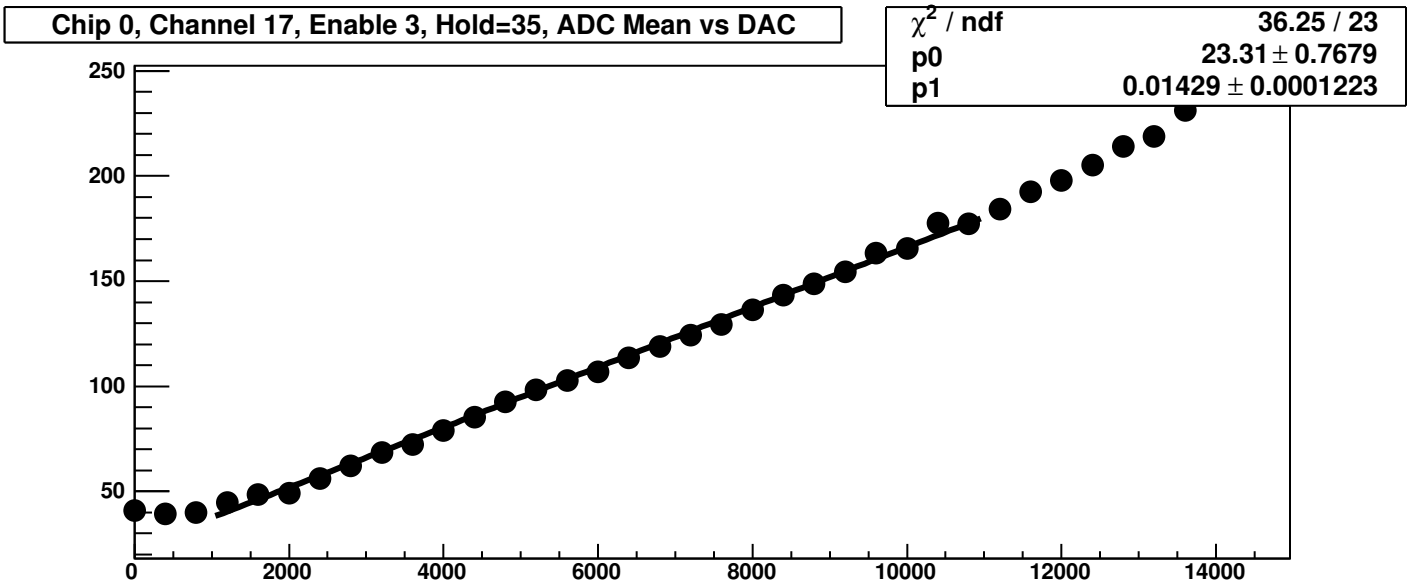
Chip 0, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

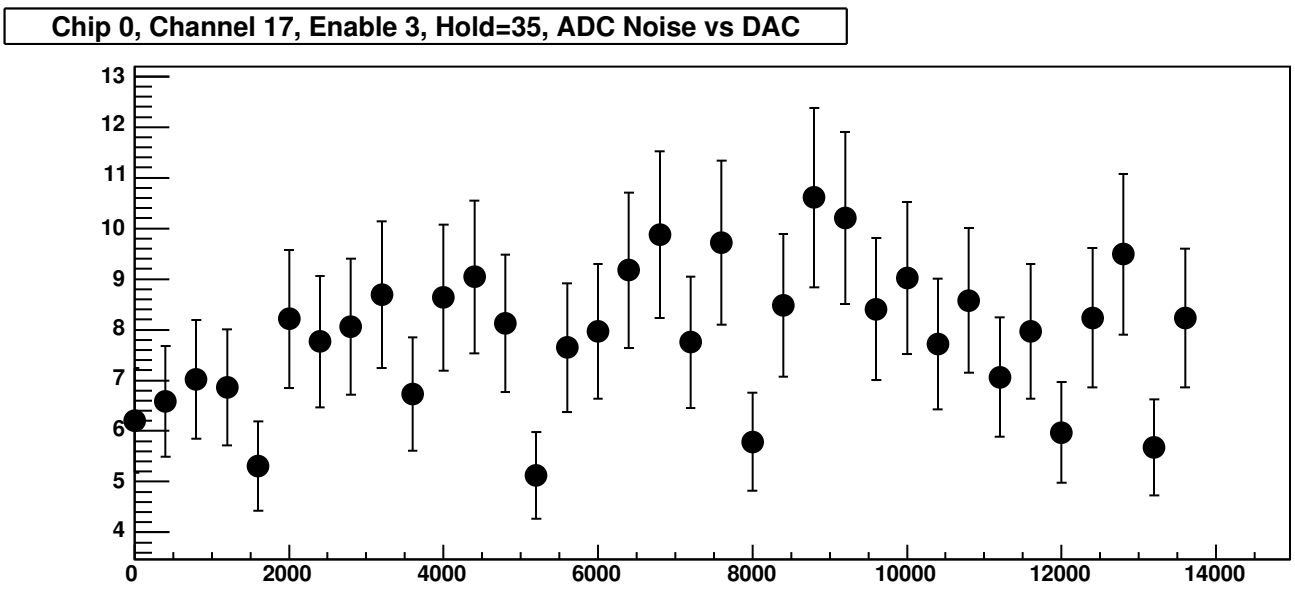


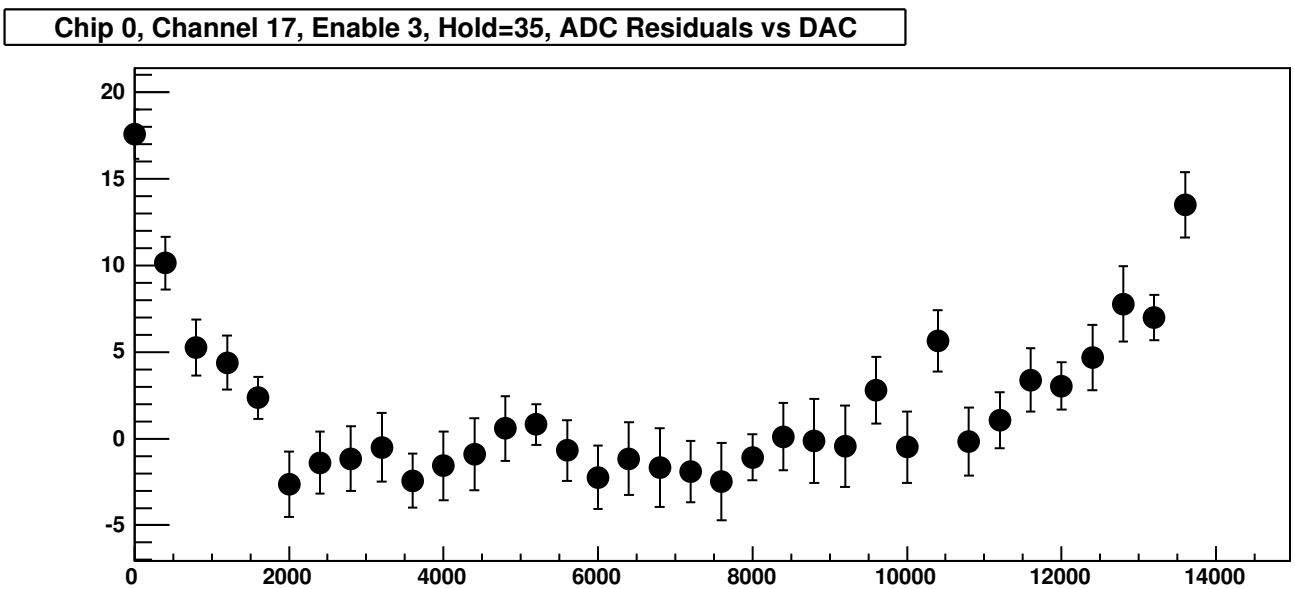

Chip 0, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 451.4 / 23
p0 74.48 ± 2.221
p1 2.188 ± 0.001031
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

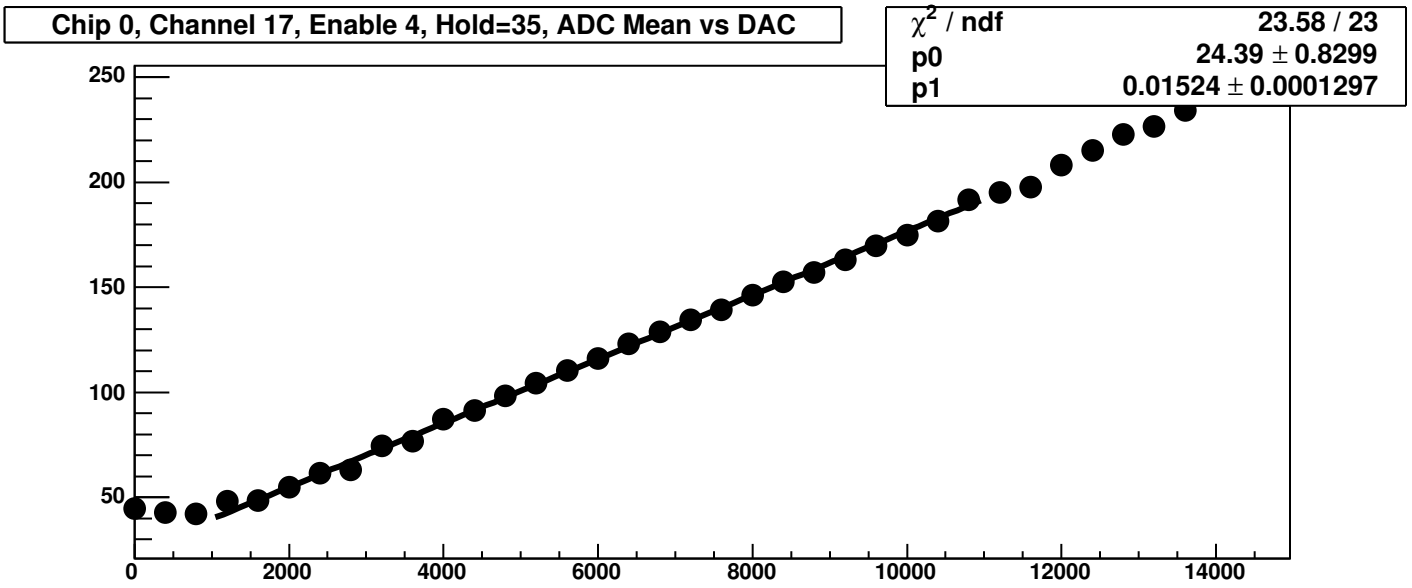

Chip 0, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

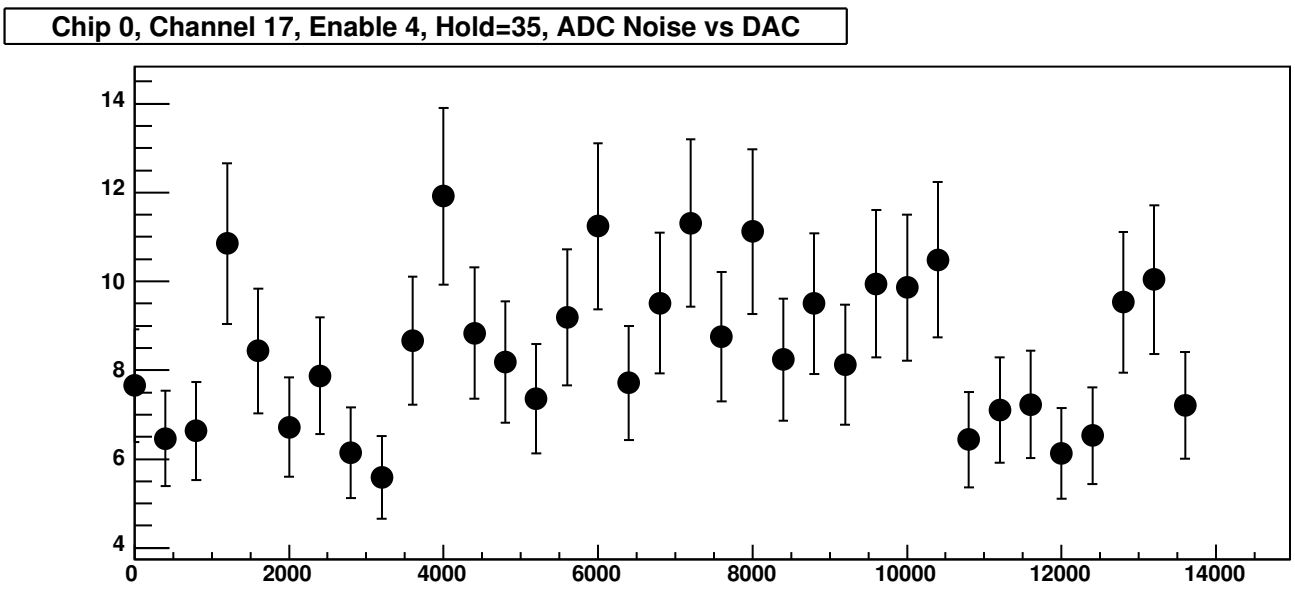

Chip 0, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

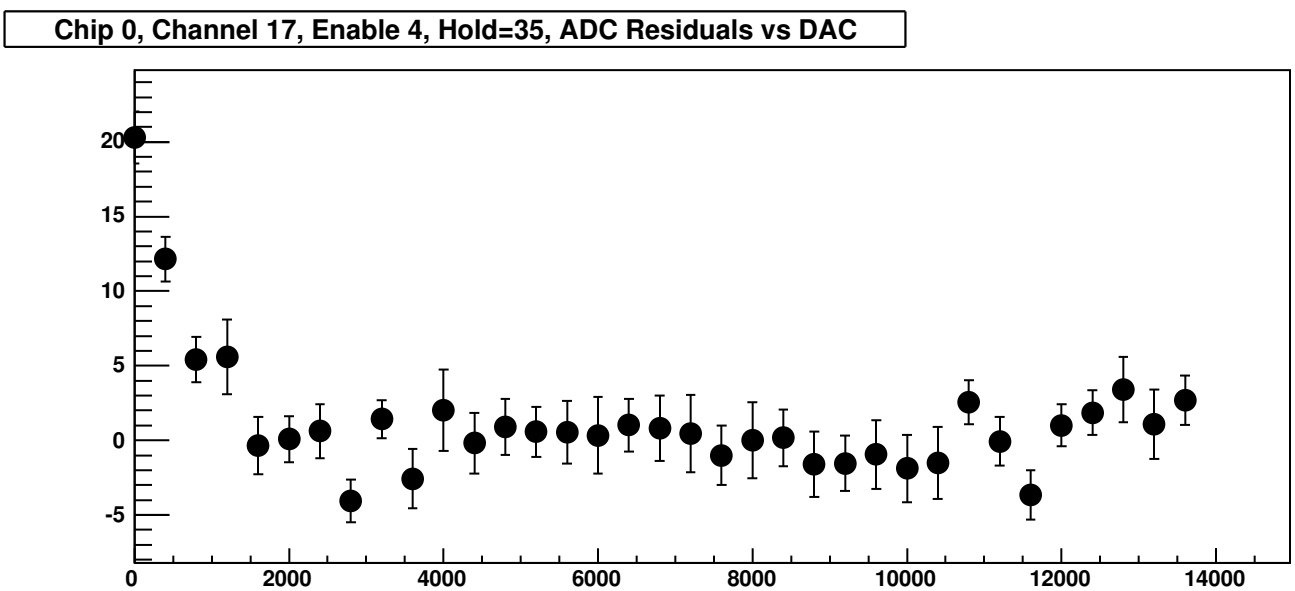

Chip 0, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.25 / 23
p0 23.31 ± 0.7679
p1 0.01429 ± 0.0001223
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

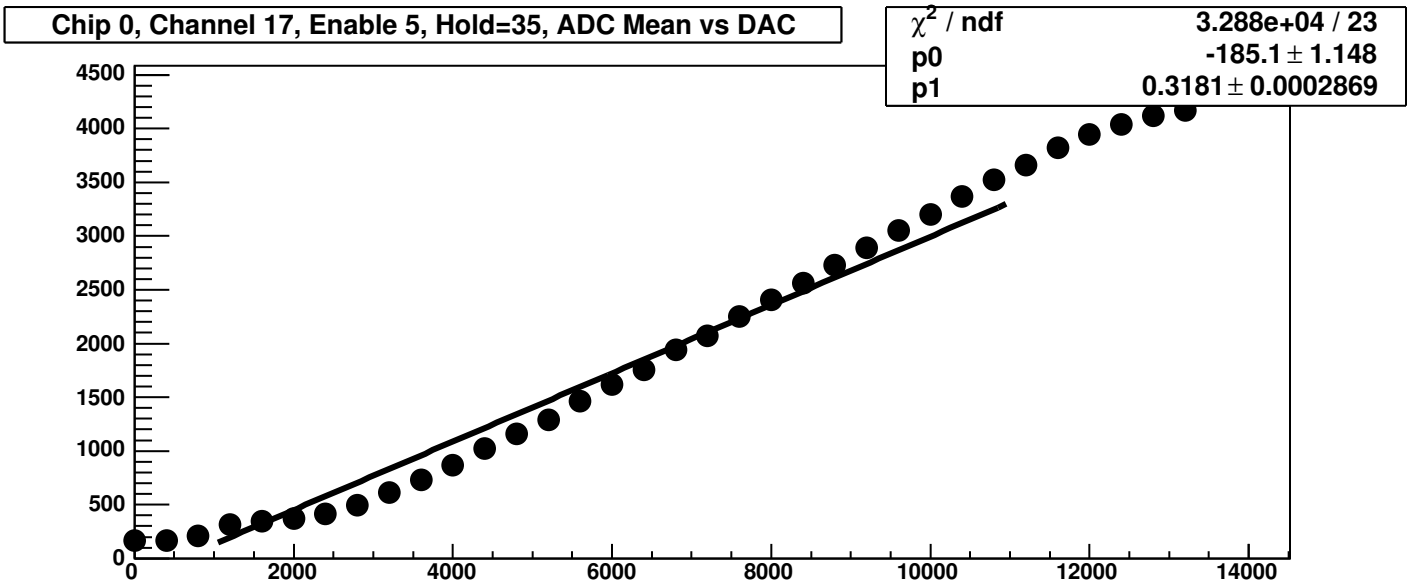

Chip 0, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

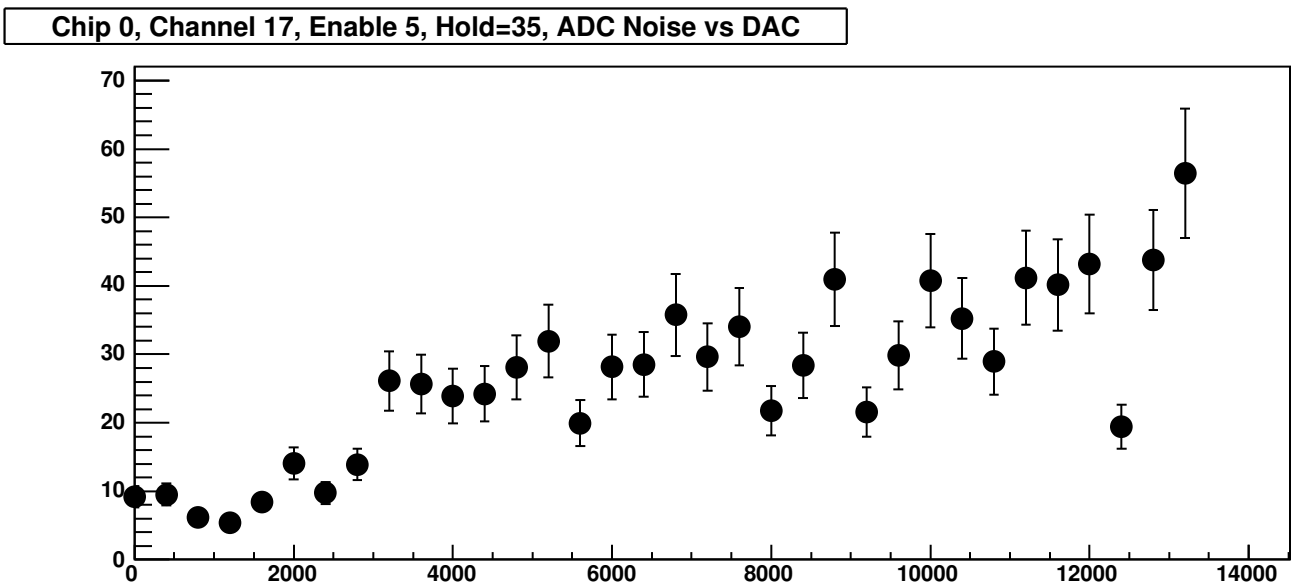

Chip 0, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

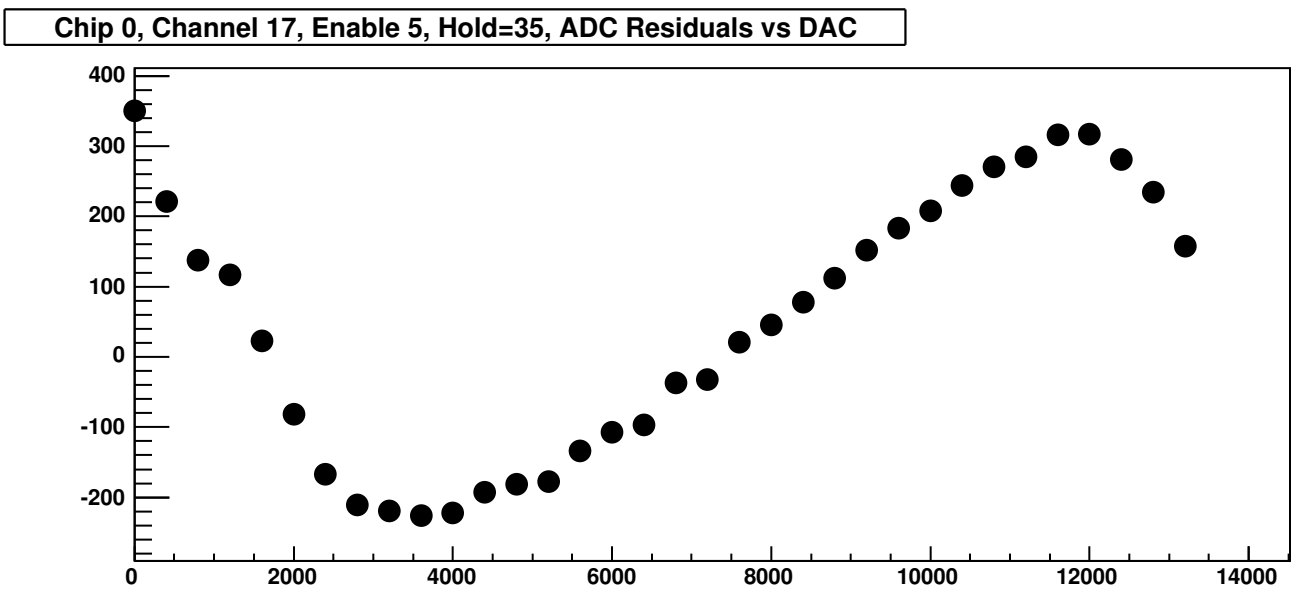

Chip 0, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 23.58 / 23
p0 24.39 ± 0.8299
p1 0.01524 ± 0.0001297
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

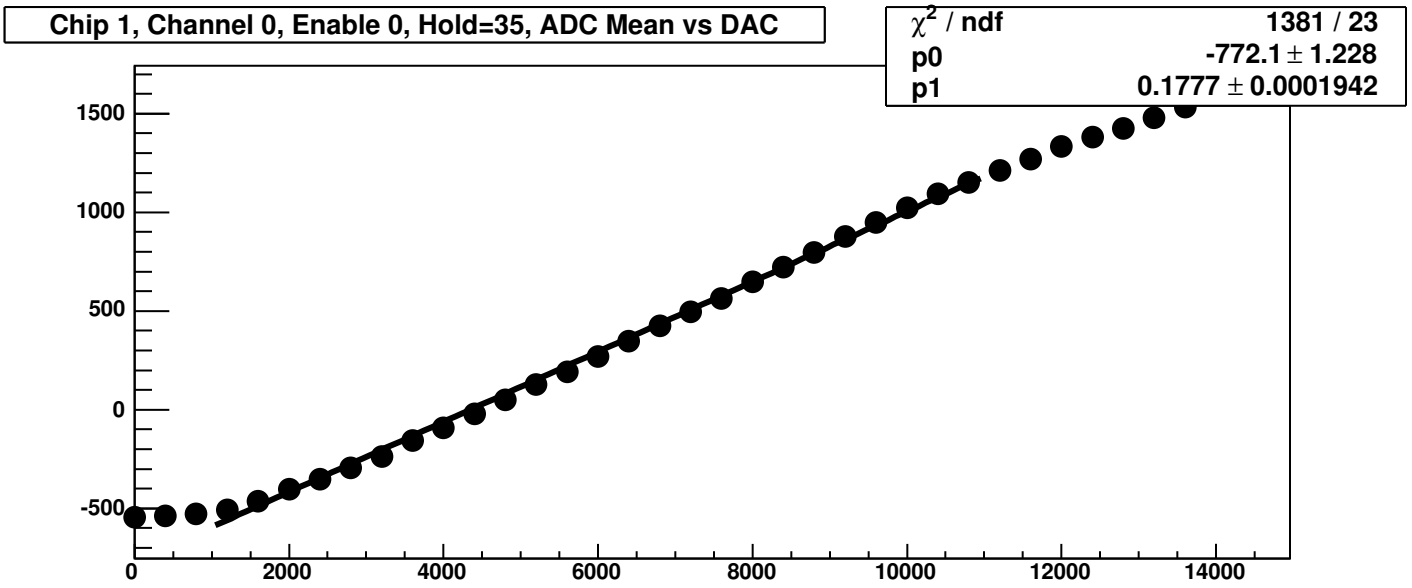

Chip 0, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

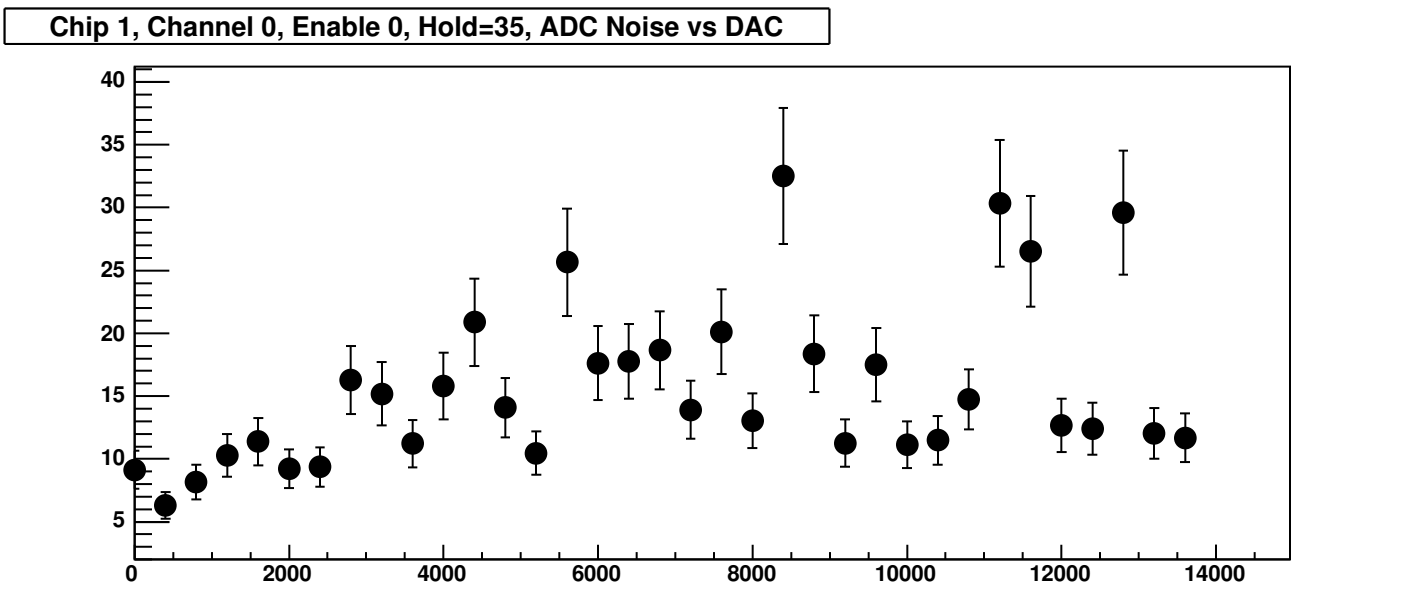

Chip 0, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

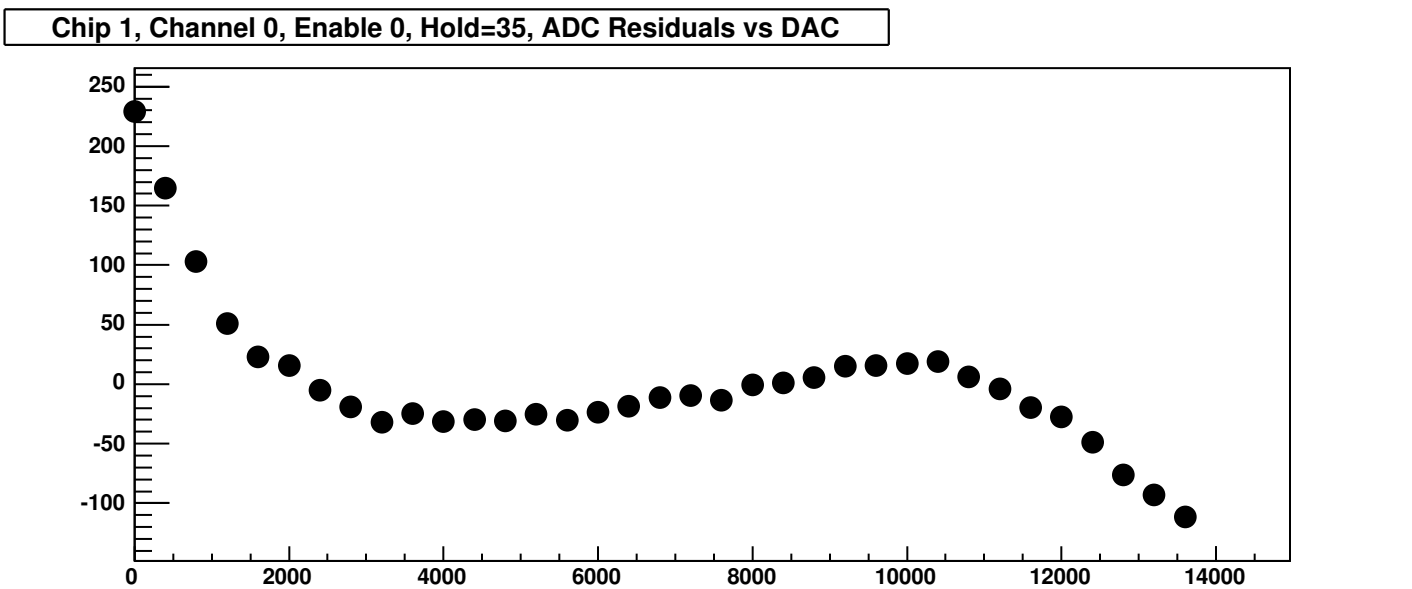

Chip 0, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.288e+04 / 23
p0 -185.1 ± 1.148
p1 0.3181 ± 0.0002869
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

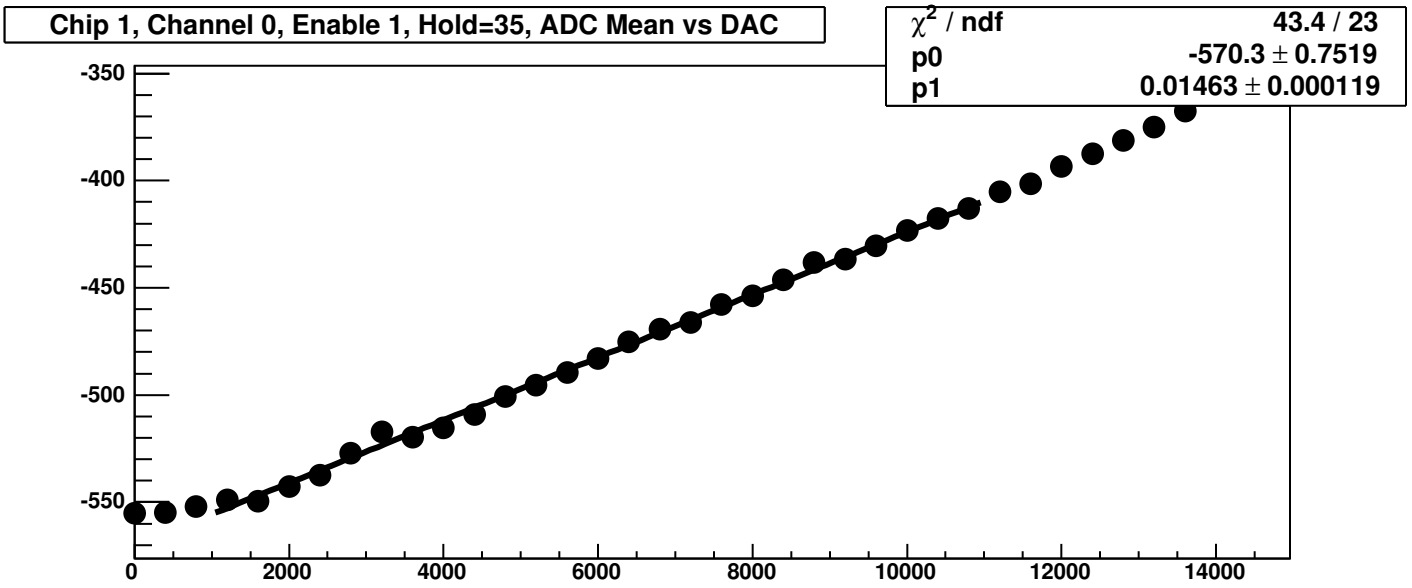

Chip 0, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

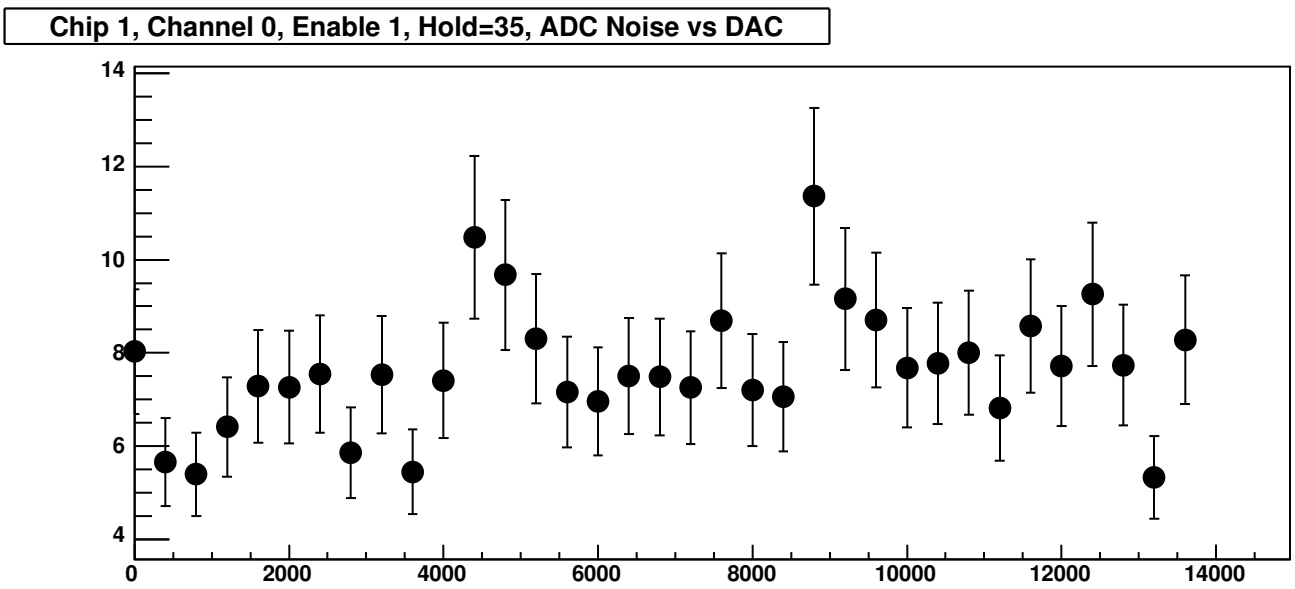

Chip 0, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

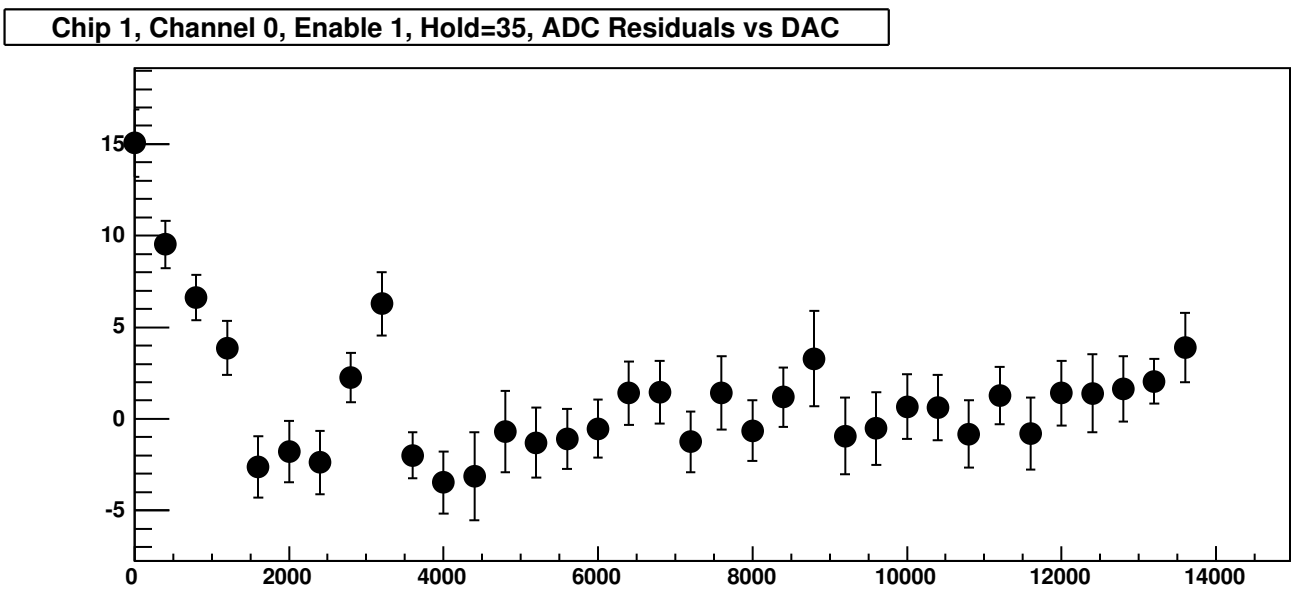

Chip 1, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1381 / 23
p0 -772.1 ± 1.228
p1 0.1777 ± 0.0001942
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

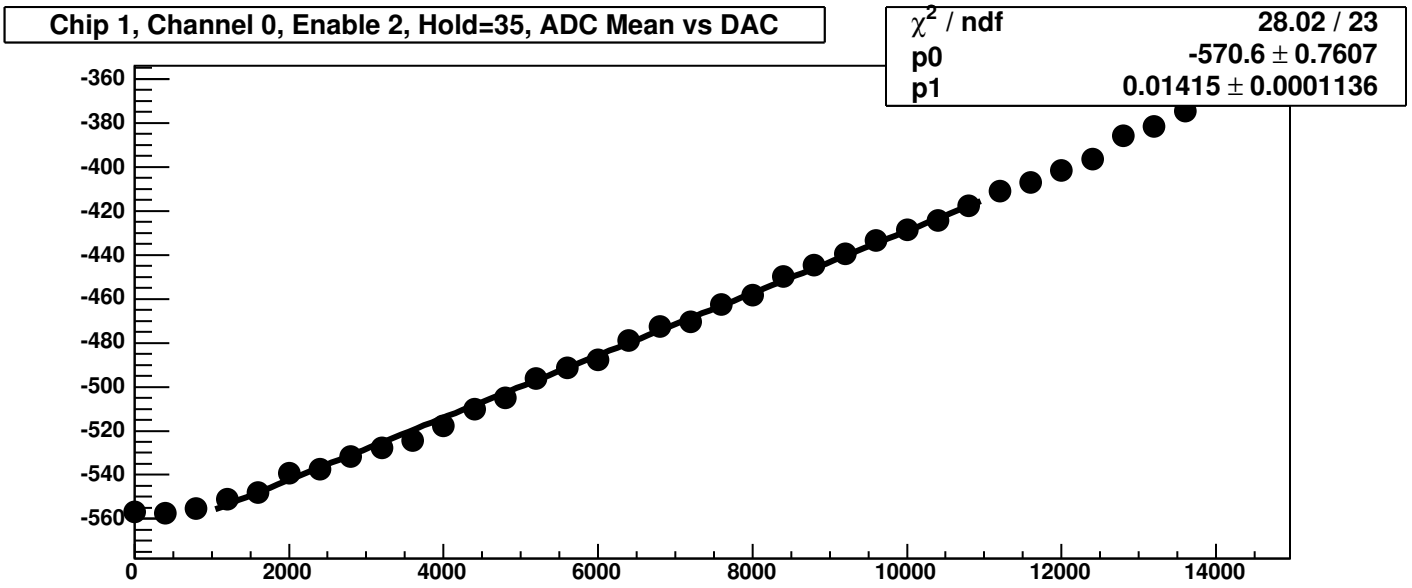

Chip 1, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

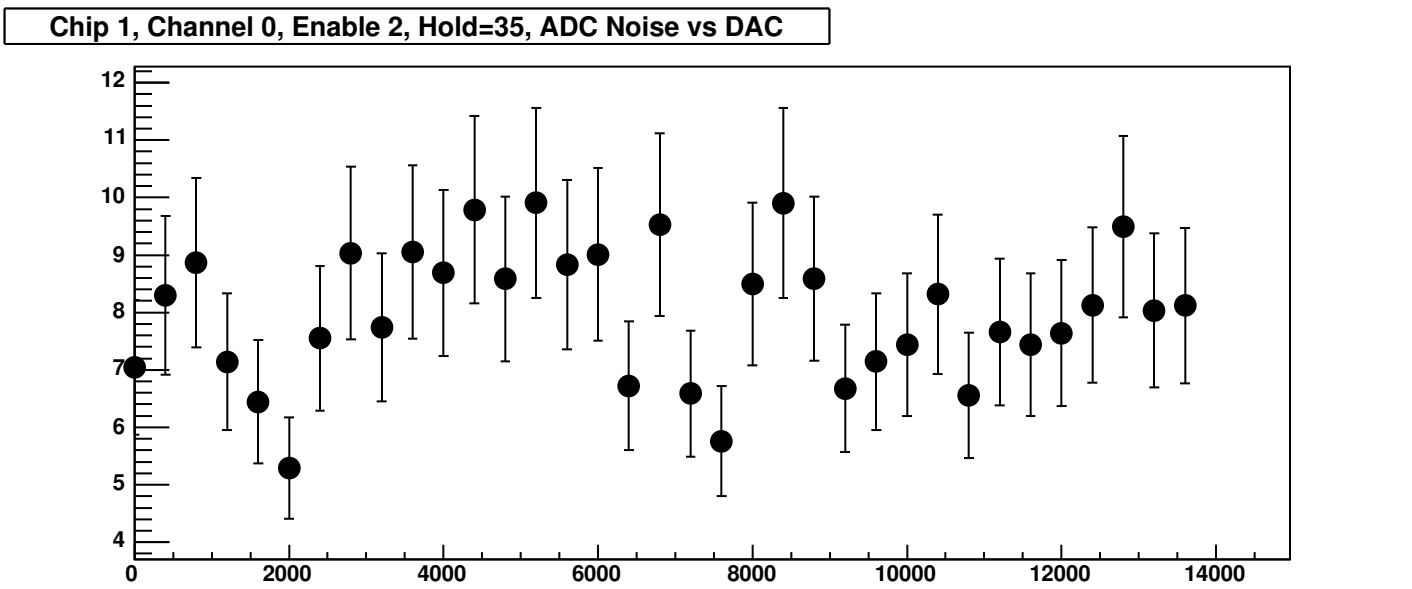

Chip 1, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

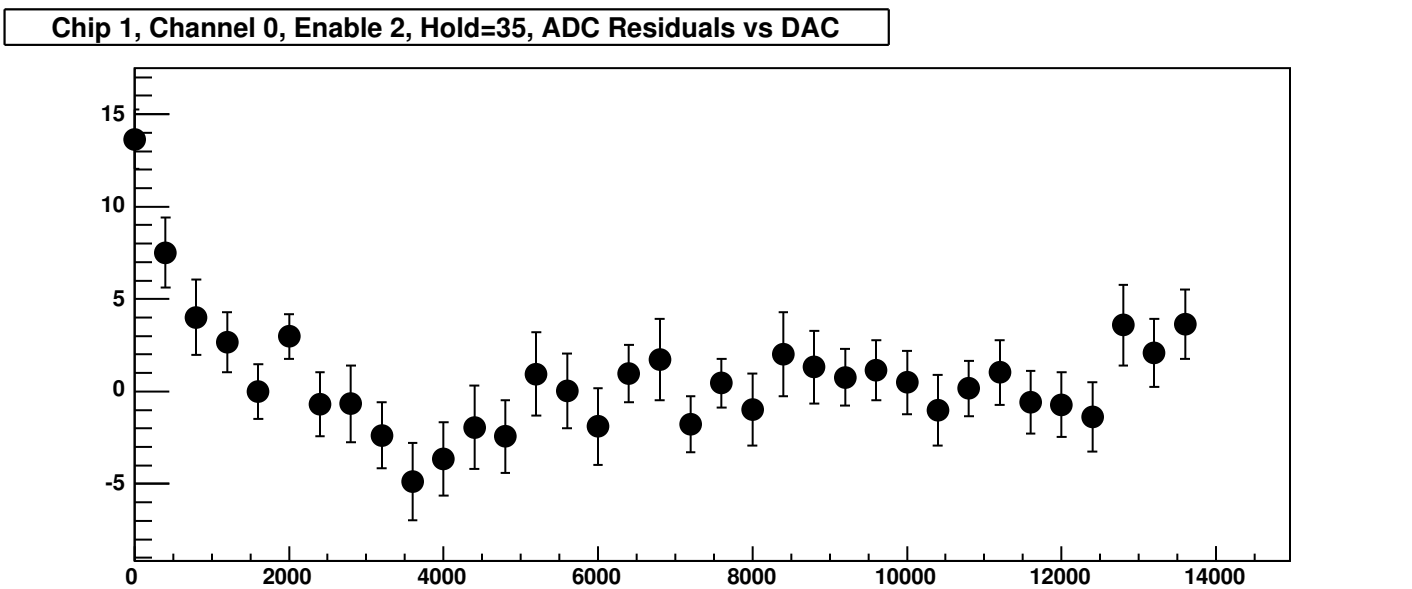

Chip 1, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 43.4 / 23
p0 -570.3 ± 0.7519
p1 0.01463 ± 0.000119
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

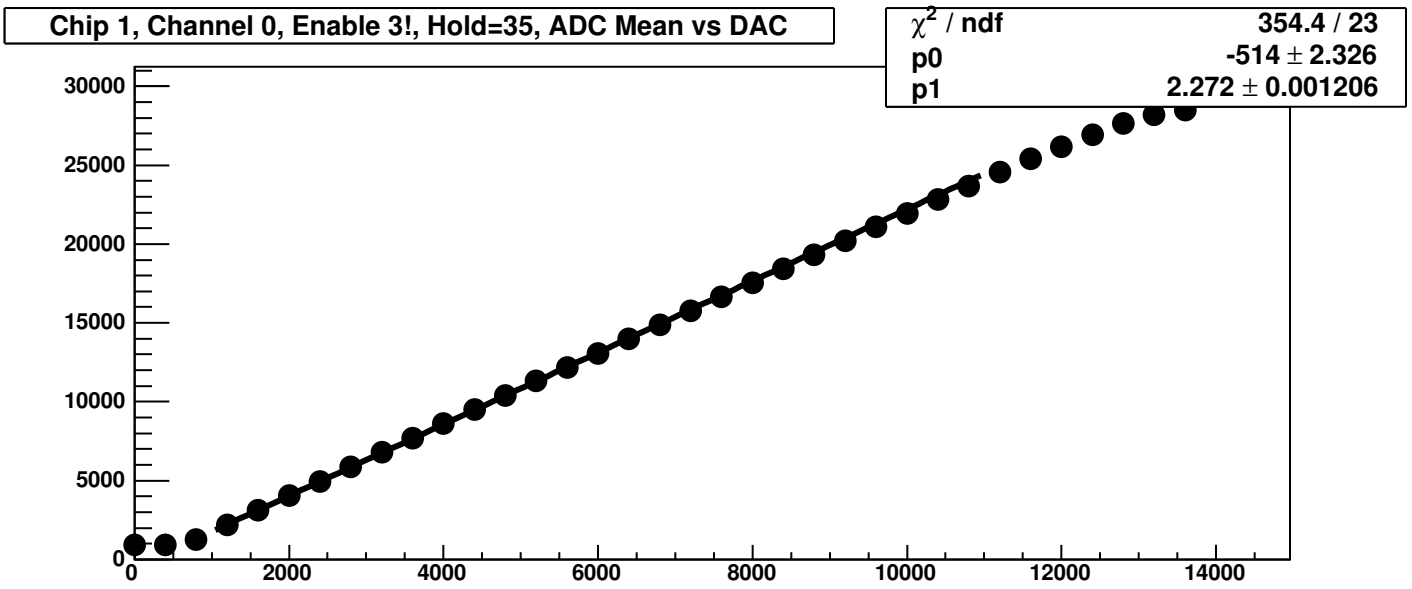

Chip 1, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

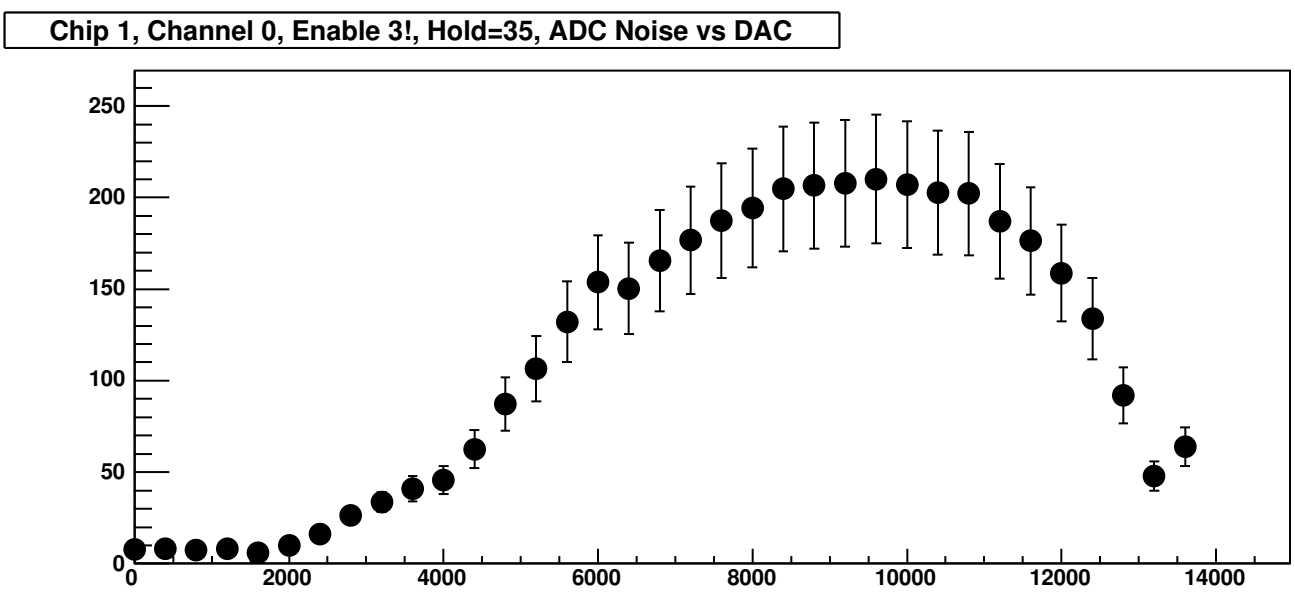

Chip 1, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

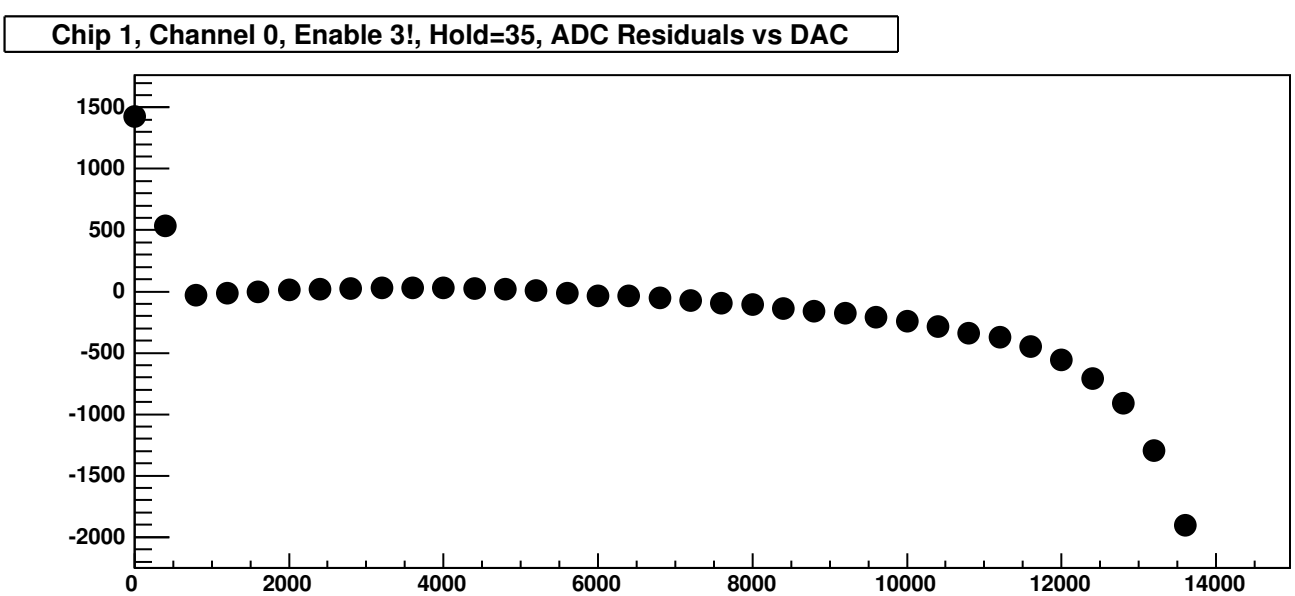

Chip 1, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 28.02 / 23
p0 -570.6 ± 0.7607
p1 0.01415 ± 0.0001136
-360
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

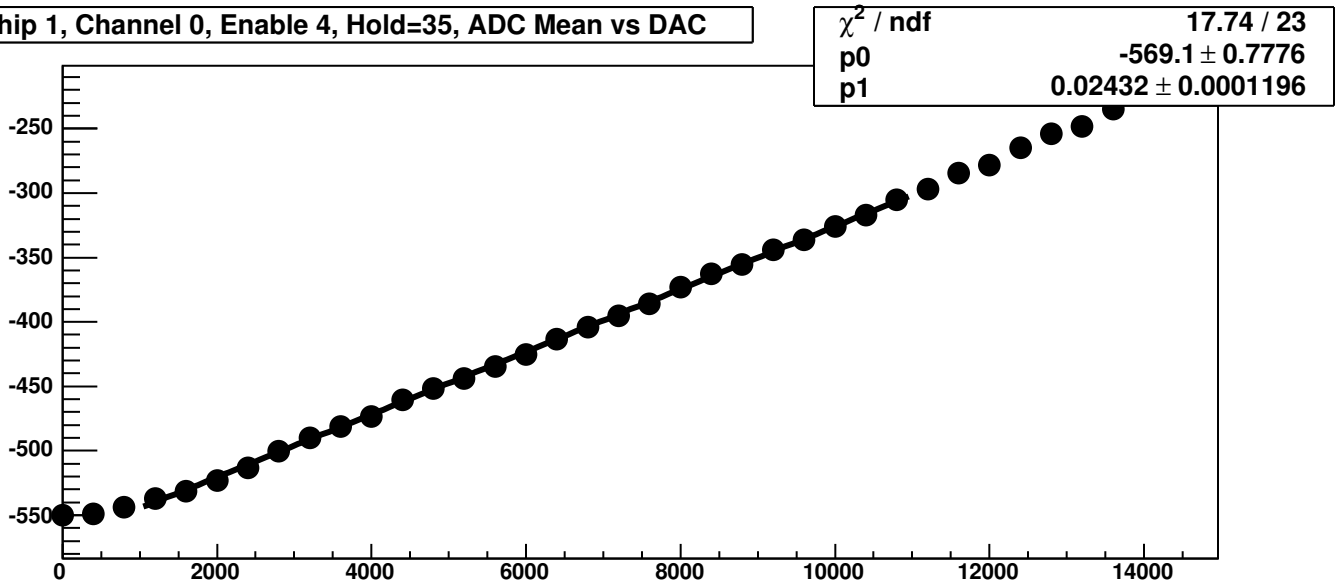

Chip 1, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 354.4 / 23
p0 -514 ± 2.326
p1 2.272 ± 0.001206
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

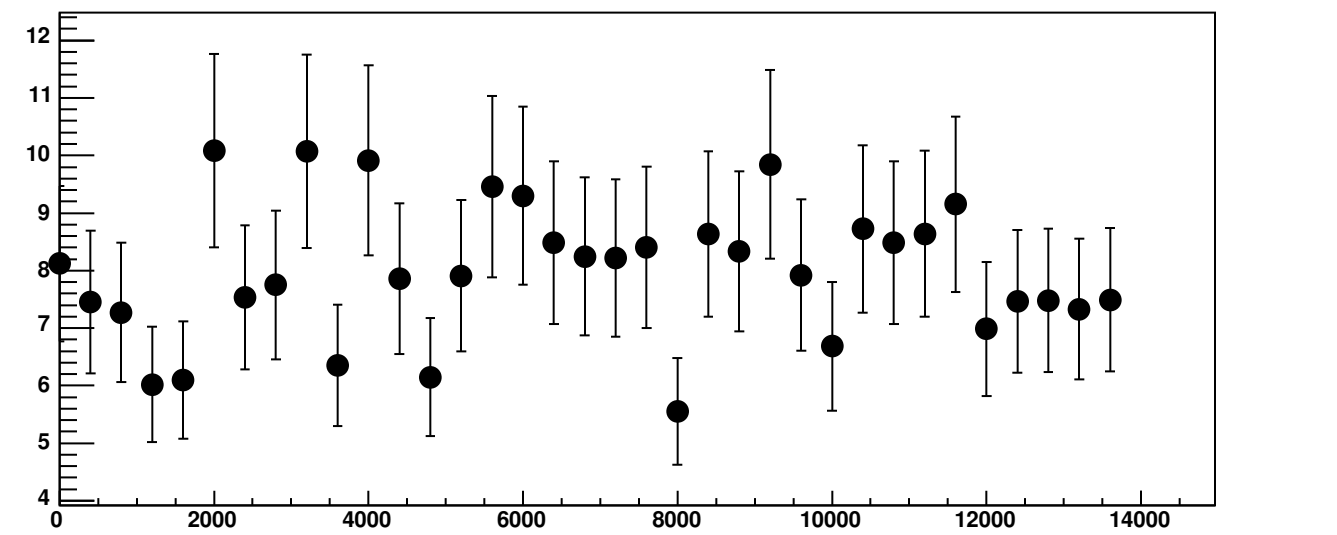

Chip 1, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

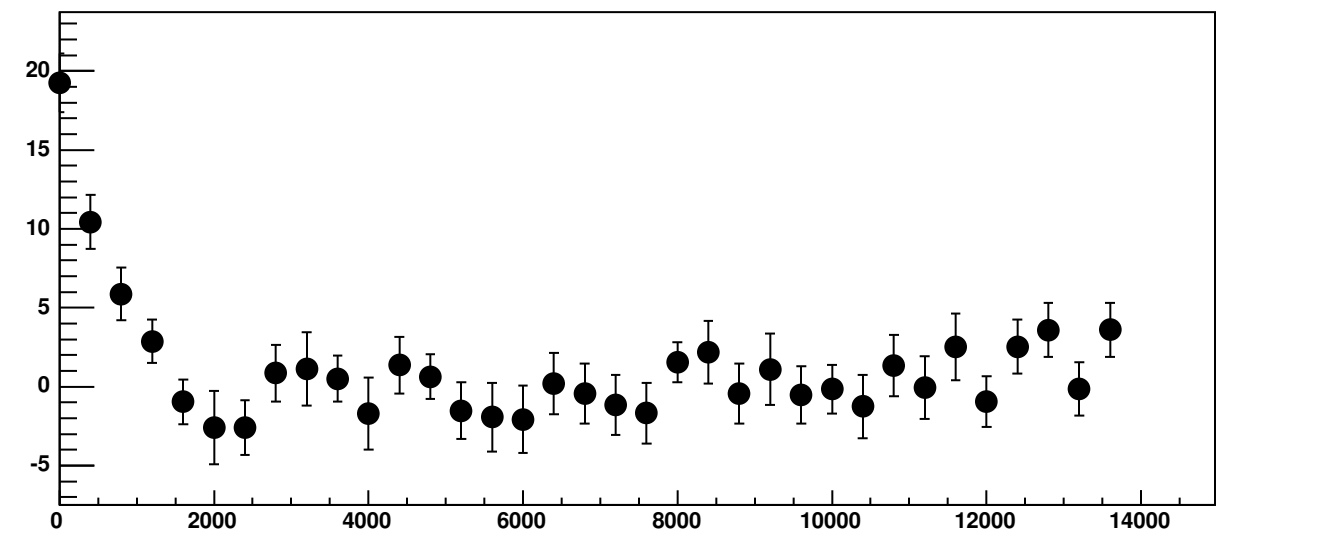
Chip 1, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

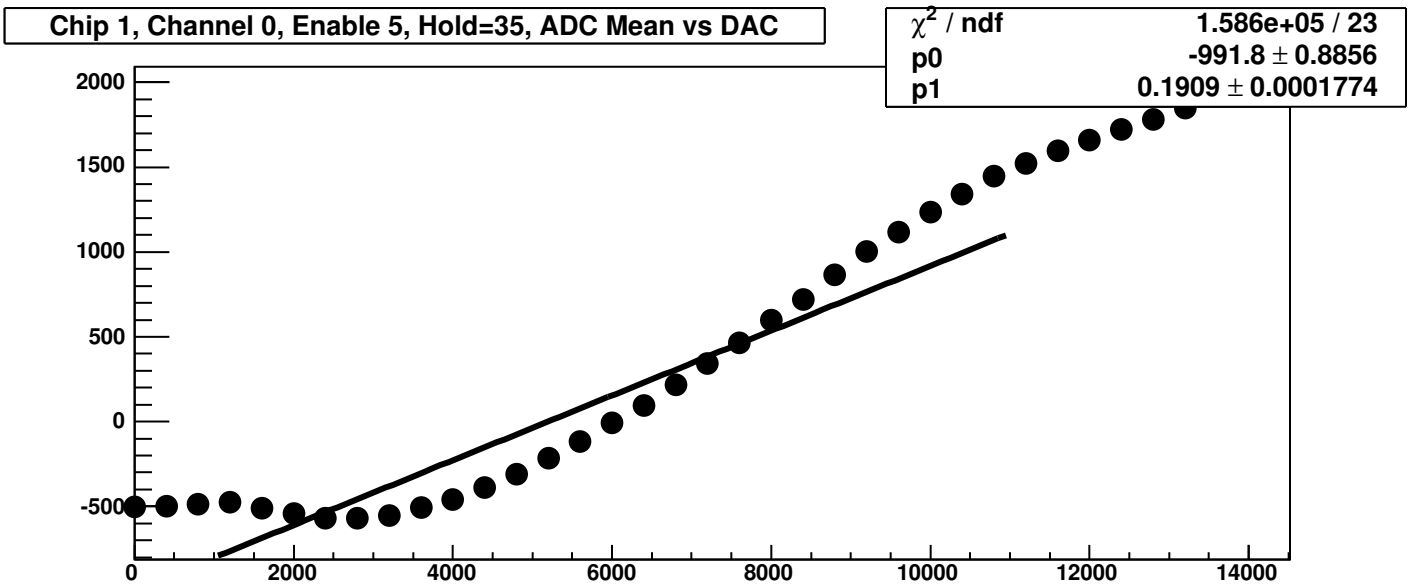


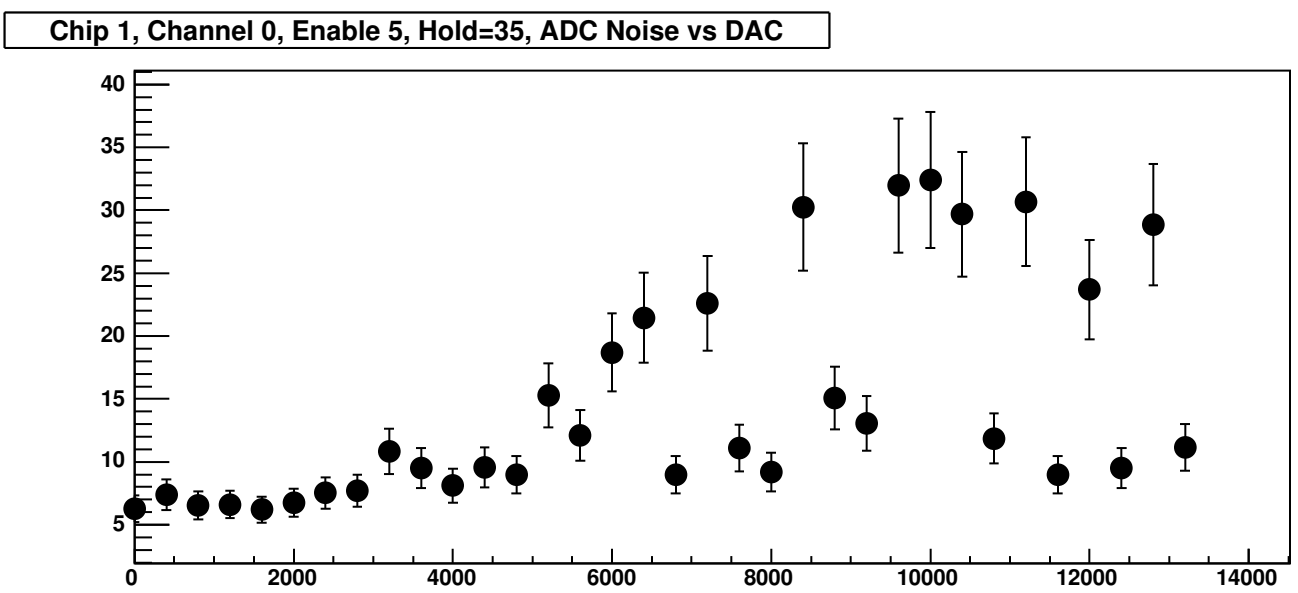
Chip 1, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

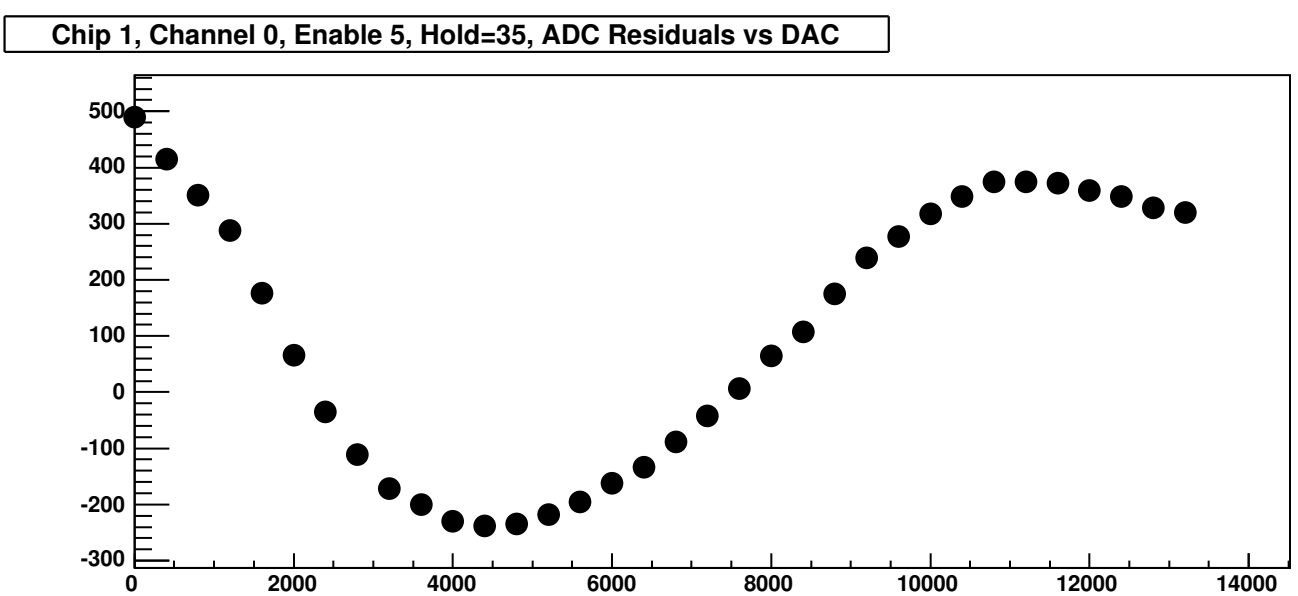


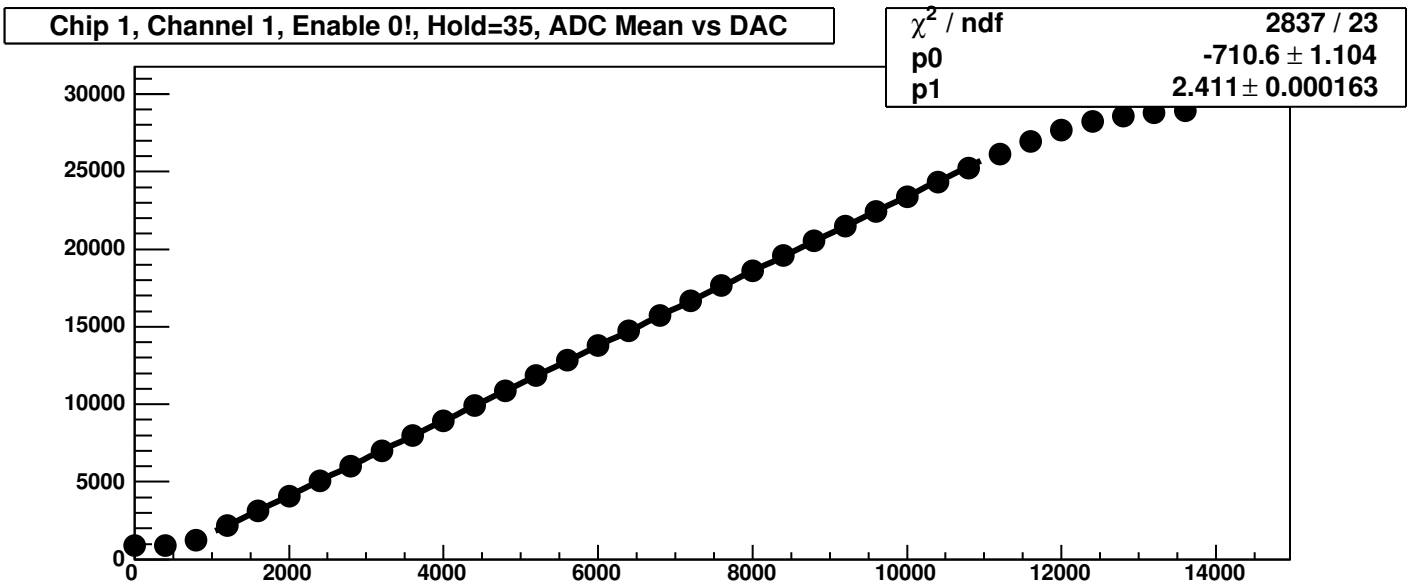
Chip 1, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

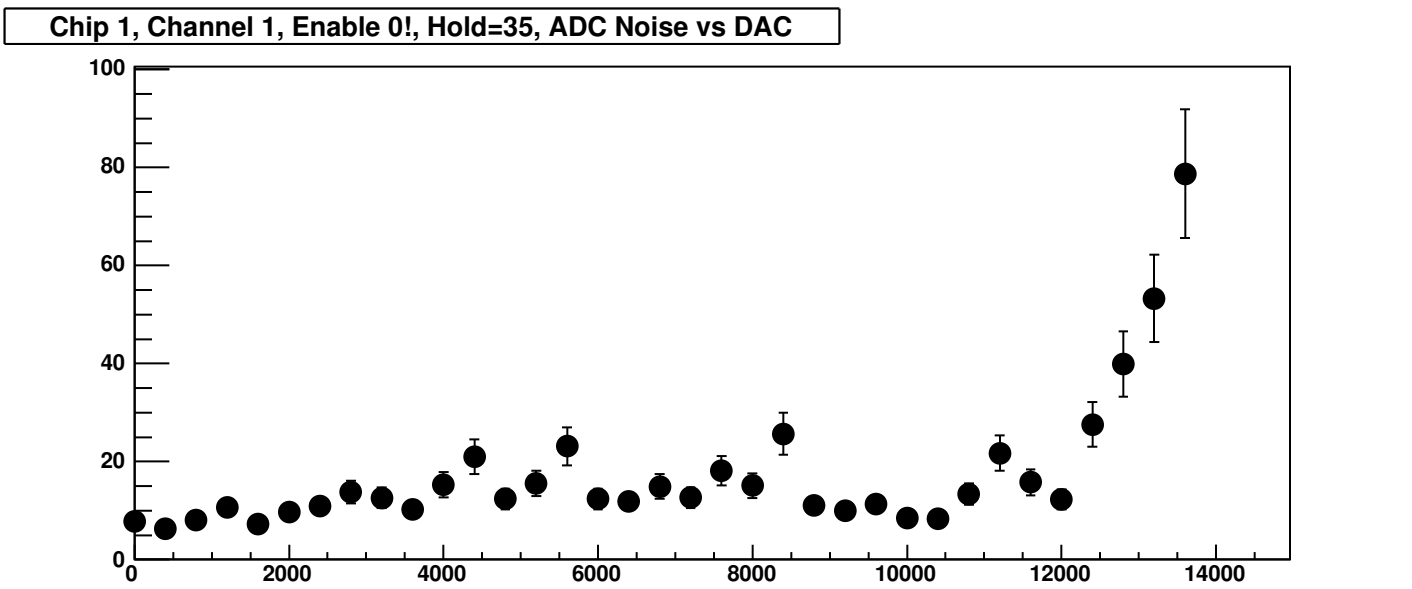


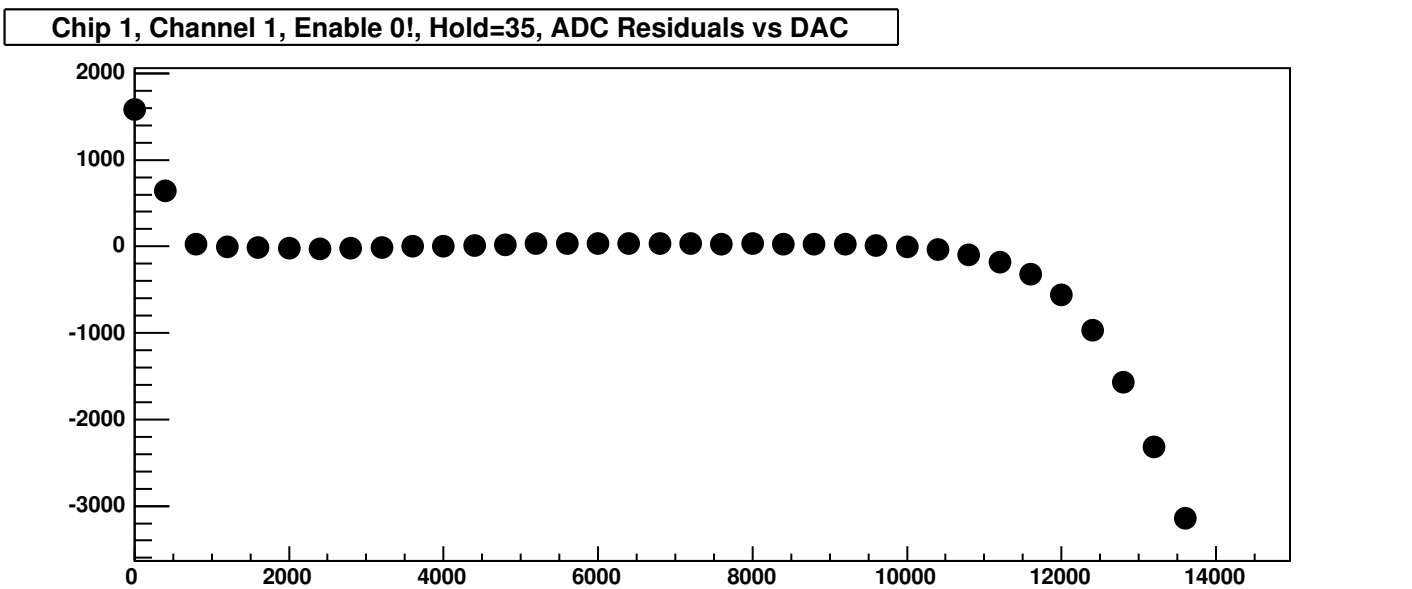

Chip 1, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.586e+05 / 23
p0 -991.8 ± 0.8856
p1 0.1909 ± 0.0001774
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

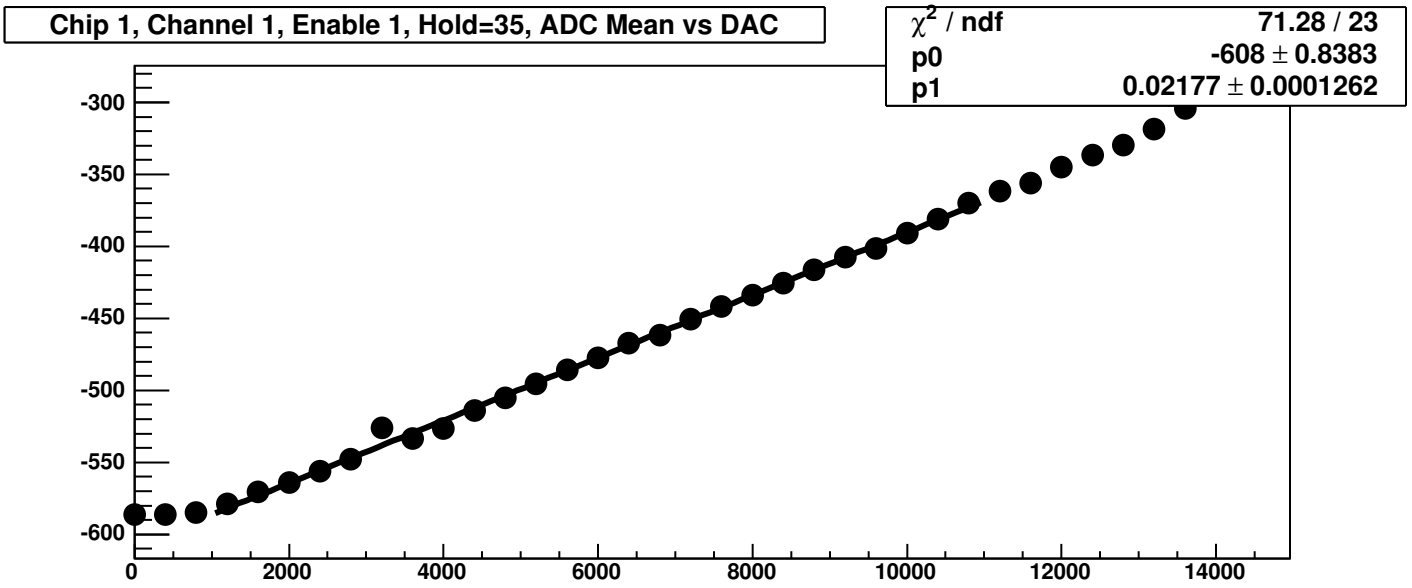

Chip 1, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

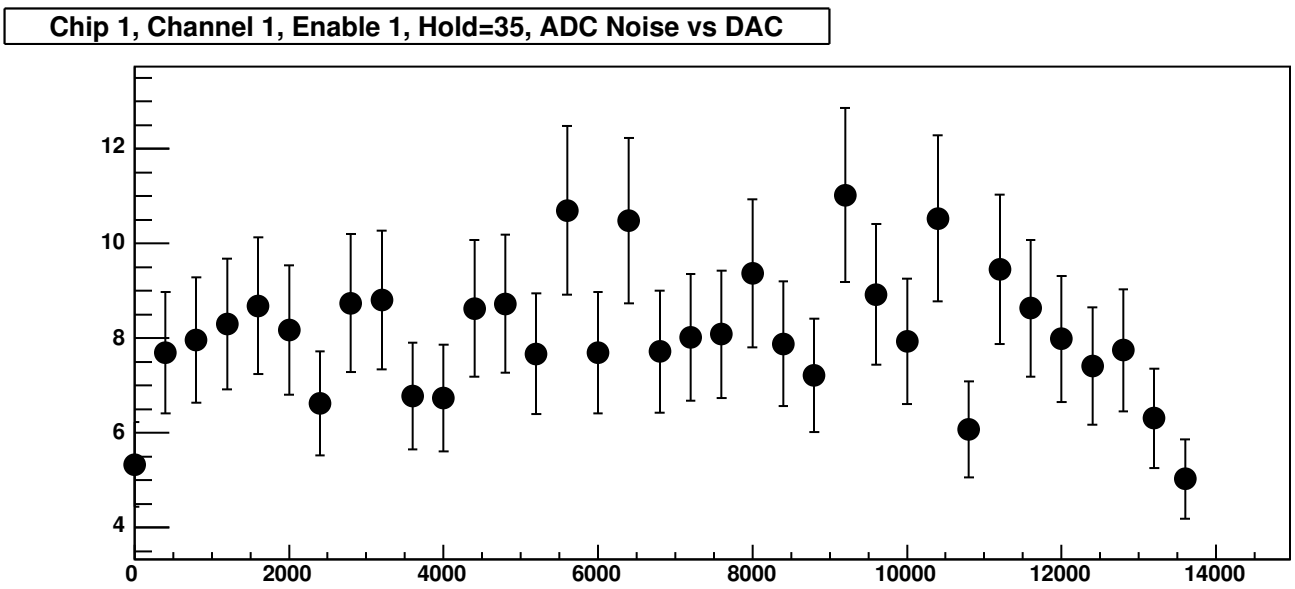

Chip 1, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

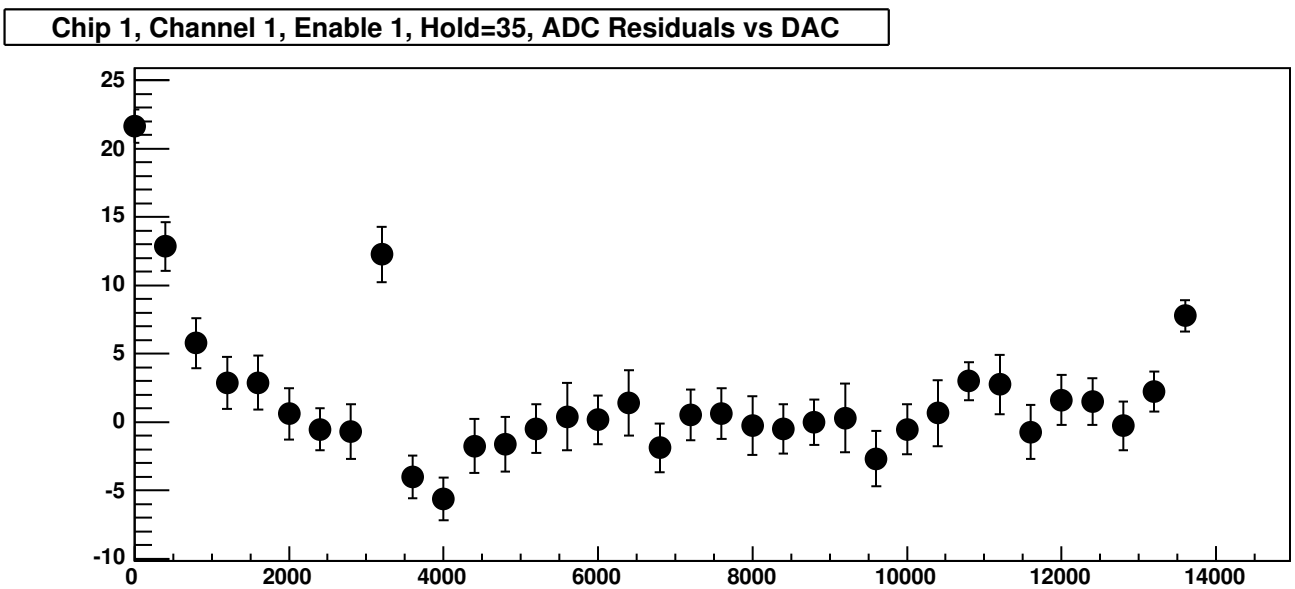

Chip 1, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 2837 / 23
p0 -710.6 ± 1.104
p1 2.411 ± 0.000163
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

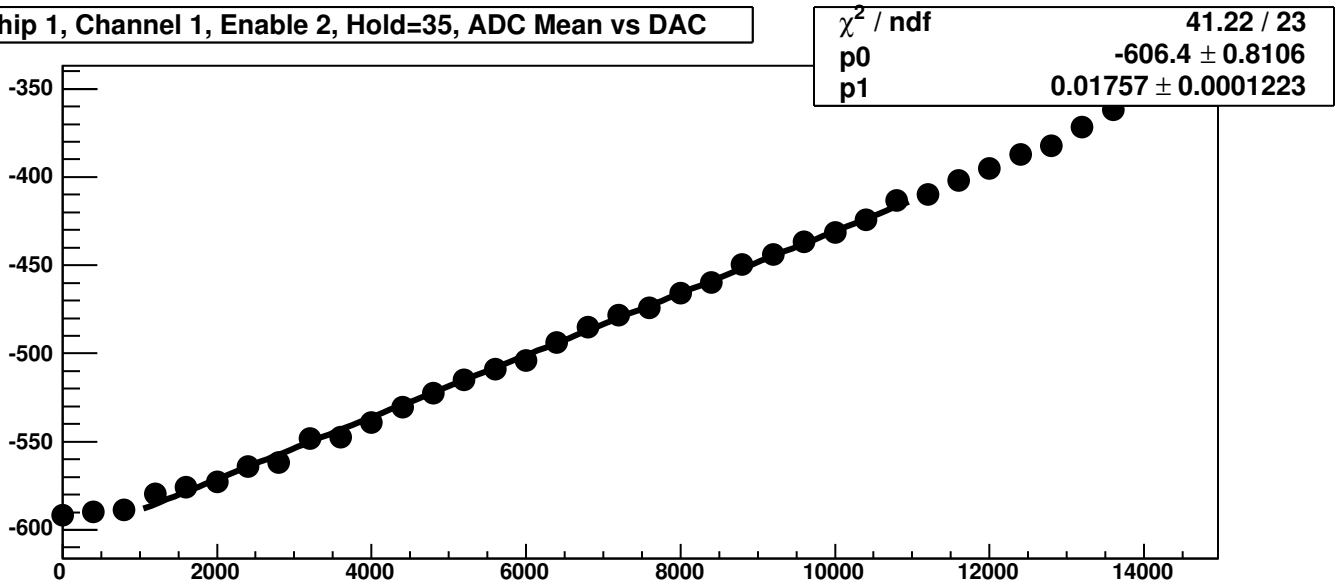

Chip 1, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 71.28 / 23
p0 -608 ± 0.8383
p1 0.02177 ± 0.0001262
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

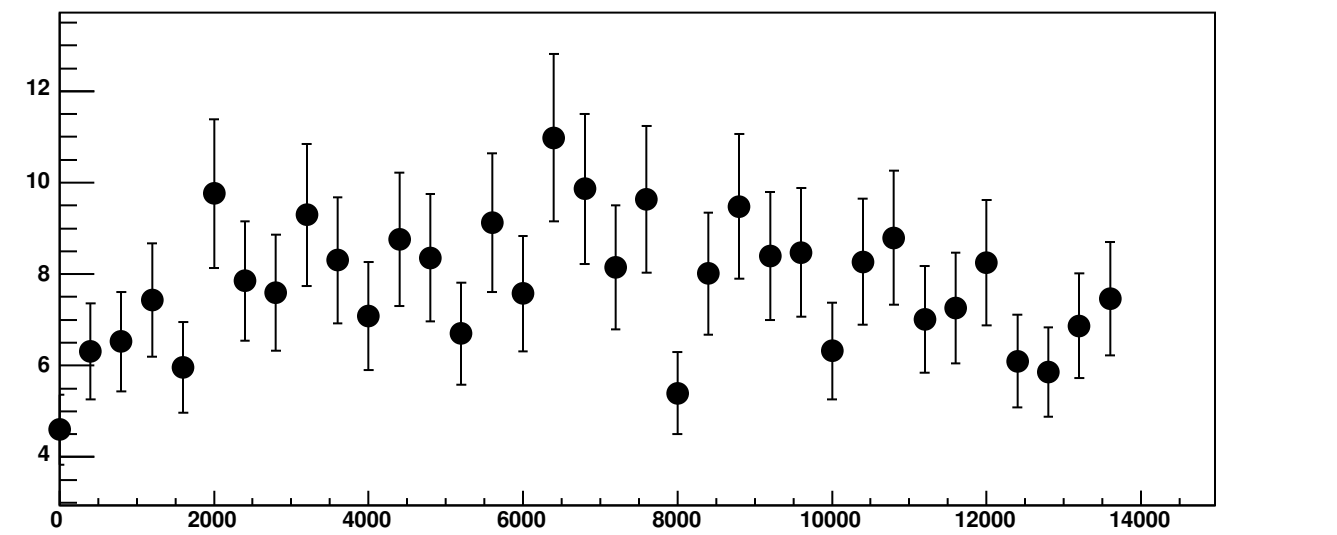

Chip 1, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

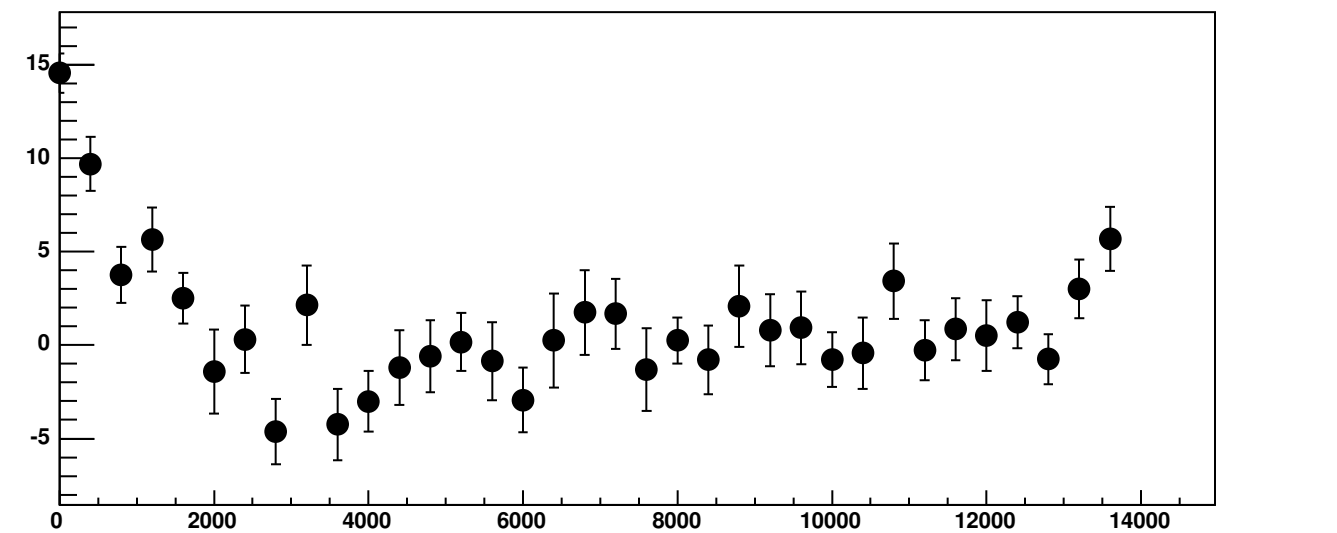
Chip 1, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

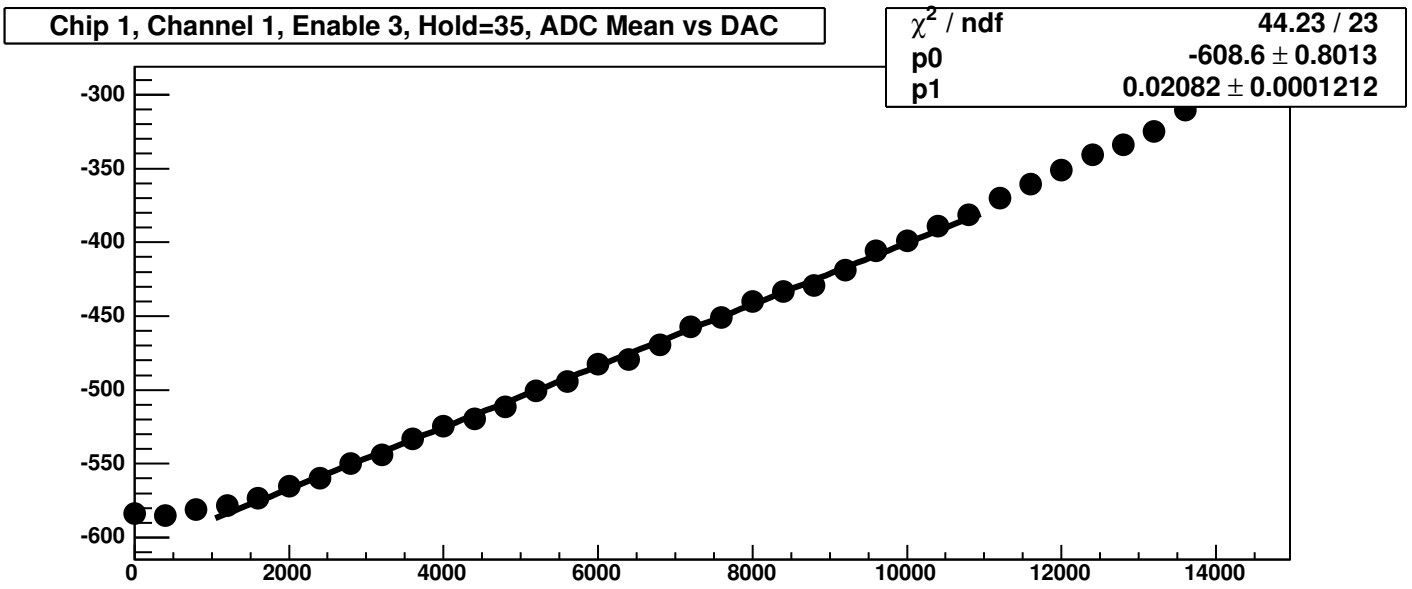


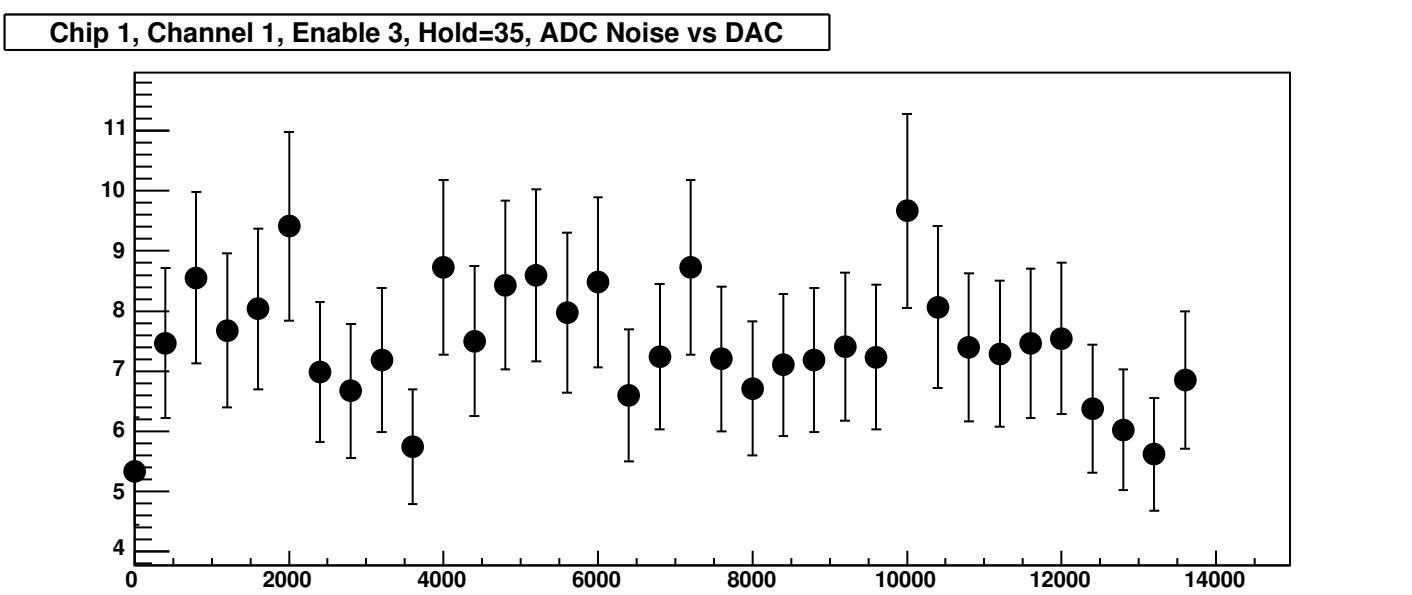
Chip 1, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC

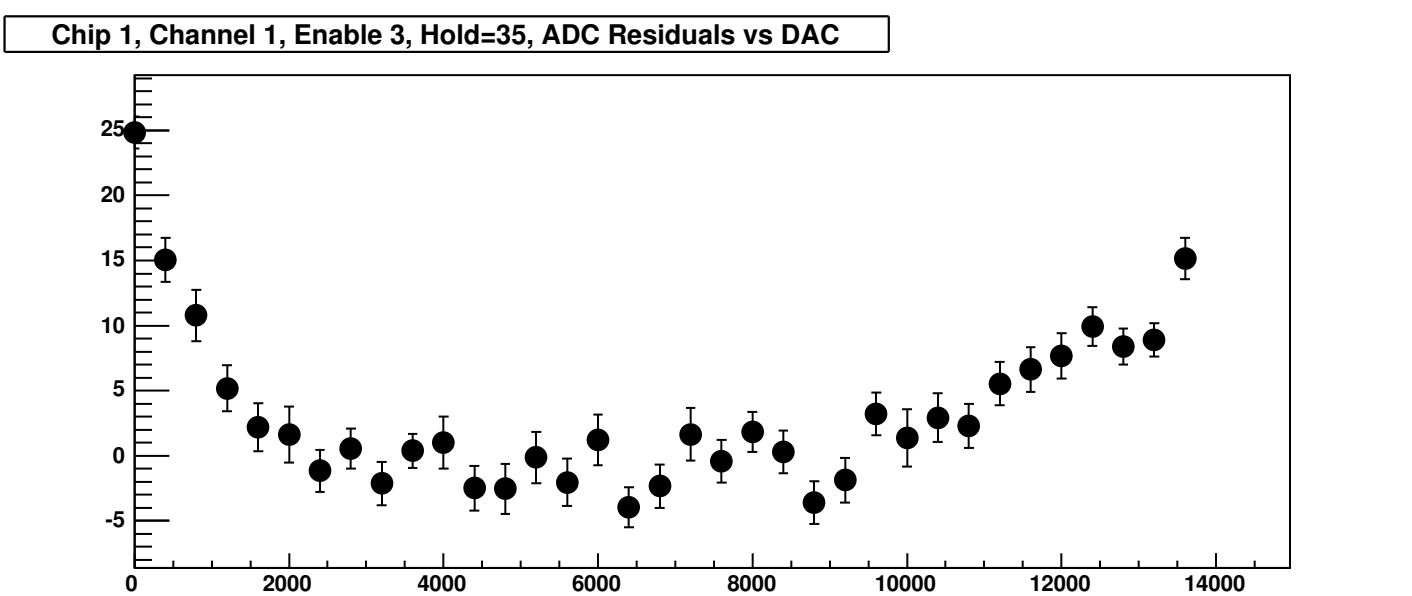


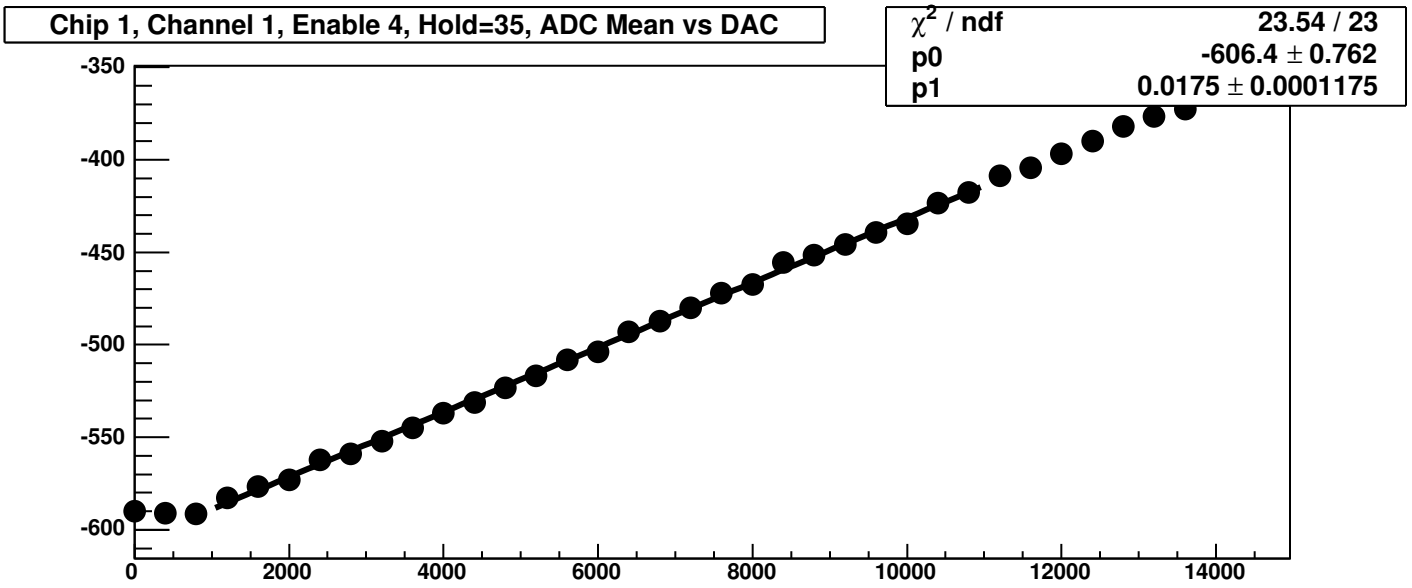
Chip 1, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

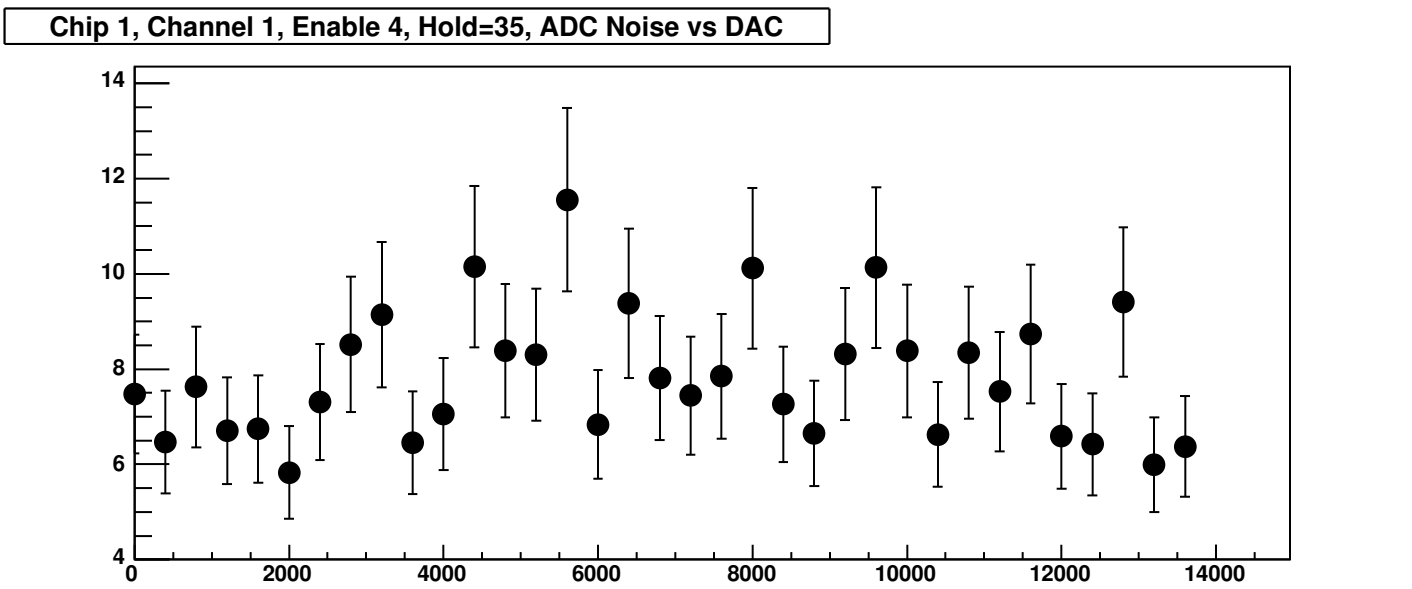


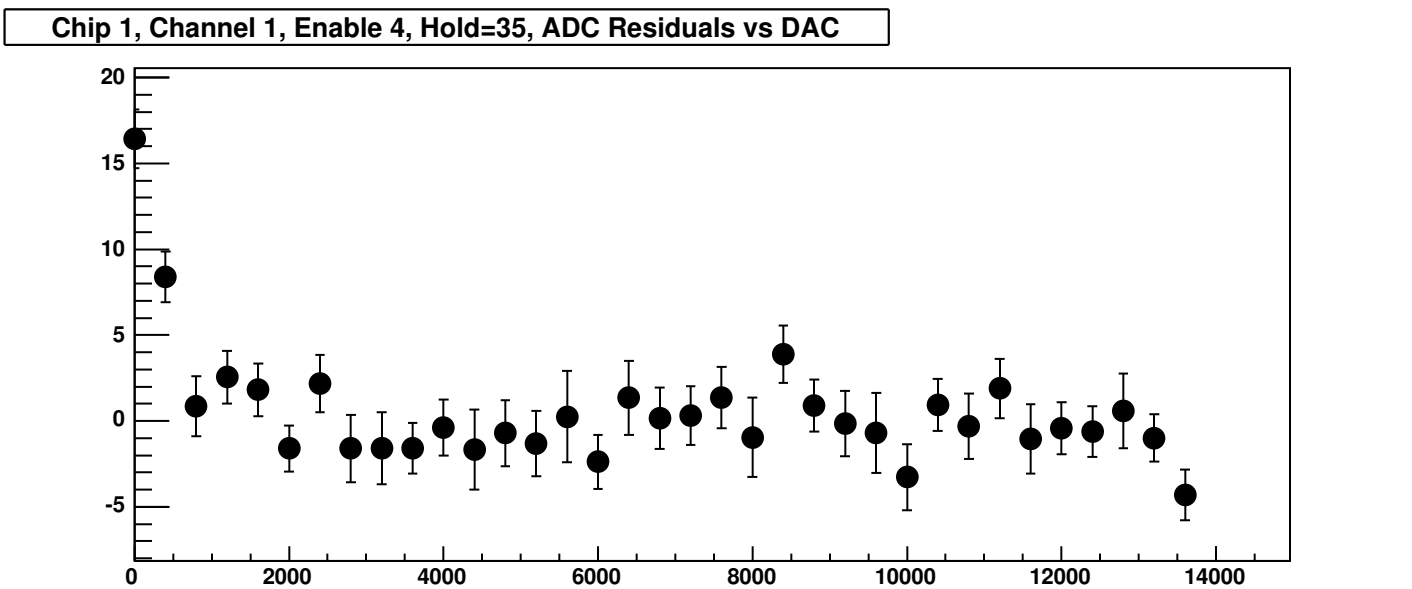

Chip 1, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 44.23 / 23
p0 -608.6 ± 0.8013
p1 0.02082 ± 0.0001212
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

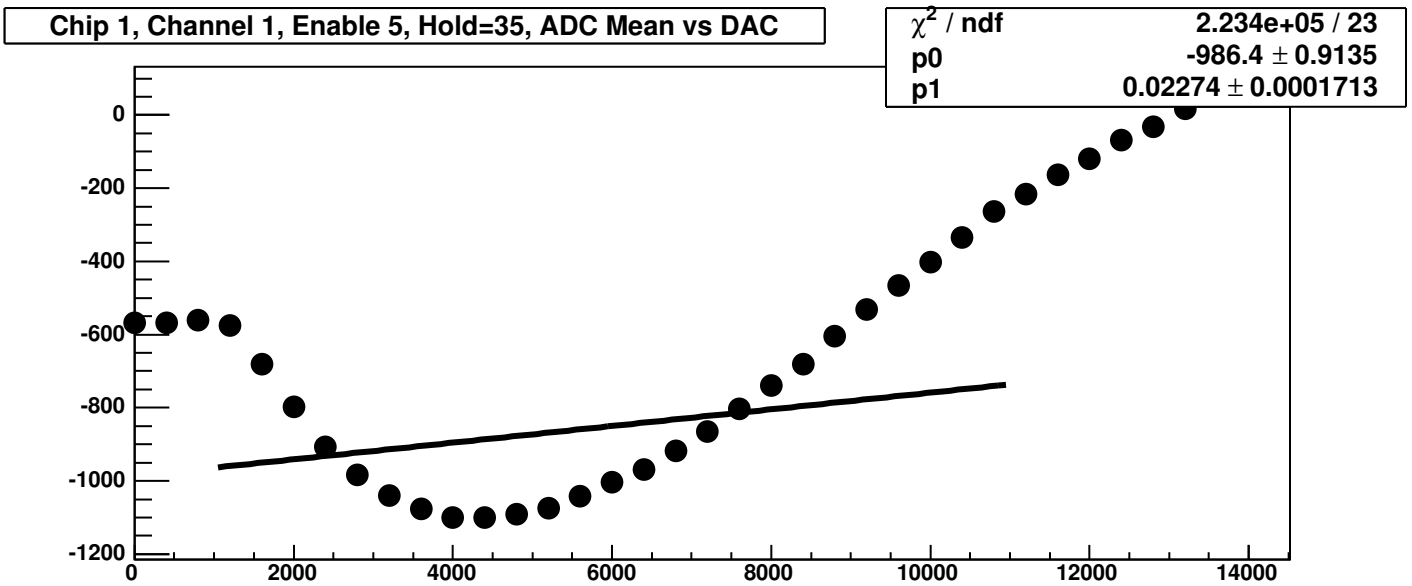

Chip 1, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

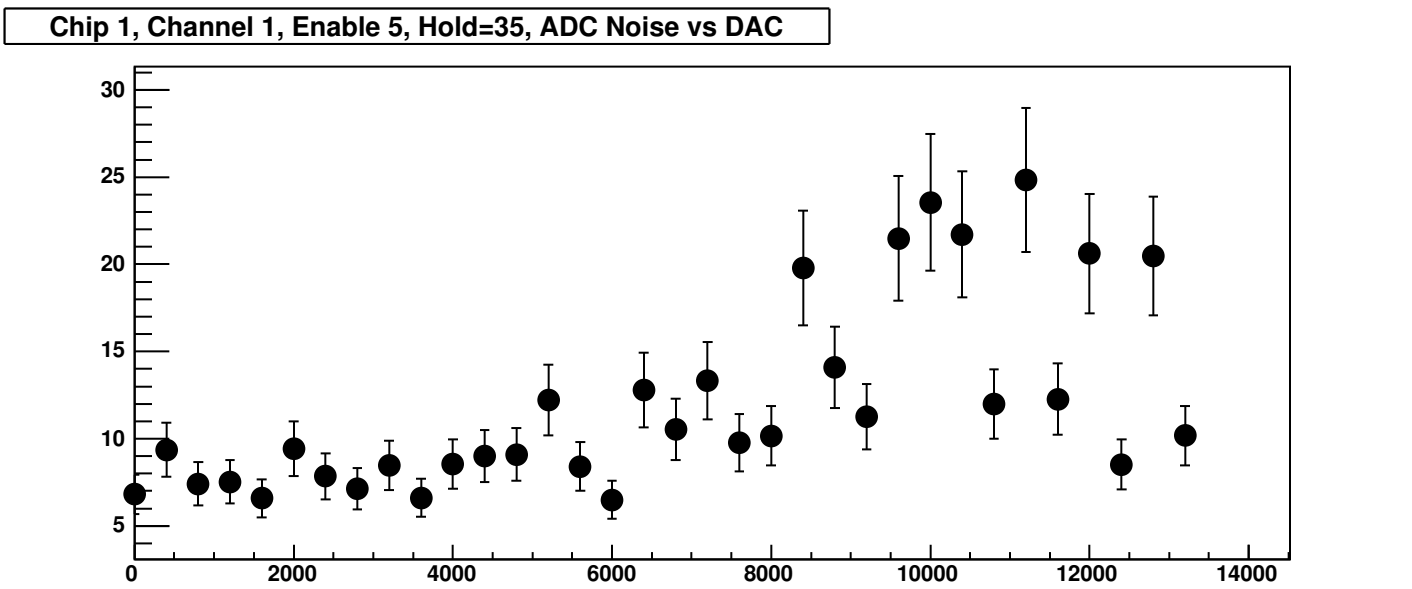

Chip 1, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

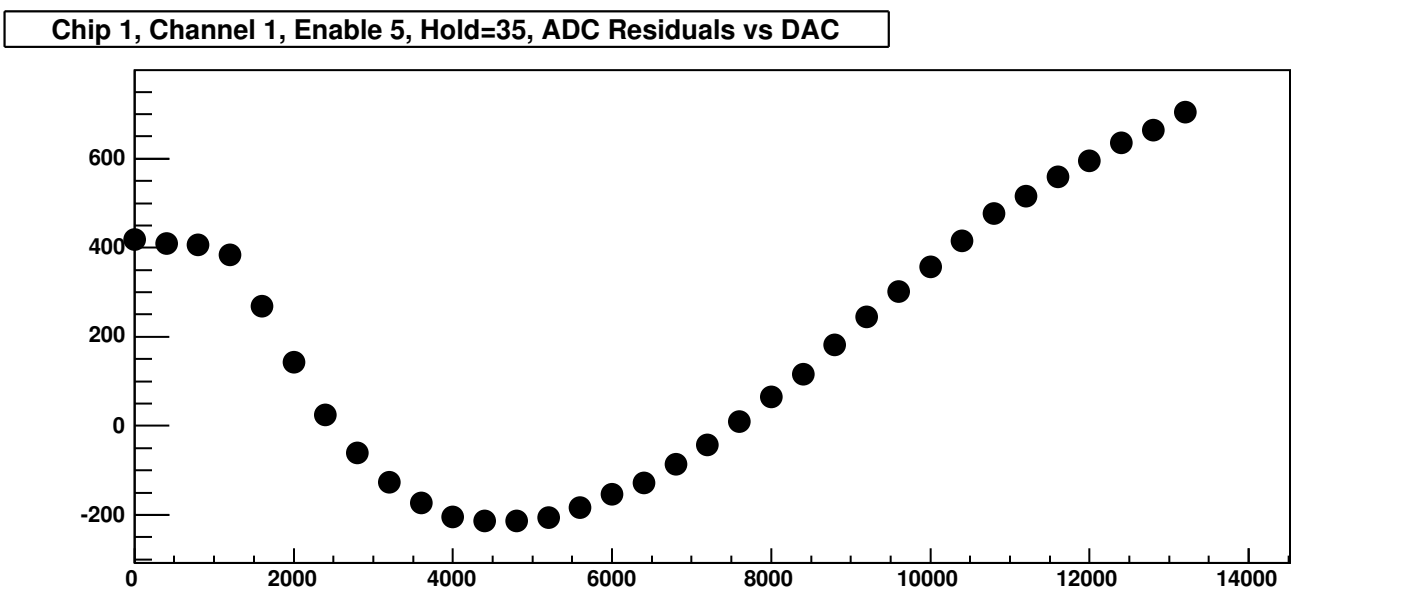

Chip 1, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 23.54 / 23
p0 -606.4 ± 0.762
p1 0.0175 ± 0.0001175
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

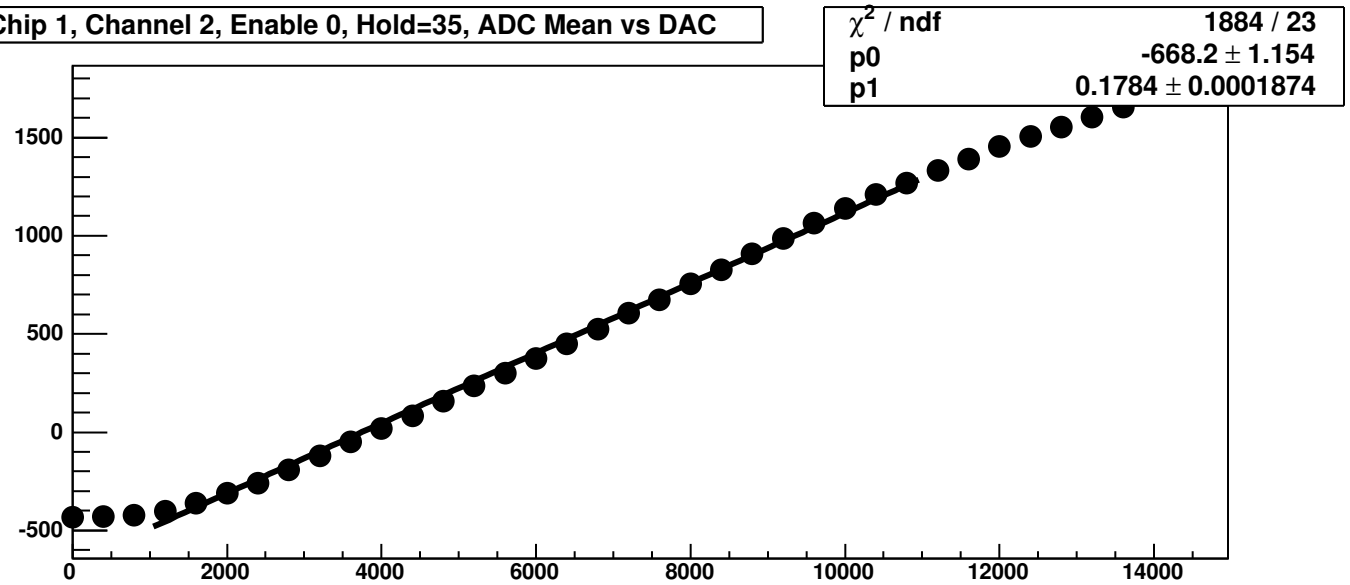

Chip 1, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2.234e+05 / 23
p0 -986.4 ± 0.9135
p1 0.02274 ± 0.0001713
0
-200
-400
-600
-800
-1000
-1200
0
2000
4000
6000
8000
10000
12000
14000

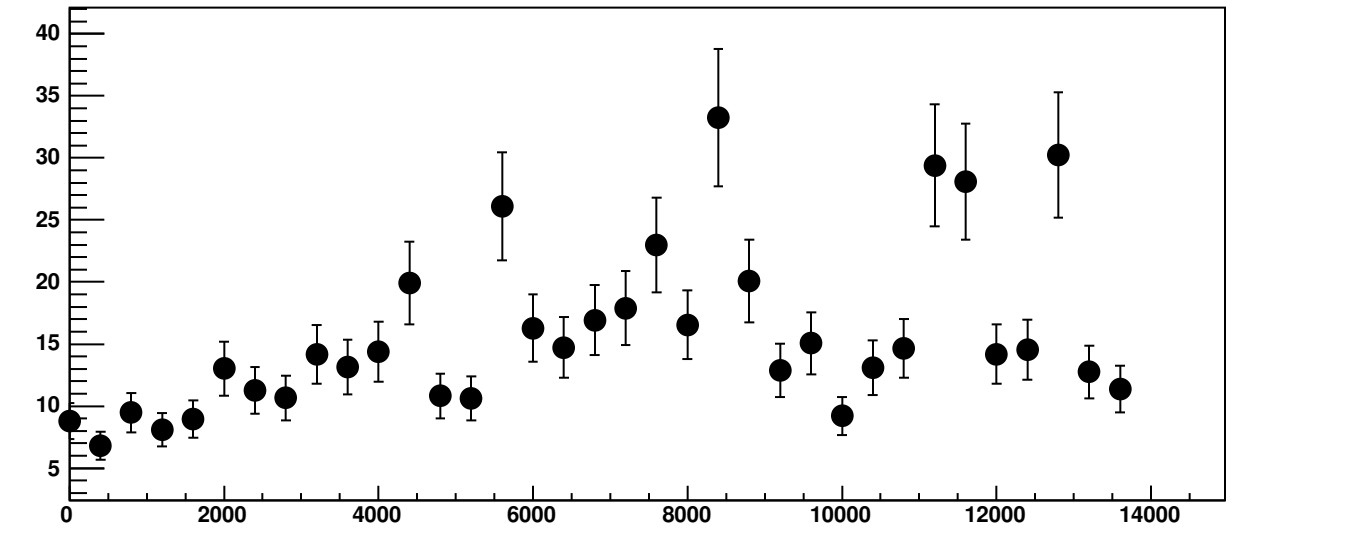

Chip 1, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

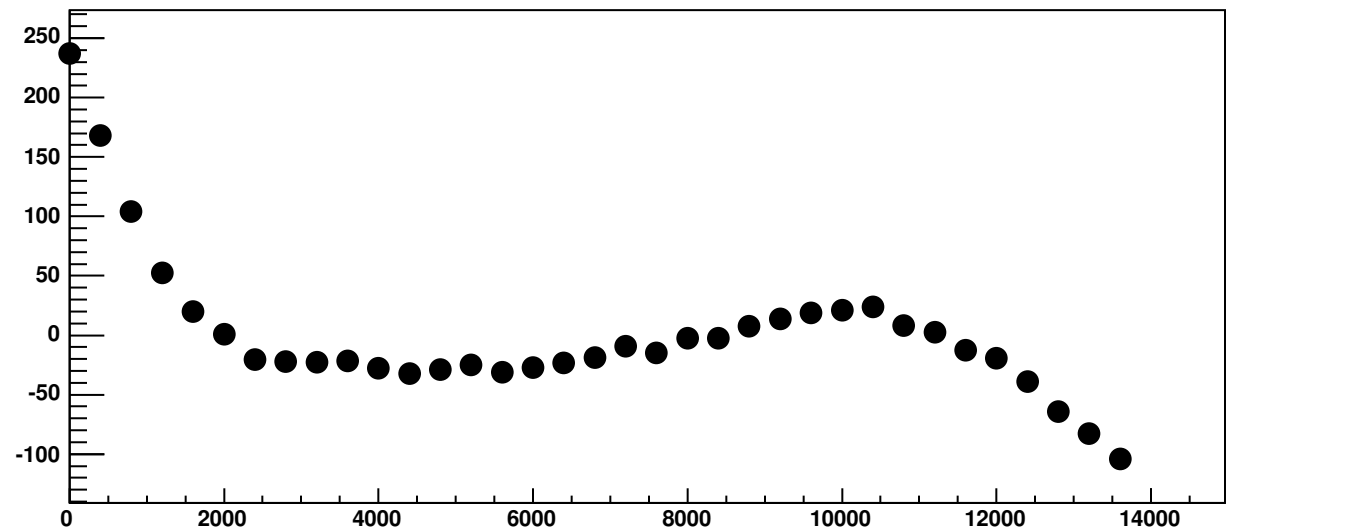
Chip 1, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC

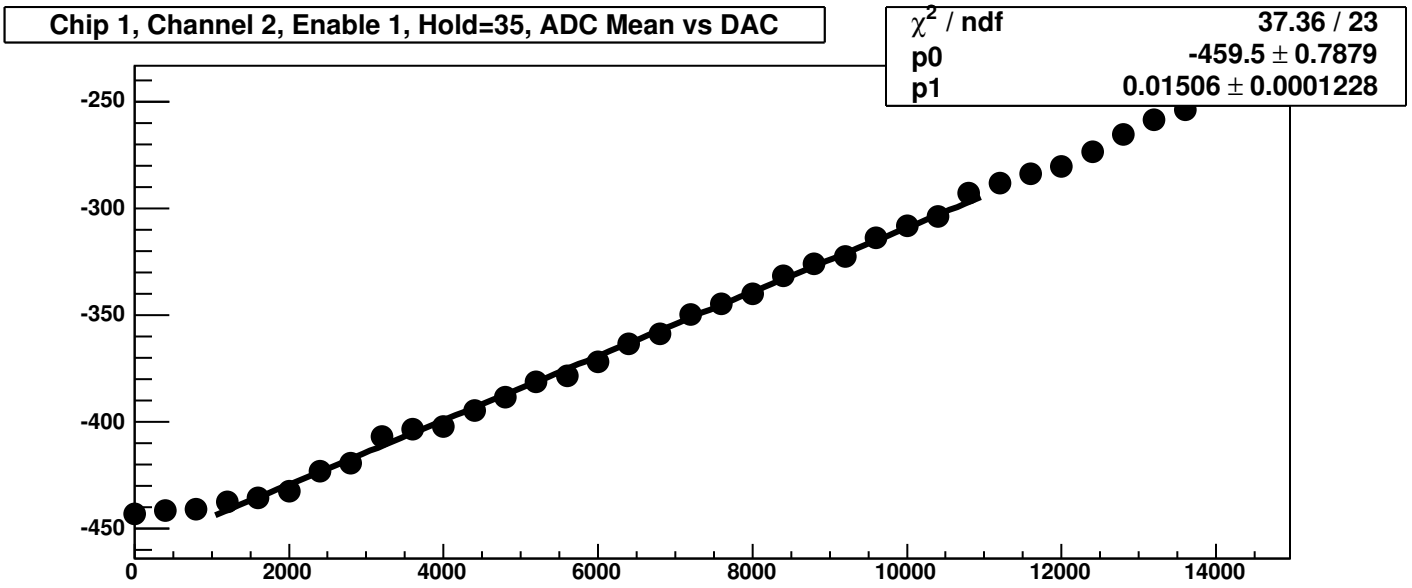


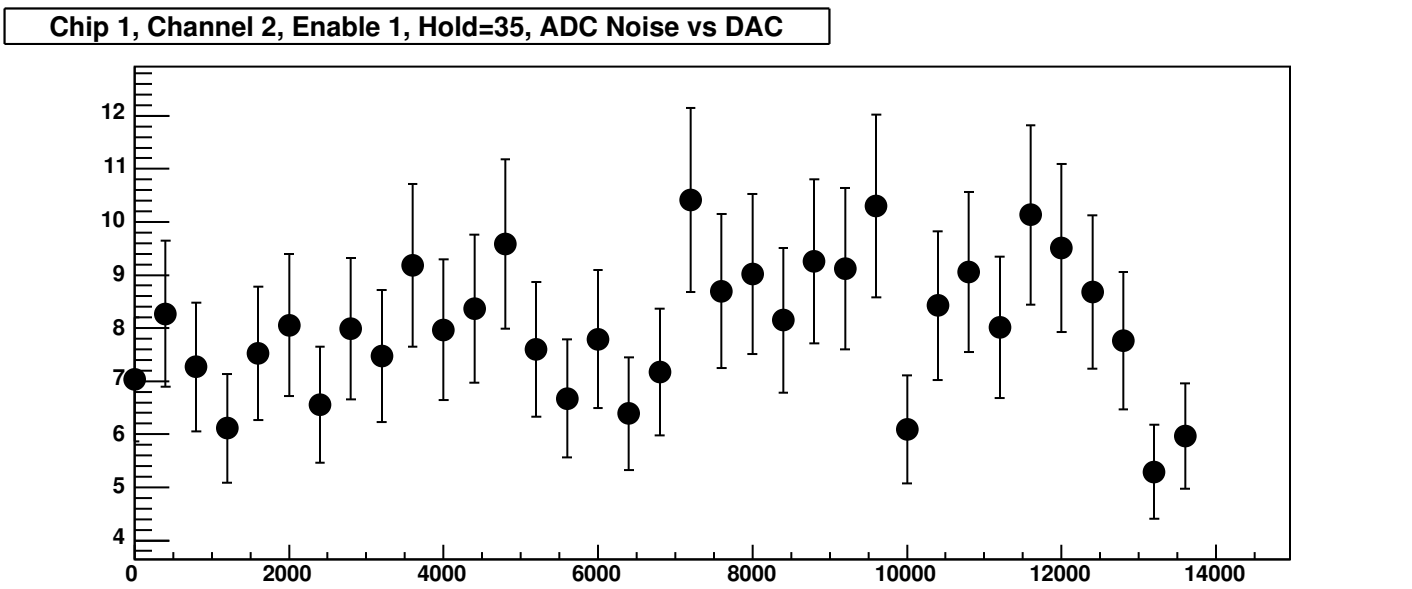
Chip 1, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC

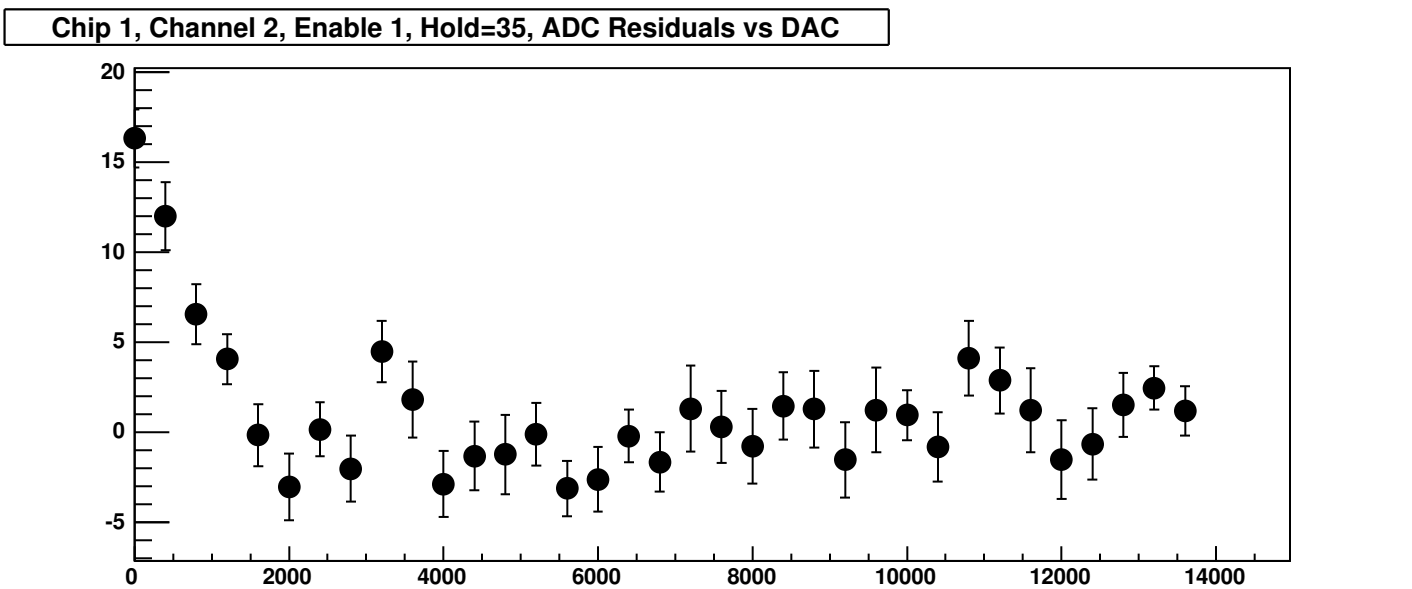


Chip 1, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC

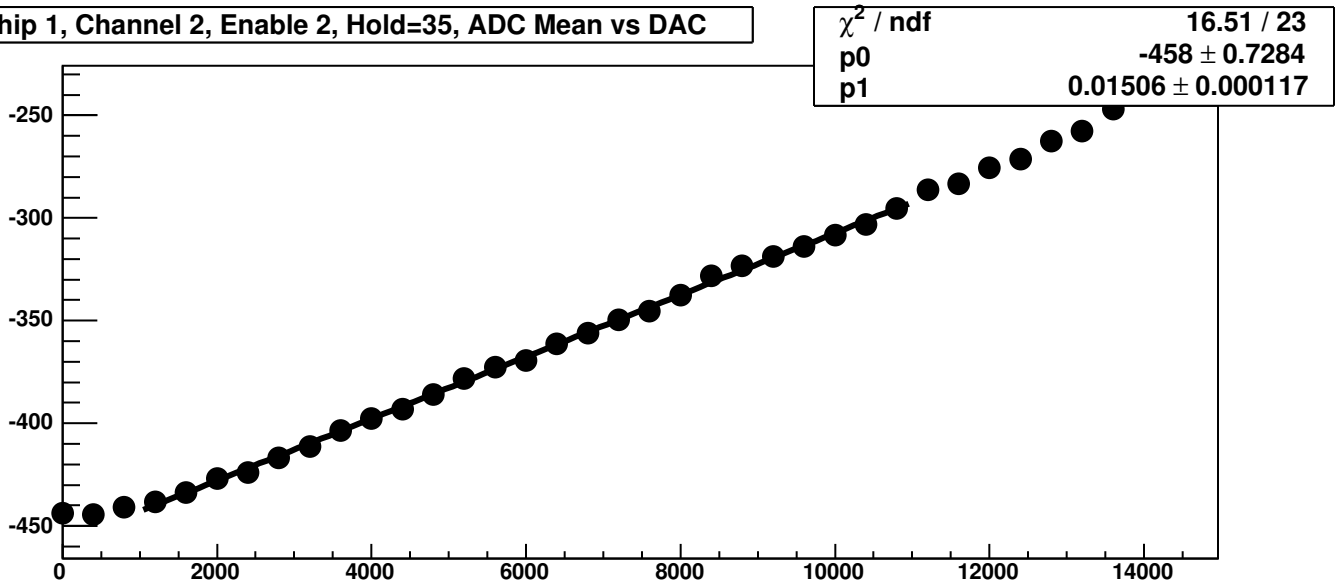



Chip 1, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 37.36 / 23
p0 -459.5 ± 0.7879
p1 0.01506 ± 0.0001228
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

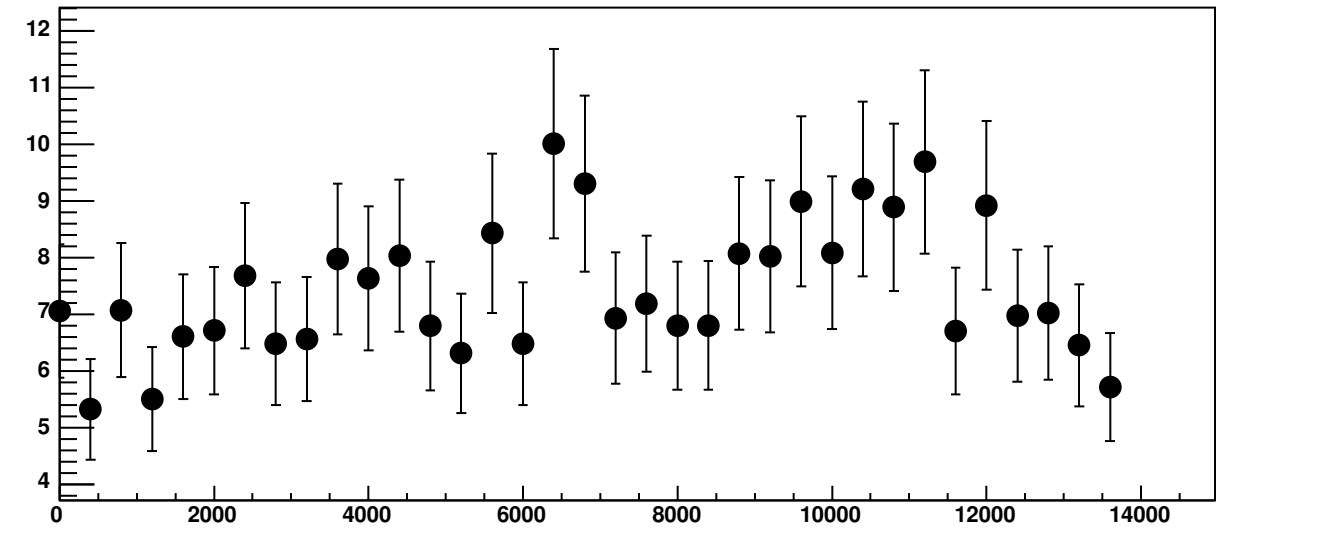

Chip 1, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

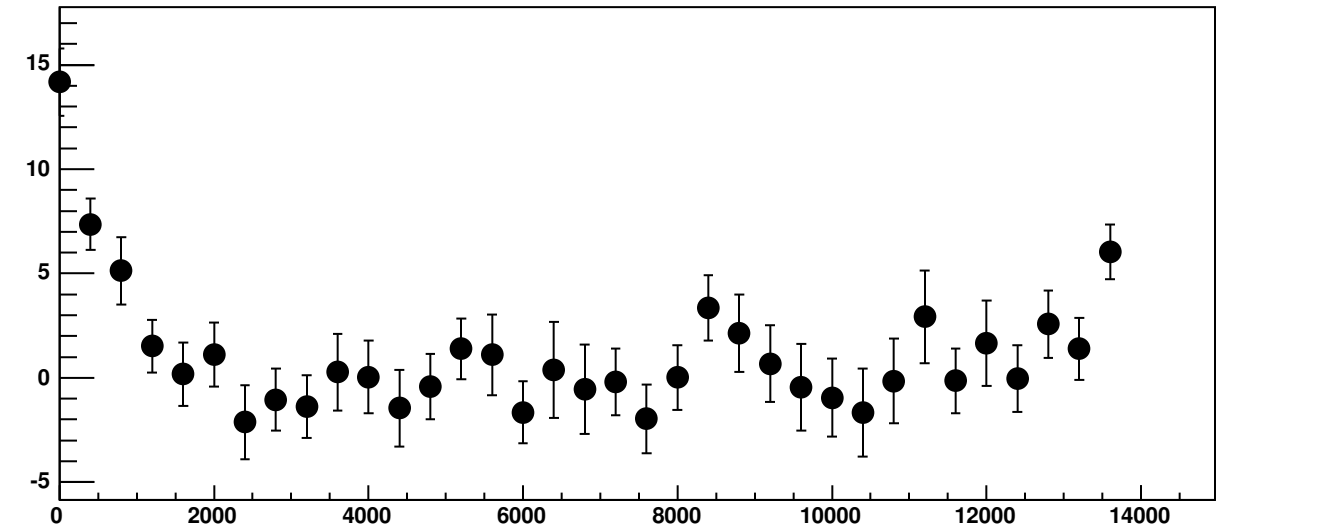
Chip 1, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

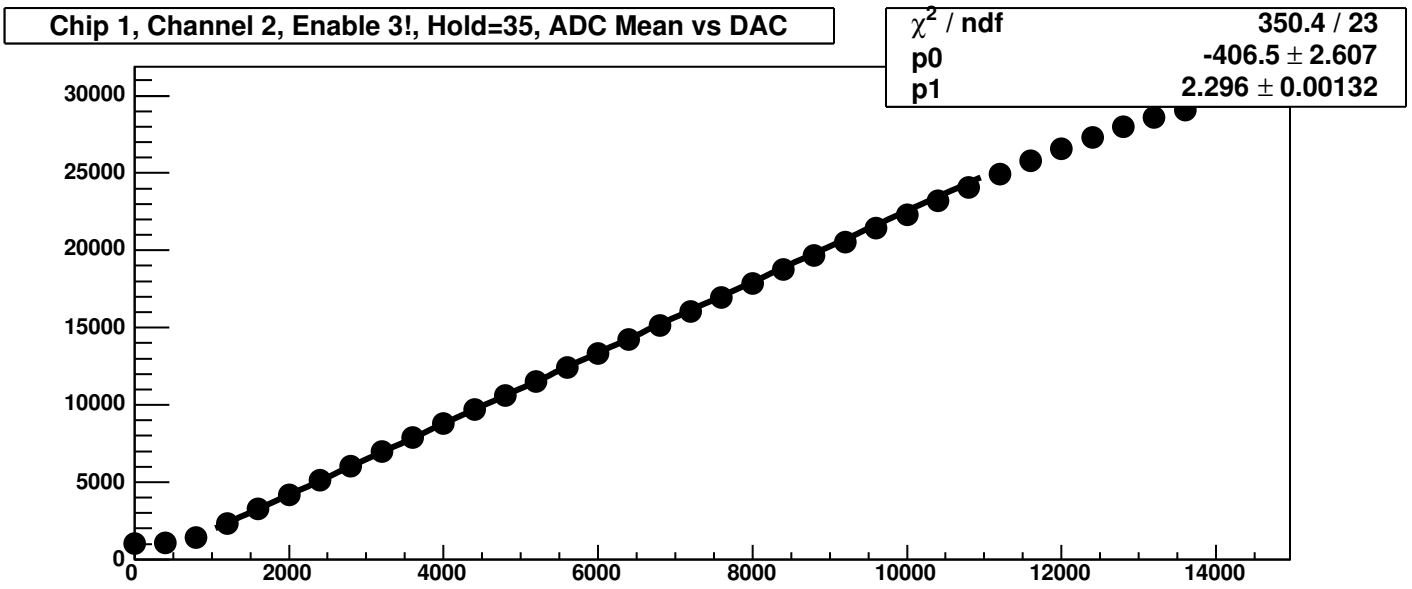


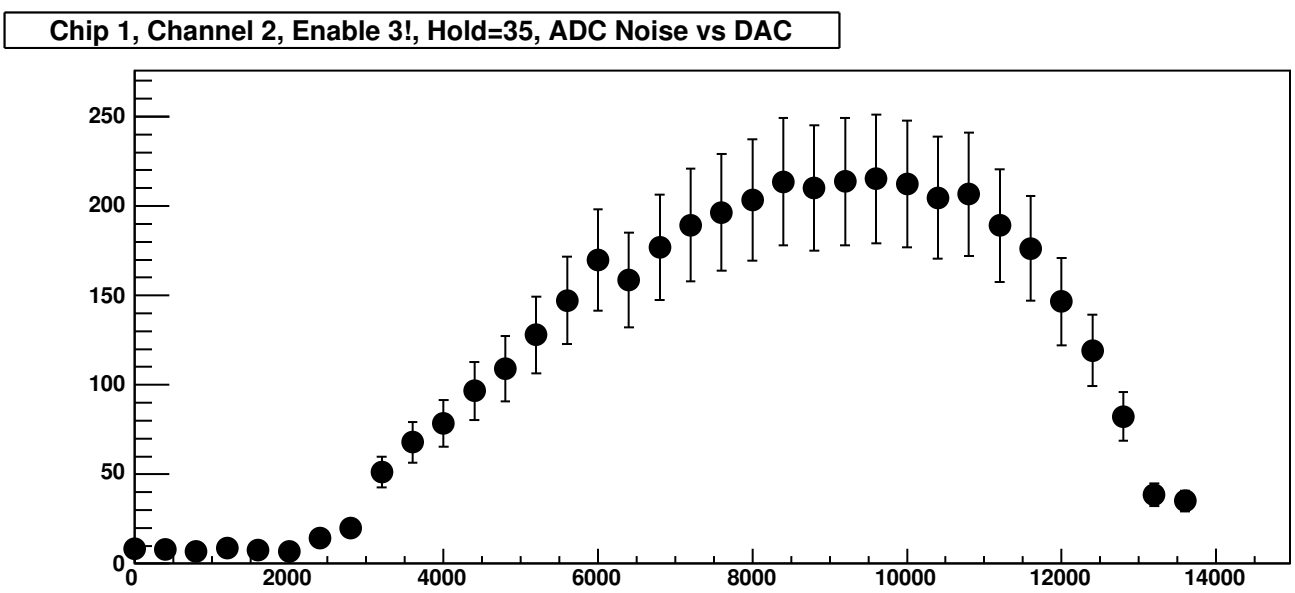
Chip 1, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

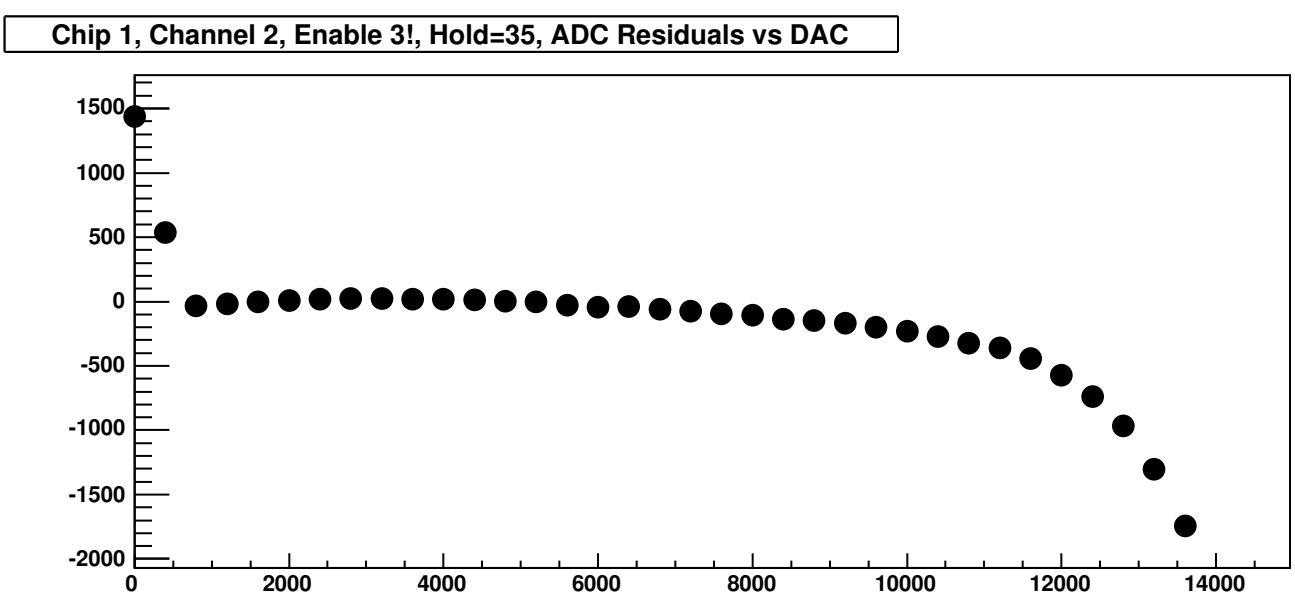


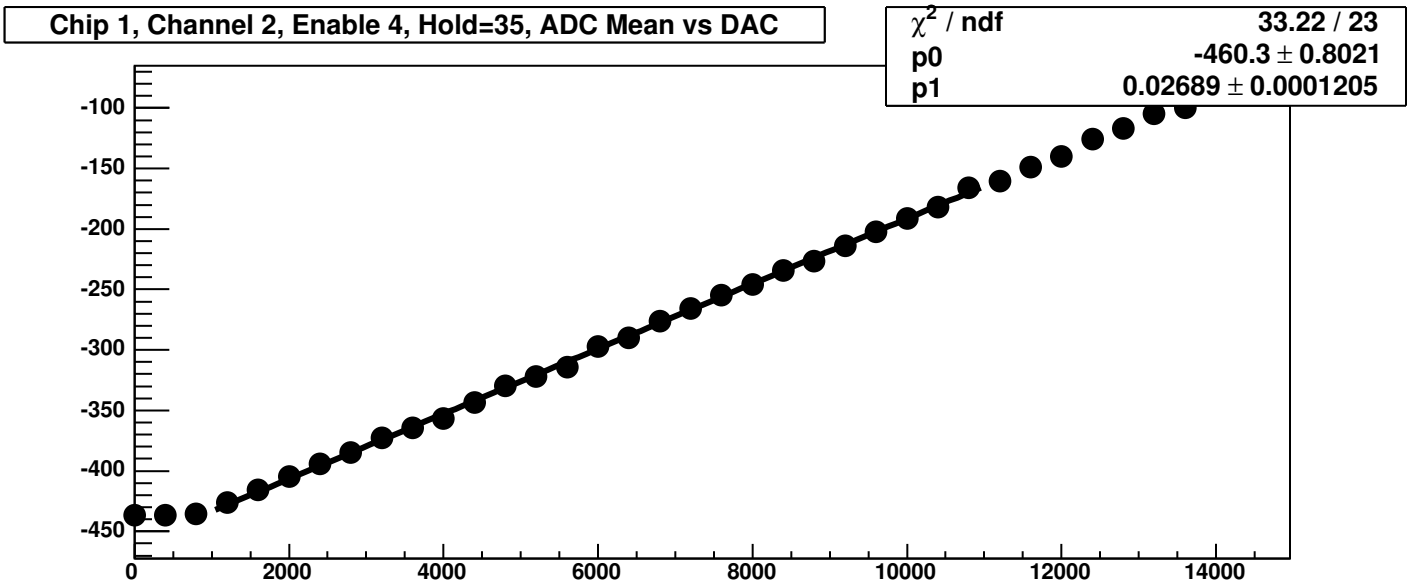
Chip 1, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

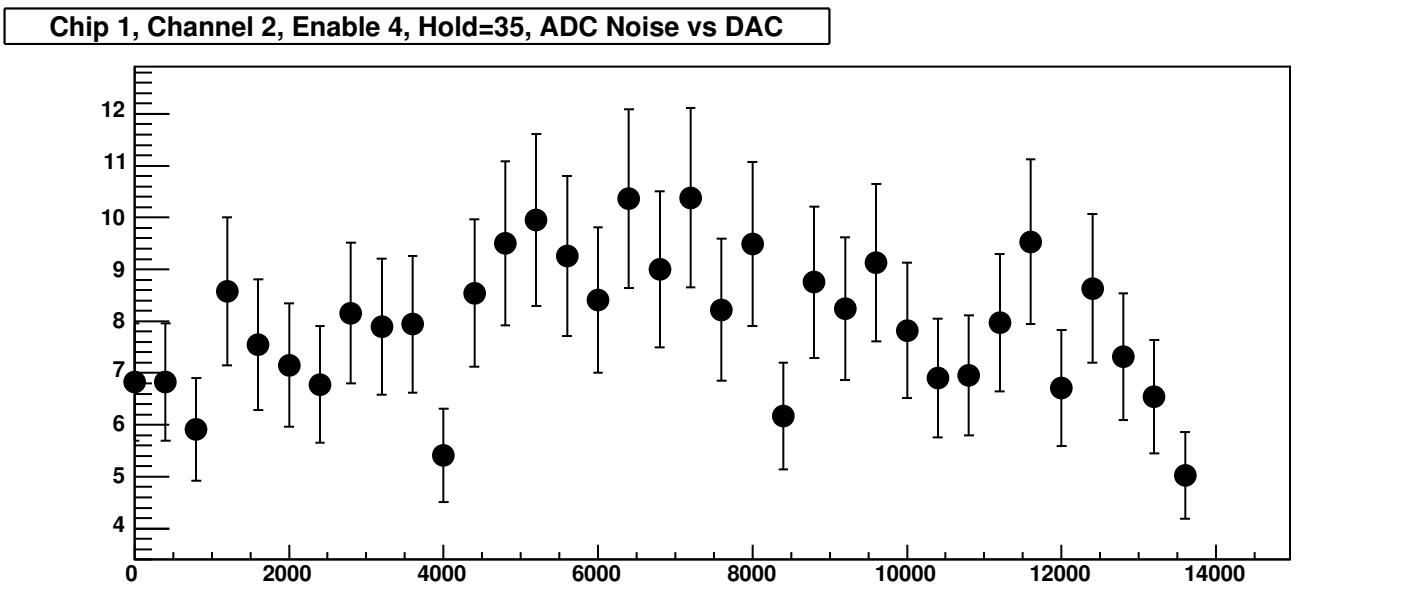


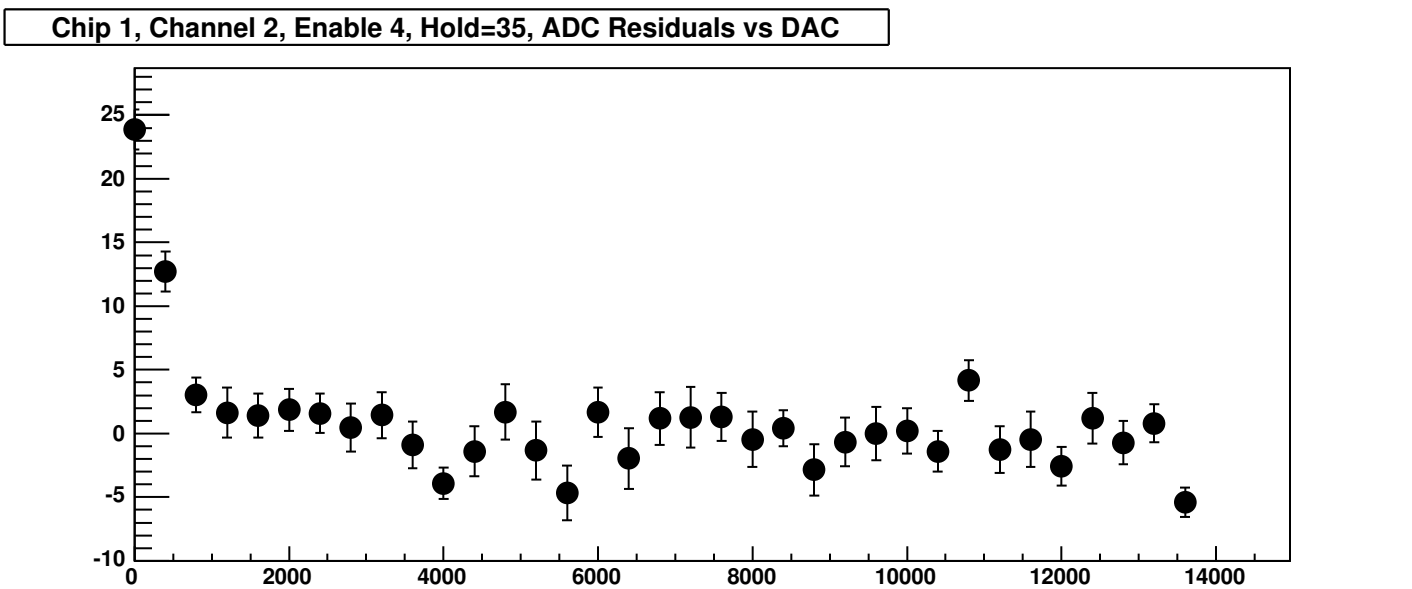

Chip 1, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 350.4 / 23
p0 -406.5 ± 2.607
p1 2.296 ± 0.00132
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

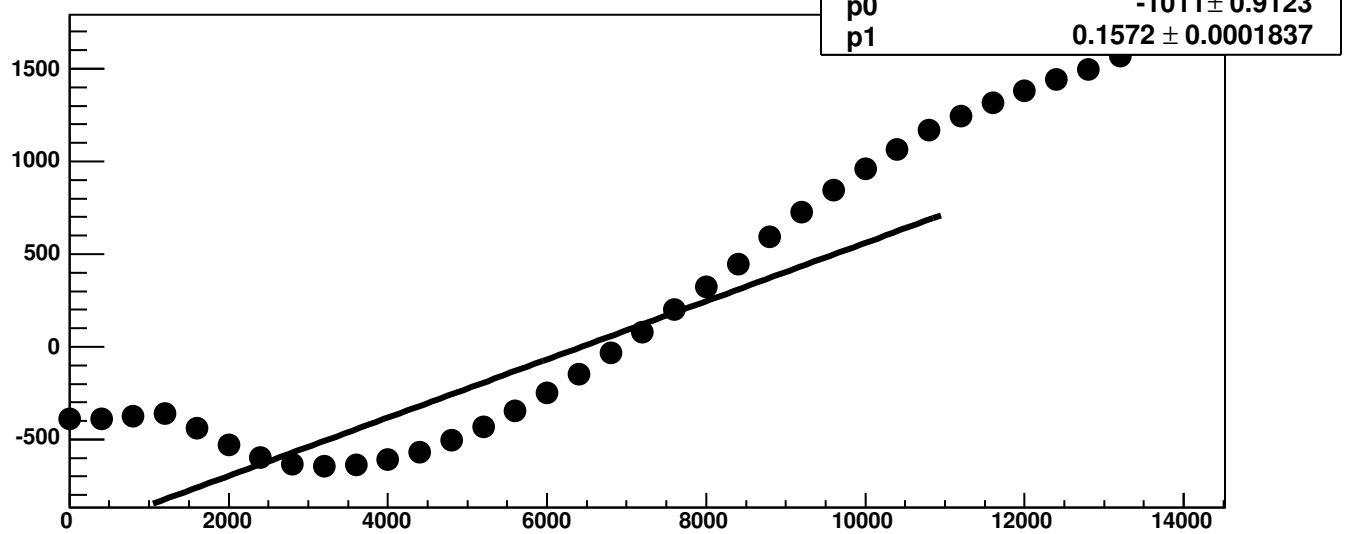

Chip 1, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 33.22 / 23
p0 -460.3 ± 0.8021
p1 0.02689 ± 0.0001205
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

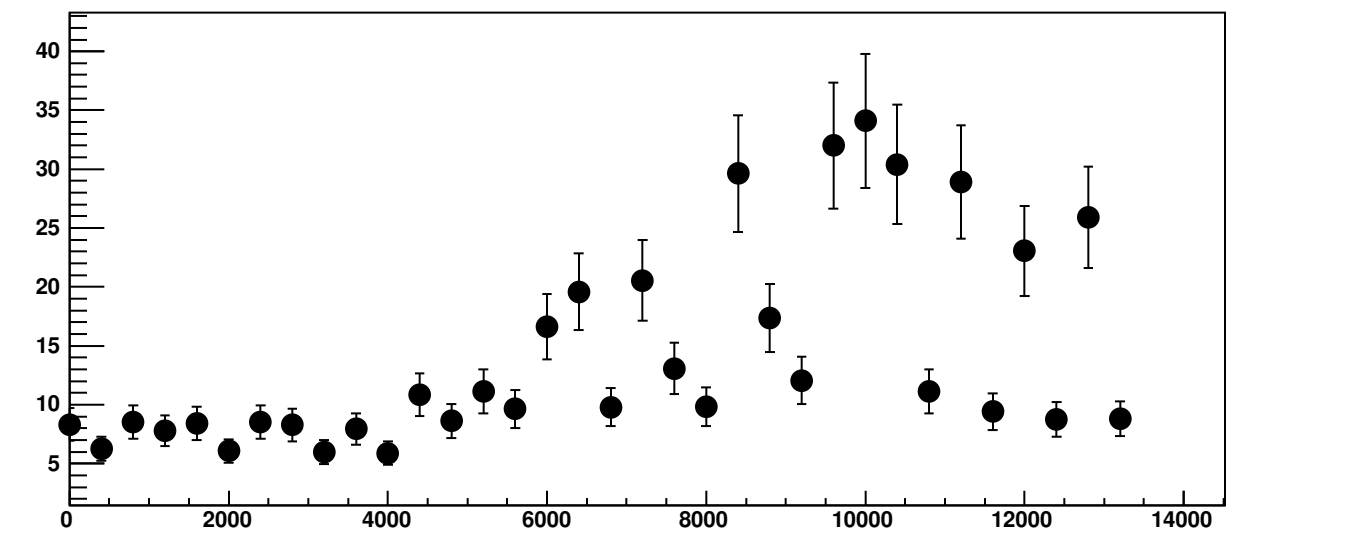

Chip 1, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

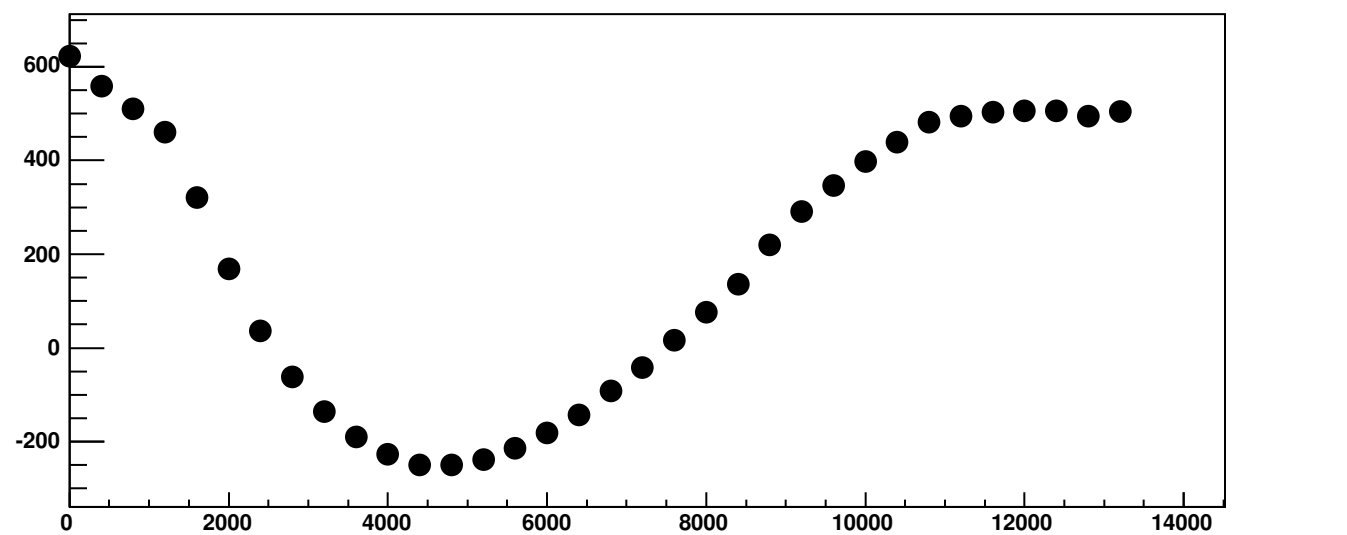
Chip 1, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

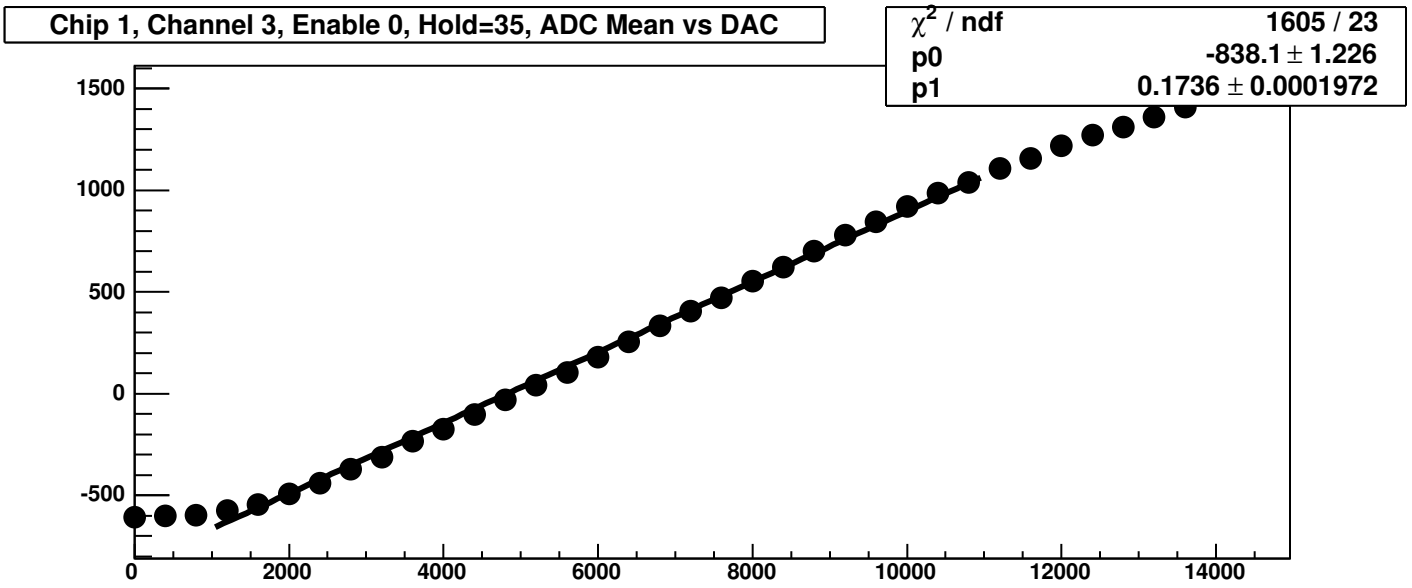


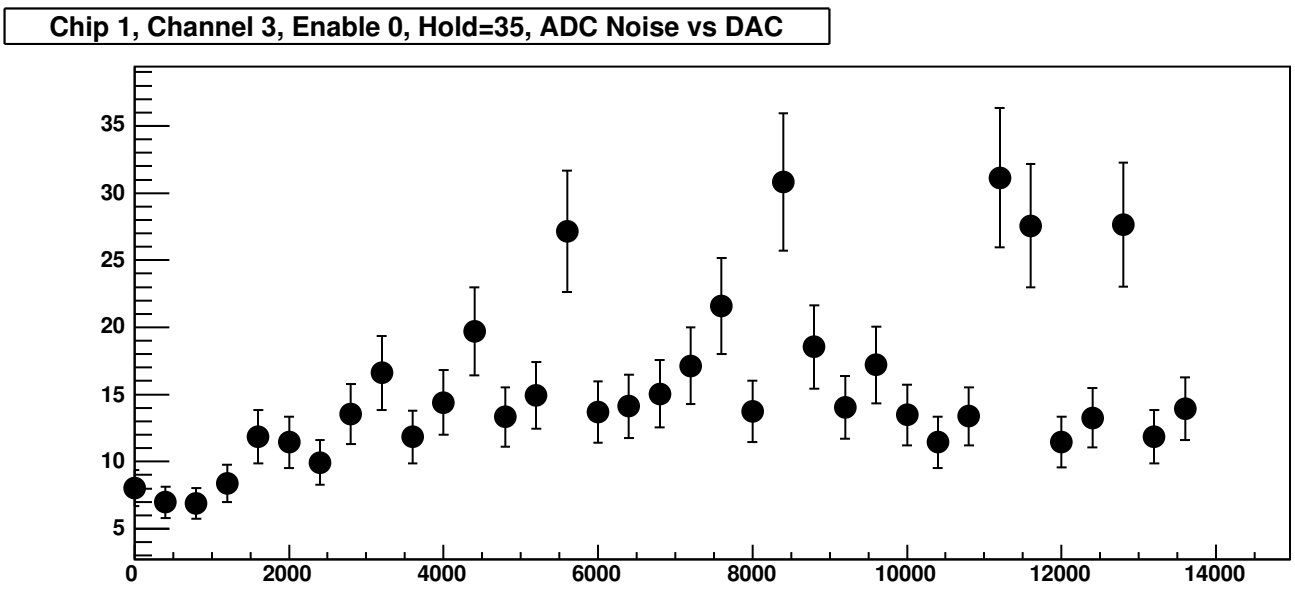
Chip 1, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC

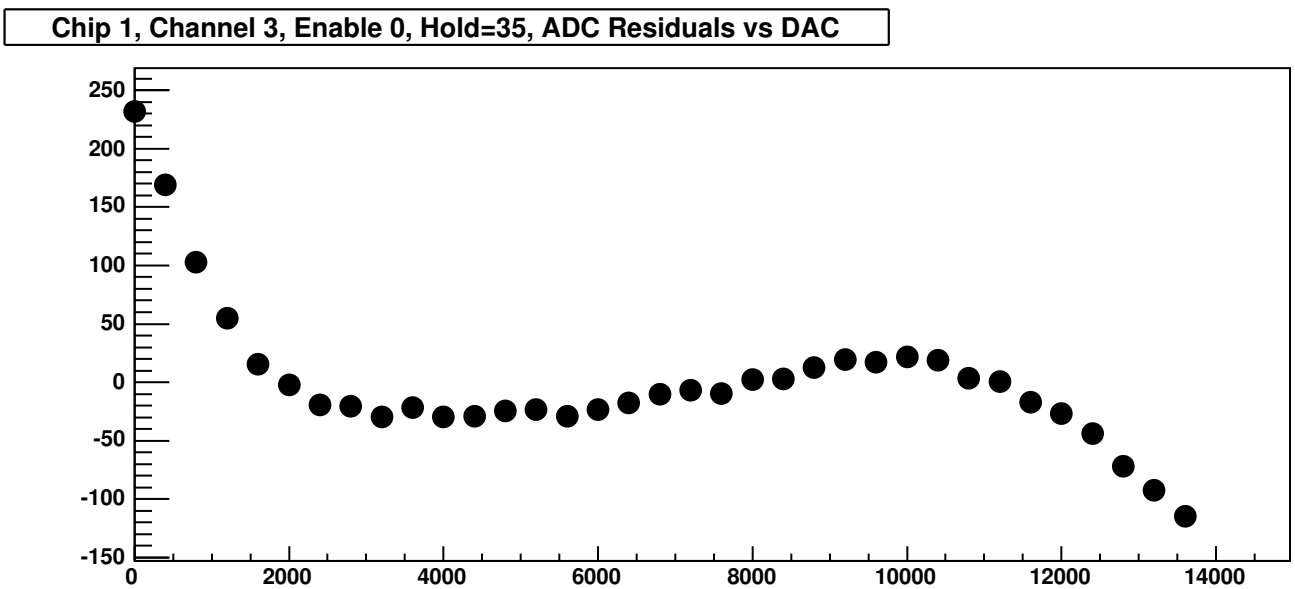


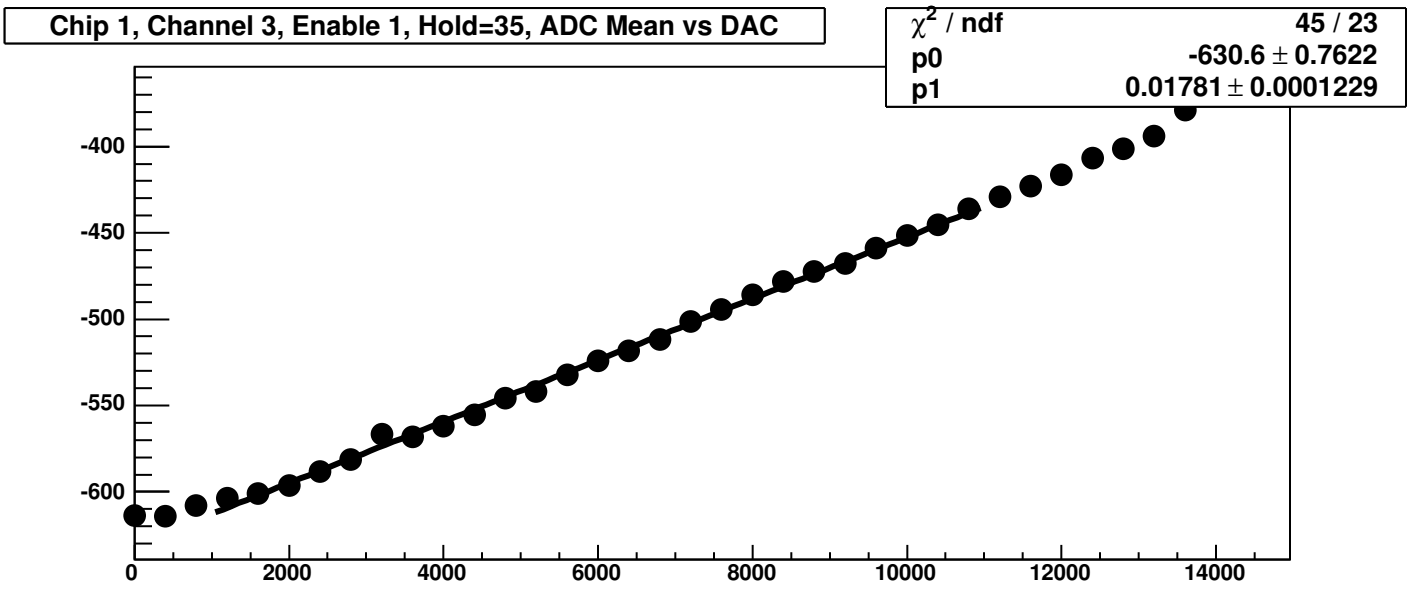
Chip 1, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

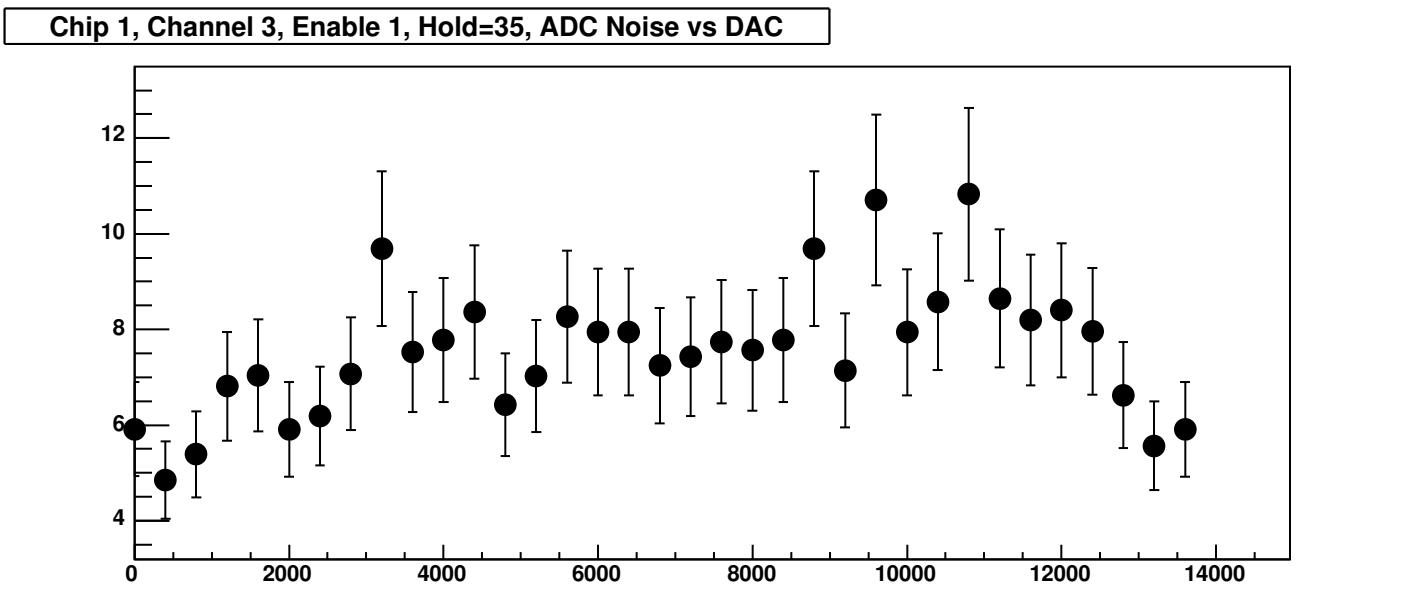


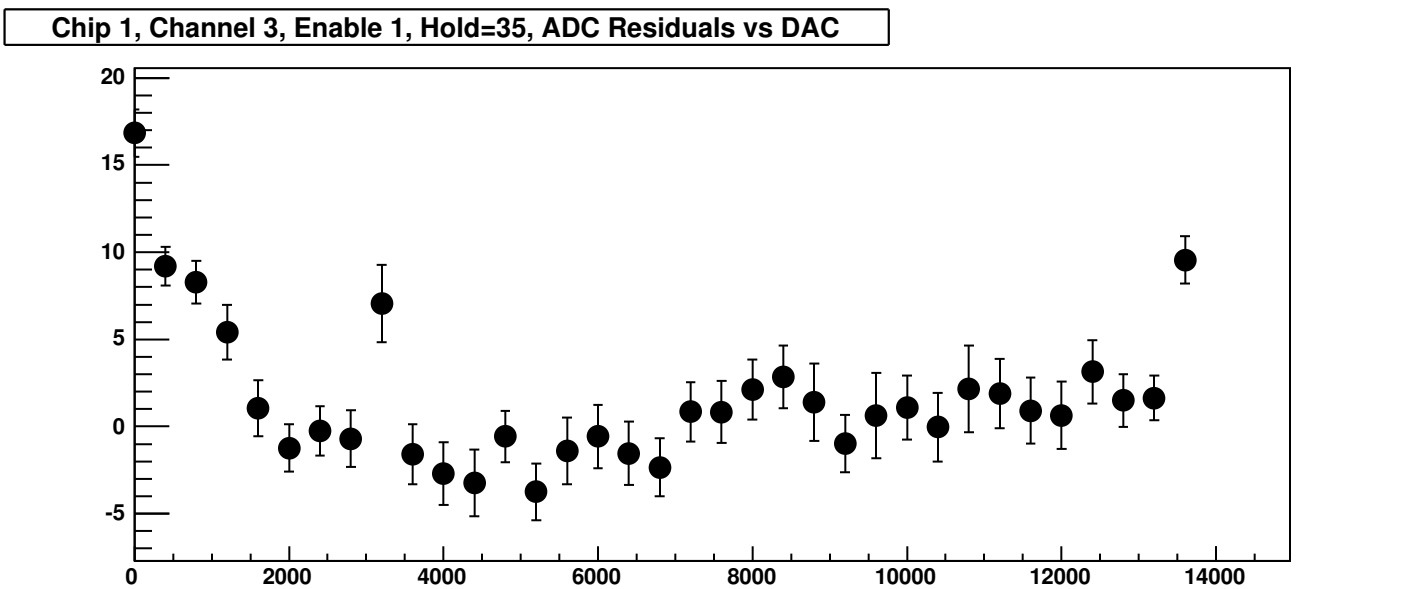

Chip 1, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1605 / 23
p0 -838.1 ± 1.226
p1 0.1736 ± 0.0001972
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

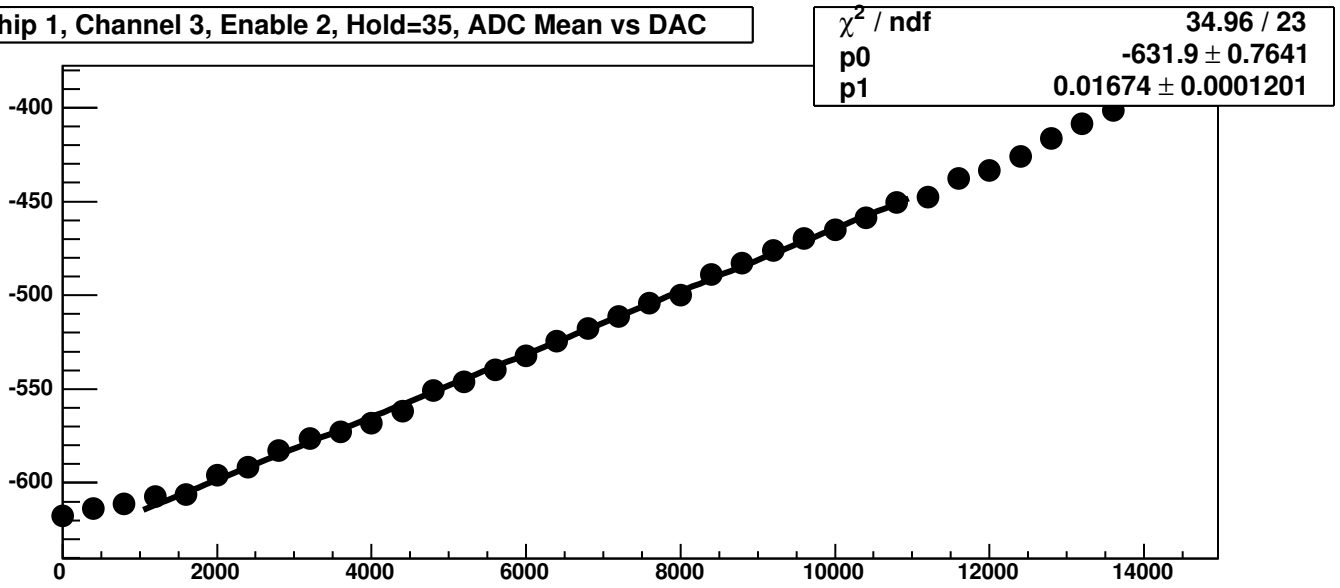

Chip 1, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 45 / 23
p0 -630.6 ± 0.7622
p1 0.01781 ± 0.0001229
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

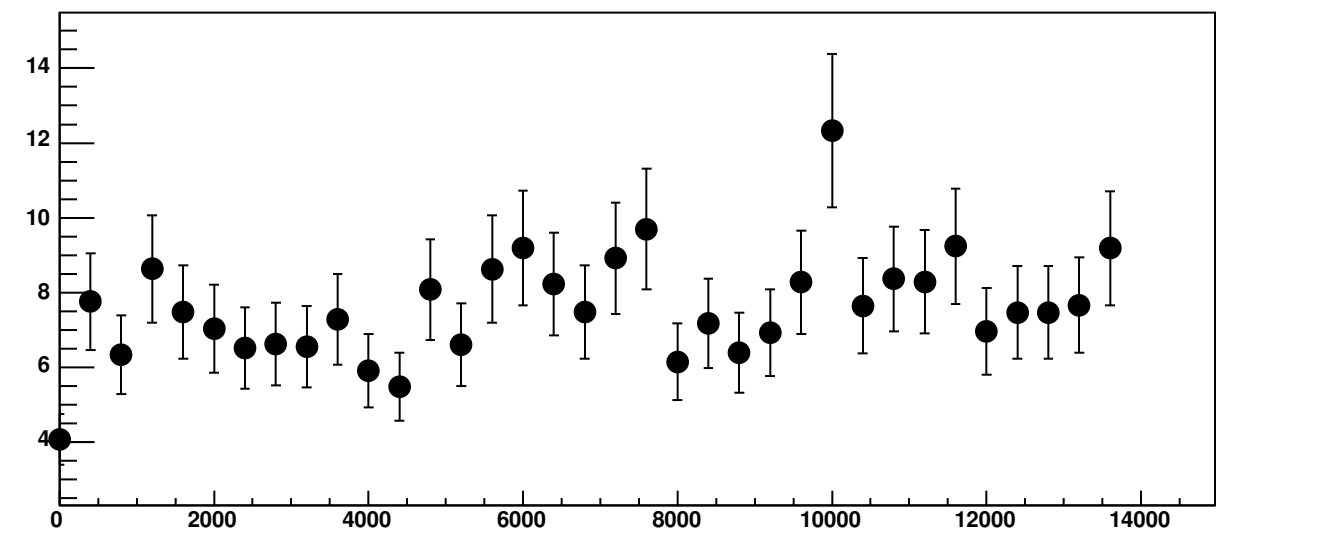

Chip 1, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

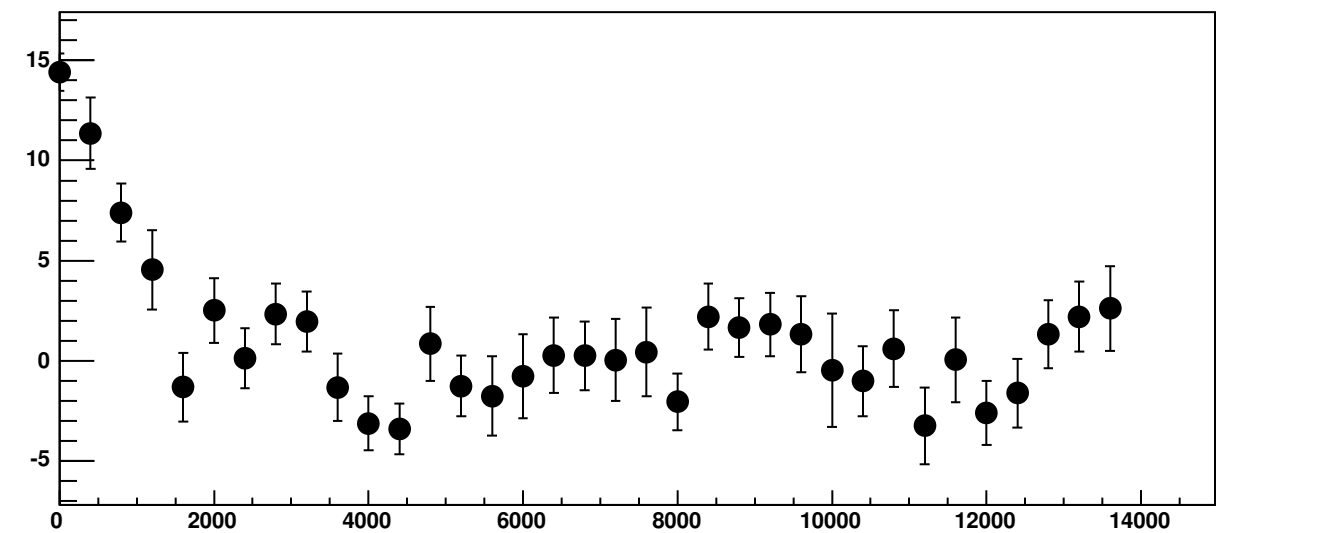
Chip 1, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

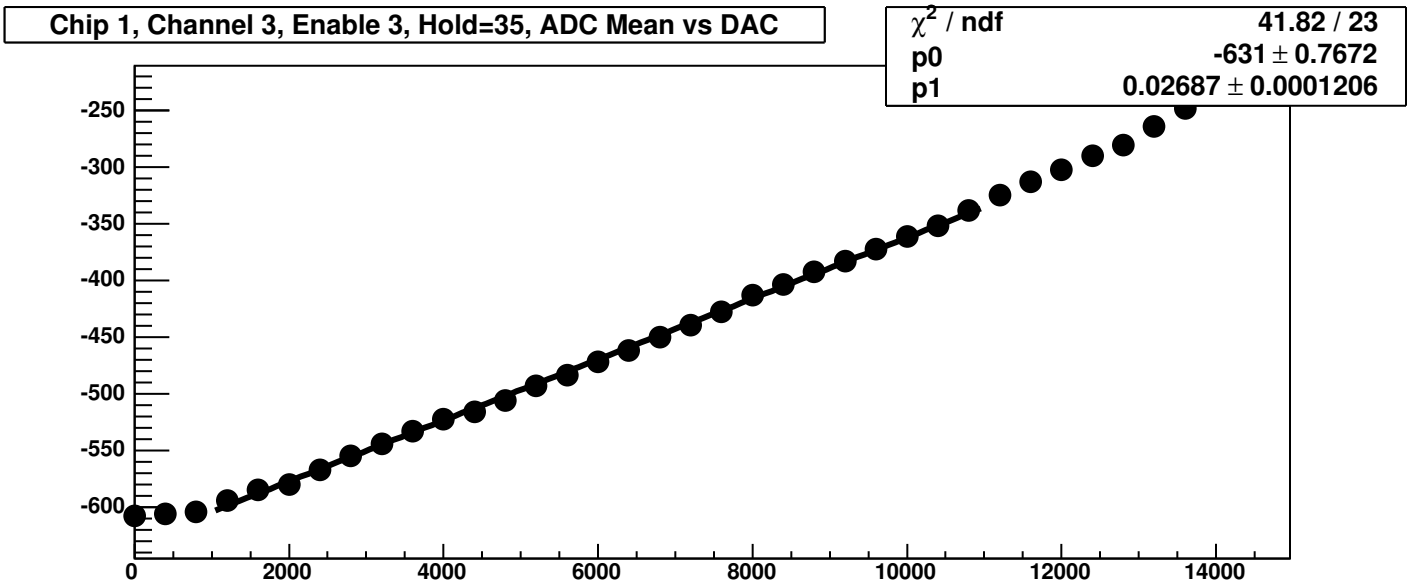


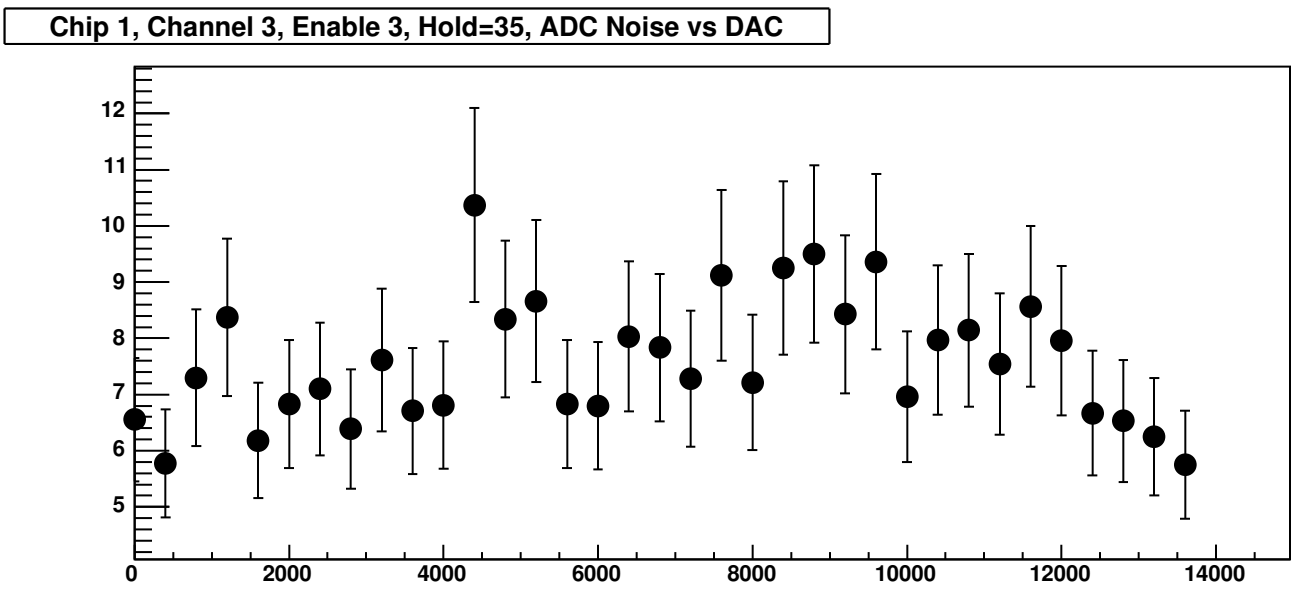
Chip 1, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC

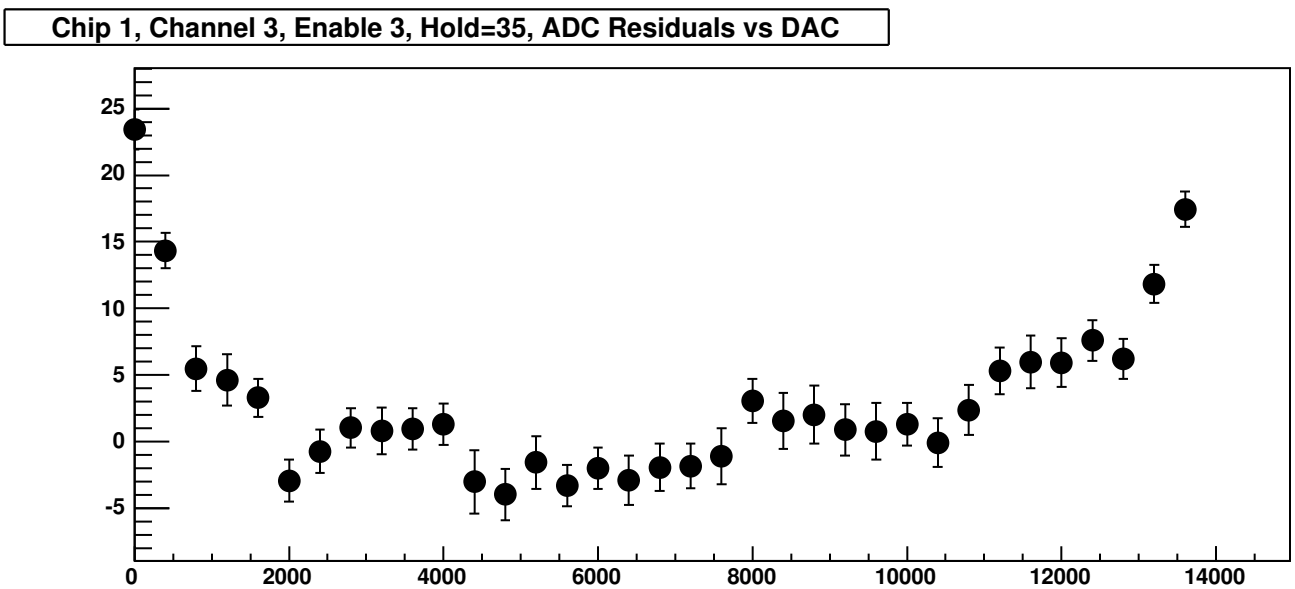


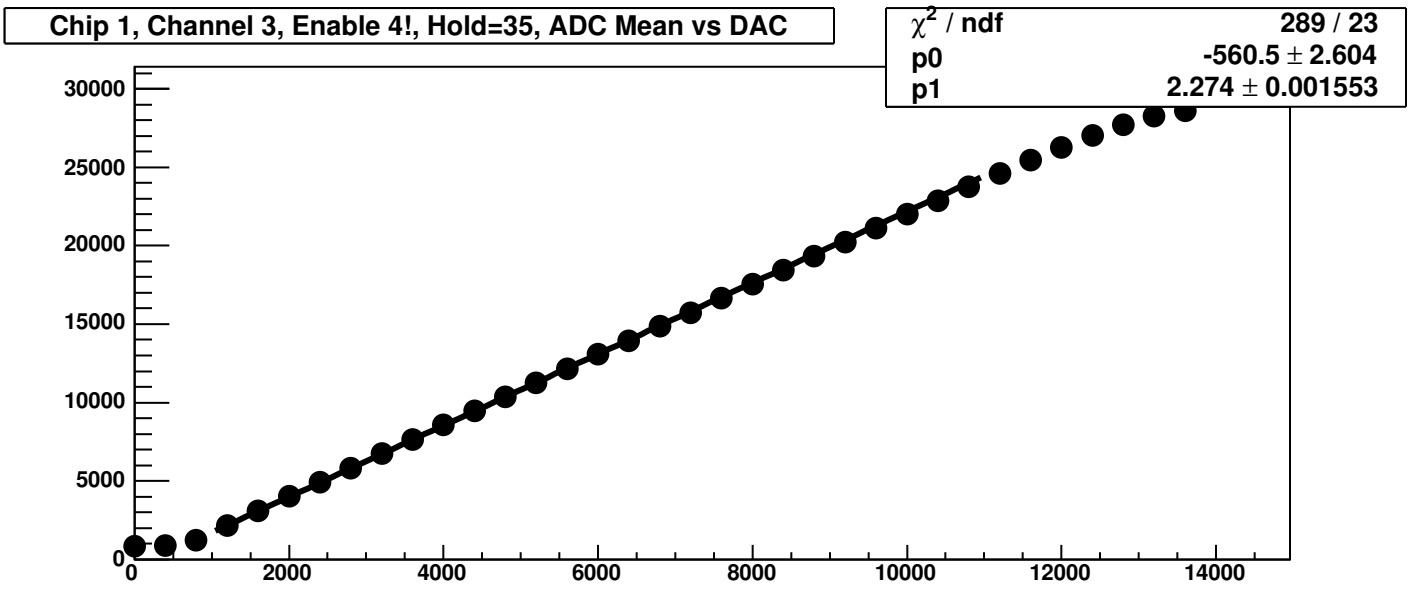
Chip 1, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

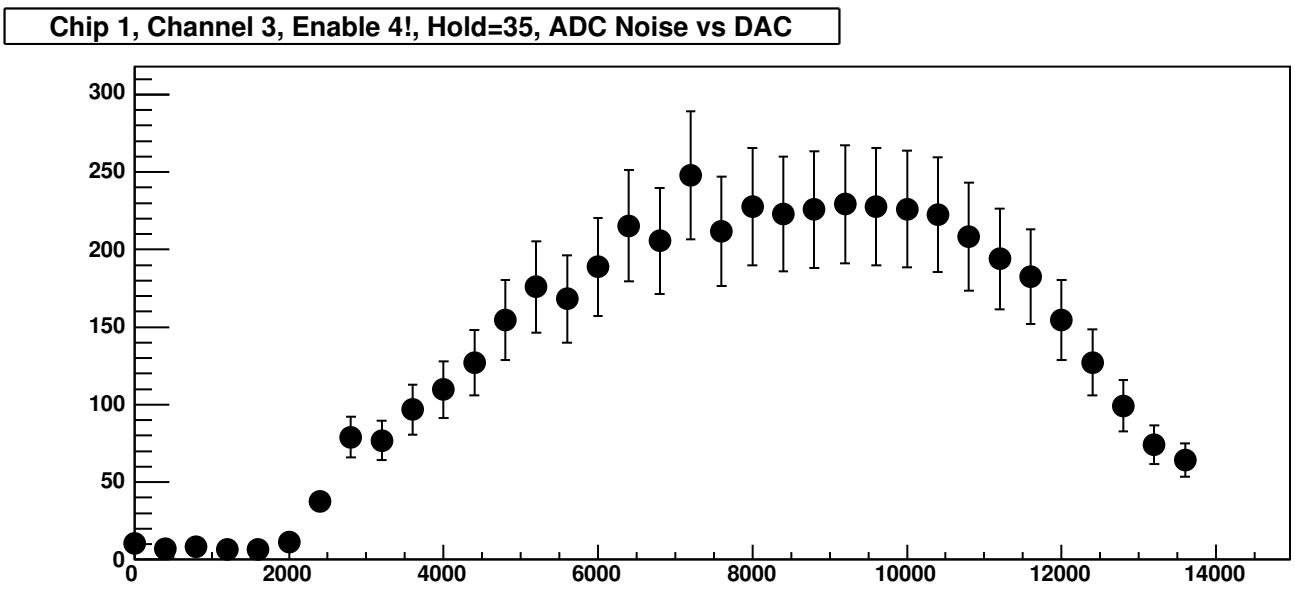


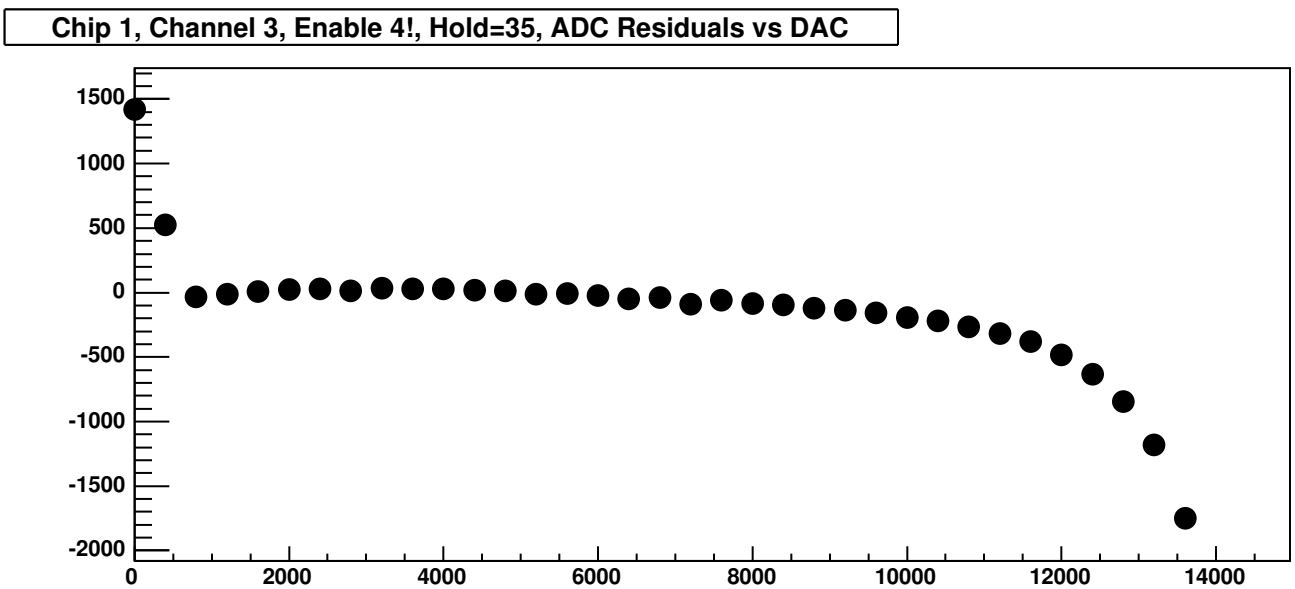

Chip 1, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 41.82 / 23
p0 -631 ± 0.7672
p1 0.02687 ± 0.0001206
-250
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

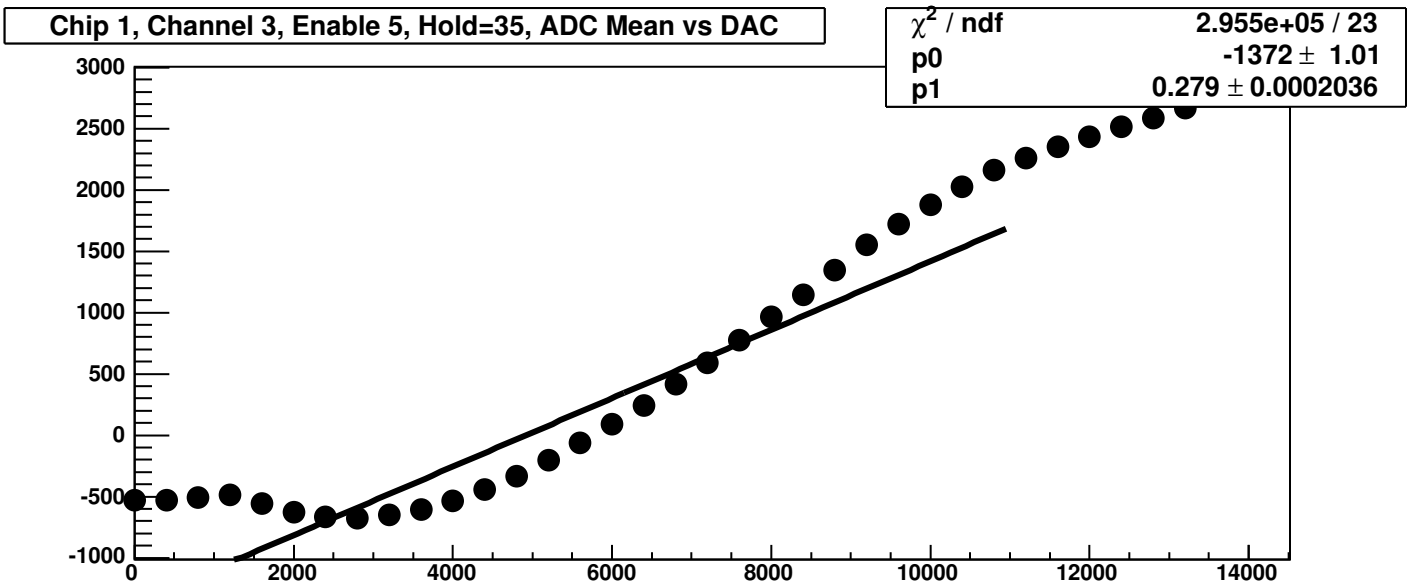

Chip 1, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

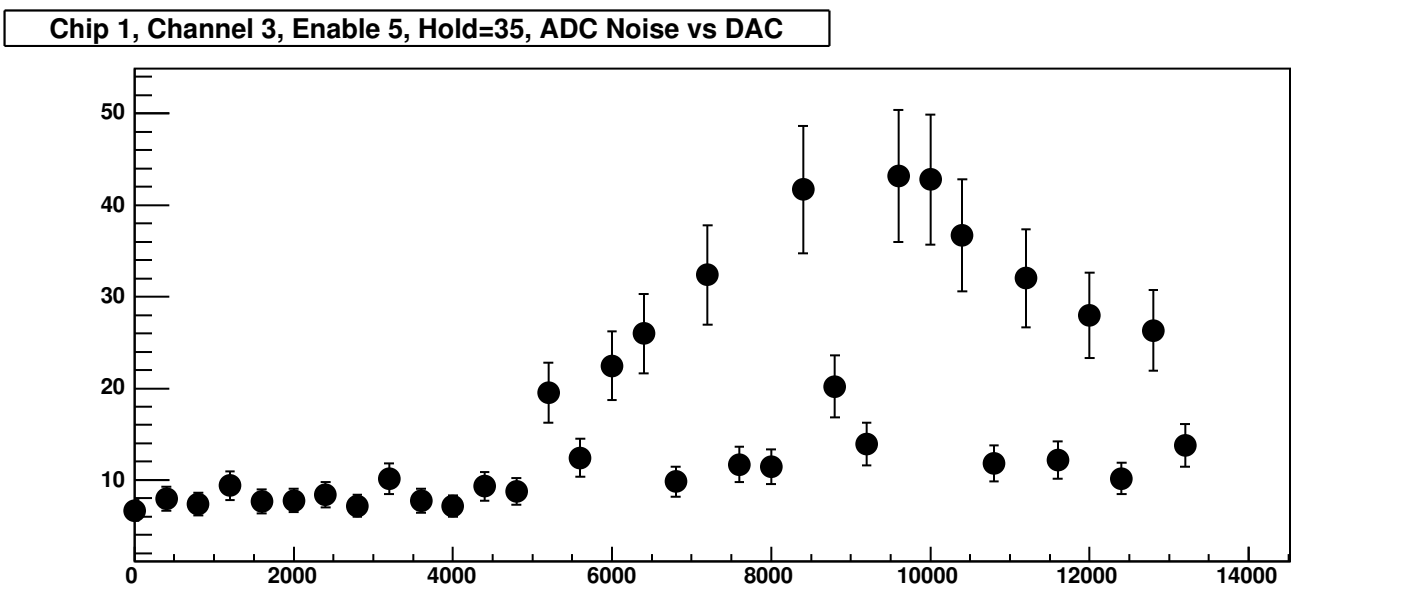

Chip 1, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

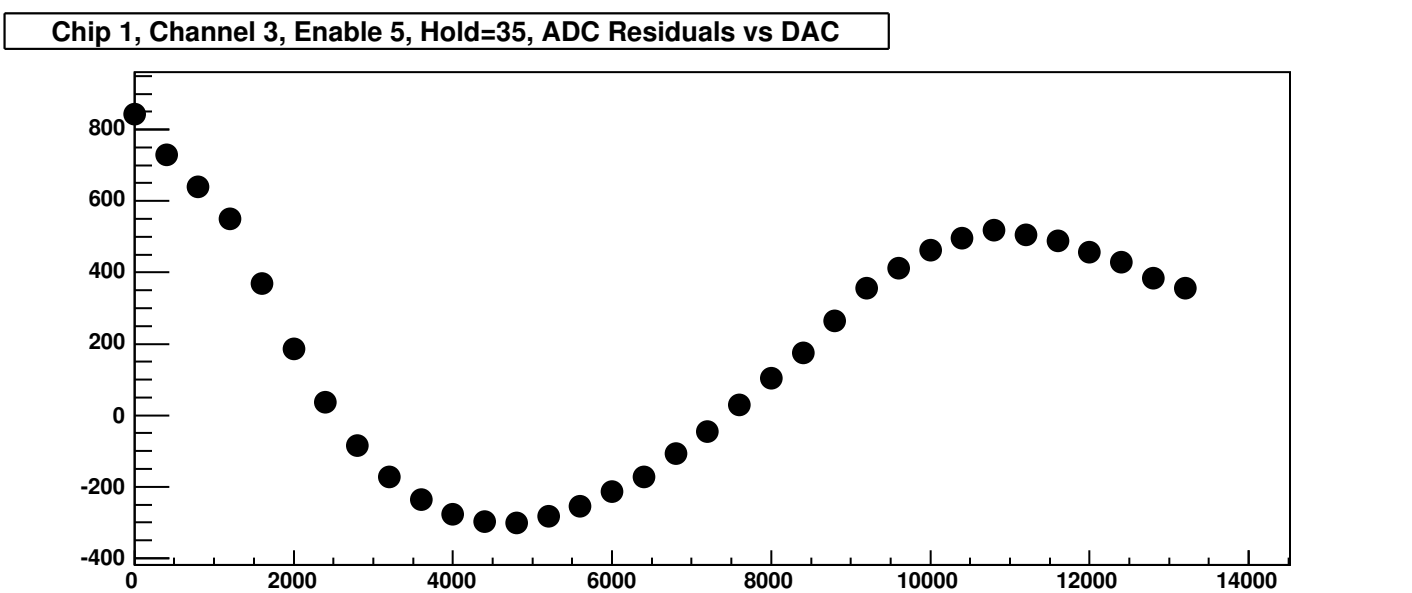

Chip 1, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 289 / 23
p0 -560.5 ± 2.604
p1 2.274 ± 0.001553
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

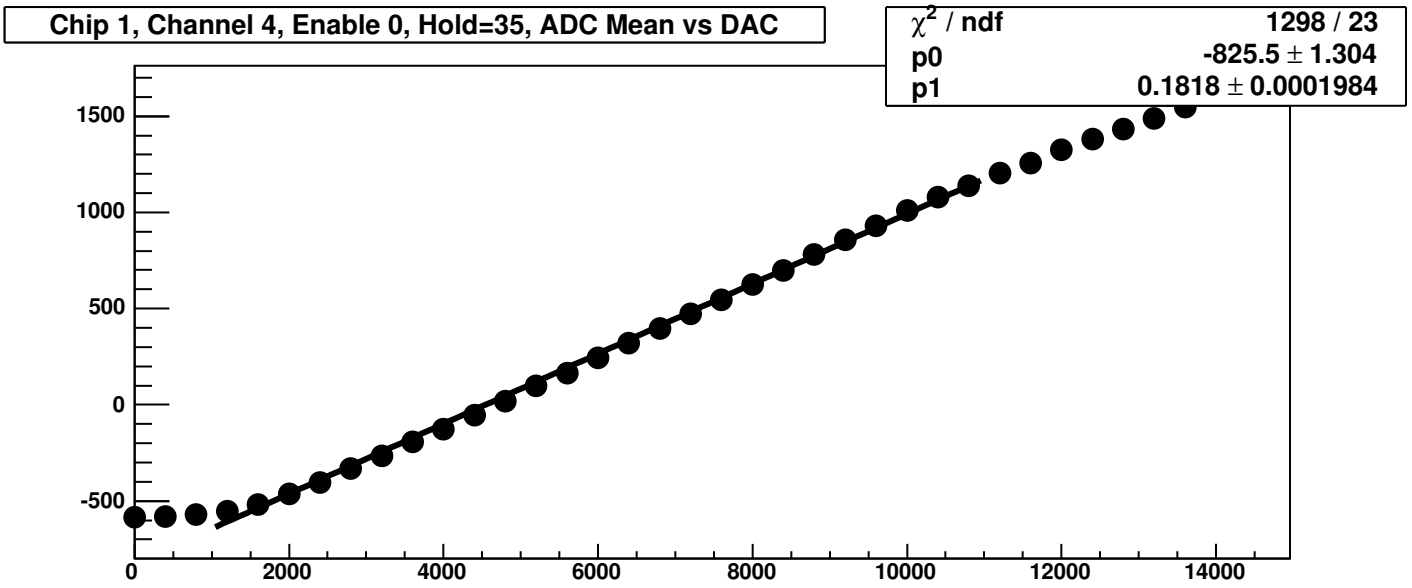

Chip 1, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

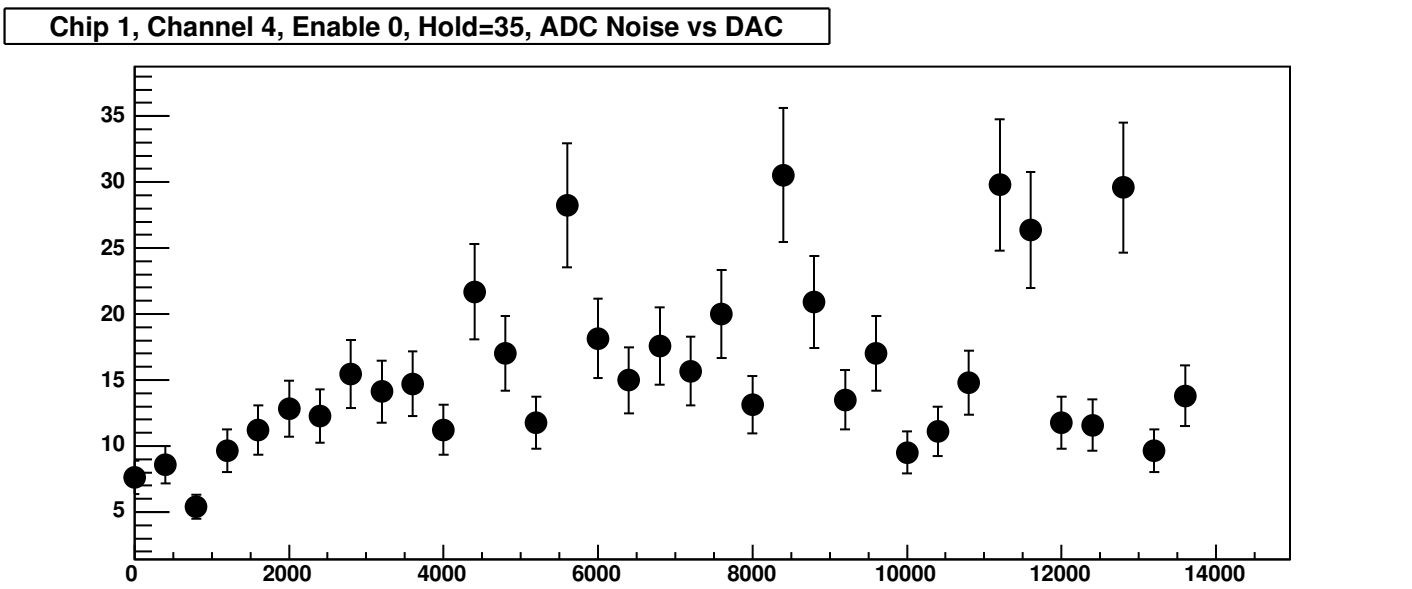

Chip 1, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

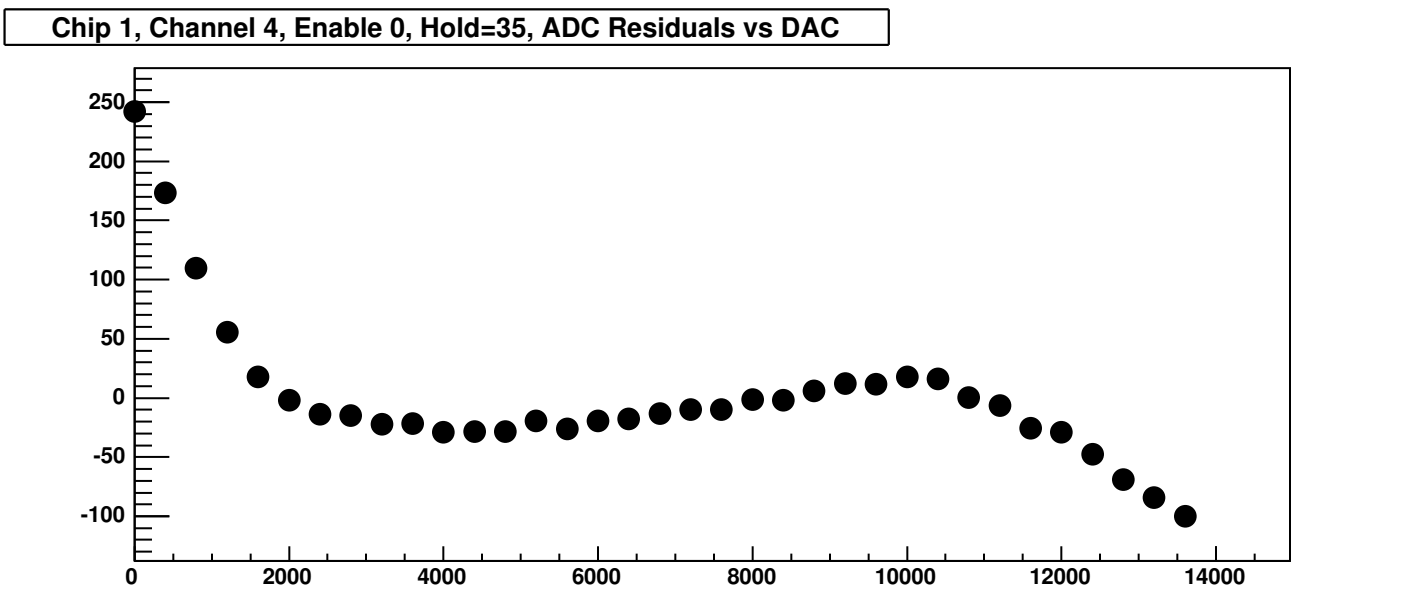

Chip 1, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2.955e+05 / 23
p0 -1372 ± 1.01
p1 0.279 ± 0.0002036
3000
2500
2000
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

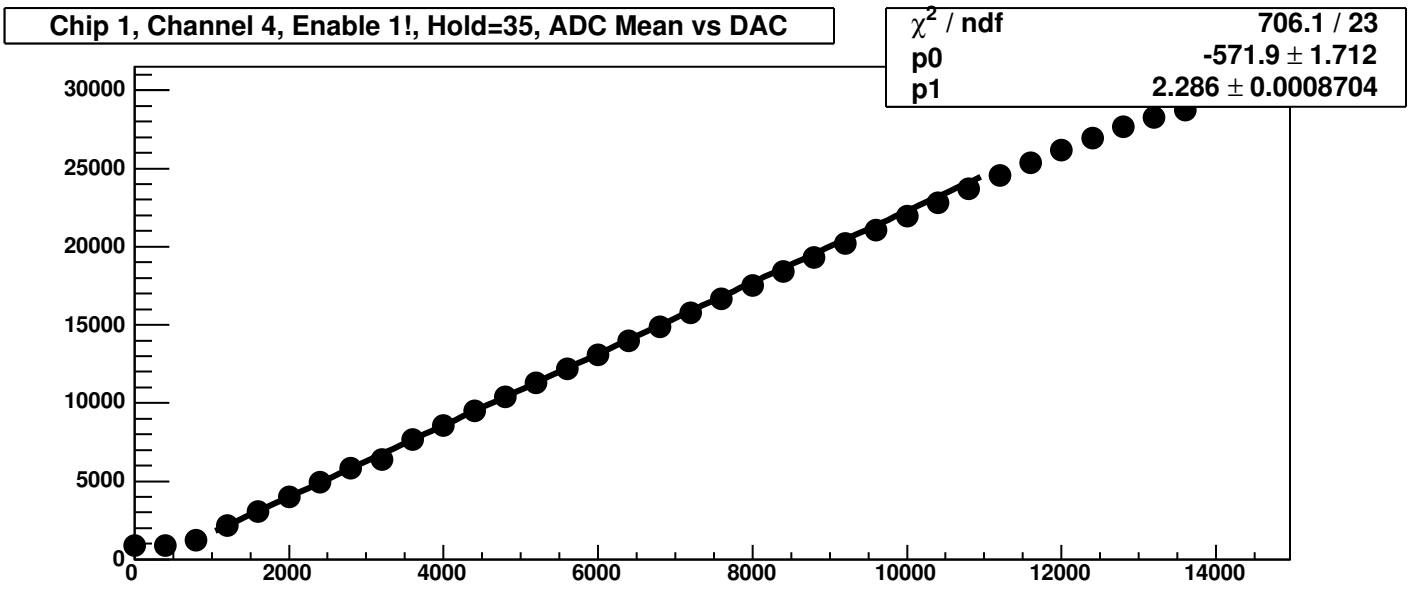

Chip 1, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

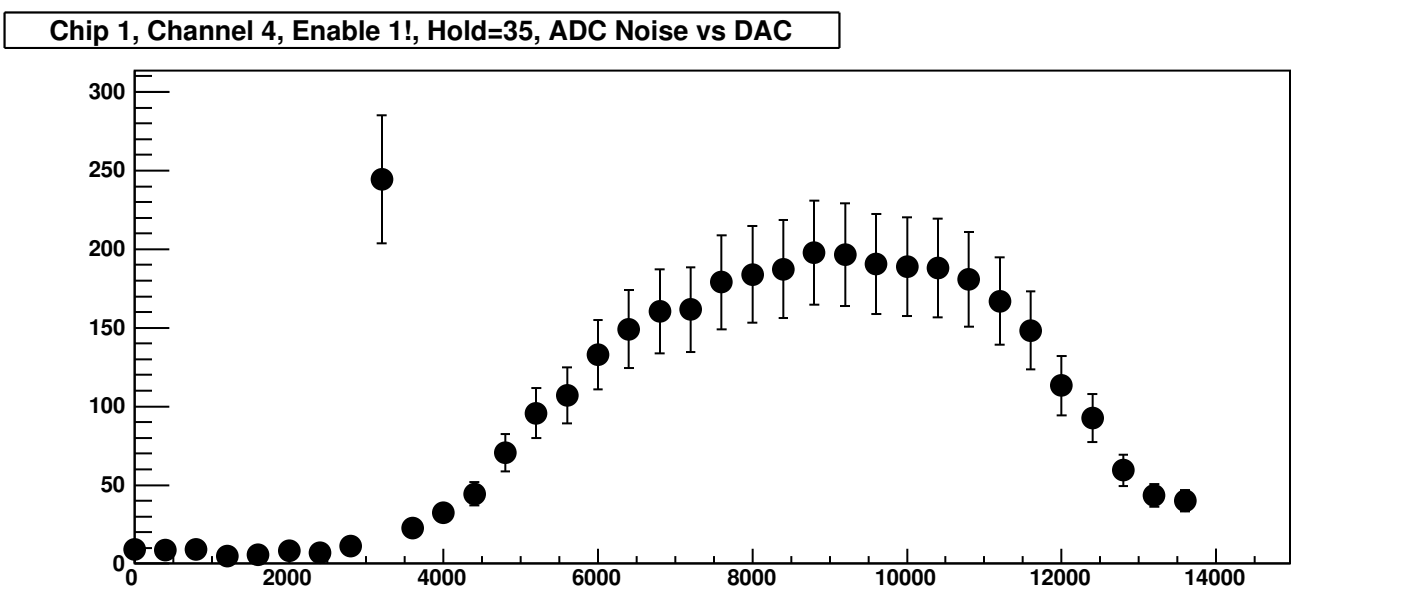

Chip 1, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

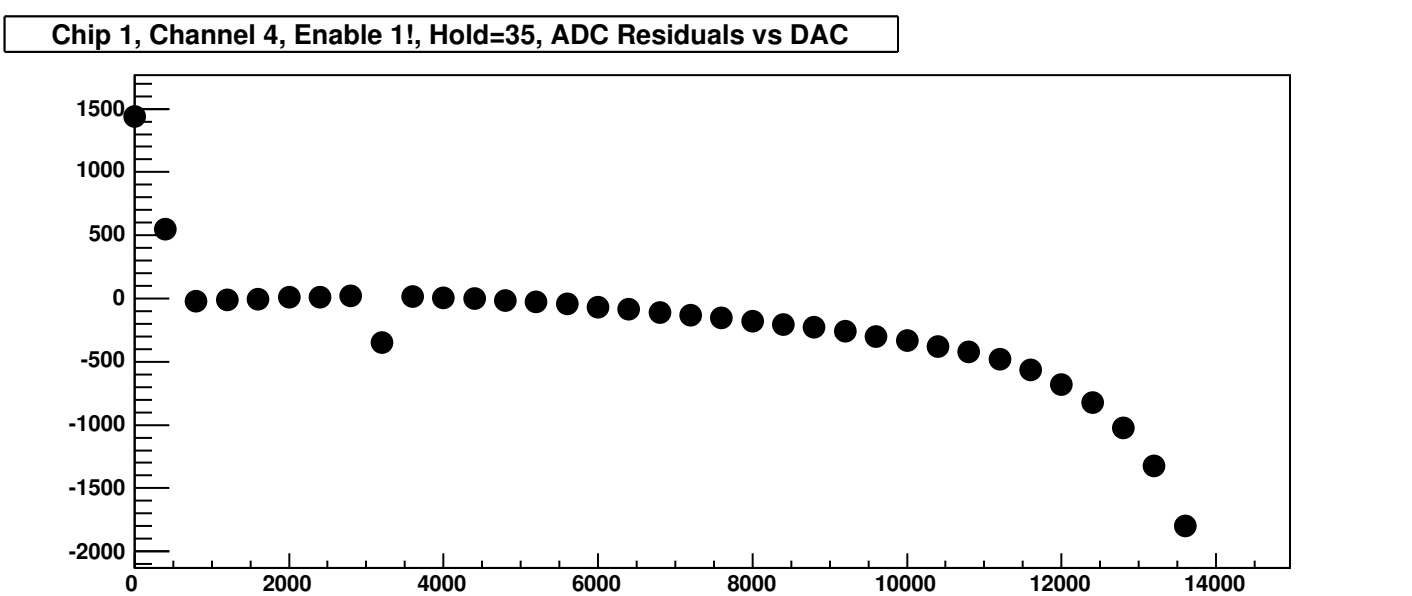

Chip 1, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1298 / 23
p0 -825.5 ± 1.304
p1 0.1818 ± 0.0001984
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

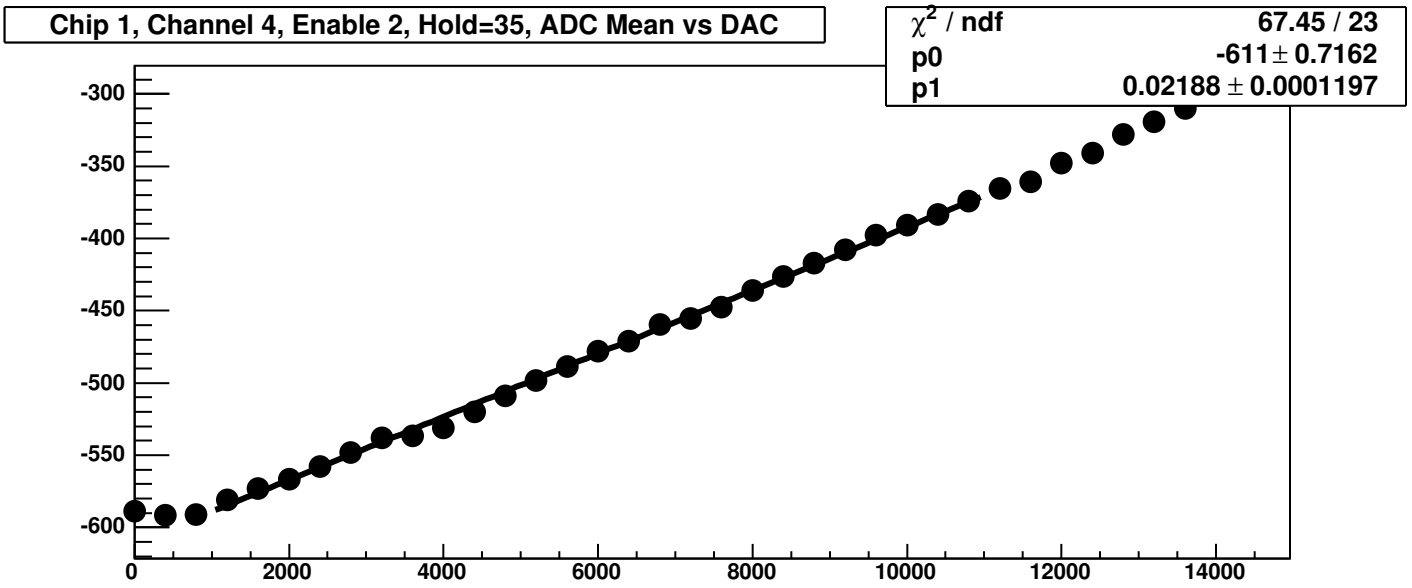

Chip 1, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

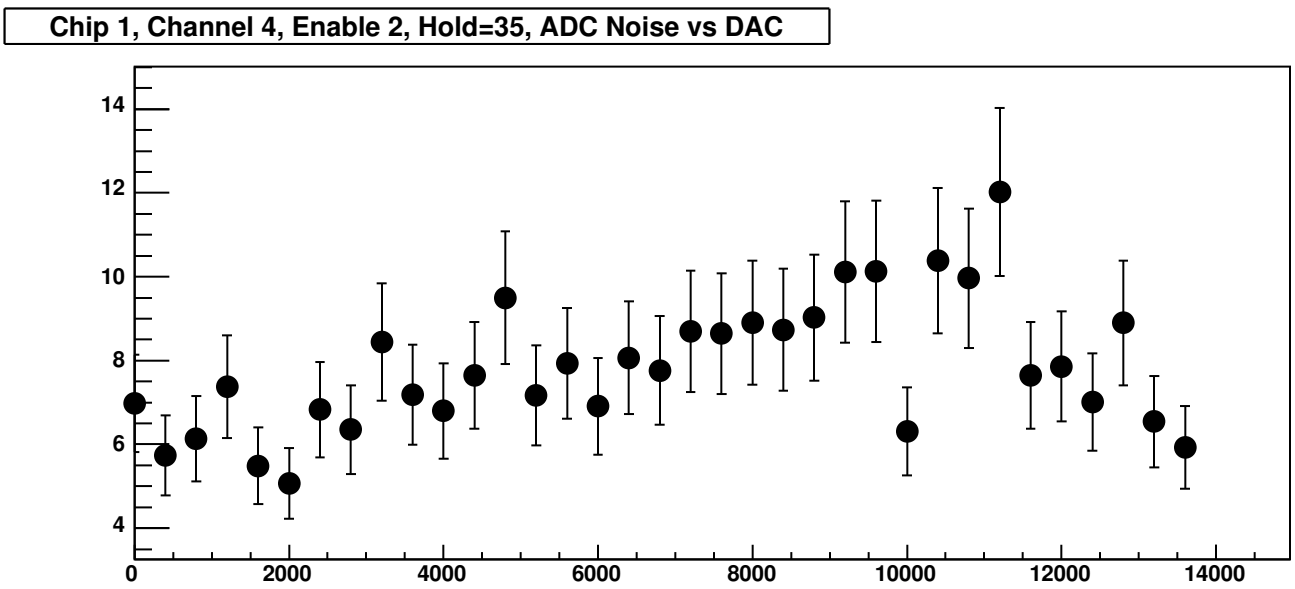

Chip 1, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

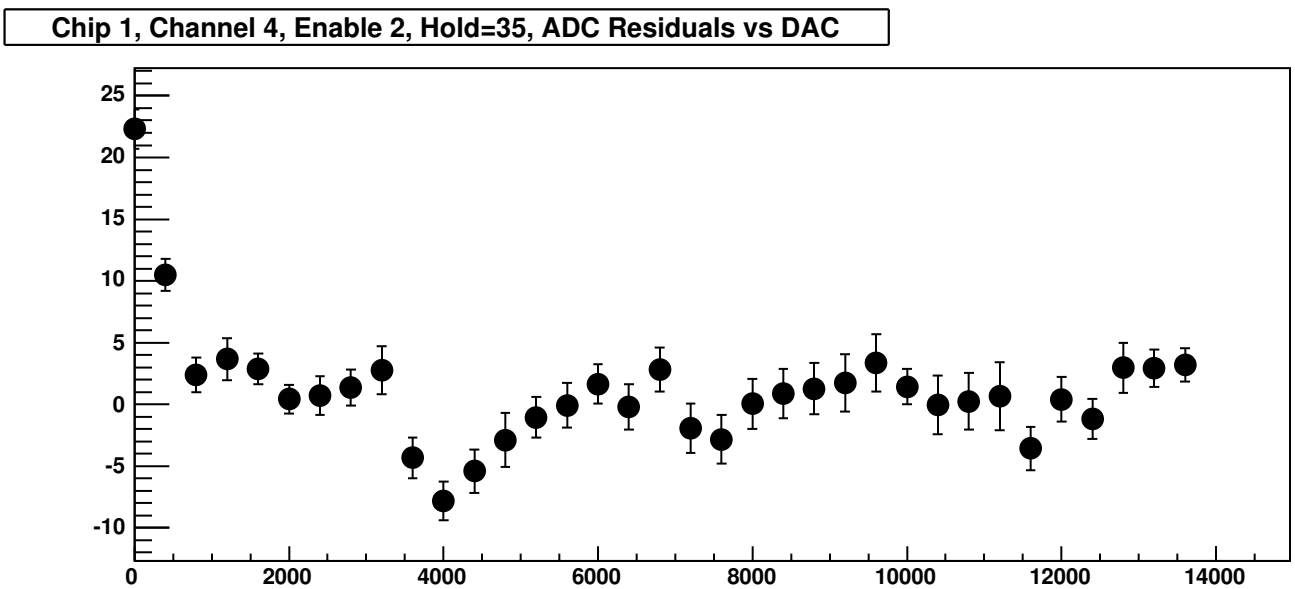

Chip 1, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 706.1 / 23
p0 -571.9 ± 1.712
p1 2.286 ± 0.0008704
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

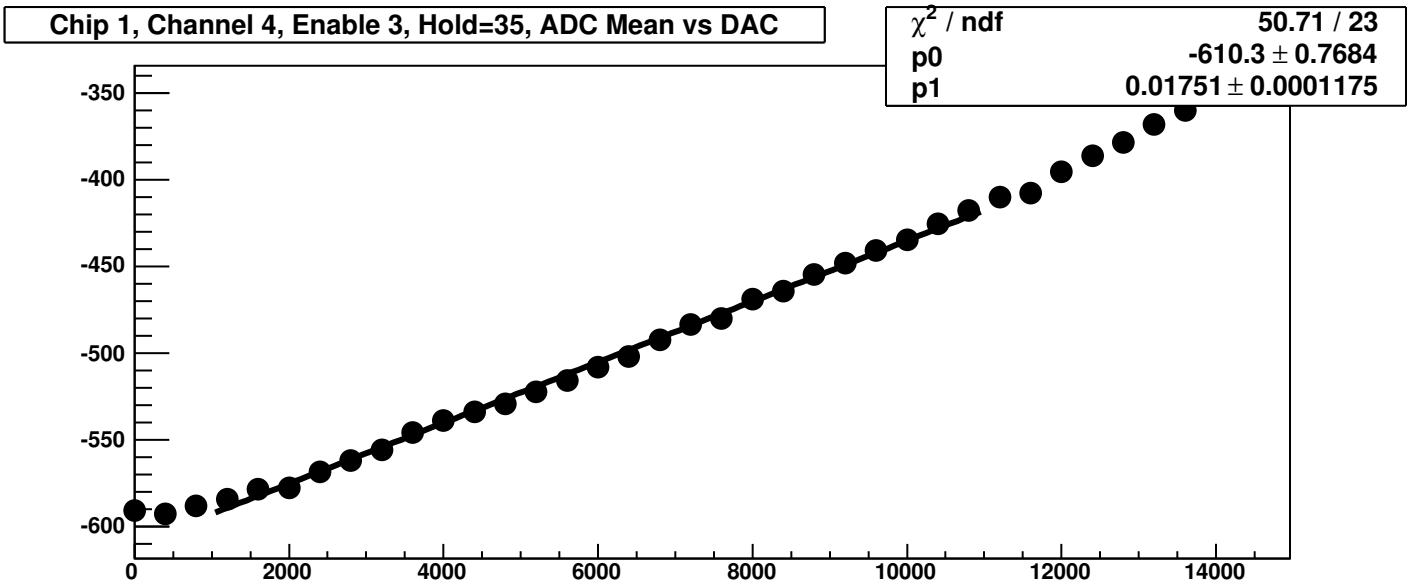

Chip 1, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

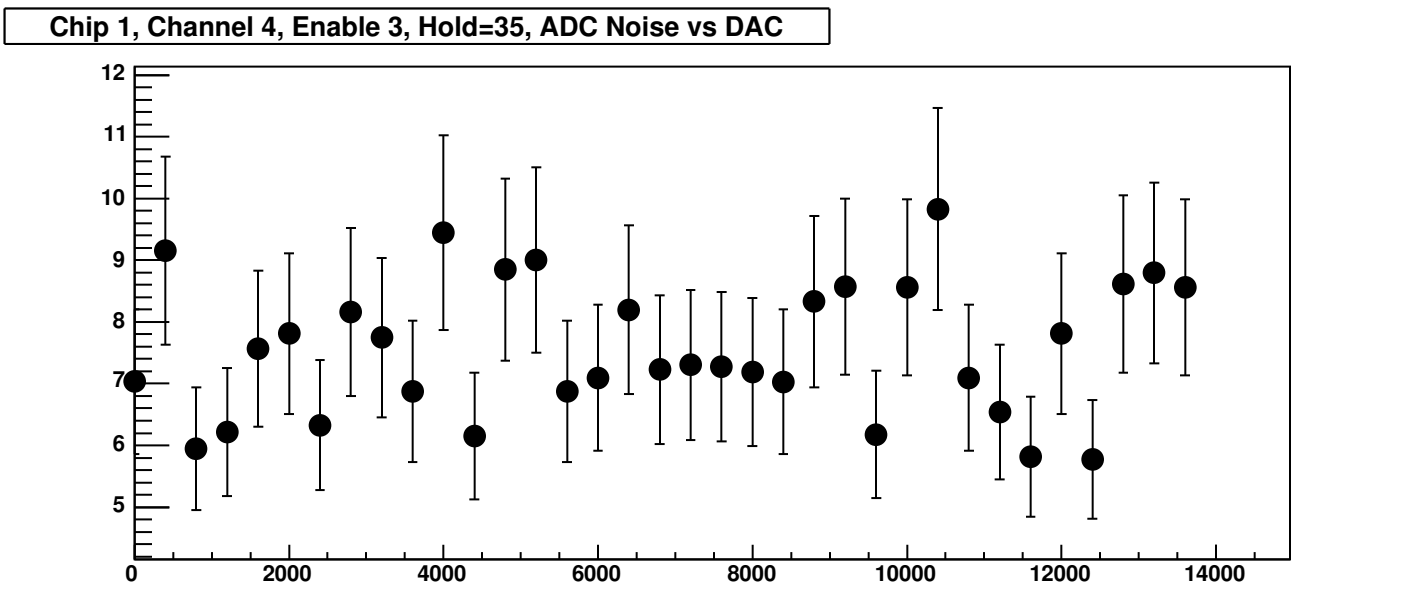

Chip 1, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

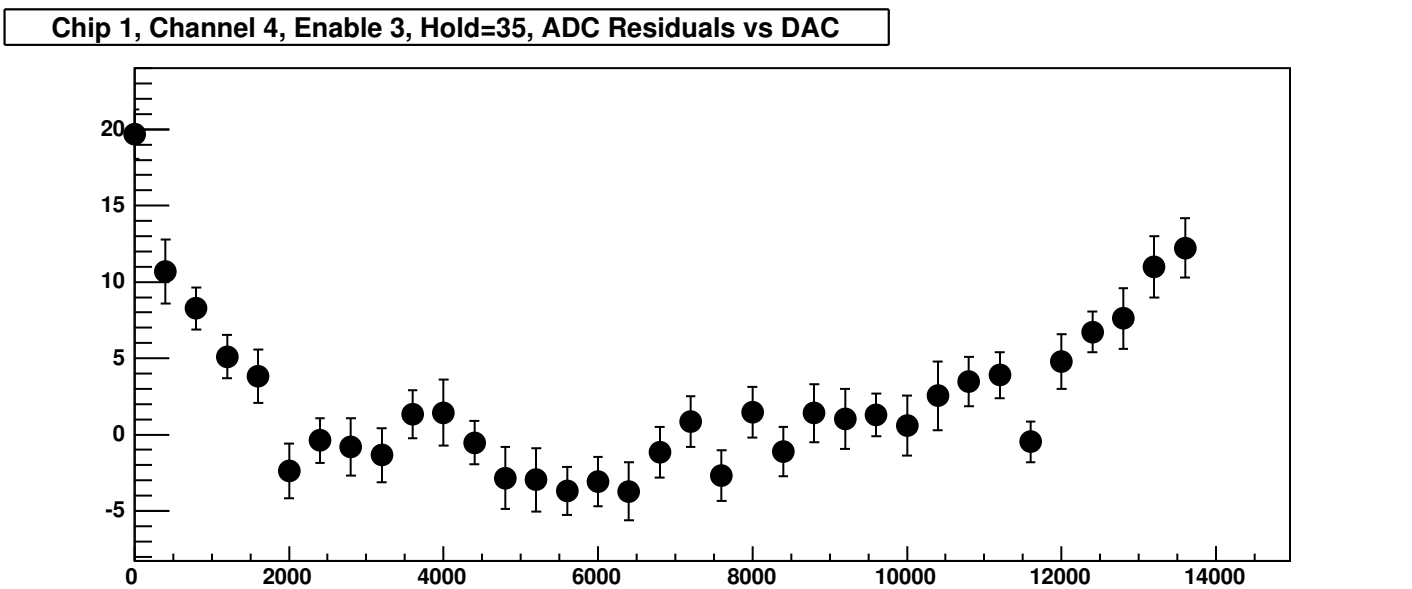

Chip 1, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 67.45 / 23
p0 -611 ± 0.7162
p1 0.02188 ± 0.0001197
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

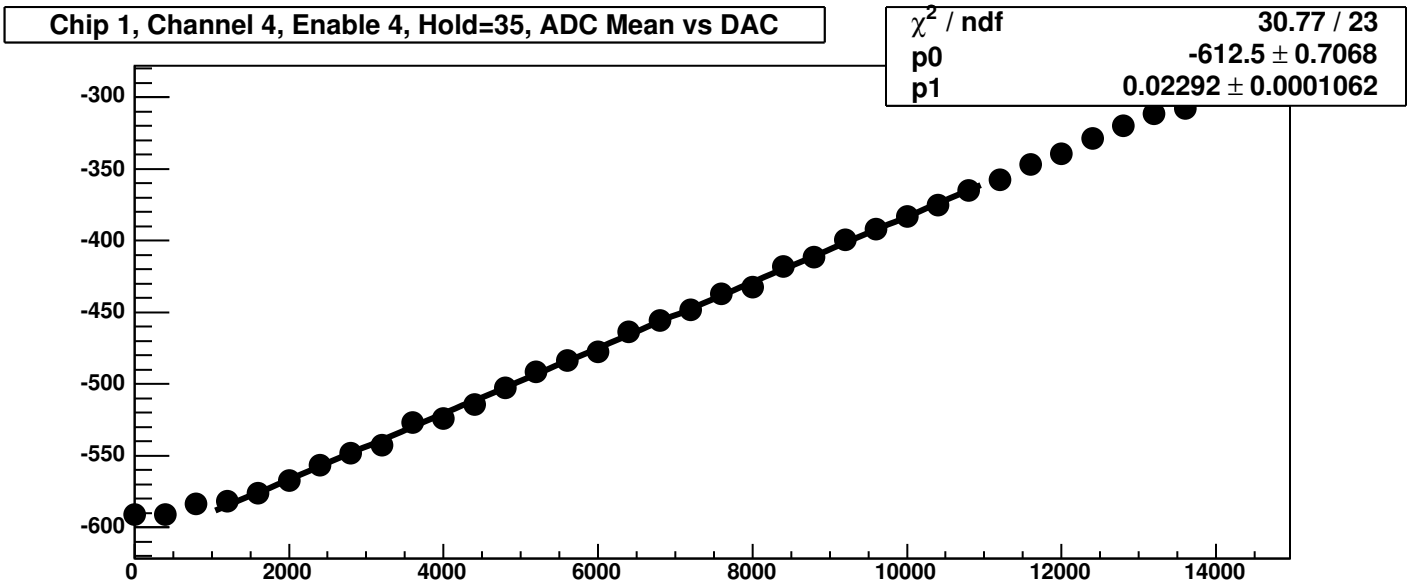

Chip 1, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

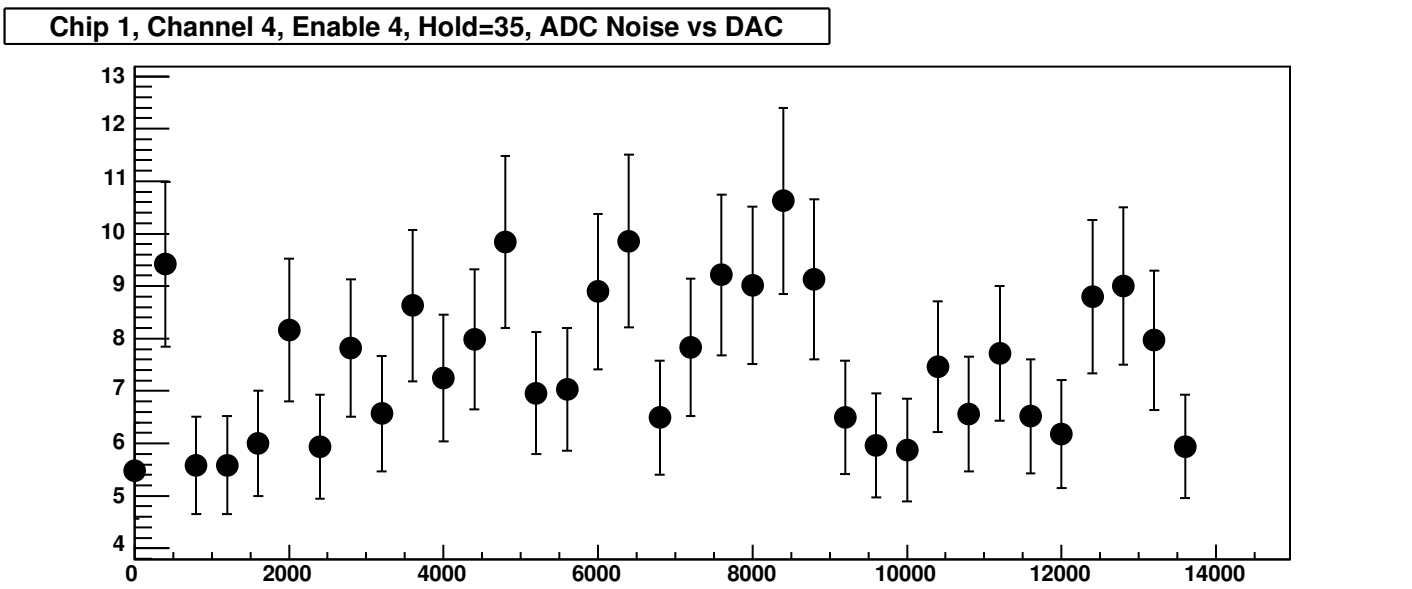

Chip 1, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

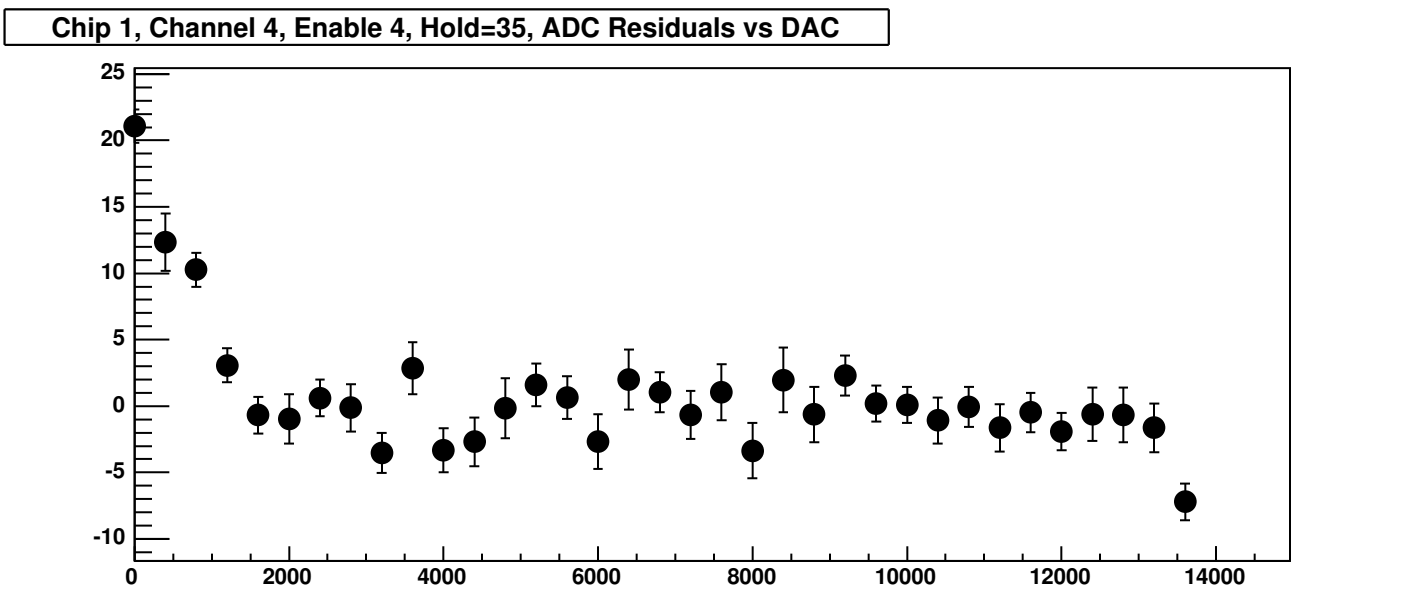

Chip 1, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 50.71 / 23
p0 -610.3 ± 0.7684
p1 0.01751 ± 0.0001175
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

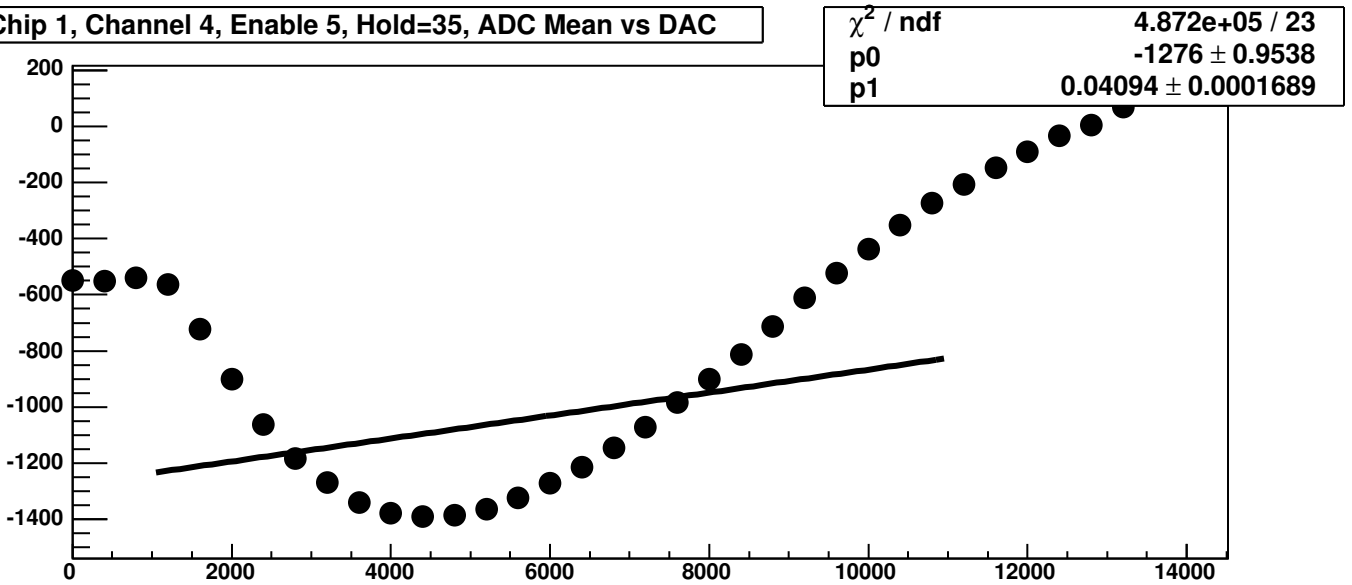

Chip 1, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.77 / 23
p0 -612.5 ± 0.7068
p1 0.02292 ± 0.0001062
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

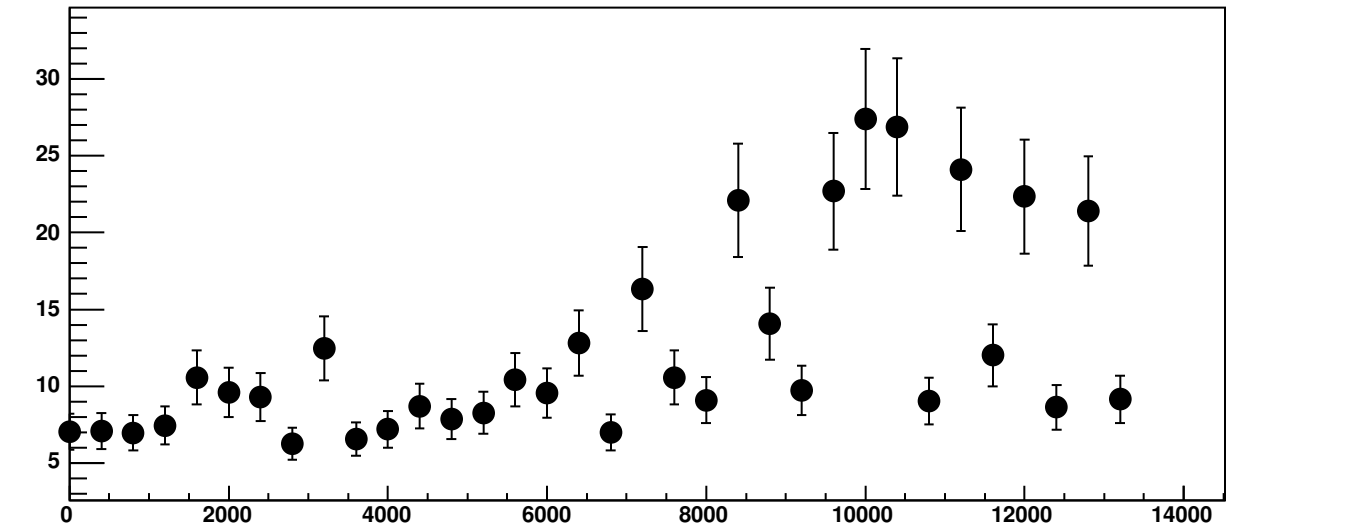

Chip 1, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

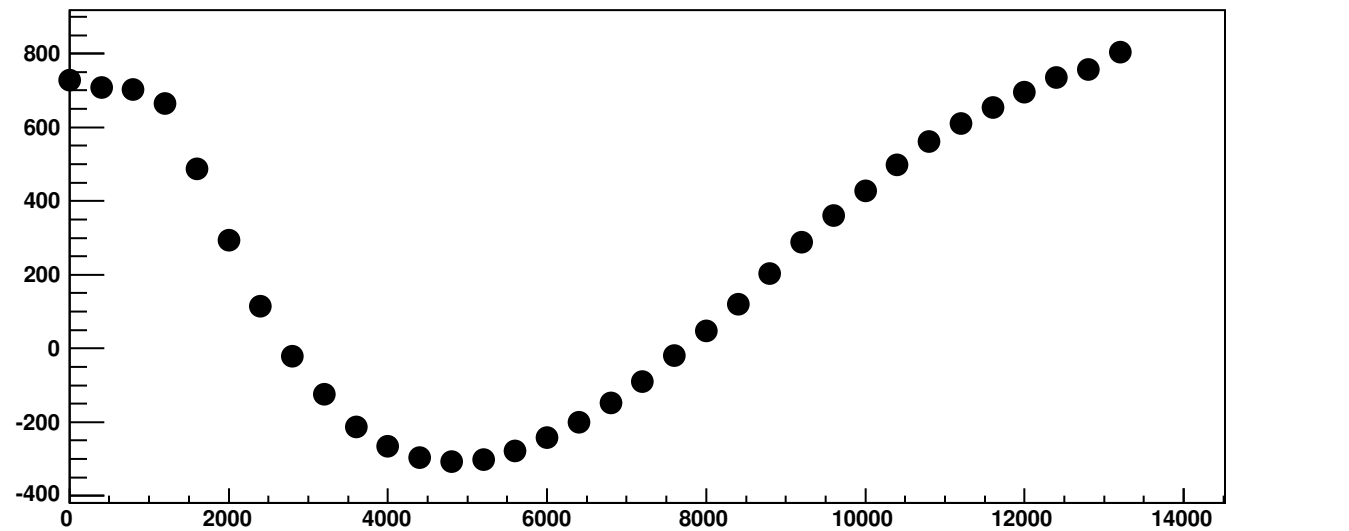
Chip 1, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

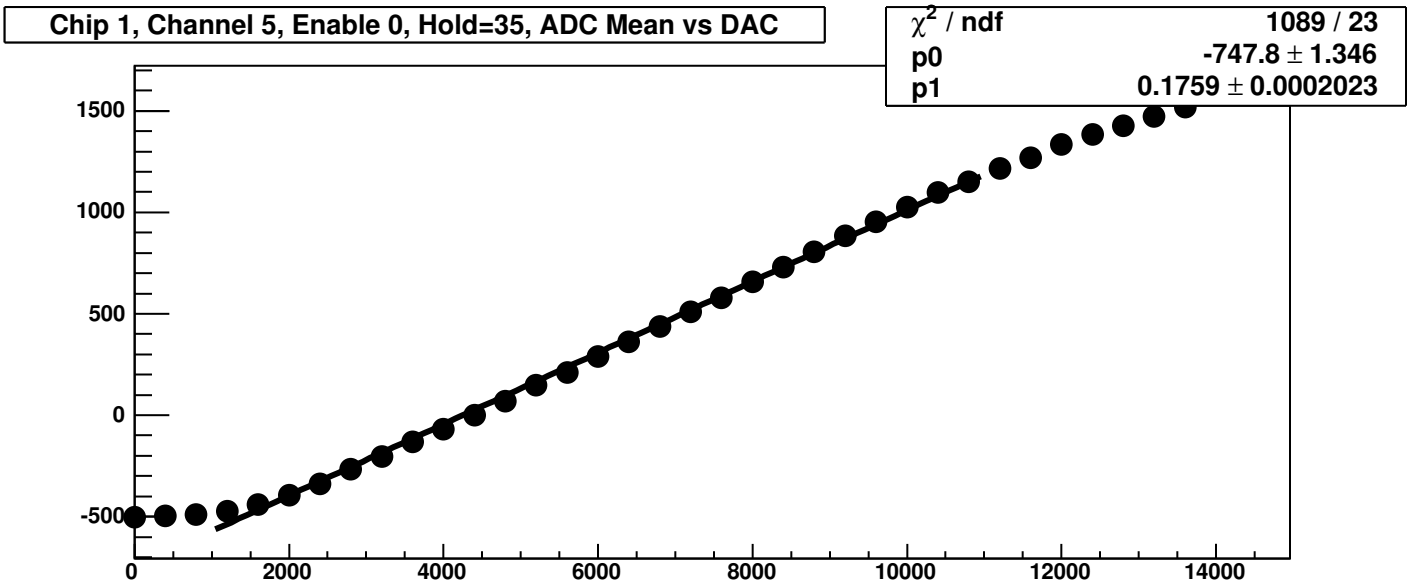


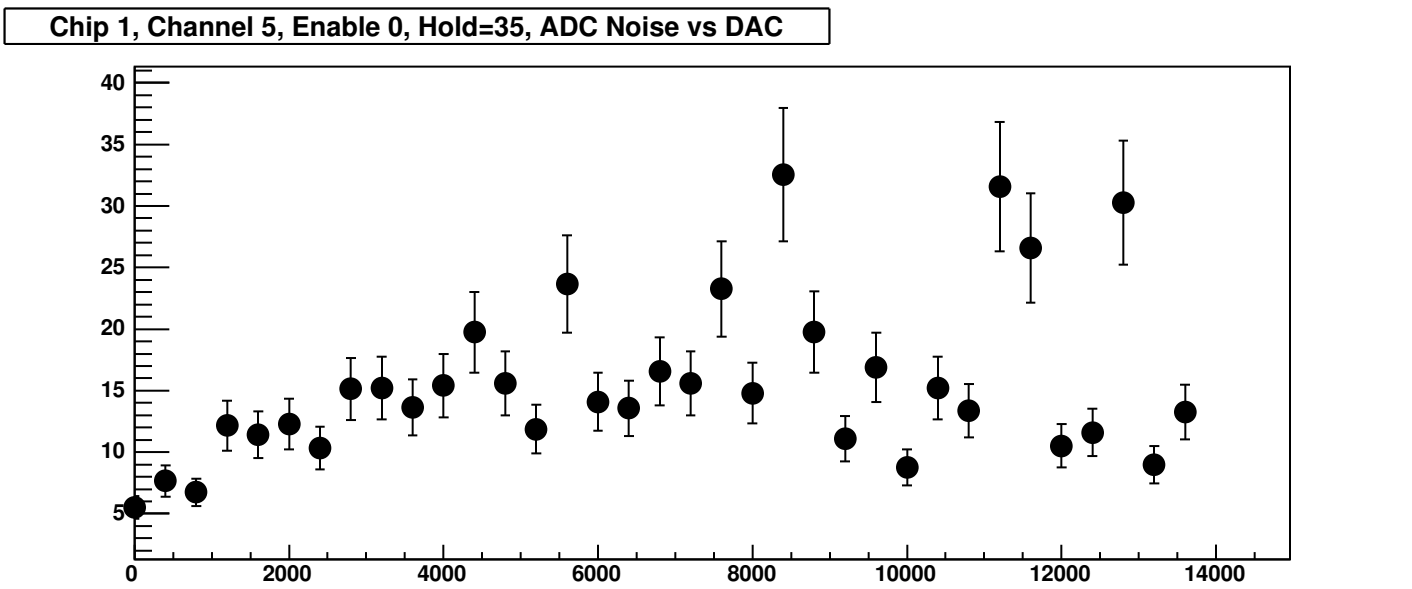
Chip 1, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC

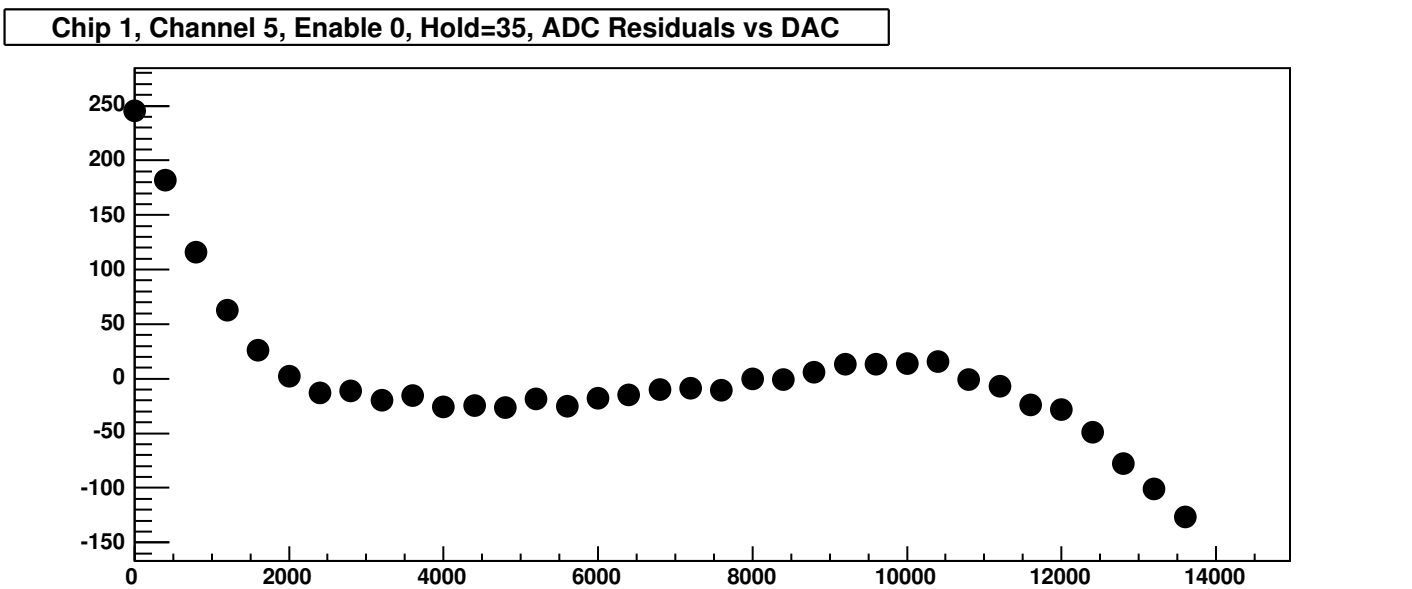


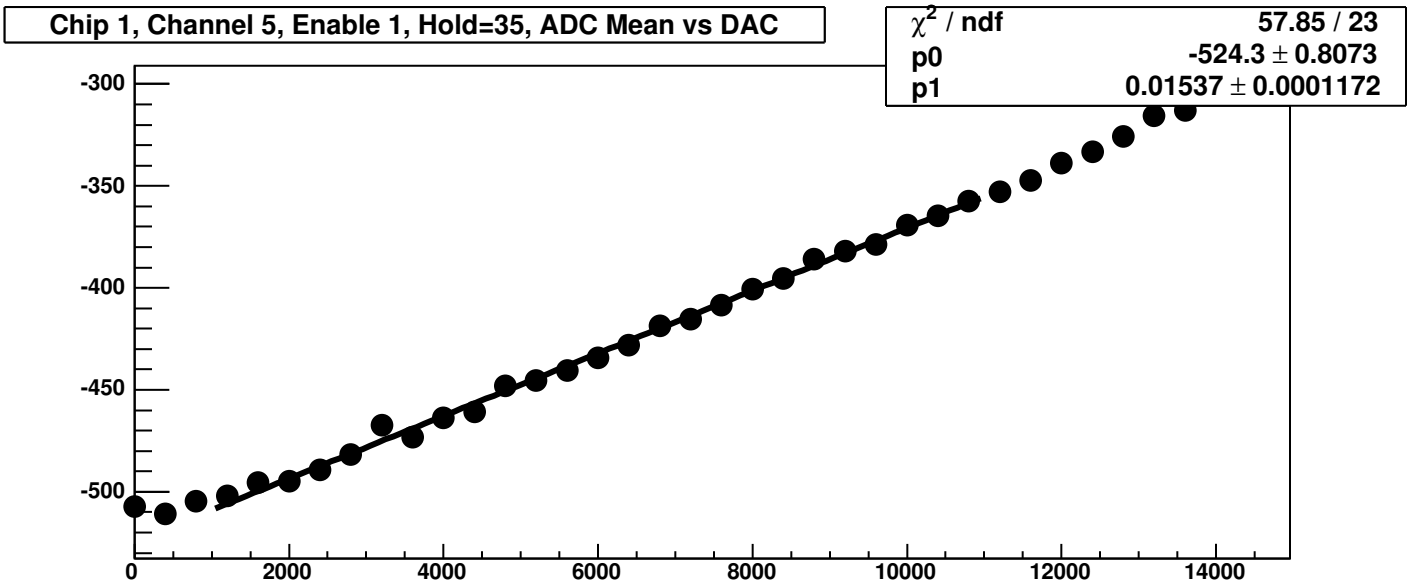
Chip 1, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

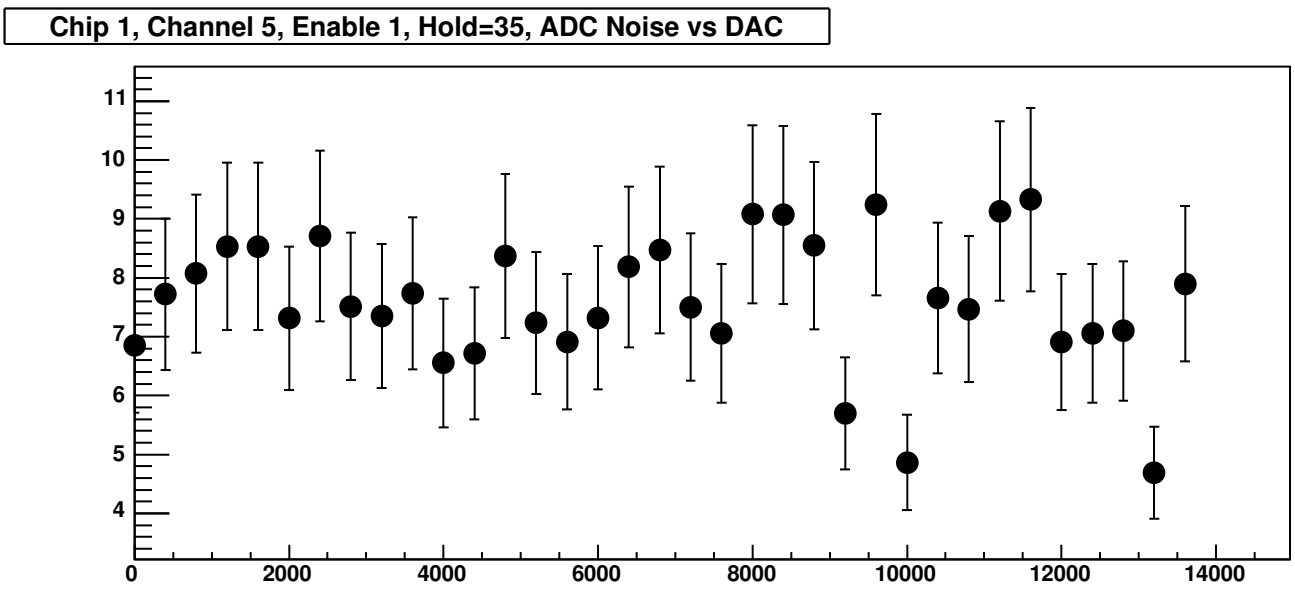


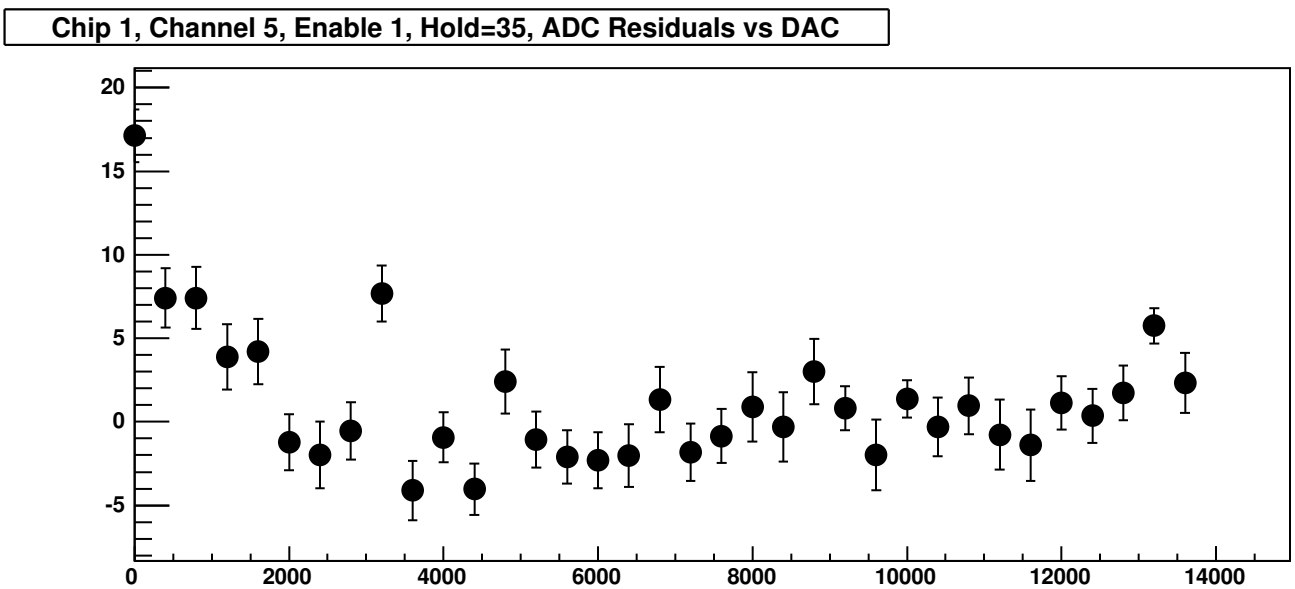

Chip 1, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1089 / 23
p0 -747.8 ± 1.346
p1 0.1759 ± 0.0002023
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

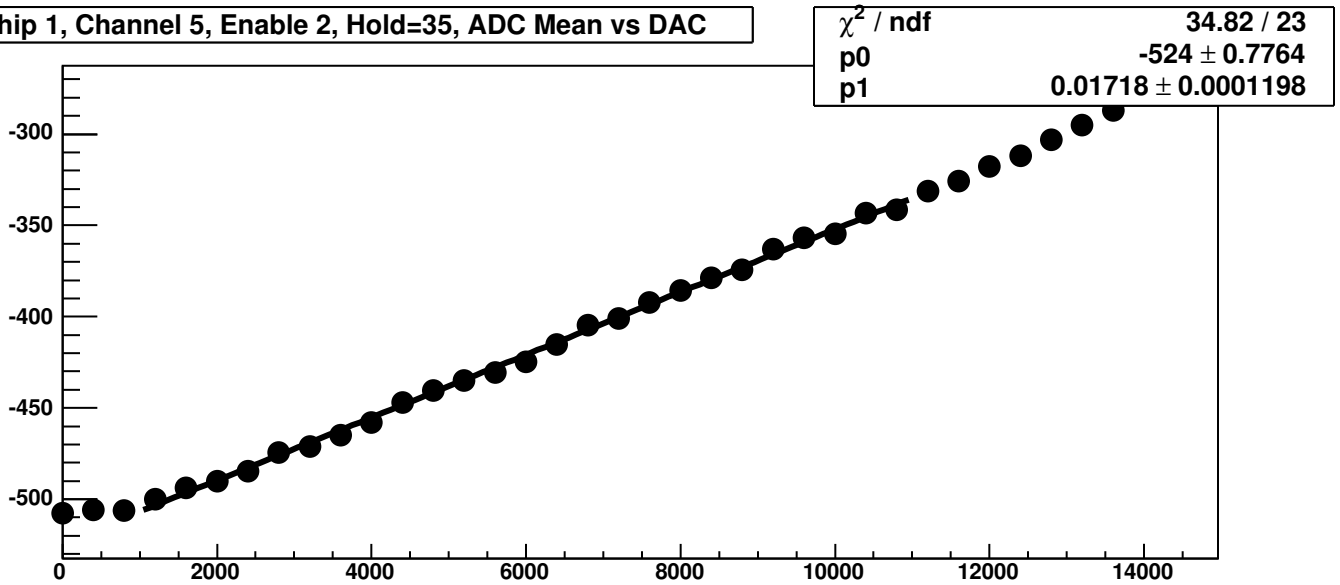

Chip 1, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 57.85 / 23
p0 -524.3 ± 0.8073
p1 0.01537 ± 0.0001172
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

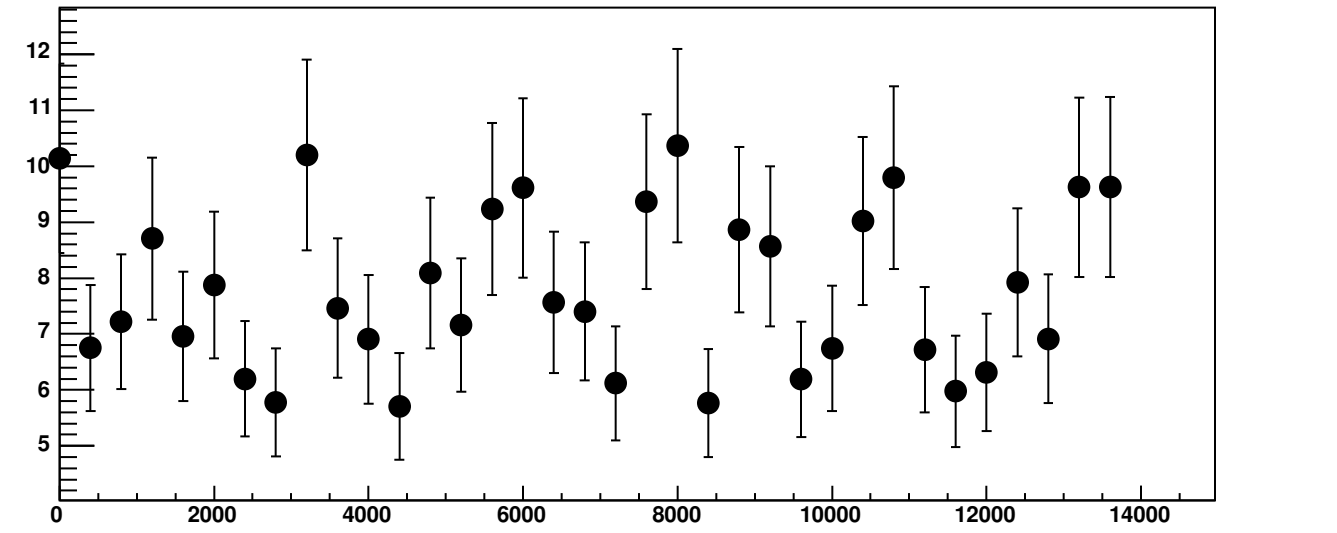

Chip 1, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

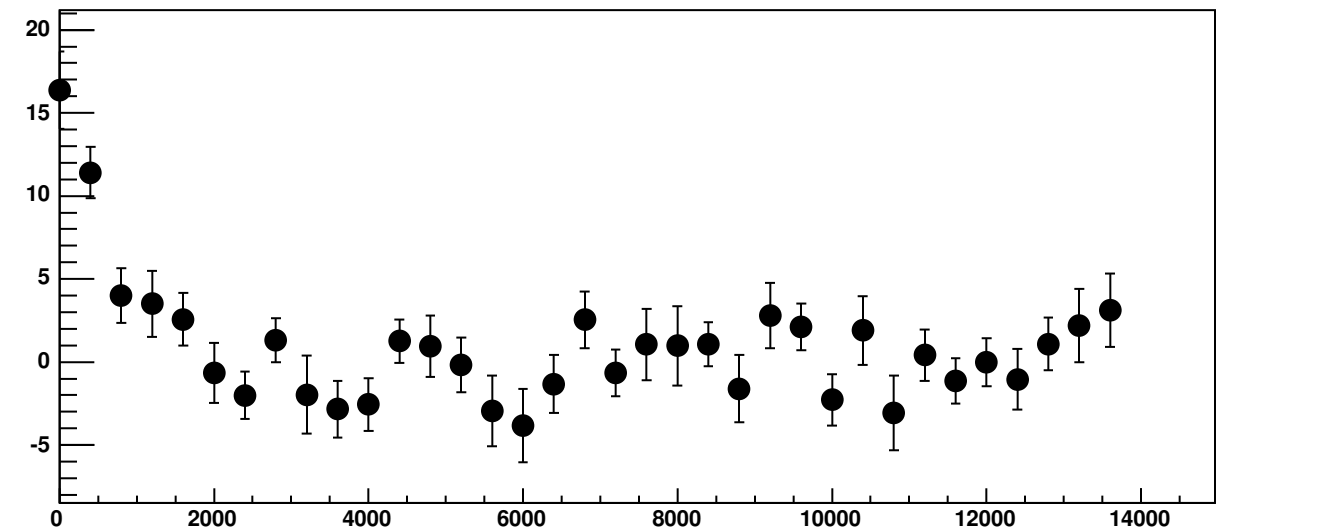
Chip 1, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

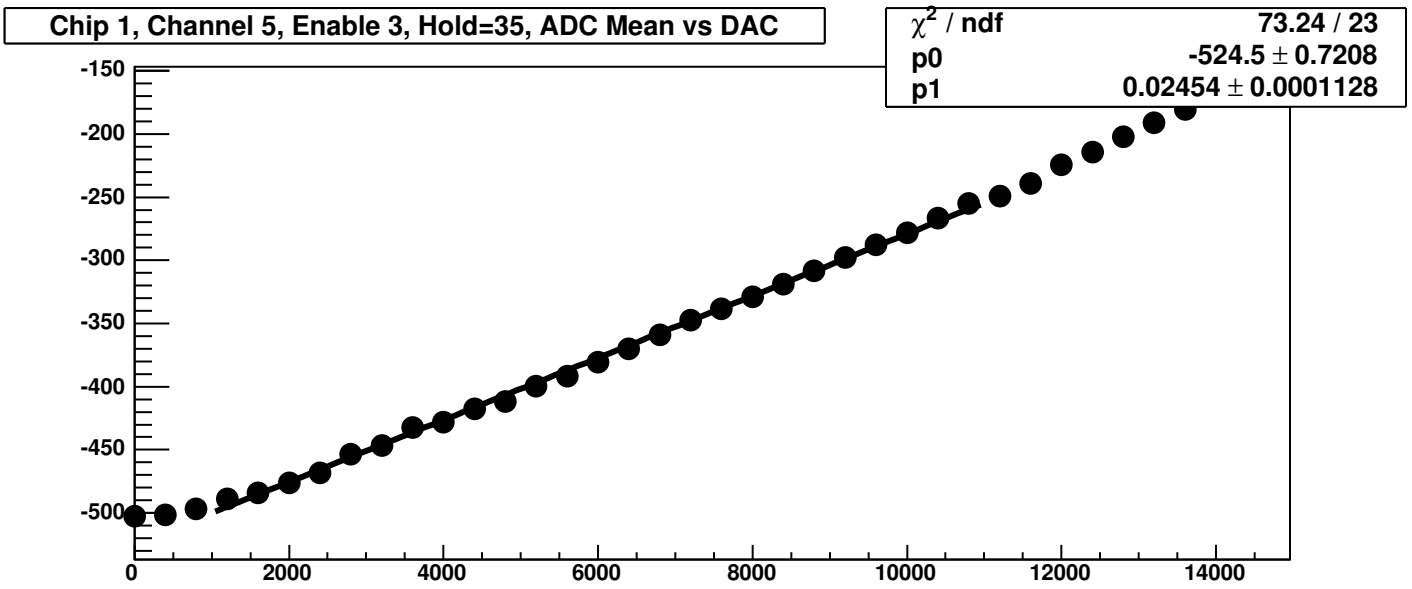


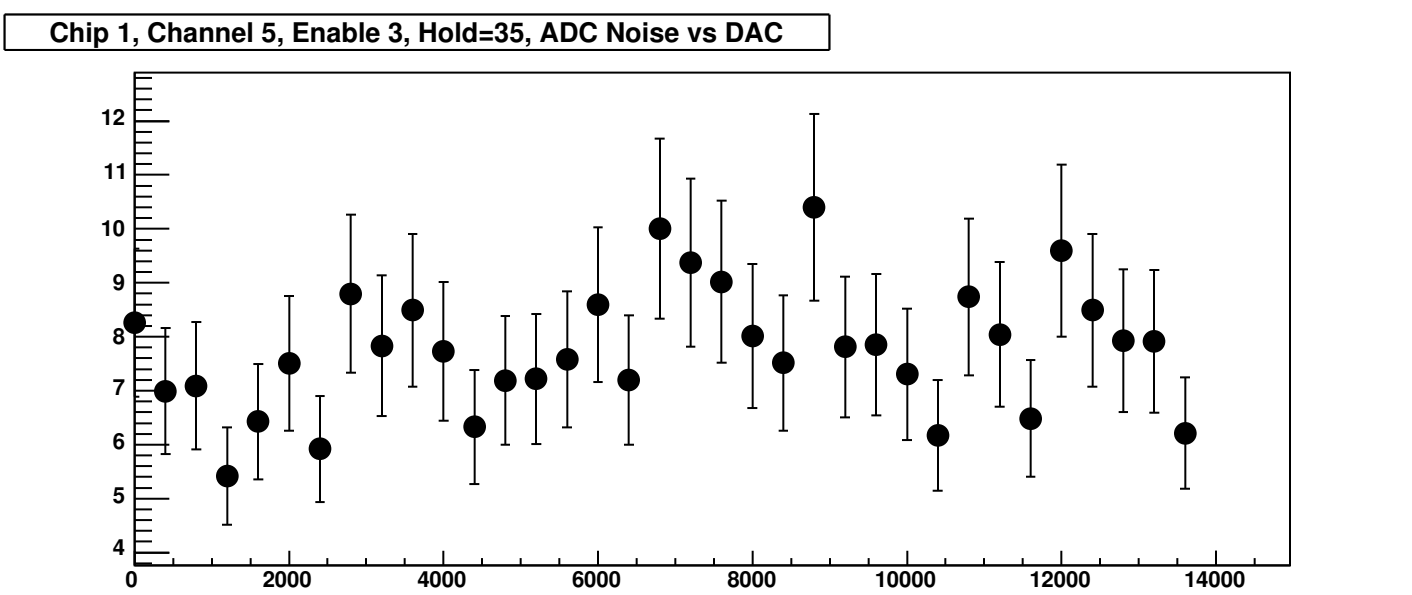
Chip 1, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC

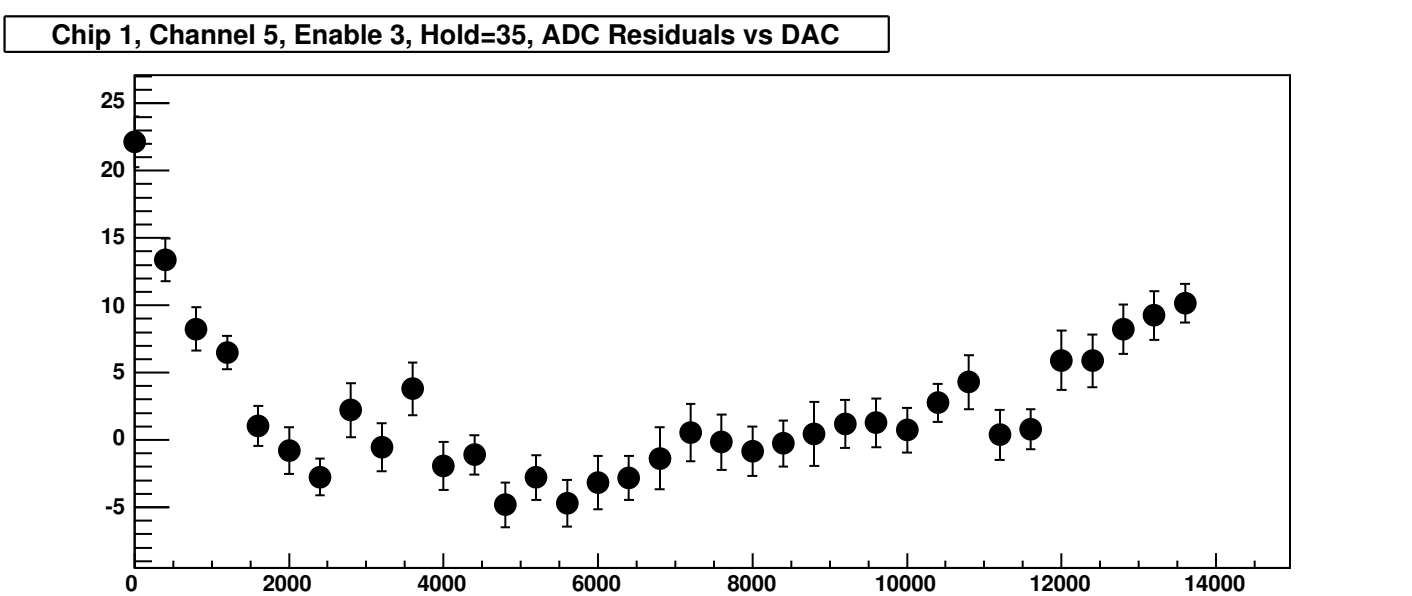


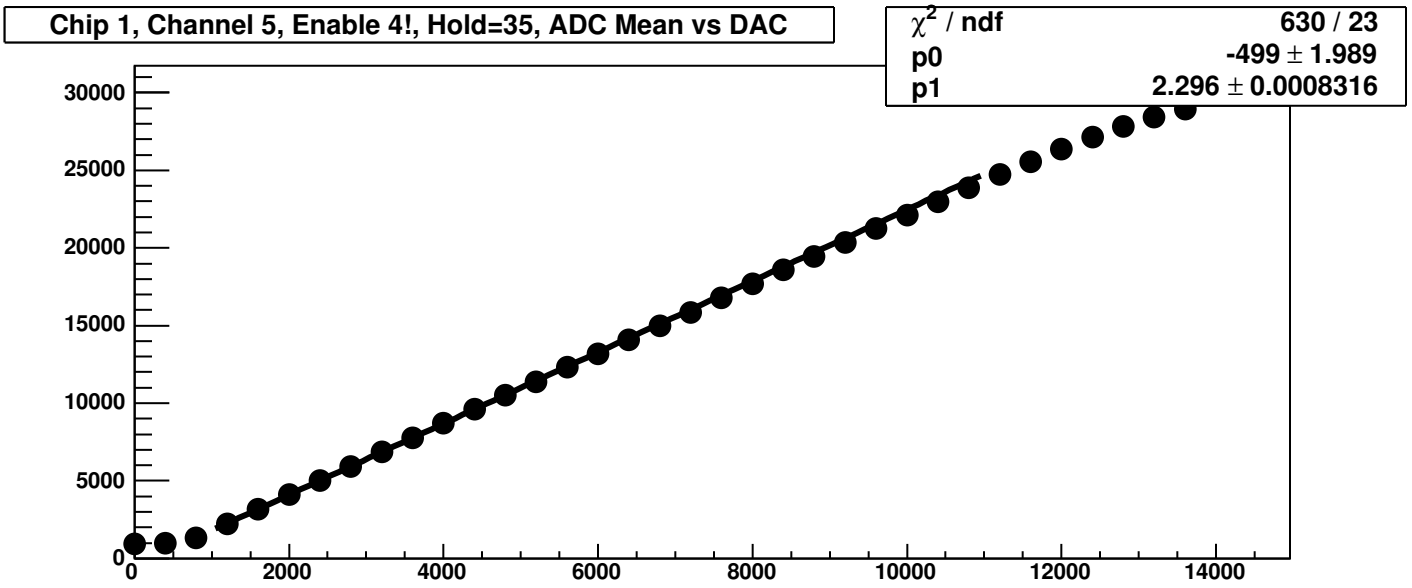
Chip 1, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

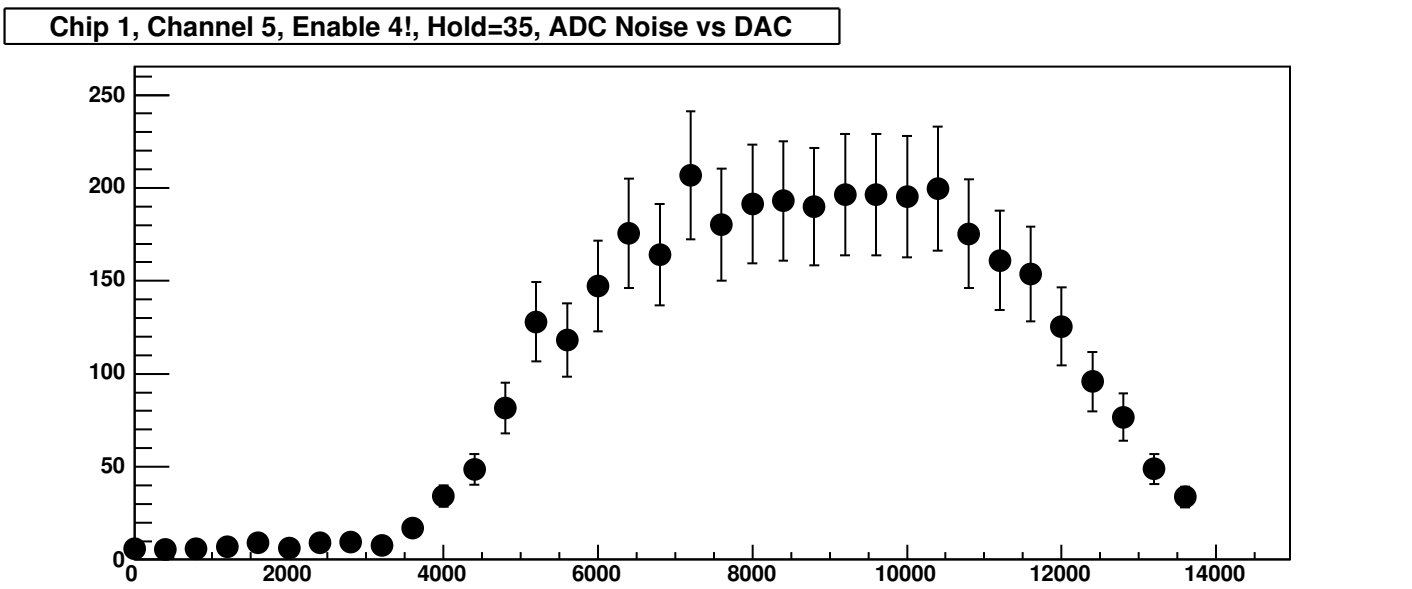


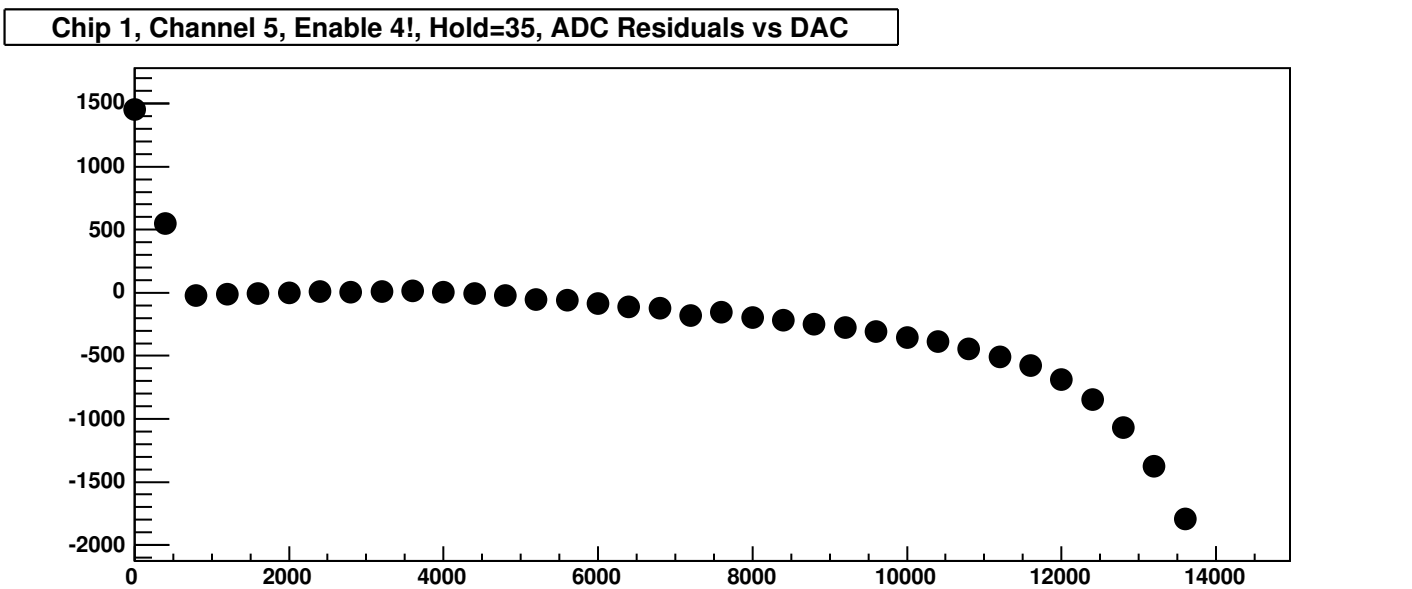

Chip 1, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 73.24 / 23
p0 -524.5 ± 0.7208
p1 0.02454 ± 0.0001128
-150
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

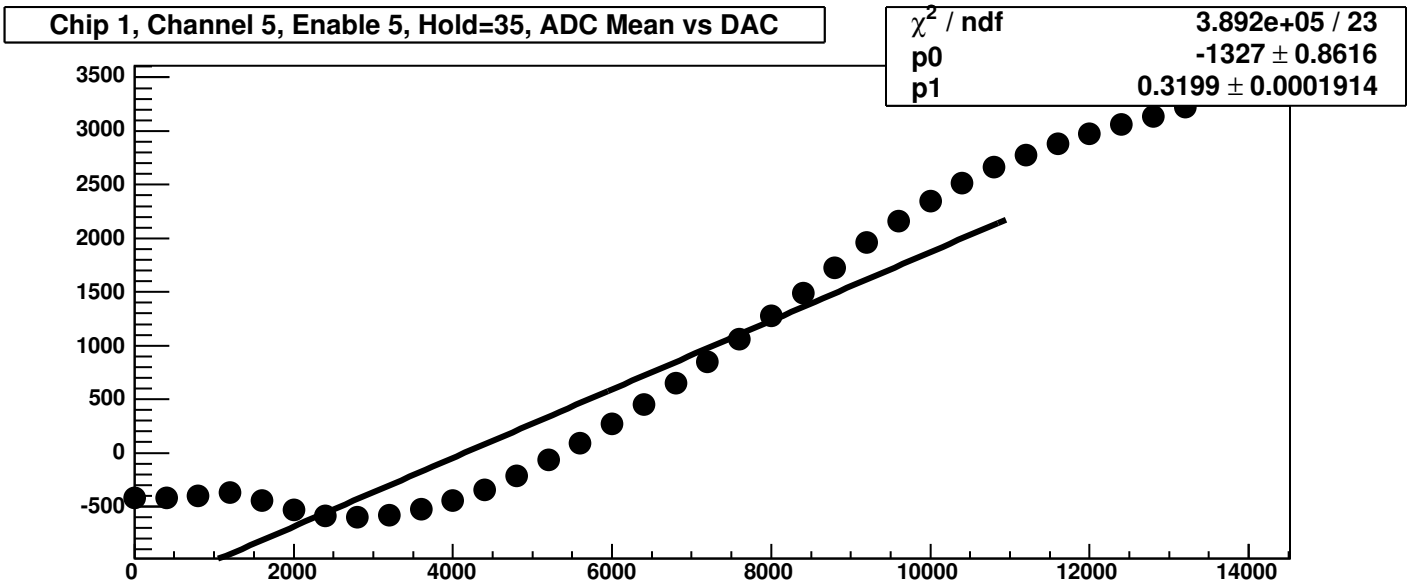

Chip 1, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

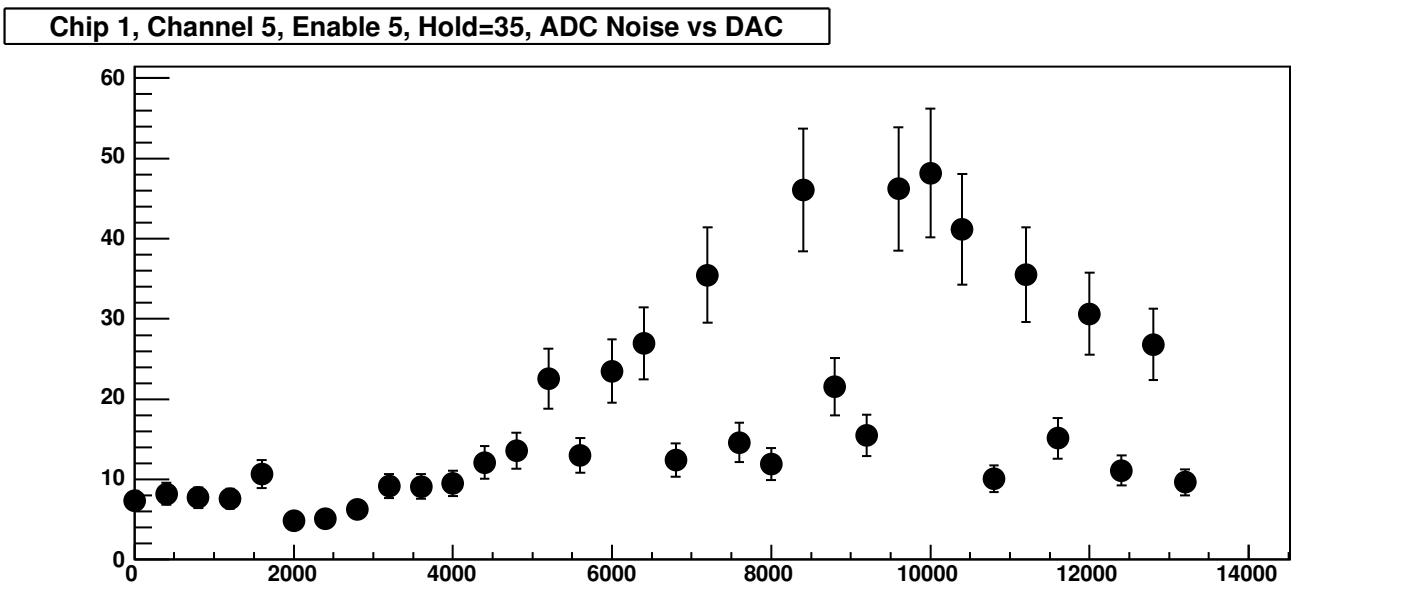

Chip 1, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

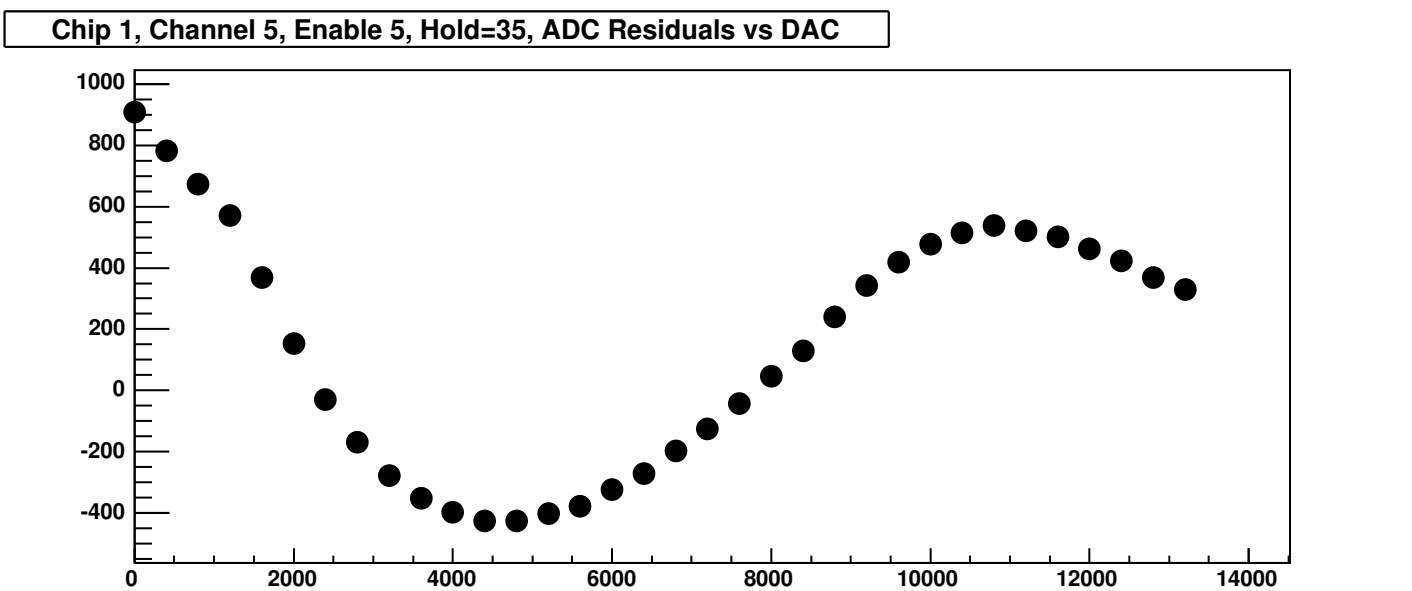

Chip 1, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 630 / 23
p0 -499 ± 1.989
p1 2.296 ± 0.0008316
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

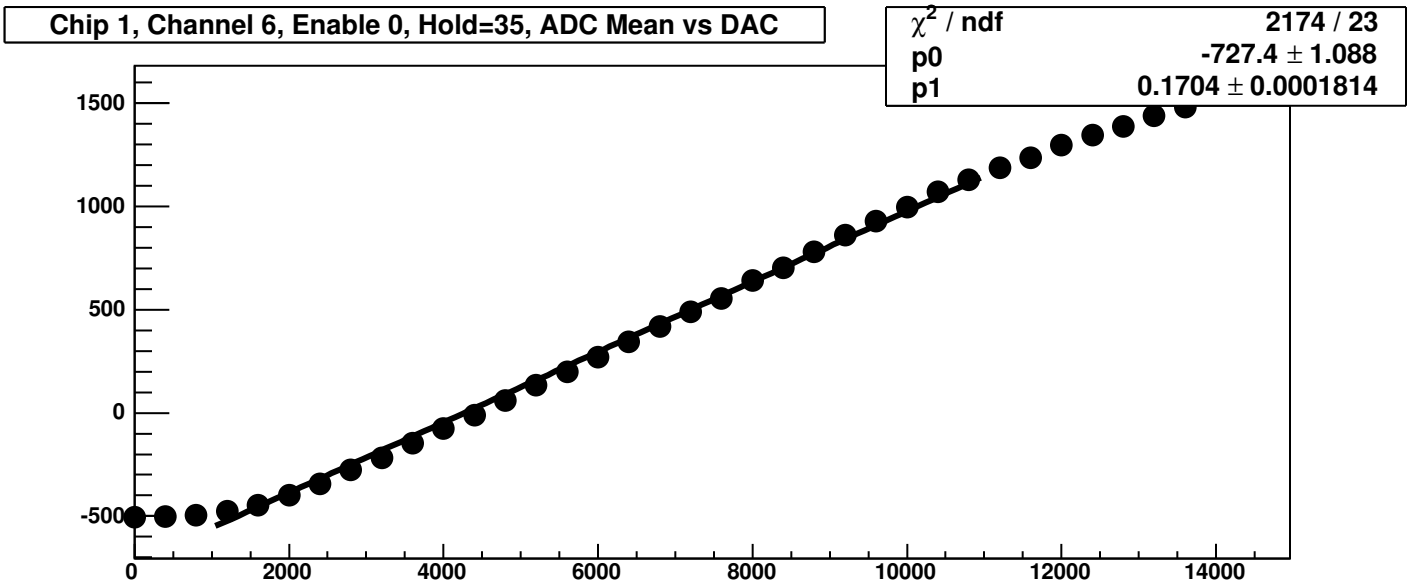

Chip 1, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

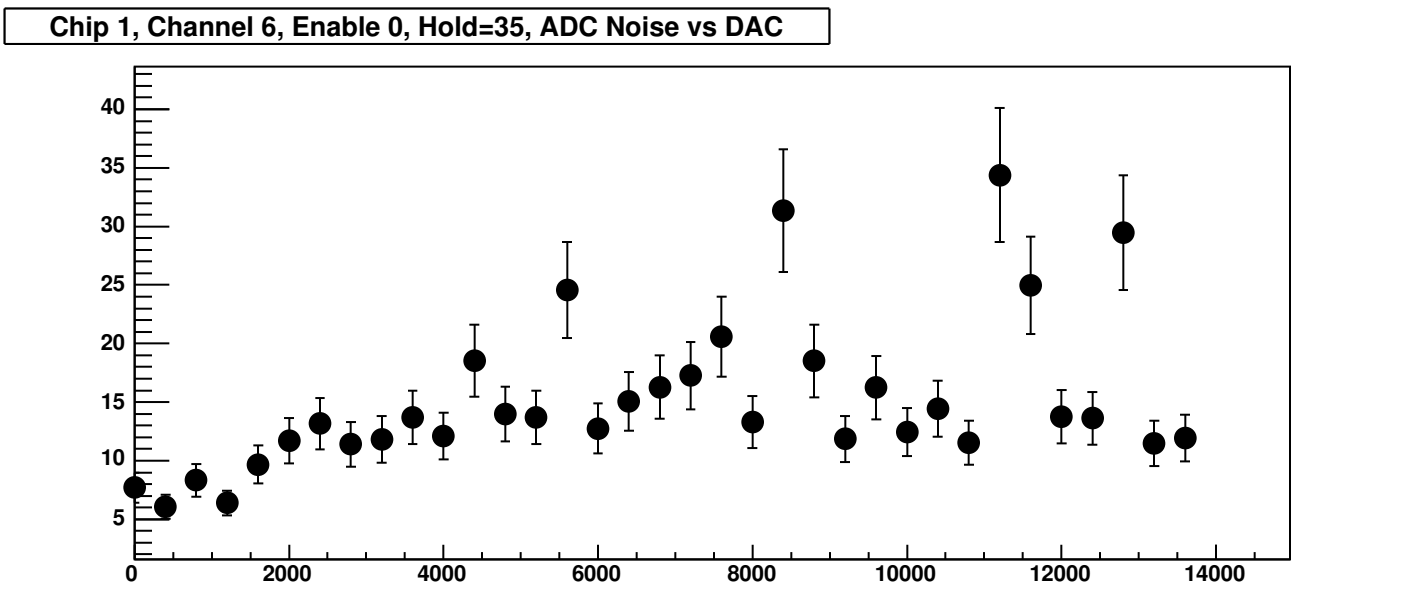

Chip 1, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

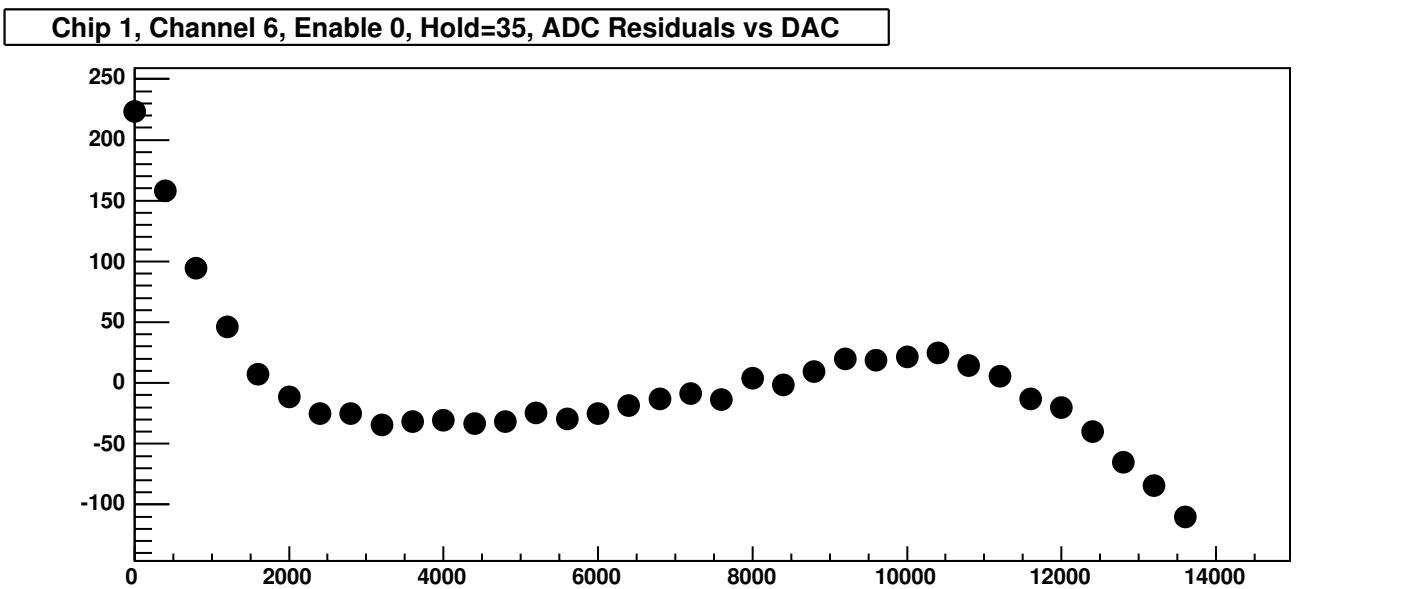

Chip 1, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.892e+05 / 23
p0 -1327 ± 0.8616
p1 0.3199 ± 0.0001914
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

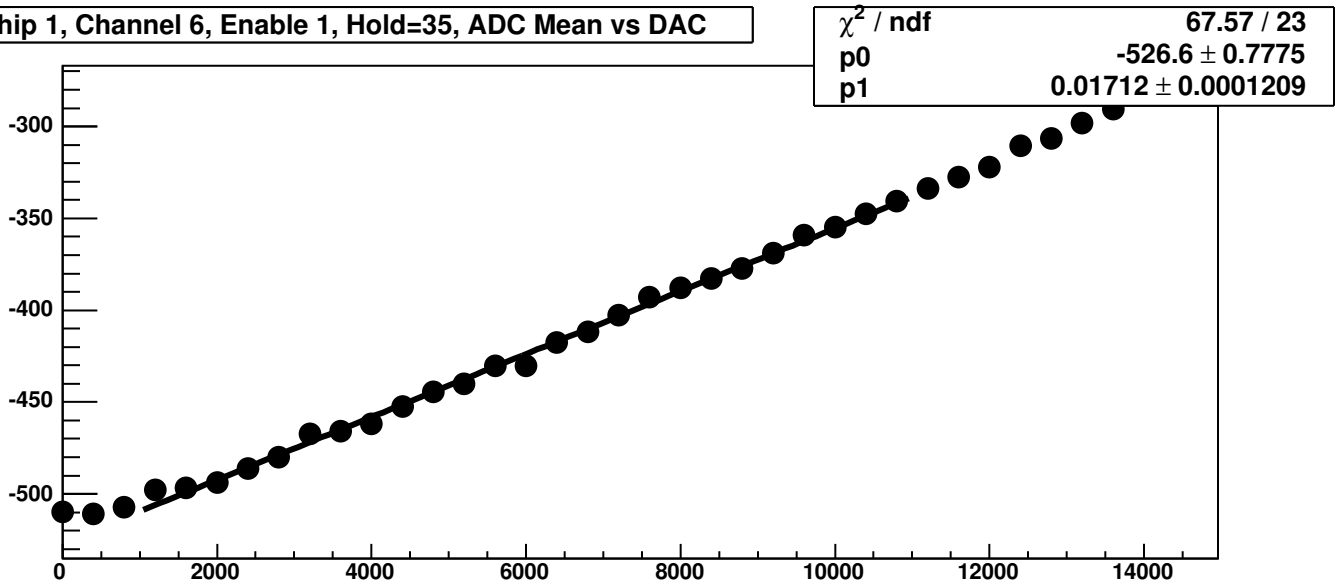

Chip 1, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2174 / 23
p0 -727.4 ± 1.088
p1 0.1704 ± 0.0001814
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

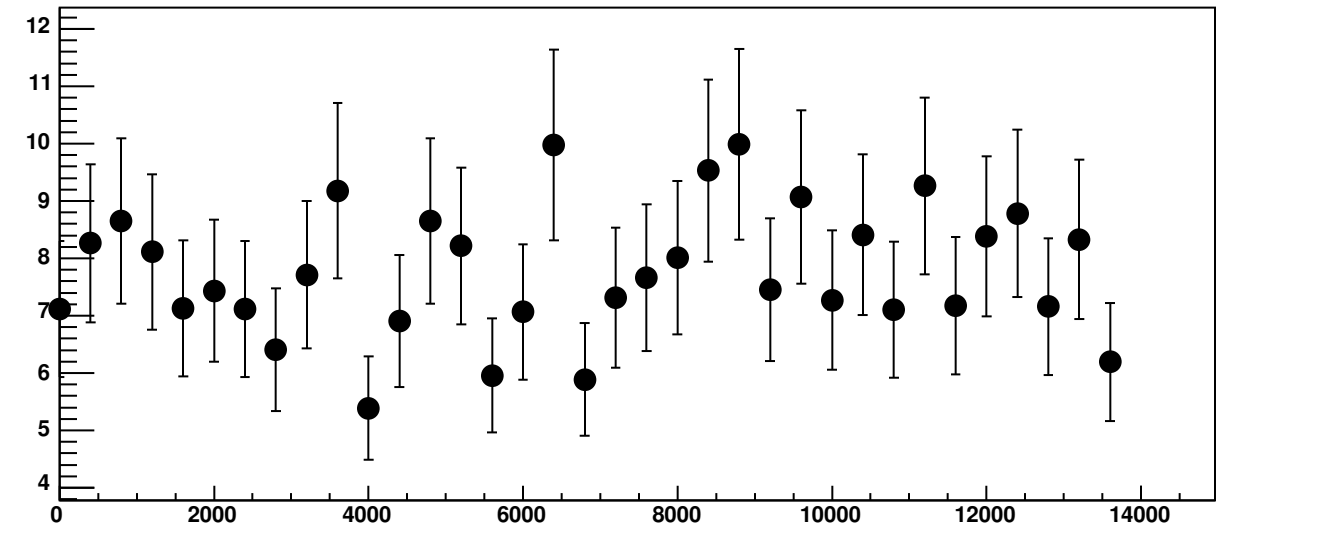

Chip 1, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

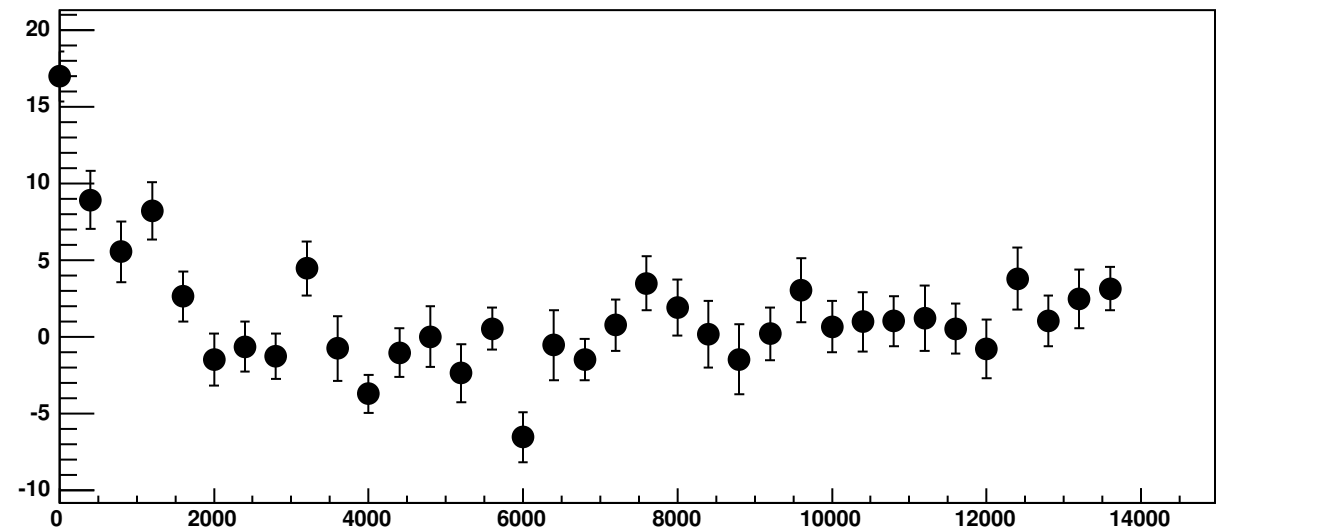
Chip 1, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

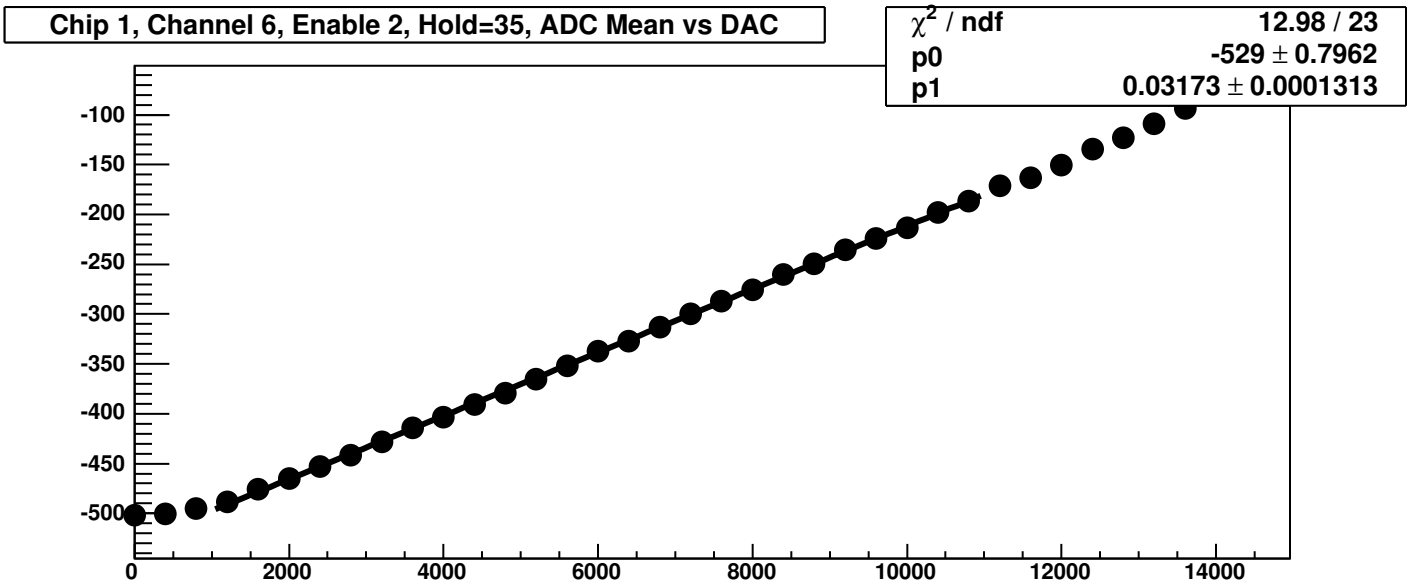


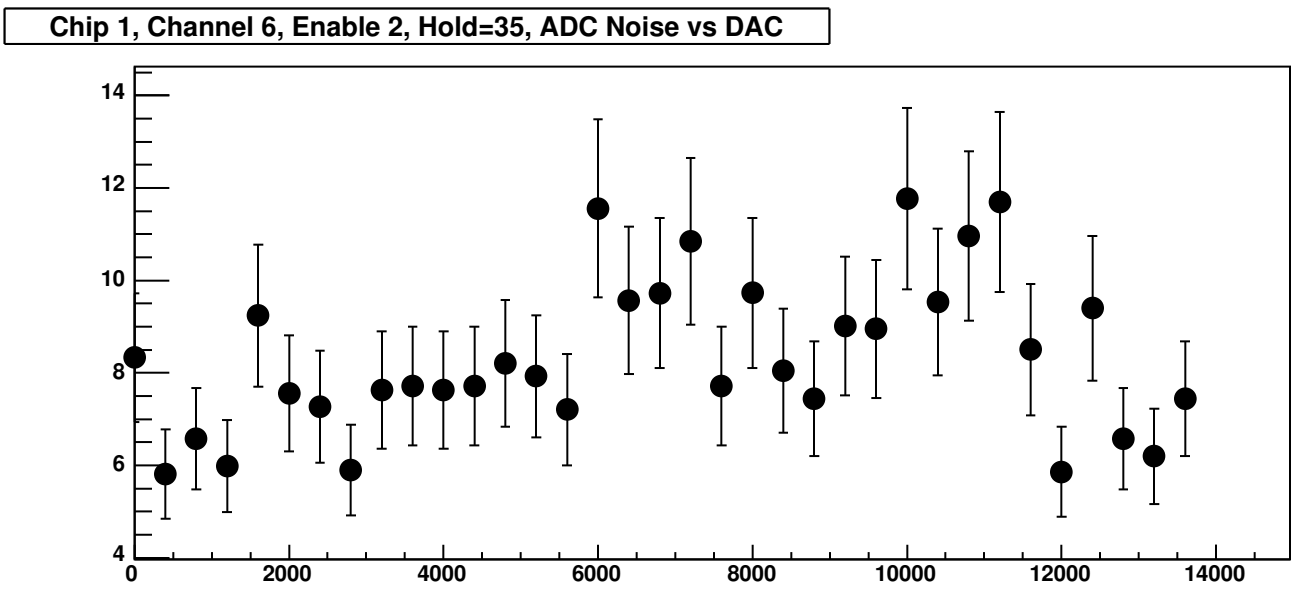
Chip 1, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC

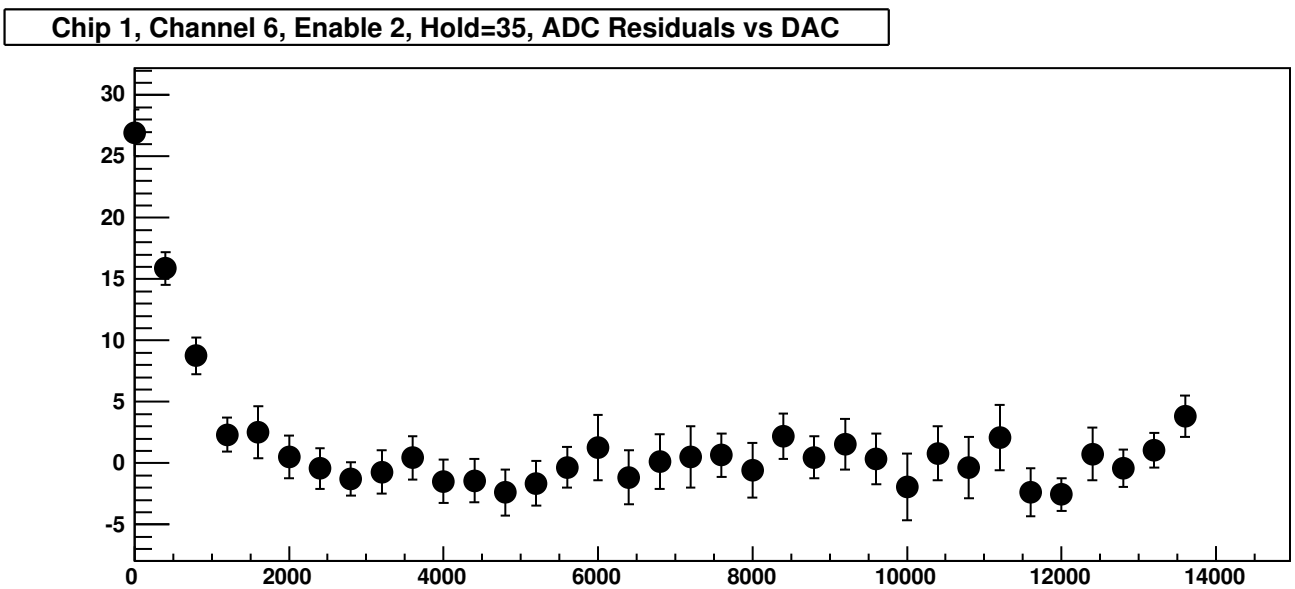


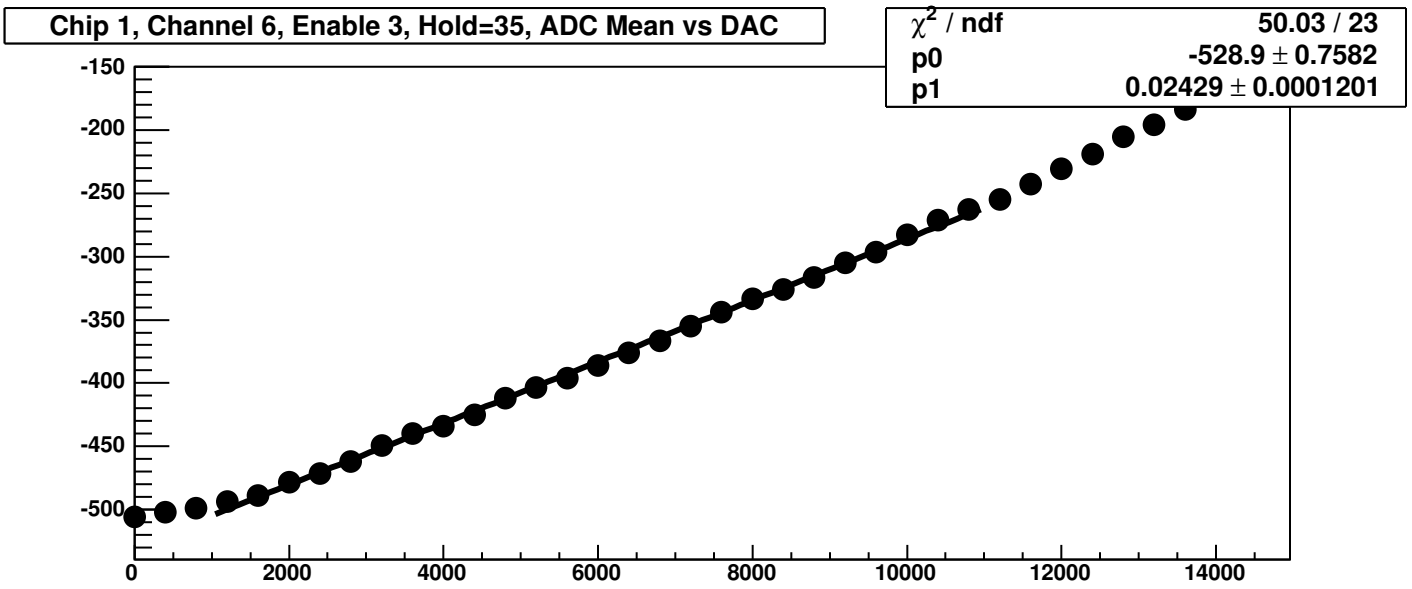
Chip 1, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

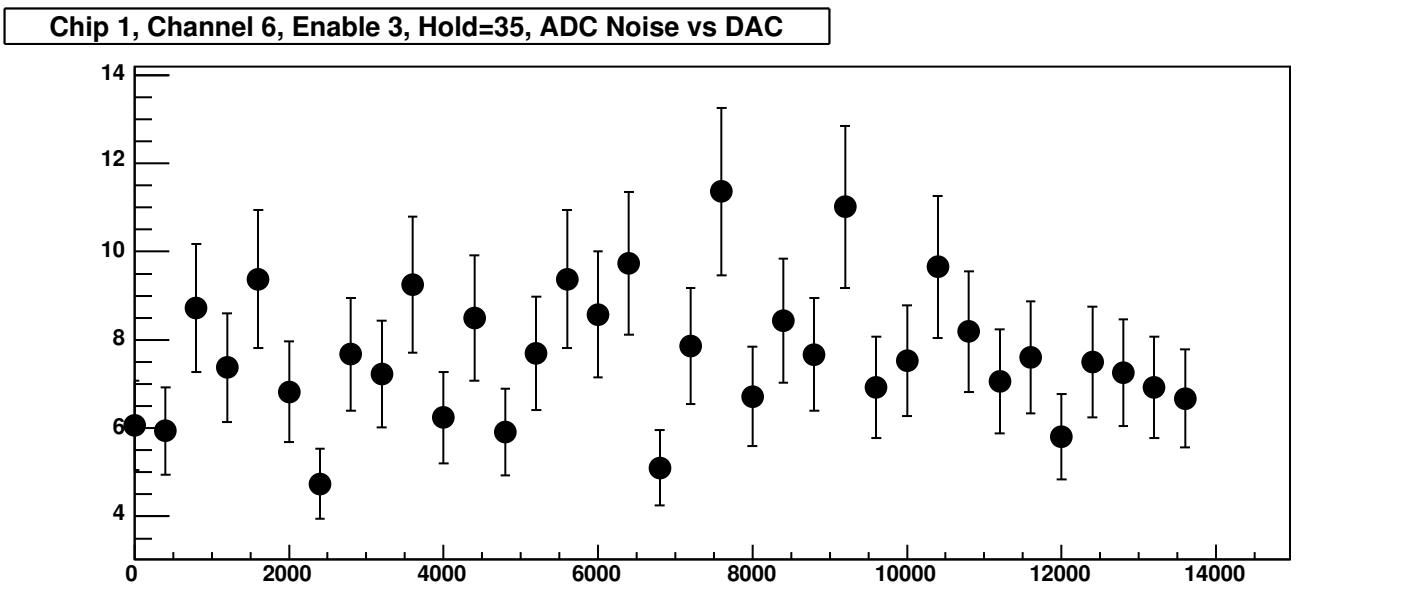


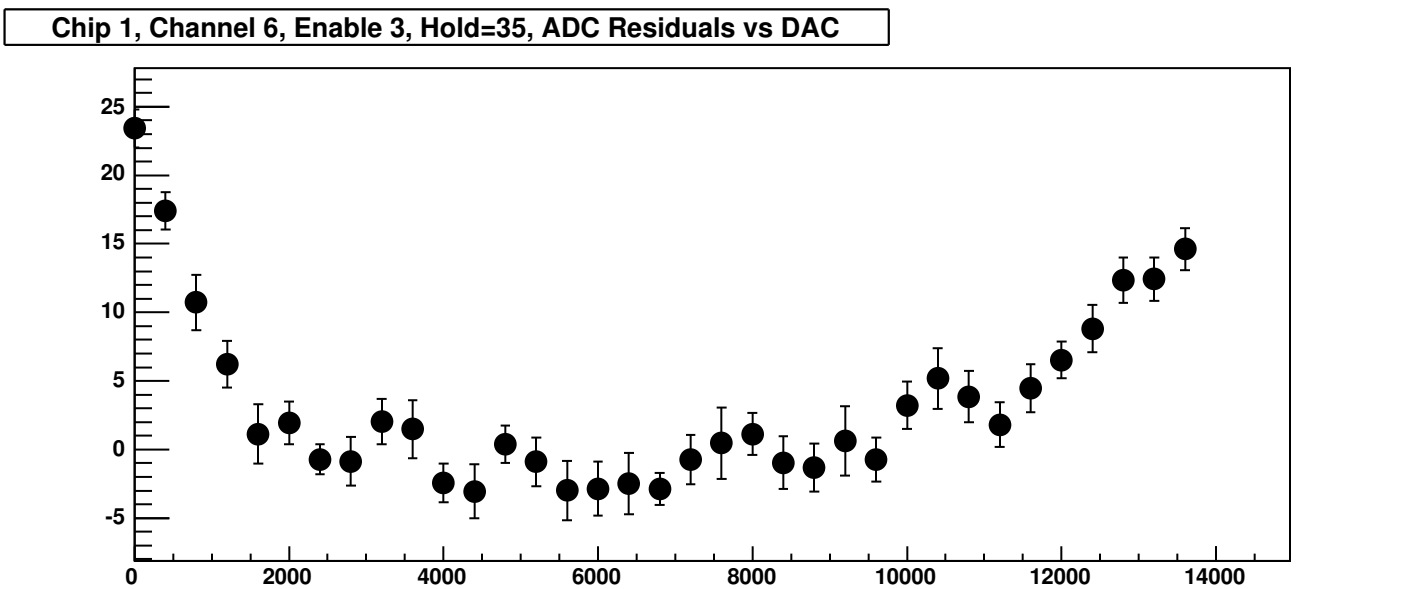

Chip 1, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 12.98 / 23
p0 -529 ± 0.7962
p1 0.03173 ± 0.0001313
-100
-150
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

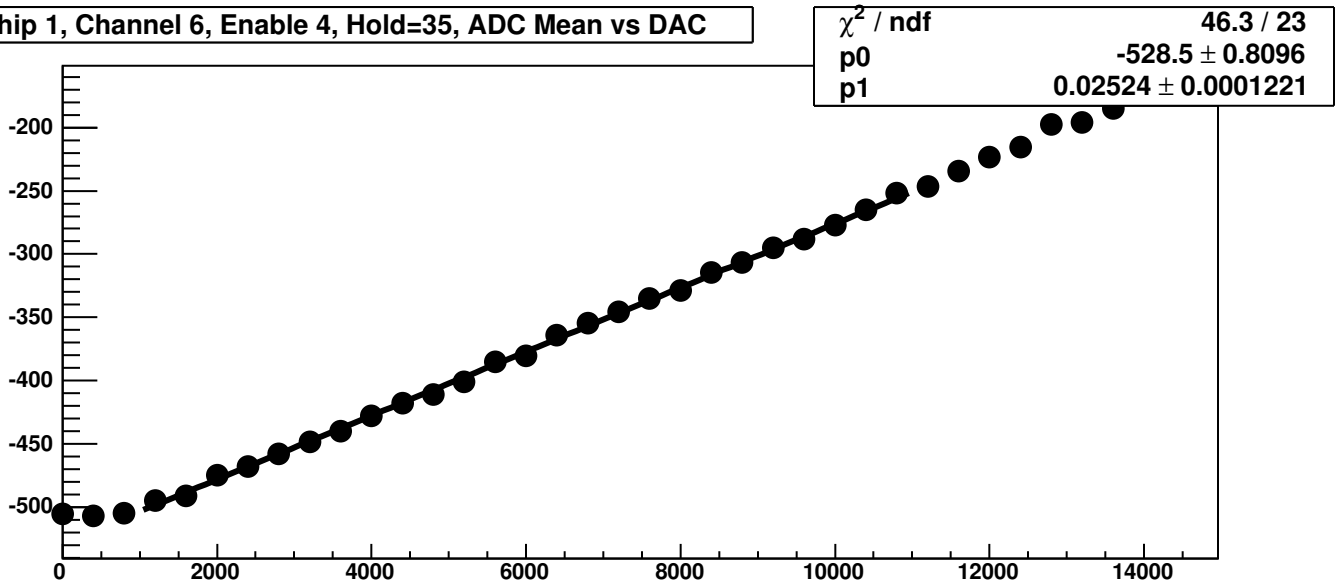

Chip 1, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 50.03 / 23
p0 -528.9 ± 0.7582
p1 0.02429 ± 0.0001201
-150
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

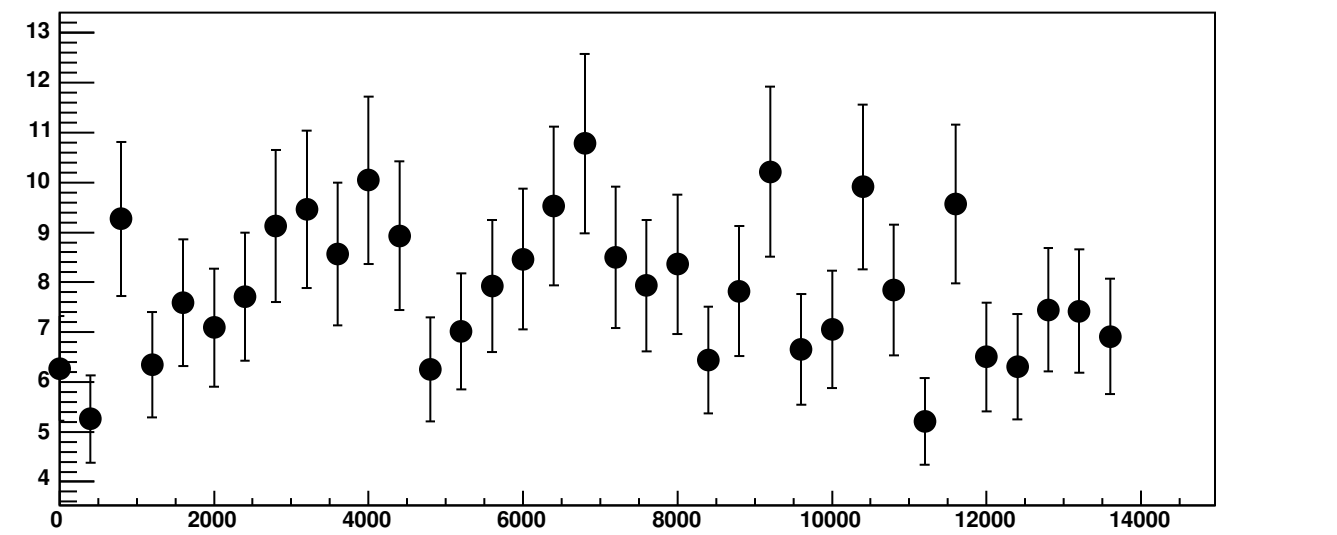

Chip 1, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

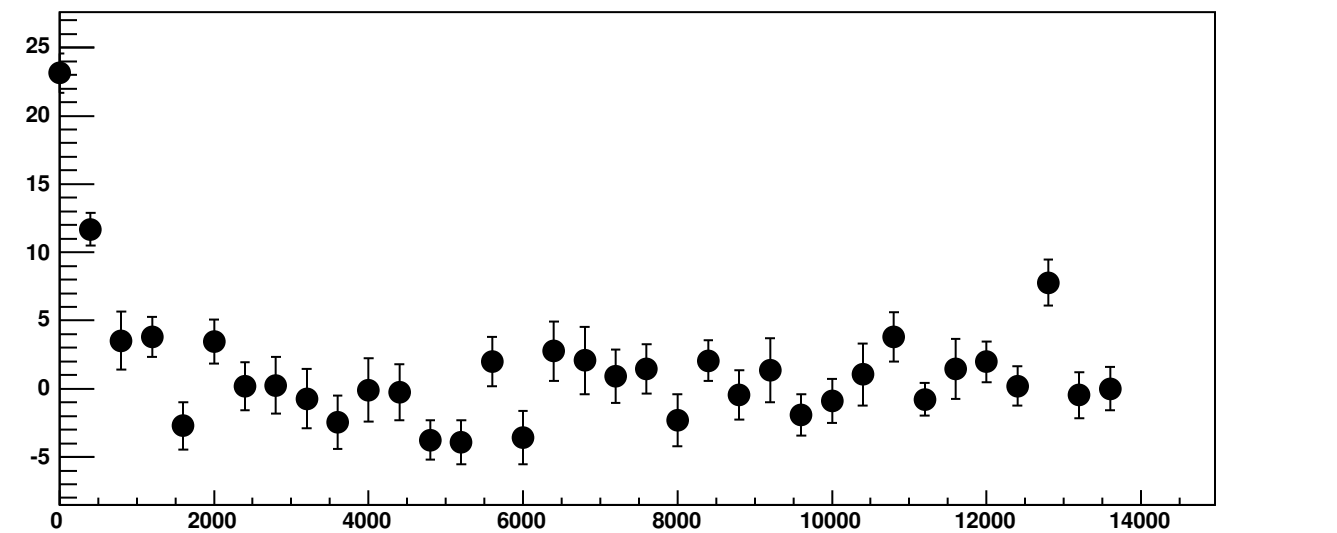
Chip 1, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

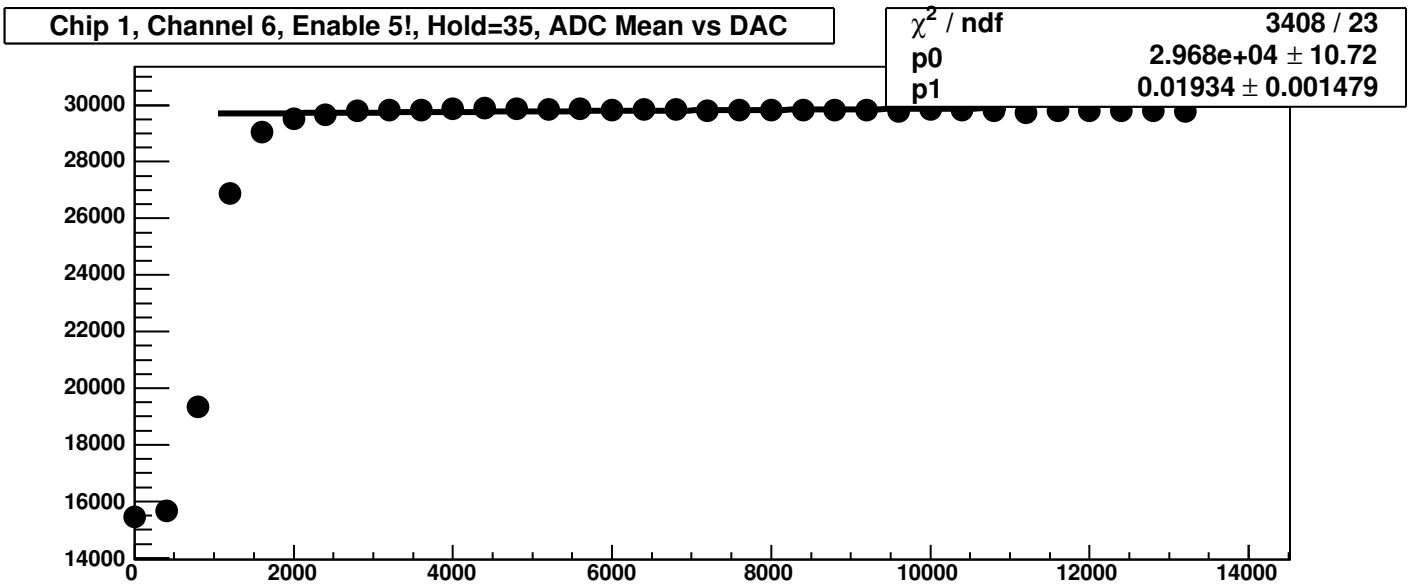


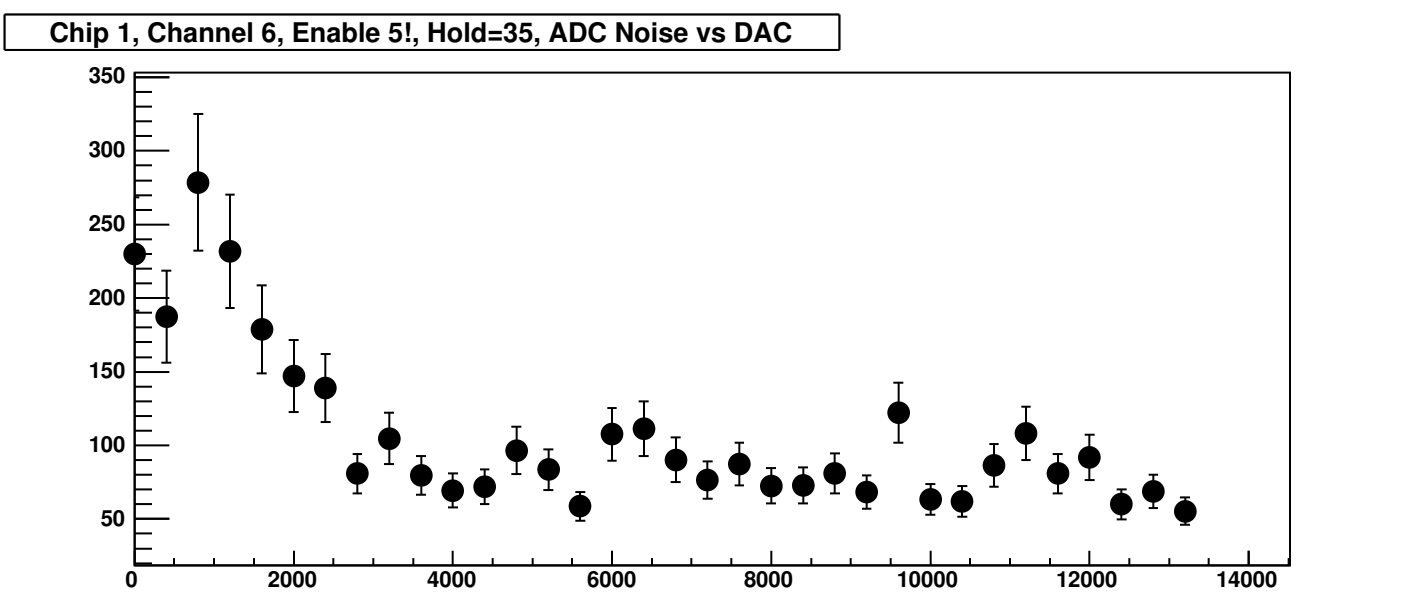
Chip 1, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

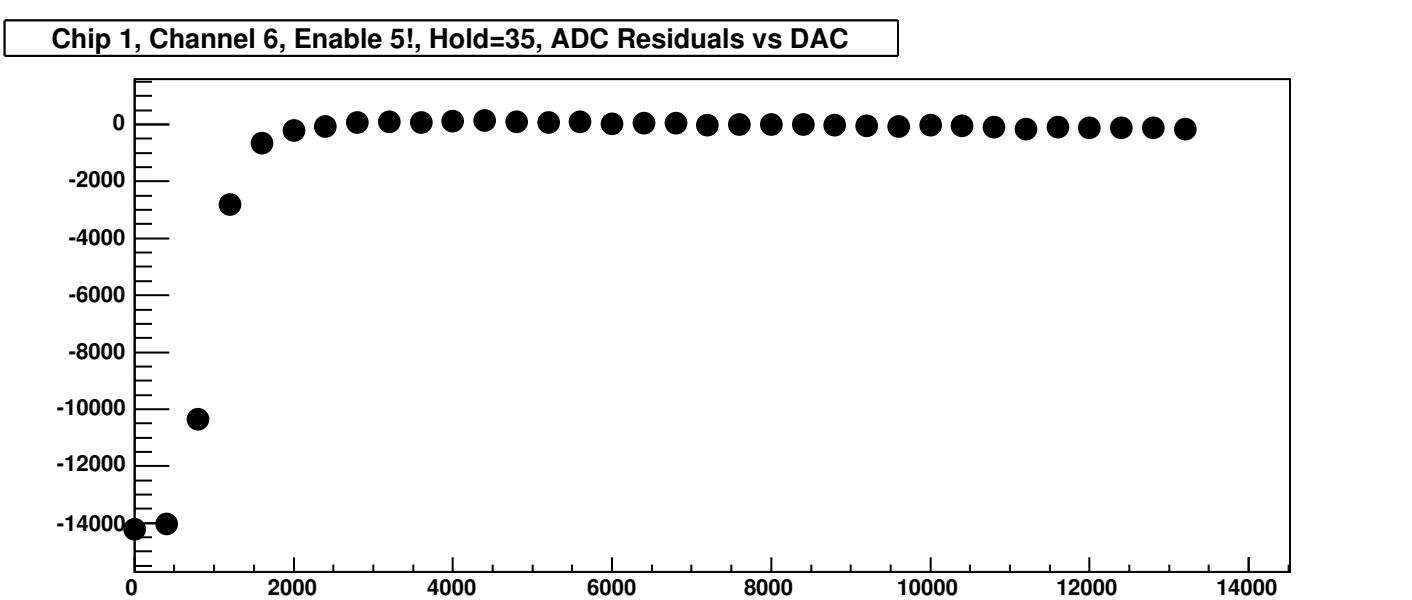


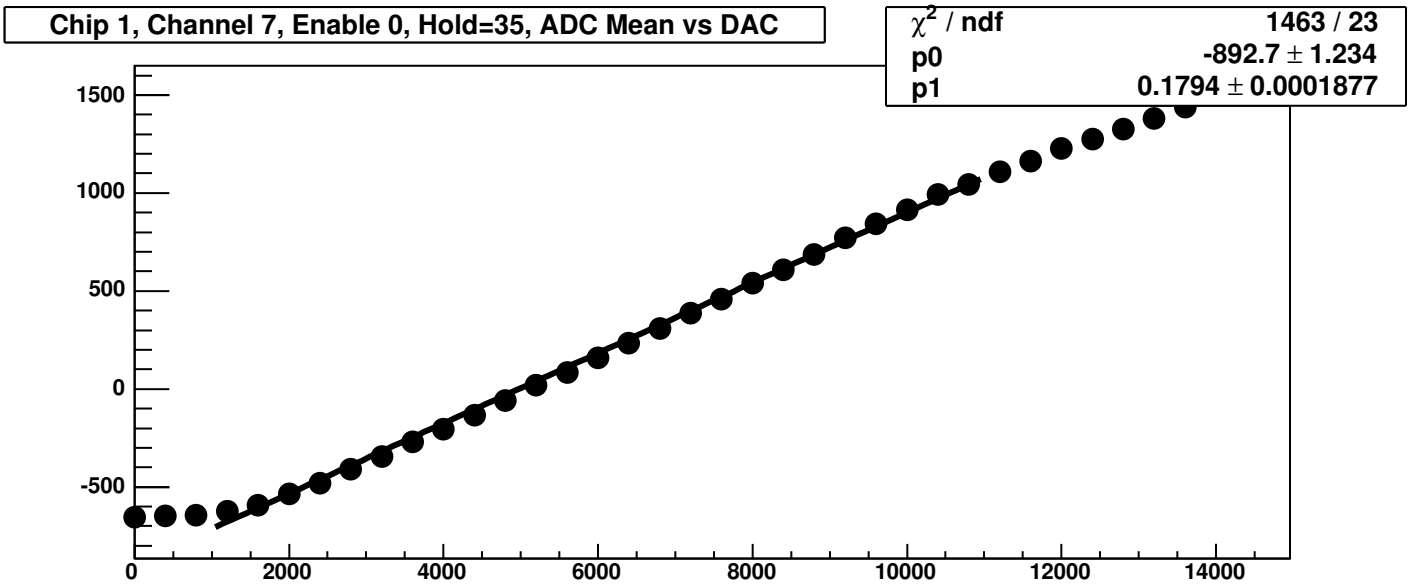
Chip 1, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

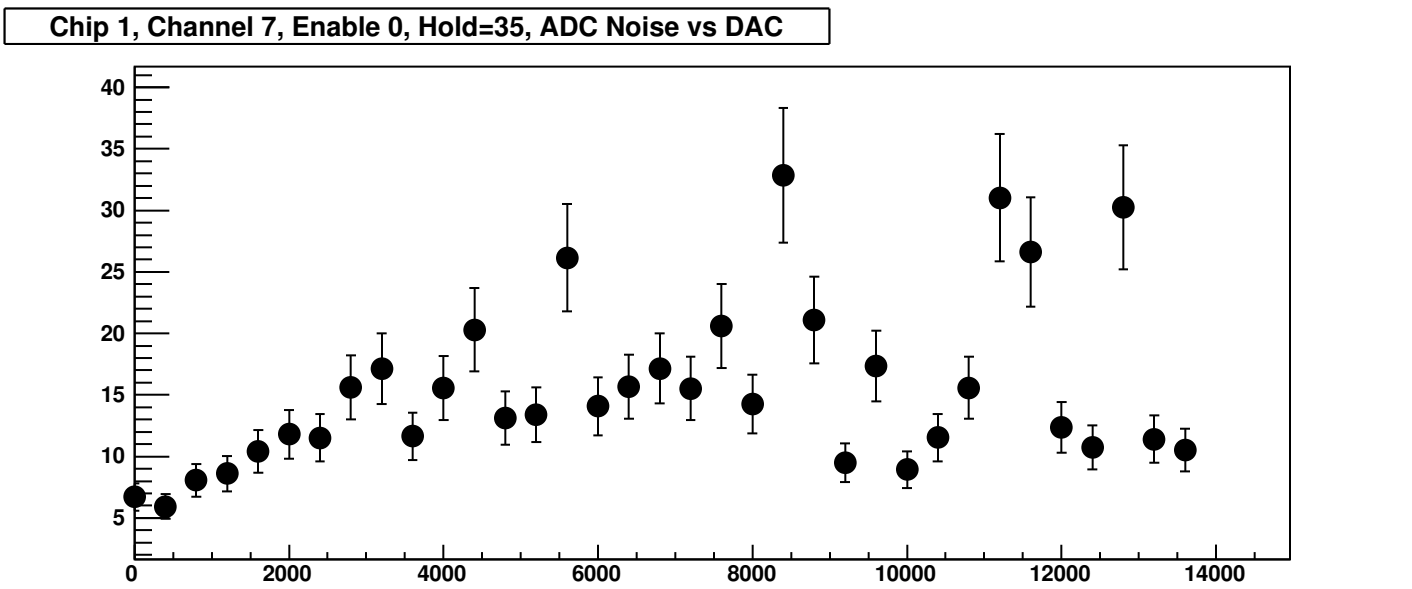


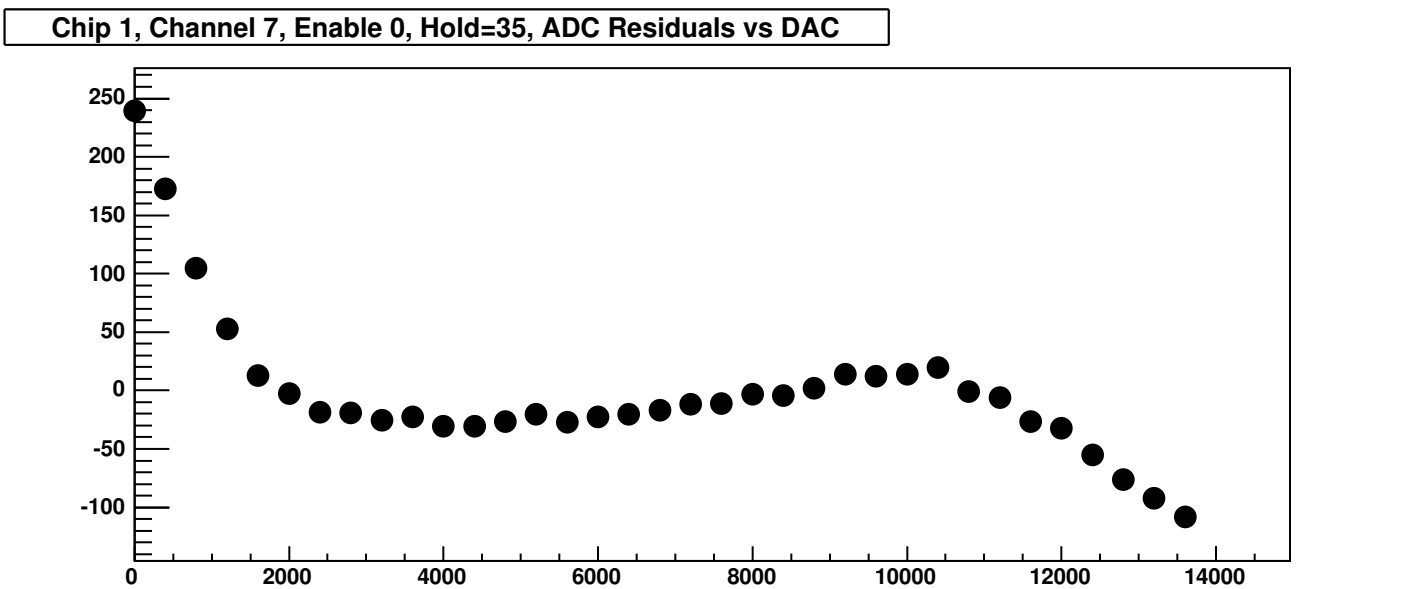

Chip 1, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3408 / 23
p0 2.968e+04 ± 10.72
p1 0.01934 ± 0.001479
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

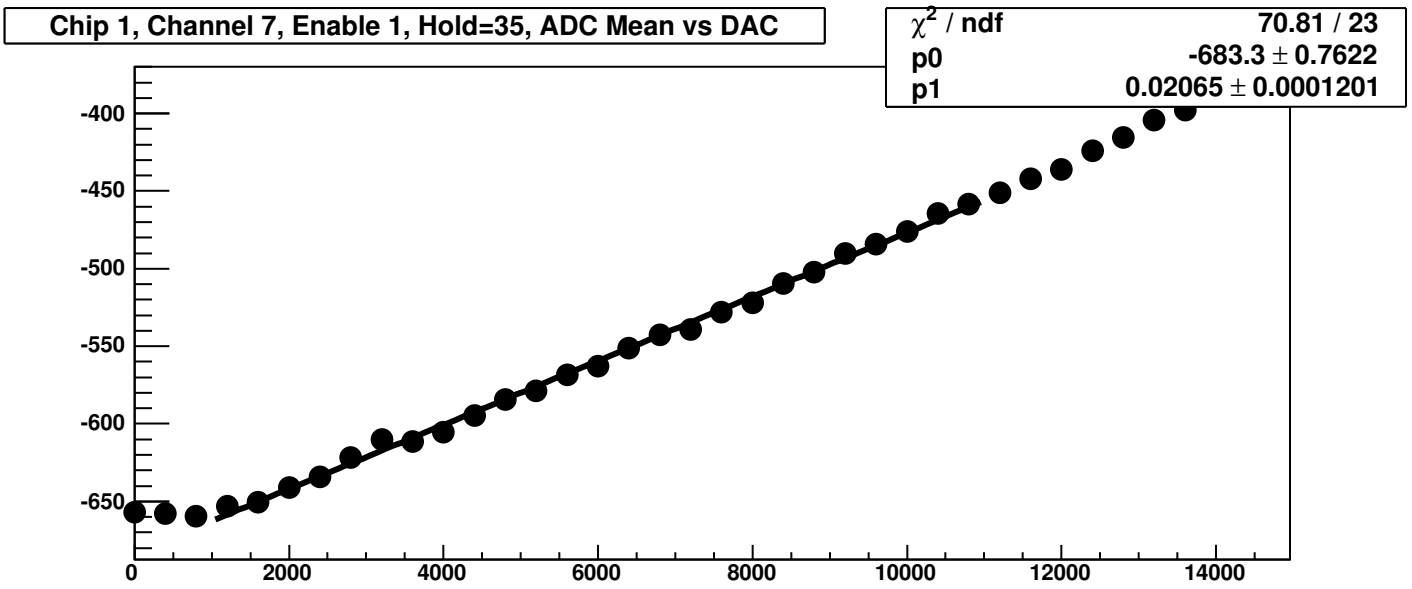

Chip 1, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

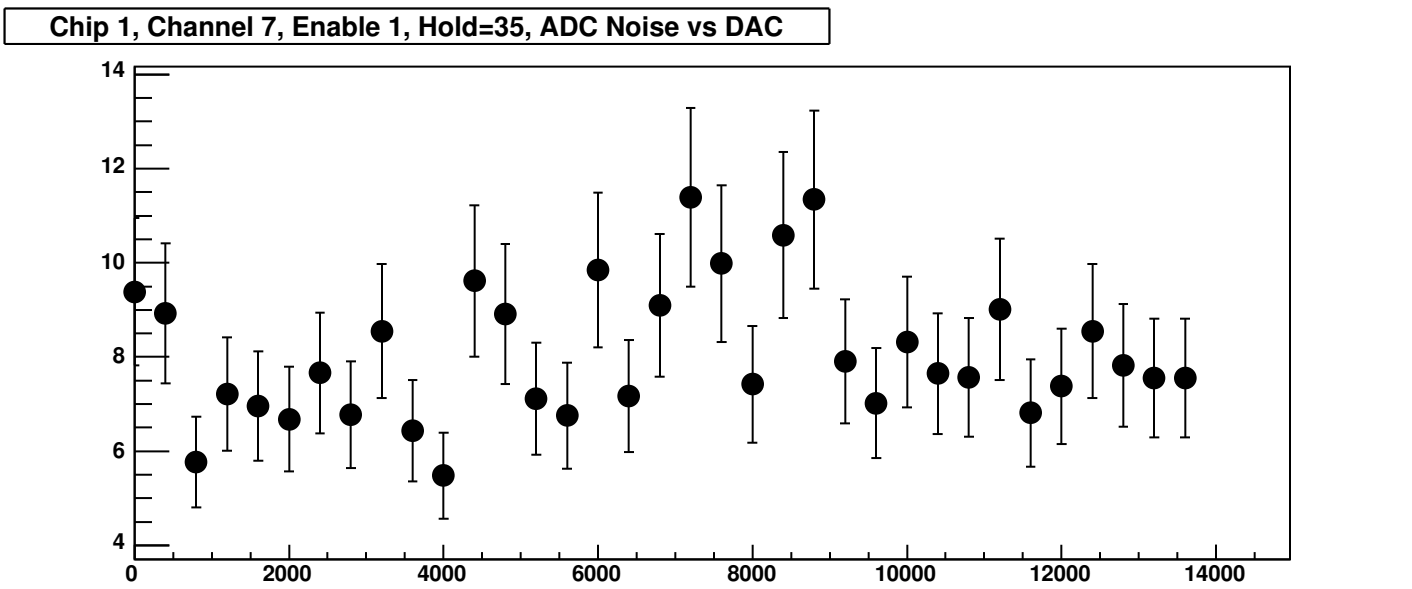

Chip 1, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

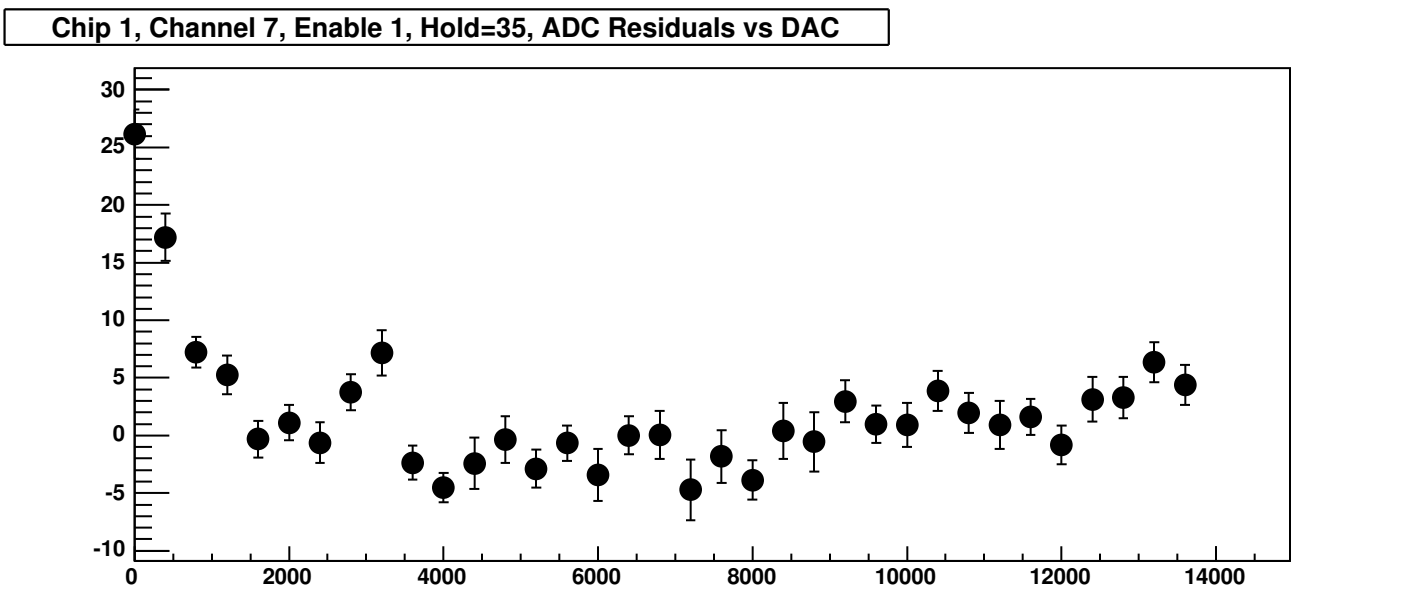

Chip 1, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1463 / 23
p0 -892.7 ± 1.234
p1 0.1794 ± 0.0001877
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

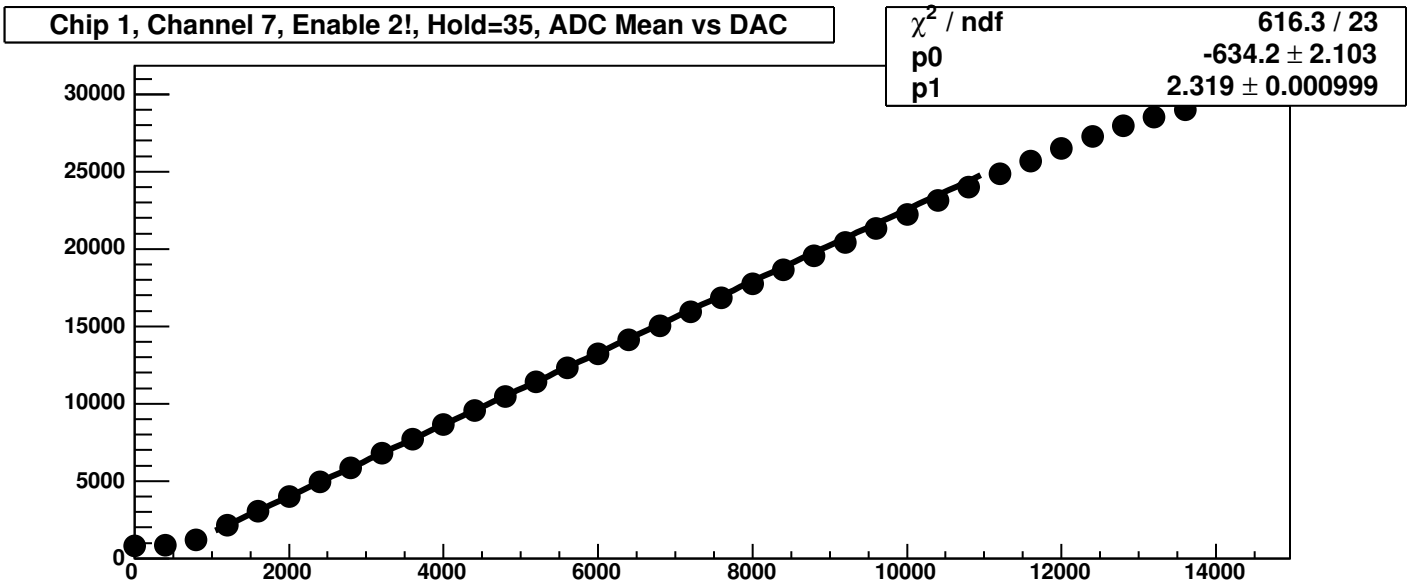

Chip 1, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

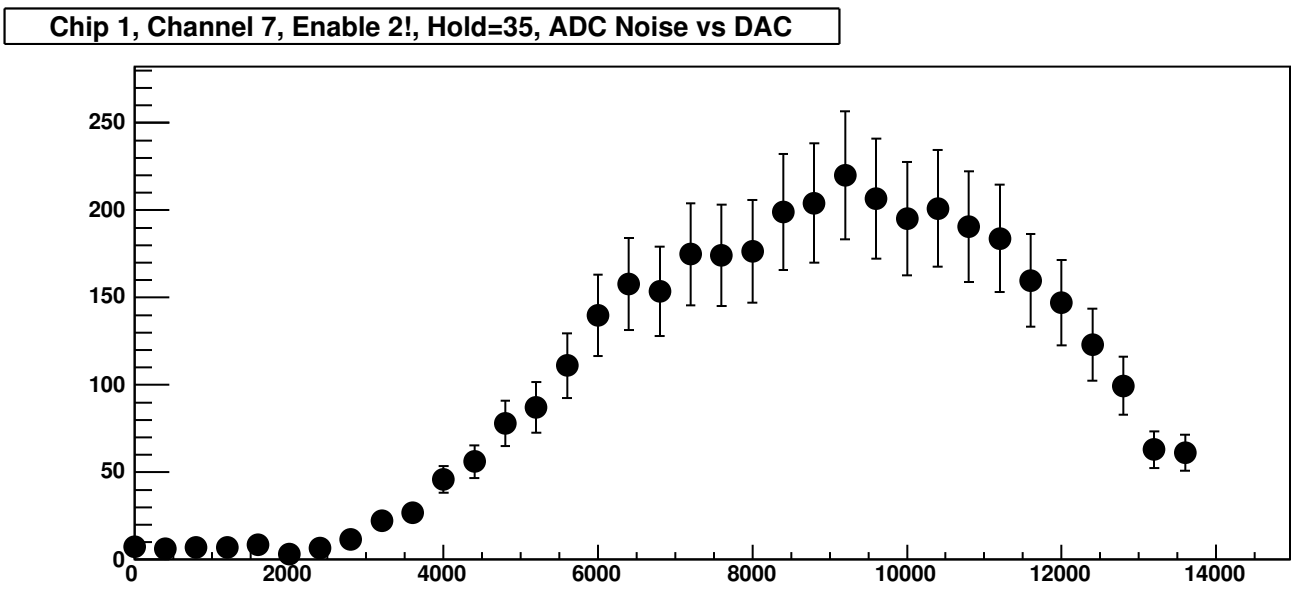

Chip 1, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

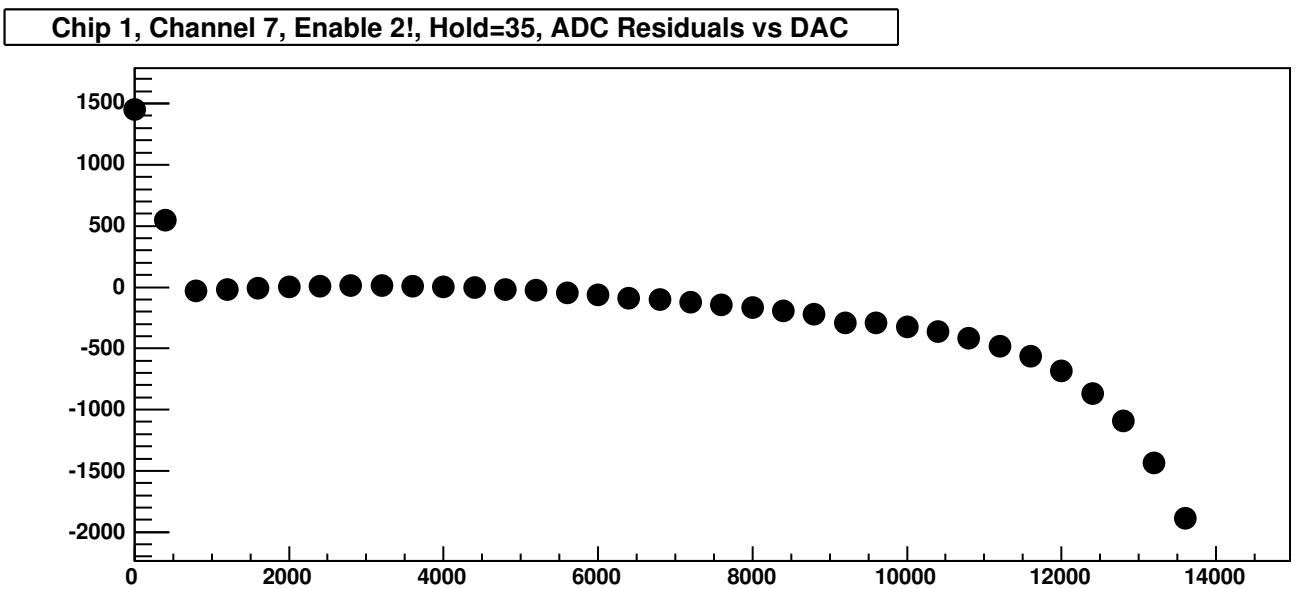

Chip 1, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 70.81 / 23
p0 -683.3 ± 0.7622
p1 0.02065 ± 0.0001201
-400
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

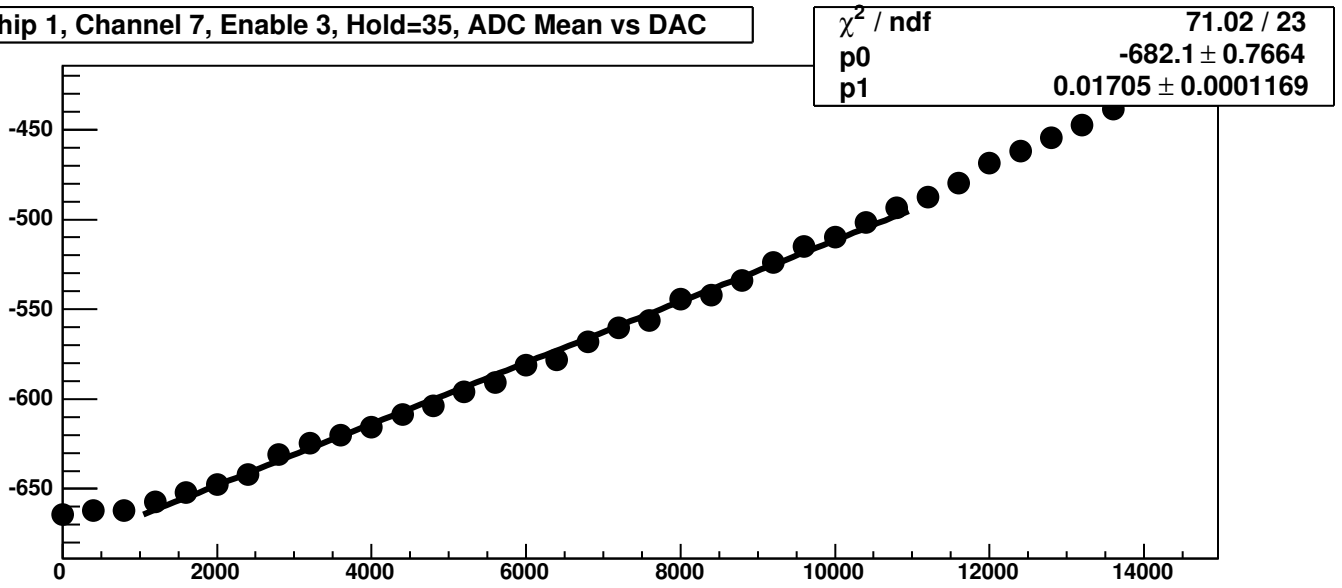

Chip 1, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 616.3 / 23
p0 -634.2 ± 2.103
p1 2.319 ± 0.000999
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

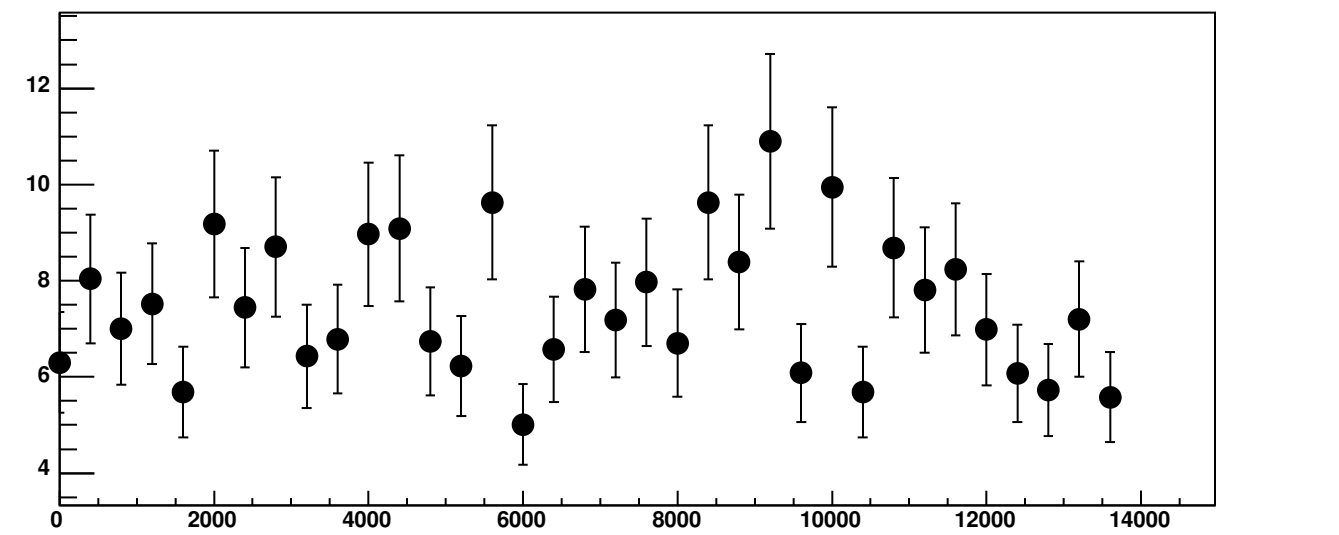

Chip 1, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

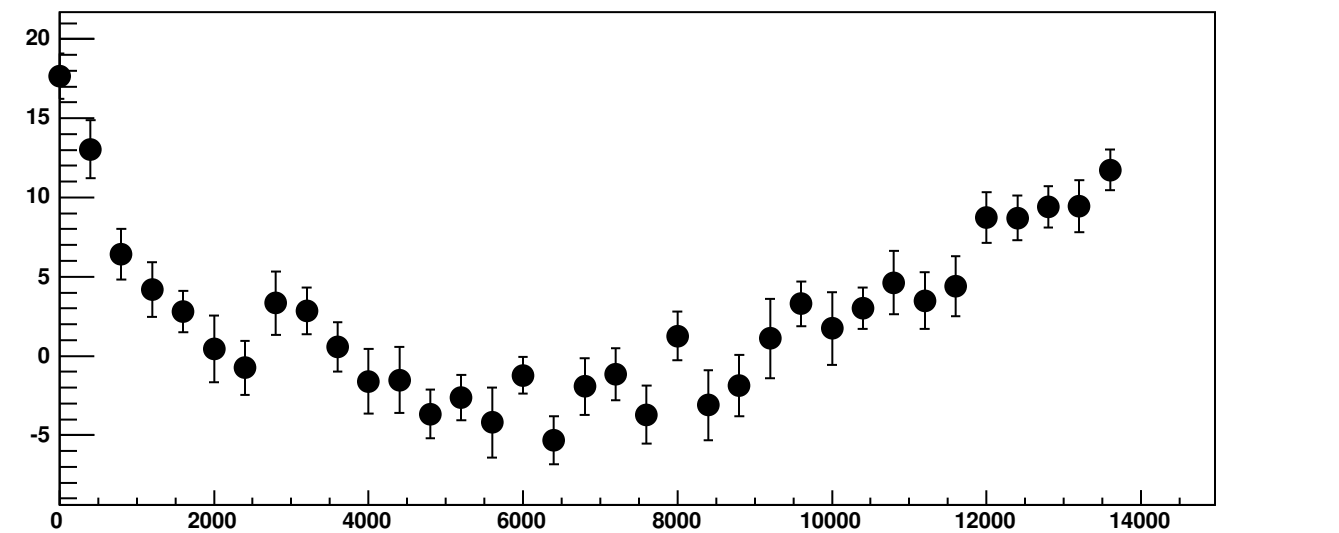
Chip 1, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

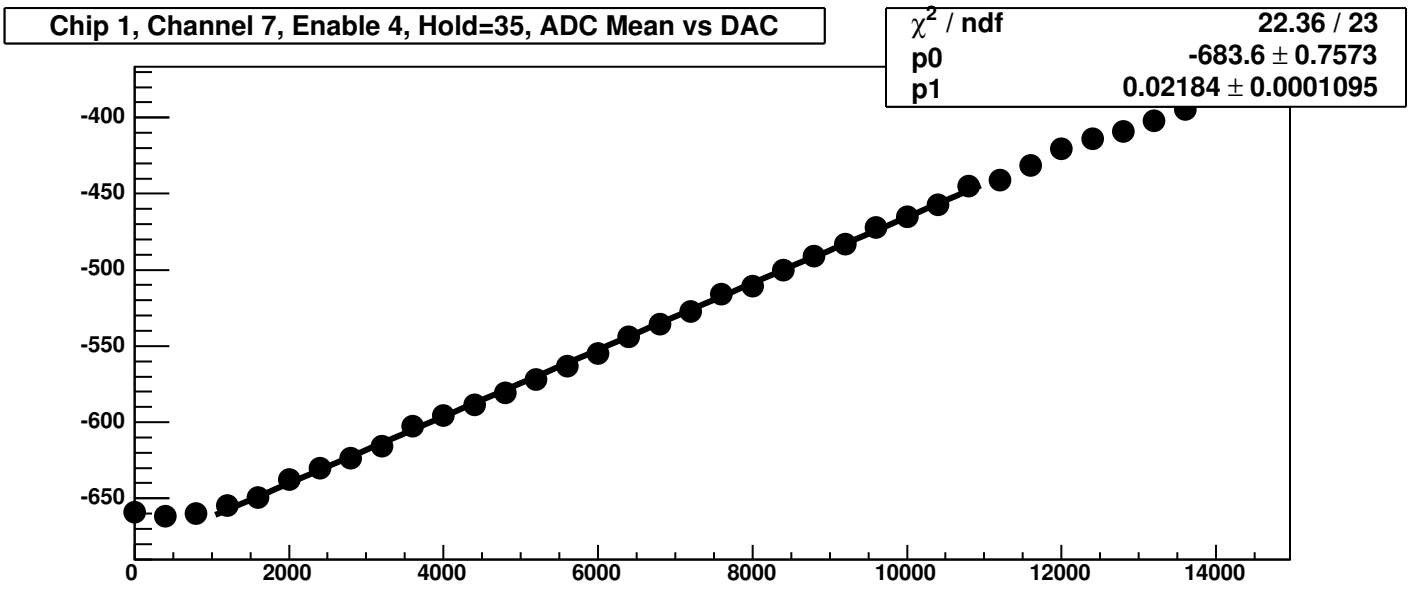


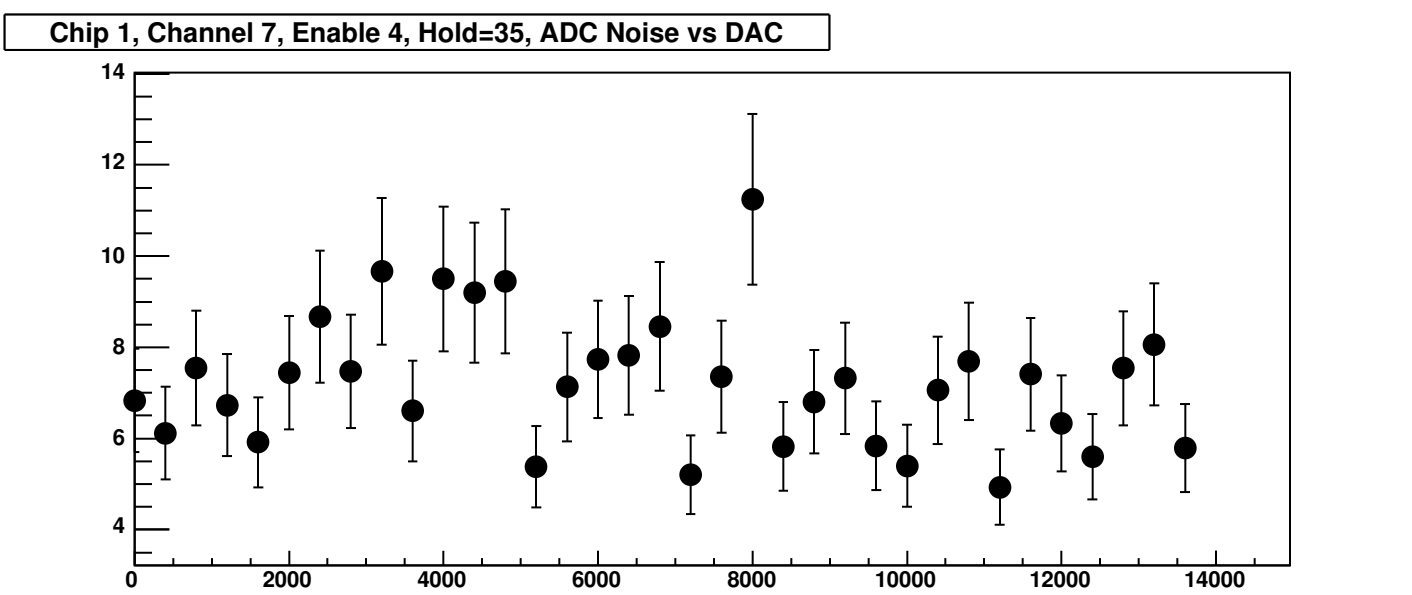
Chip 1, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC

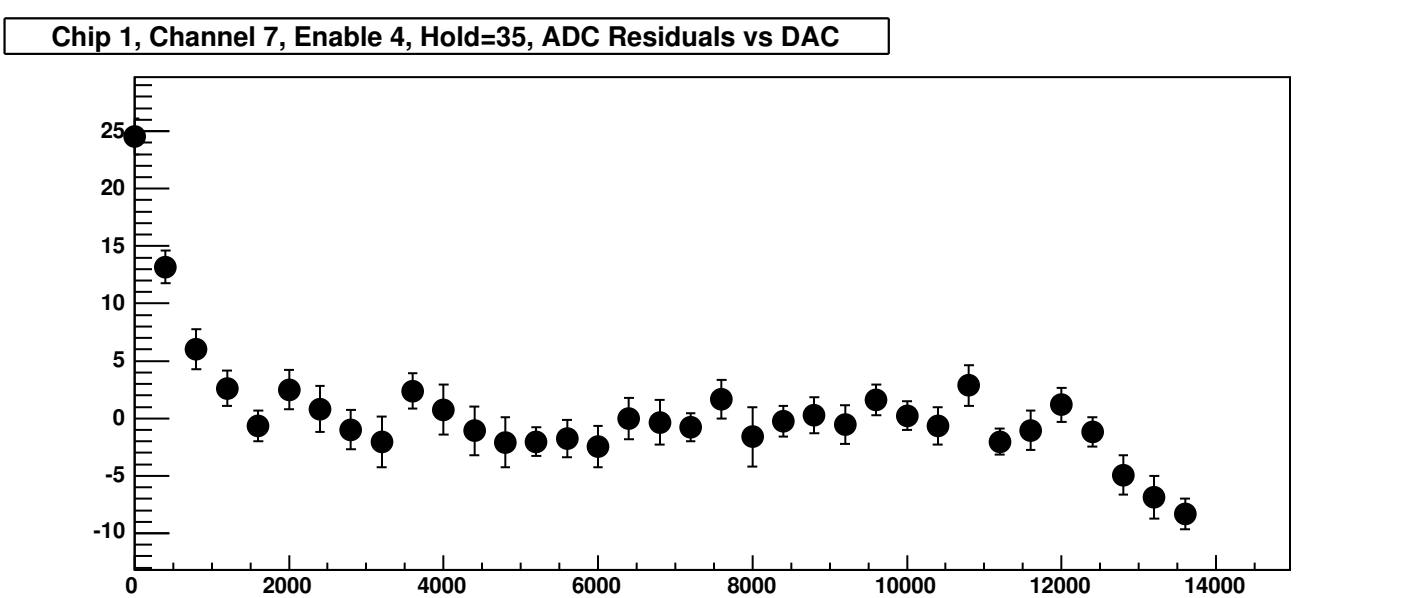


Chip 1, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

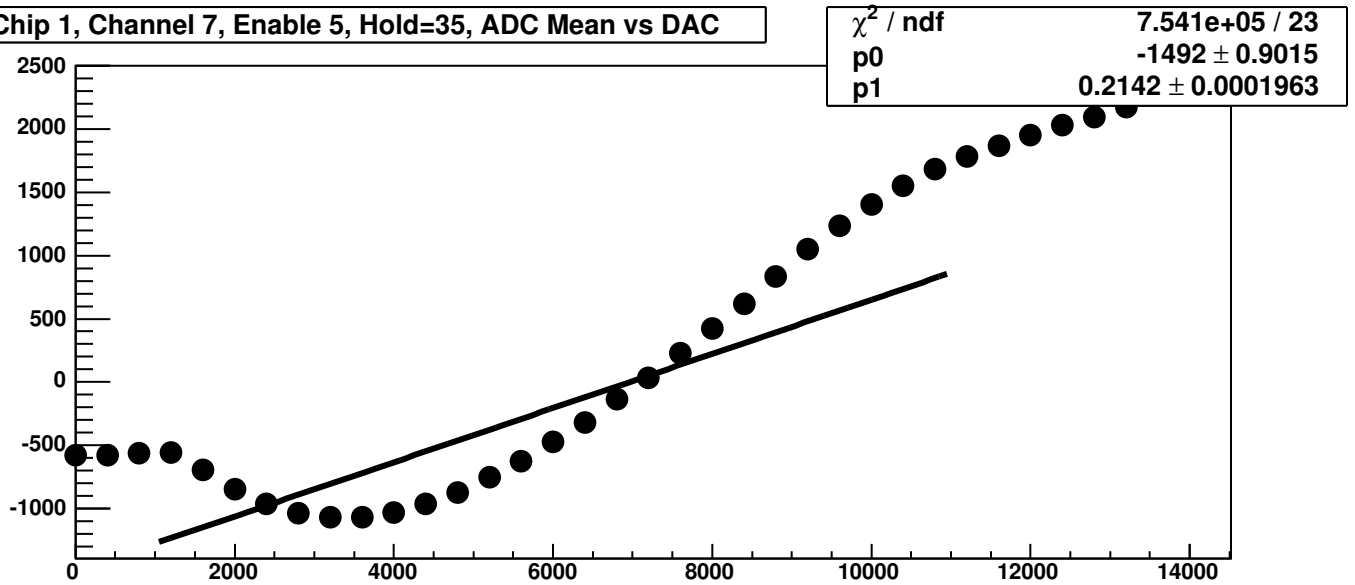



Chip 1, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 22.36 / 23
p0 -683.6 ± 0.7573
p1 0.02184 ± 0.0001095
-400
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

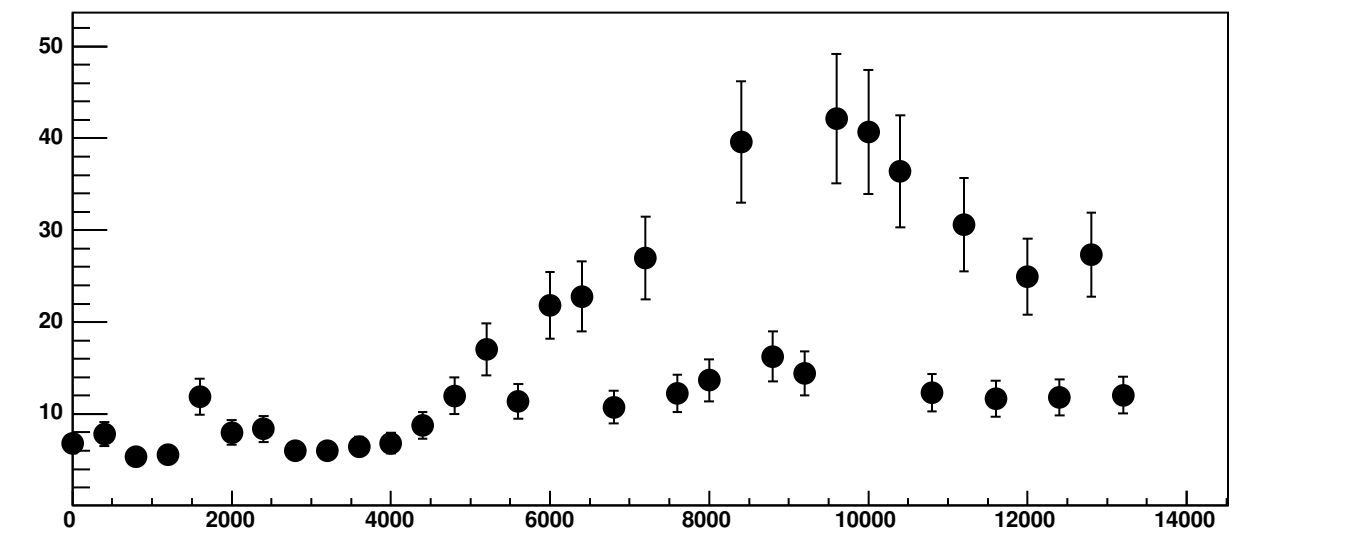

Chip 1, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

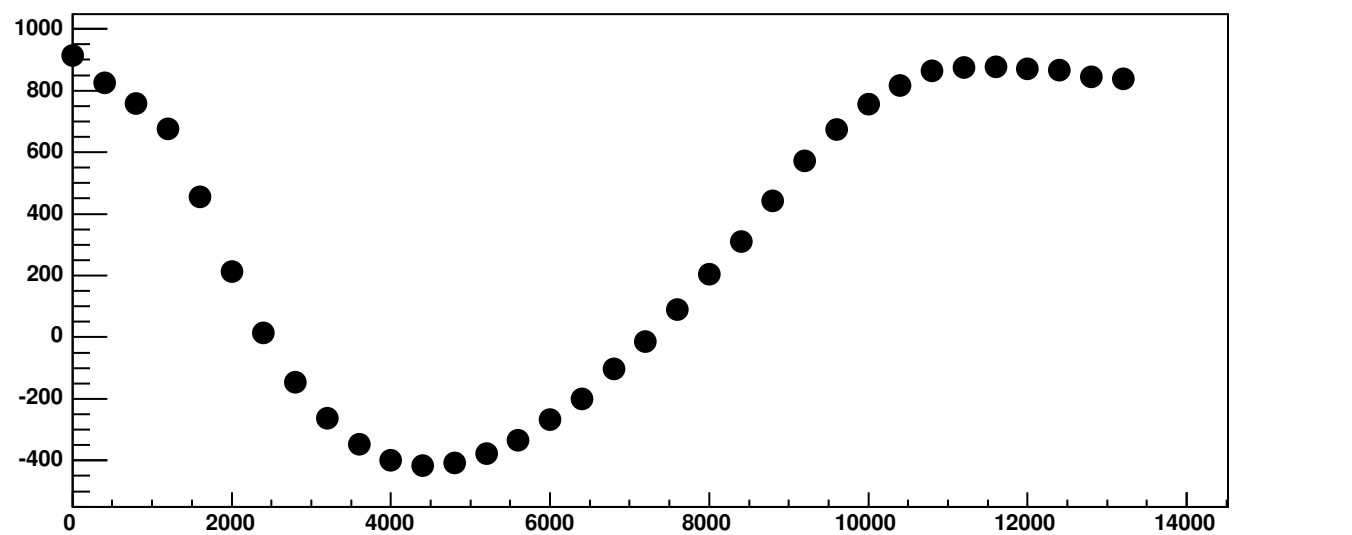
Chip 1, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC

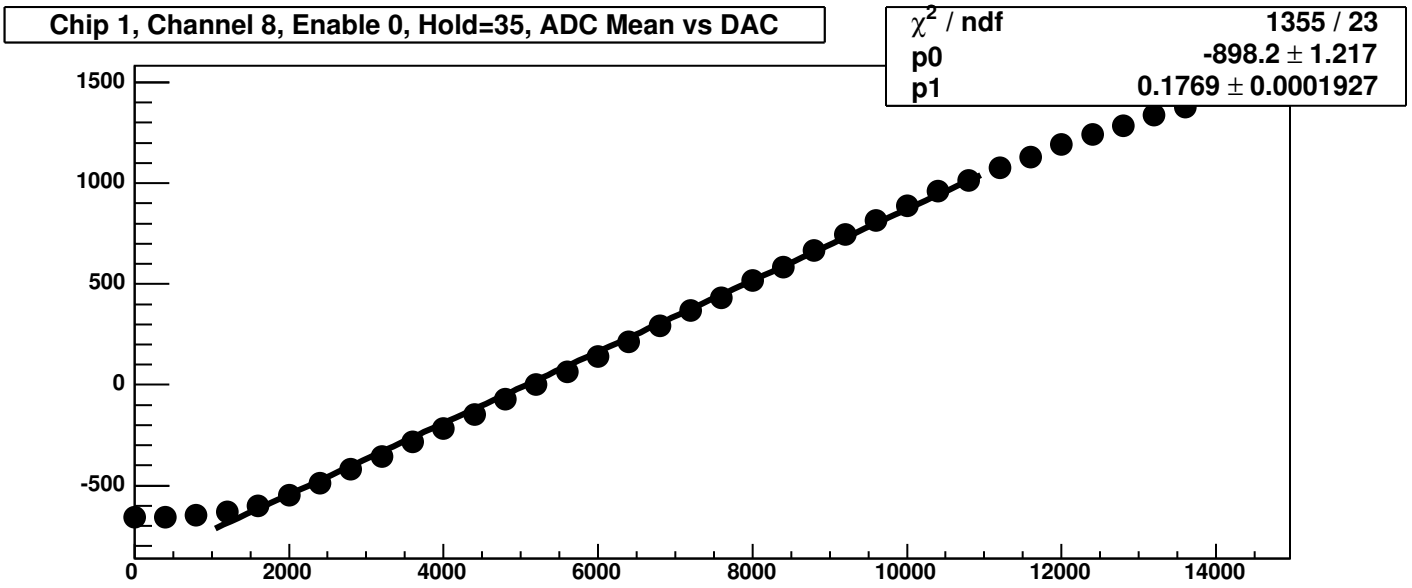


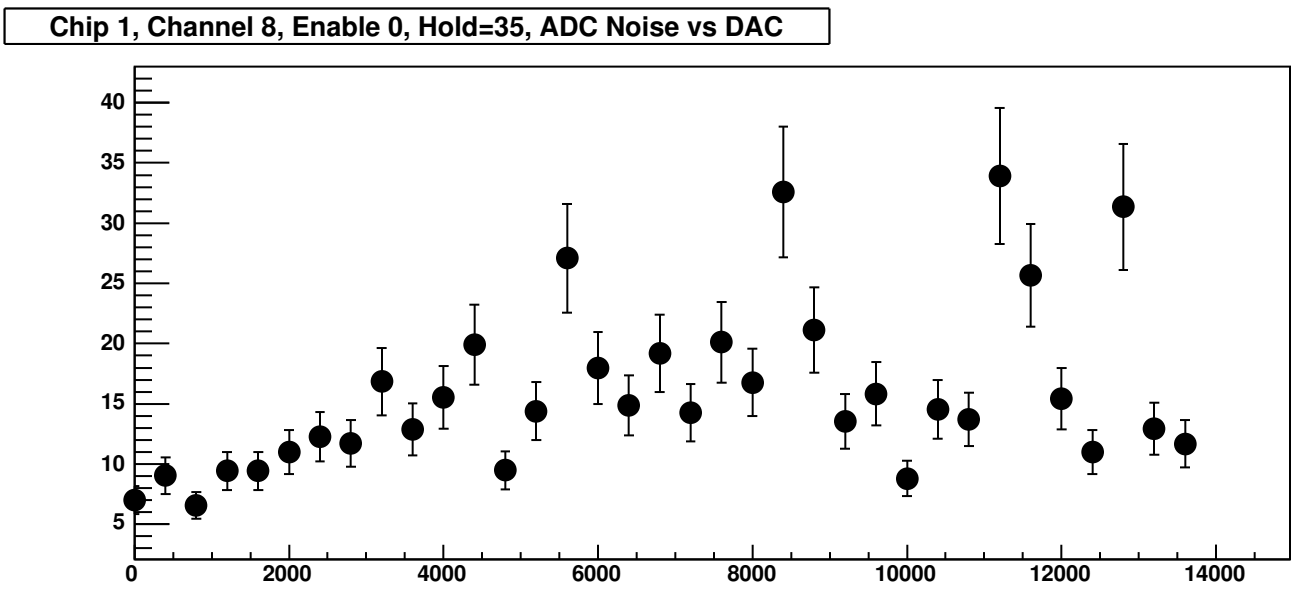
Chip 1, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC

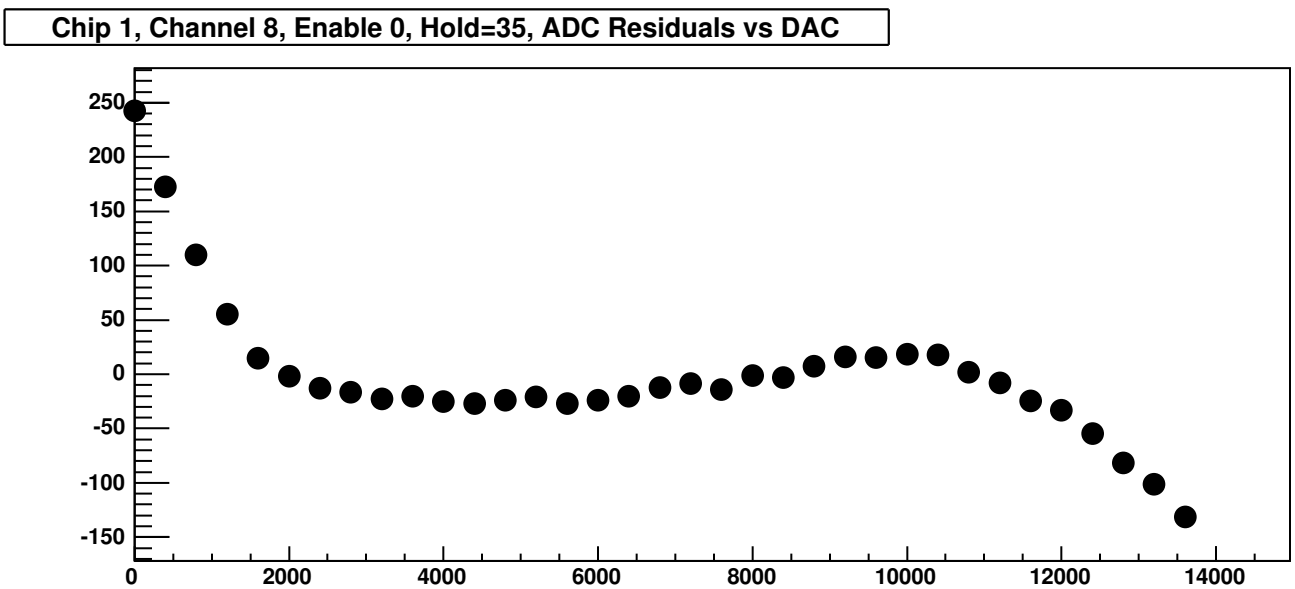


Chip 1, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC

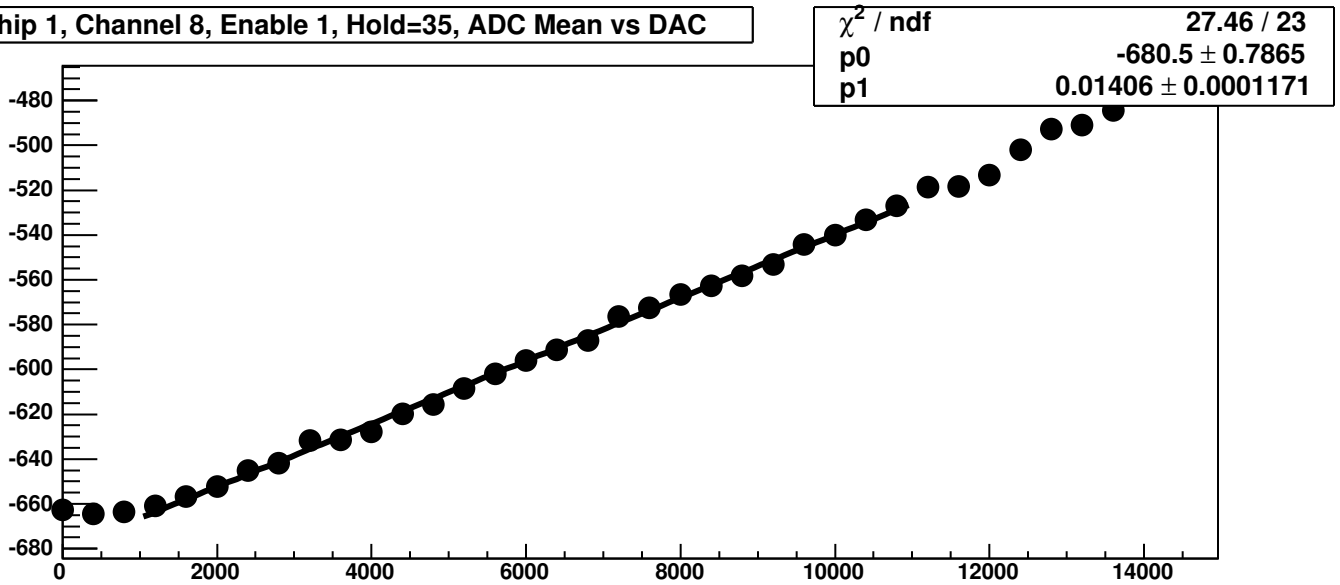



Chip 1, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1355 / 23
p0 -898.2 ± 1.217
p1 0.1769 ± 0.0001927
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

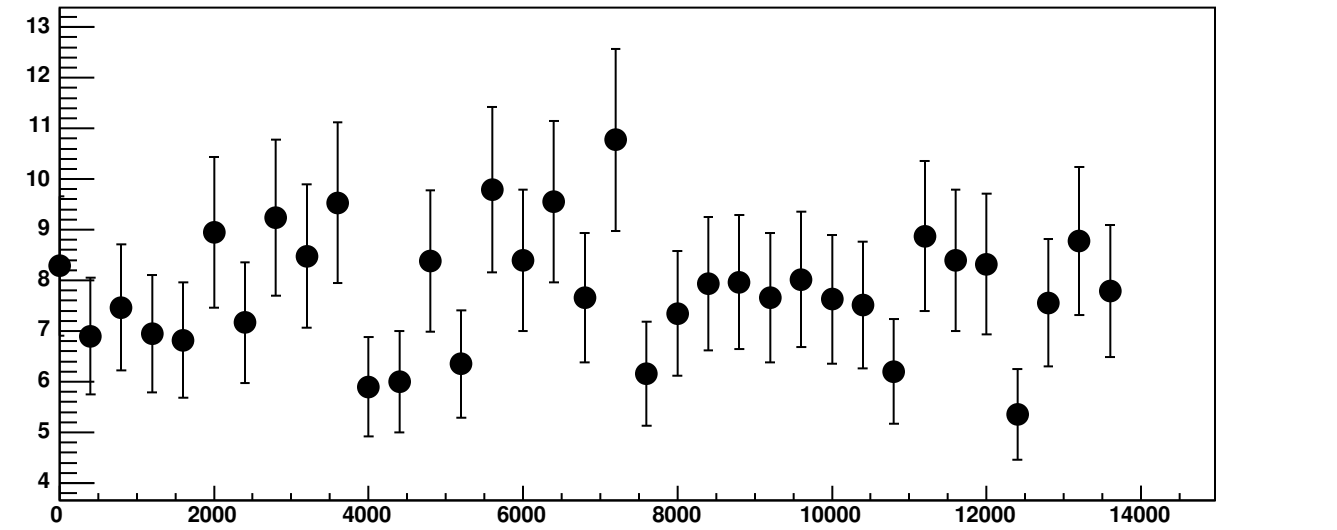

Chip 1, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

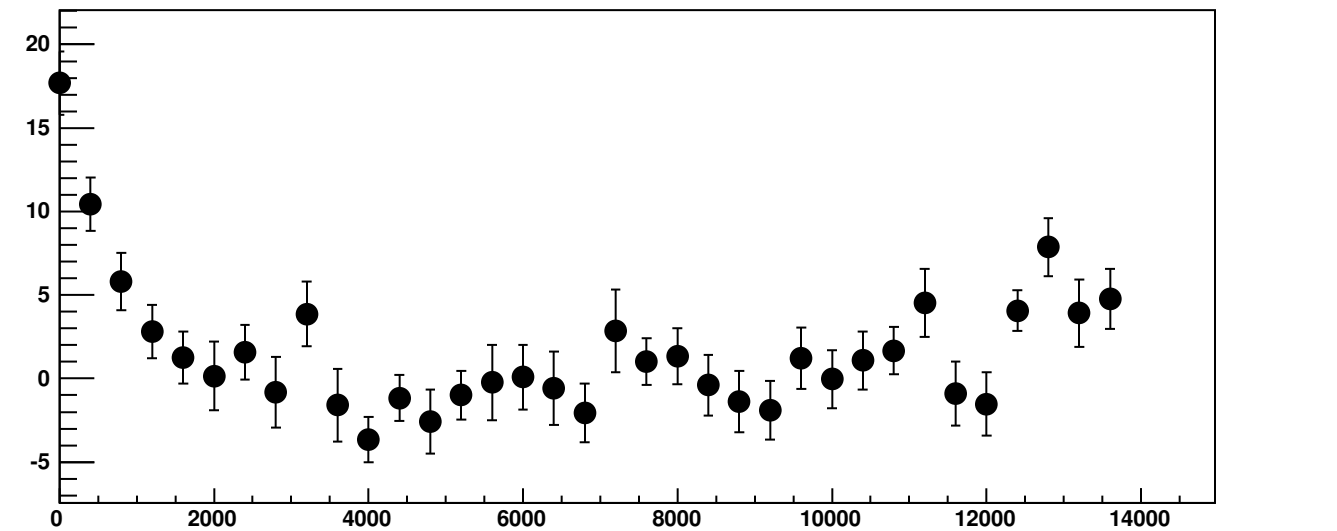
Chip 1, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

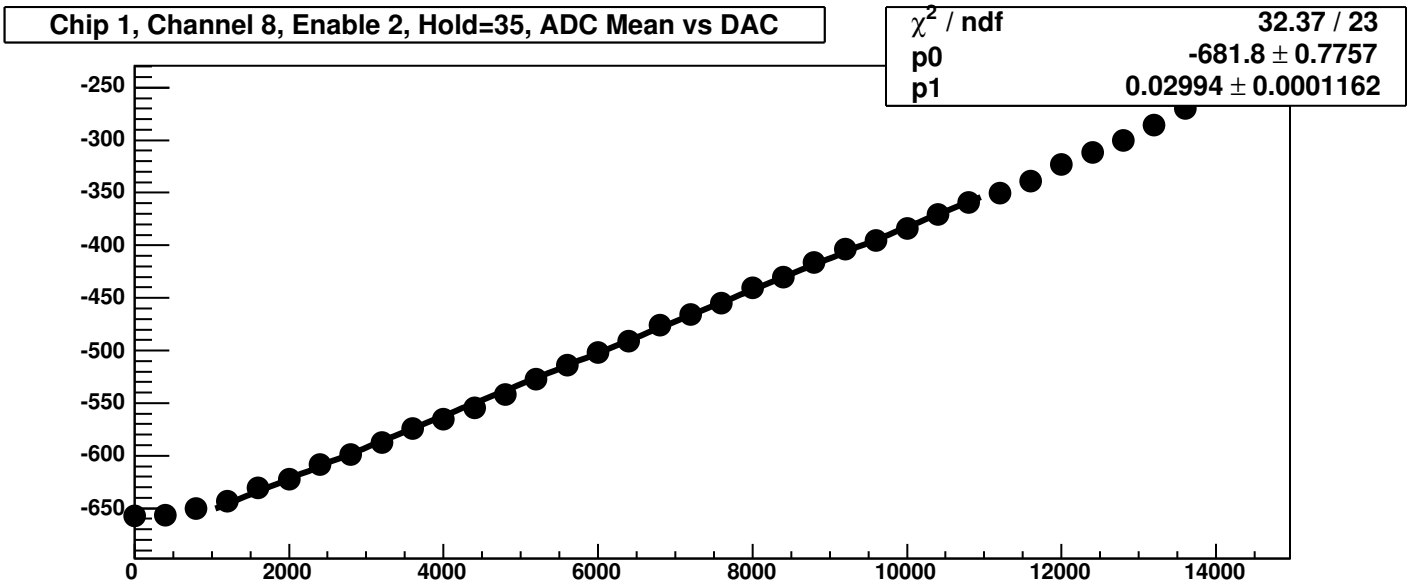


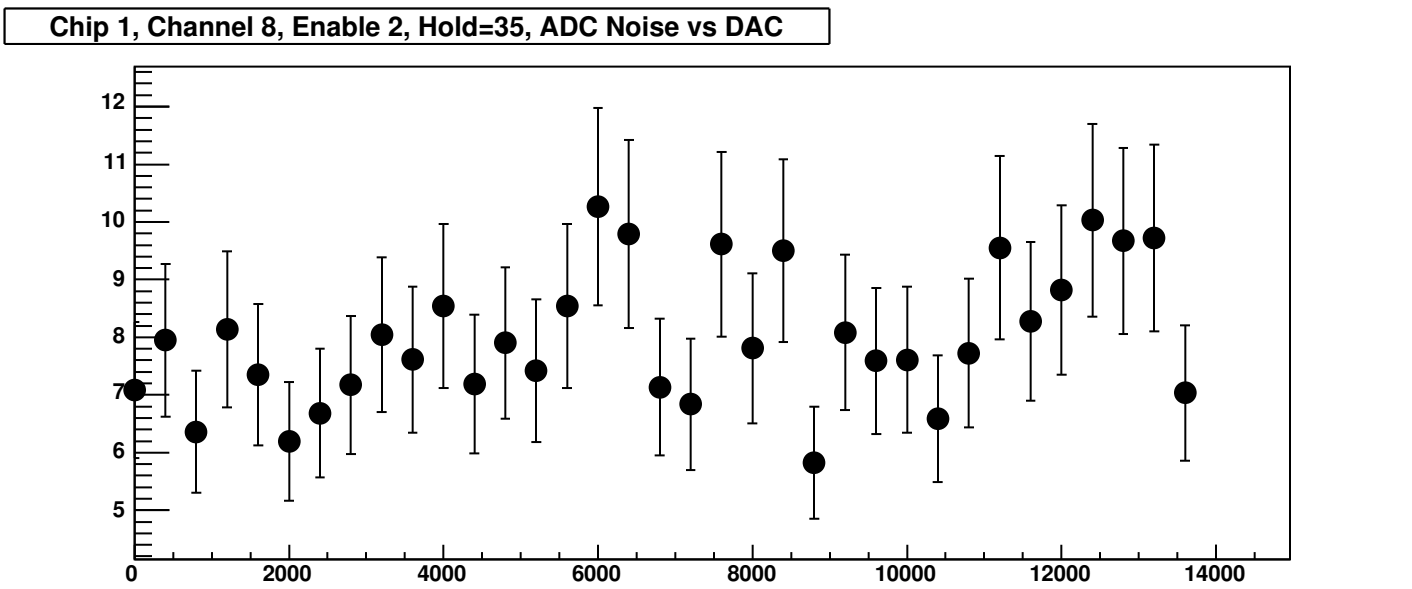
Chip 1, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC

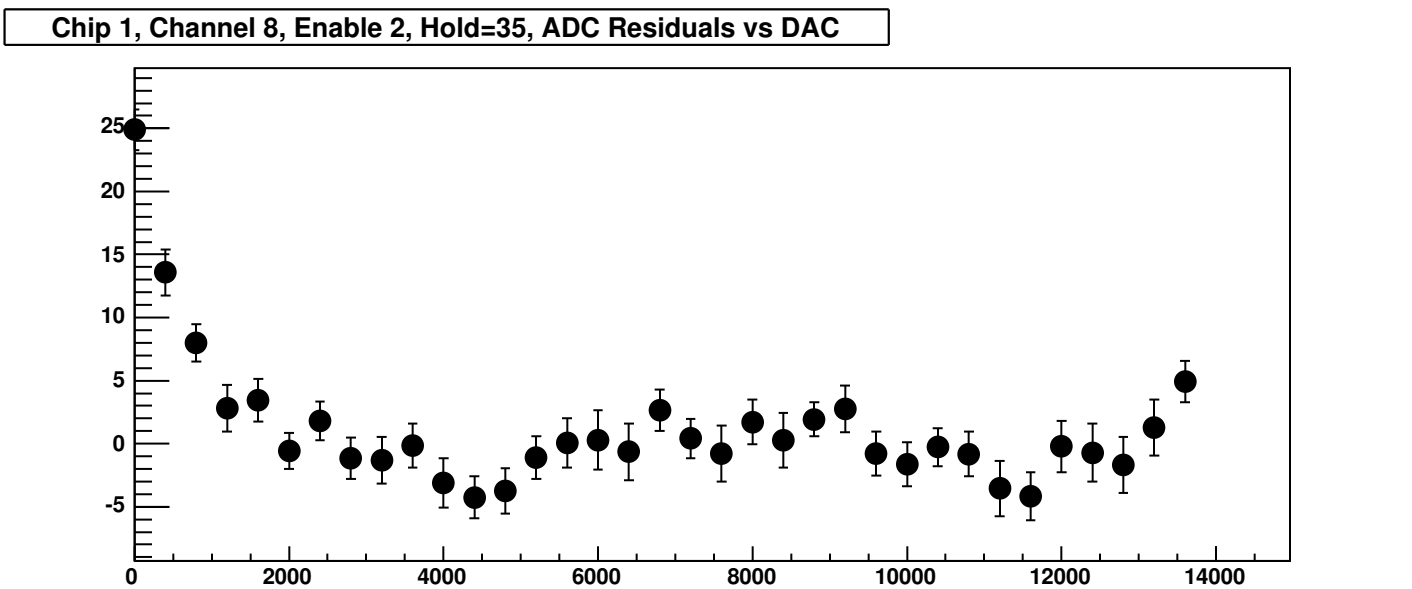


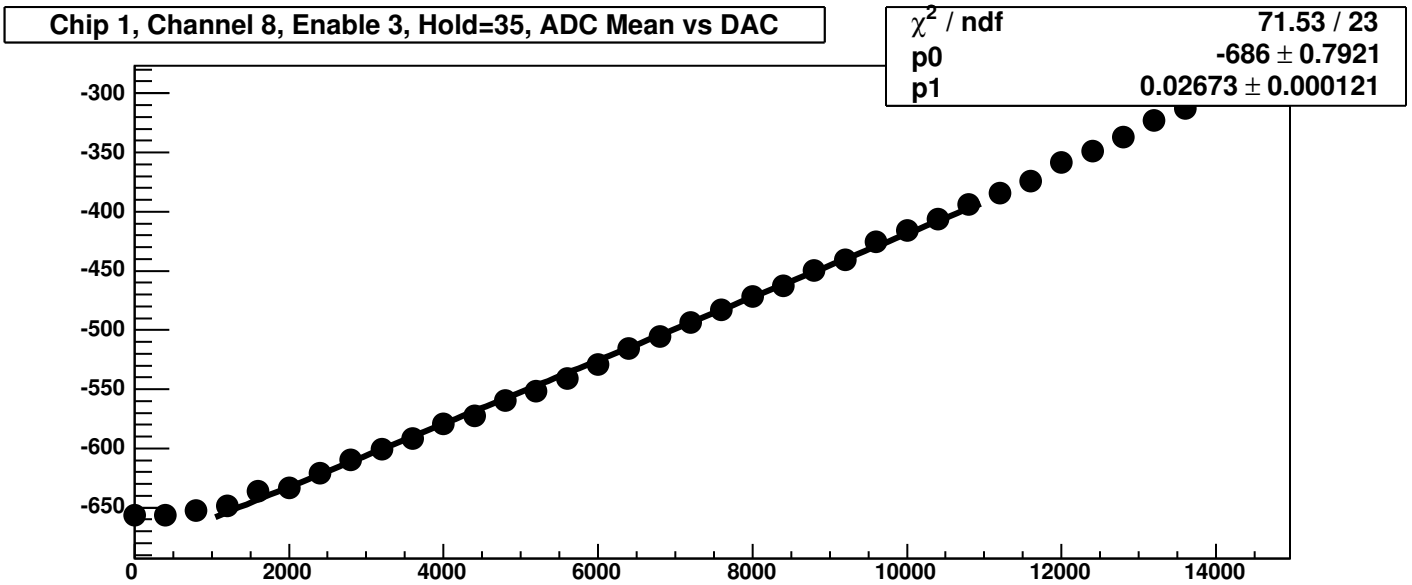
Chip 1, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

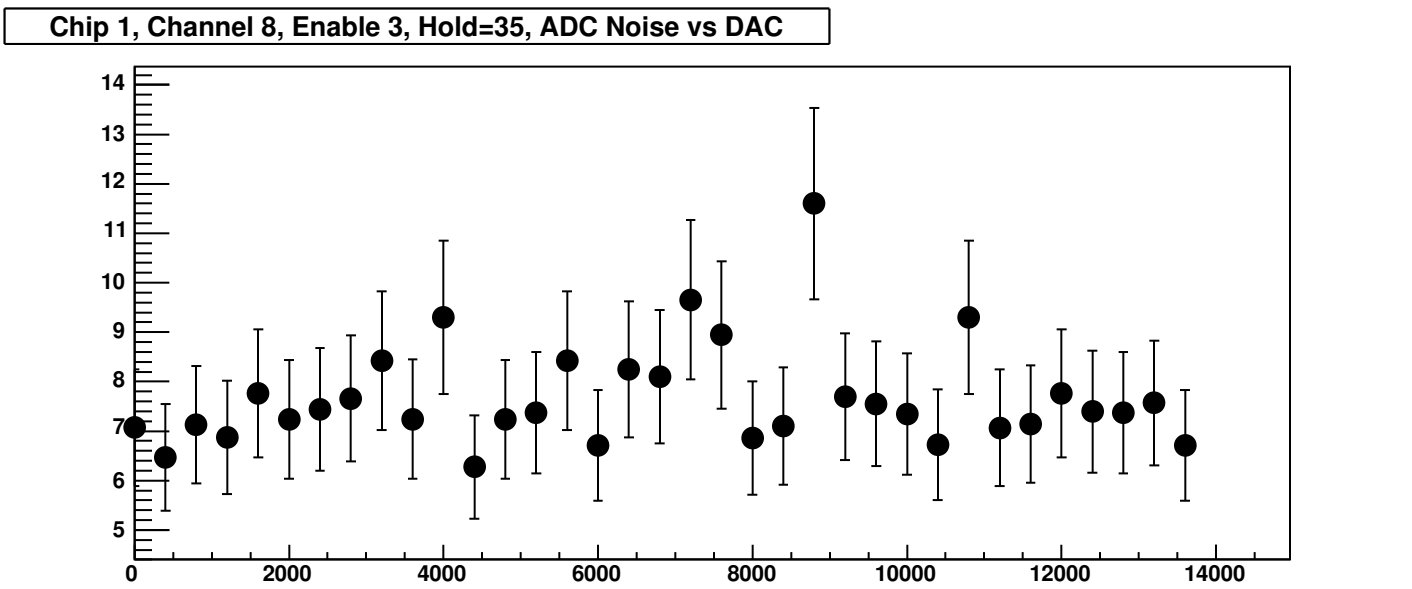


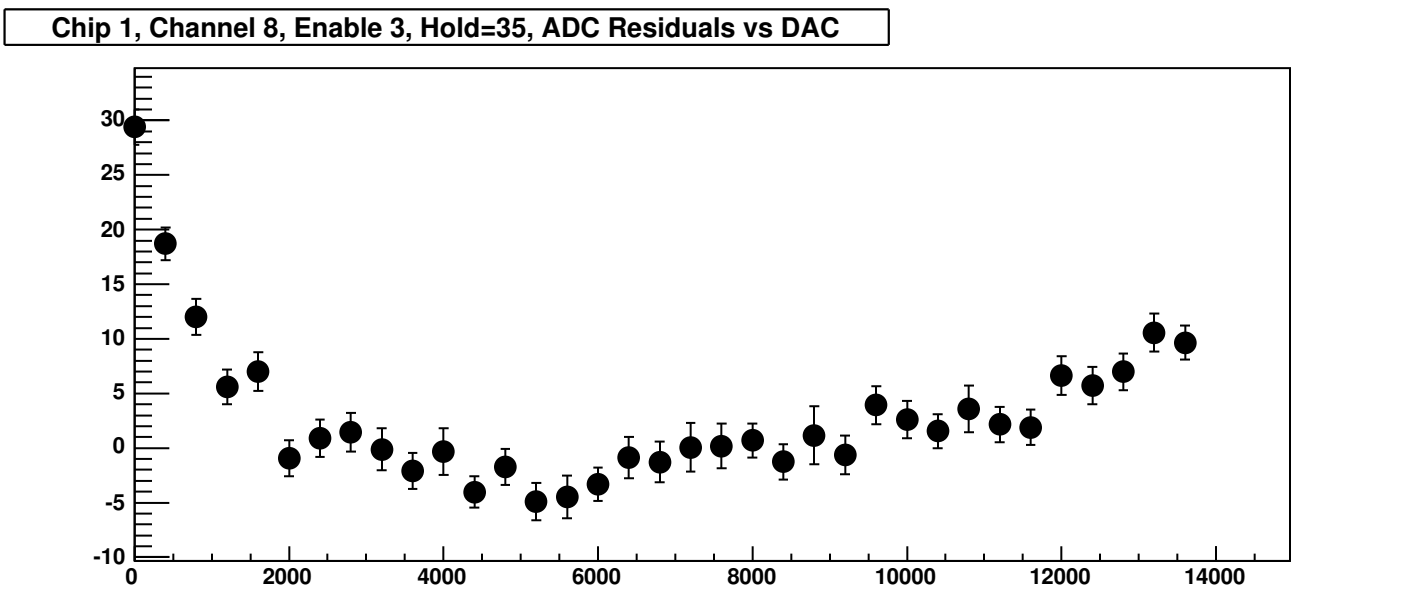

Chip 1, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 32.37 / 23
p0 -681.8 ± 0.7757
p1 0.02994 ± 0.0001162
-250
-300
-350
-400
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

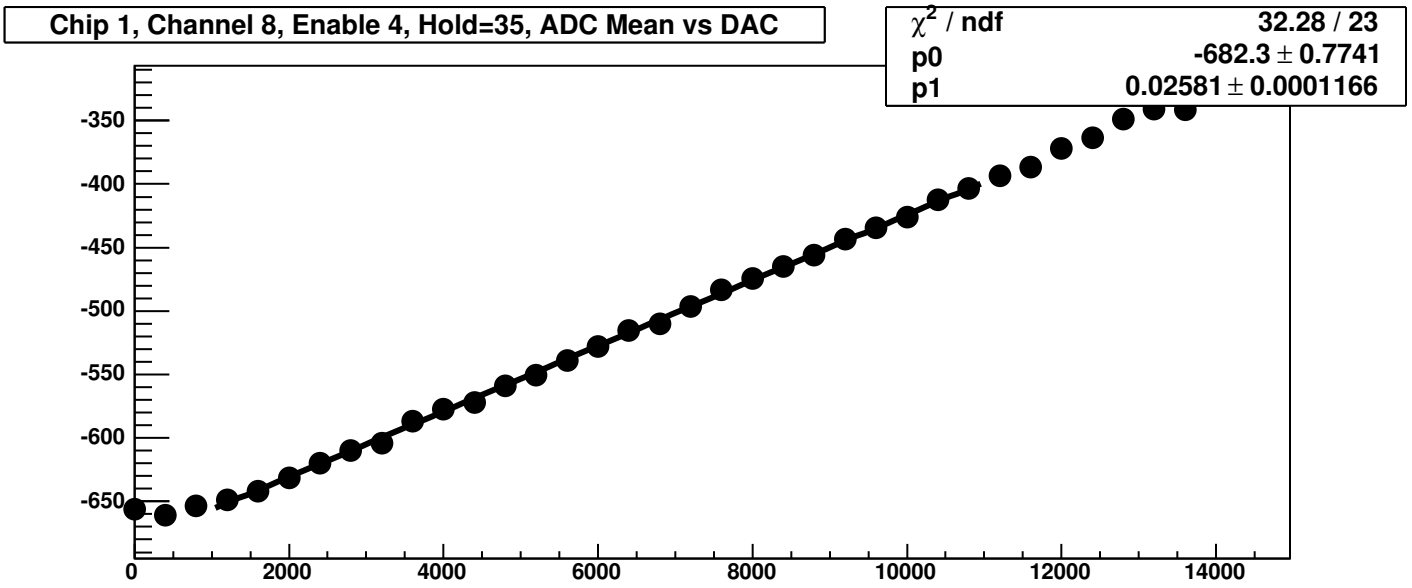

Chip 1, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

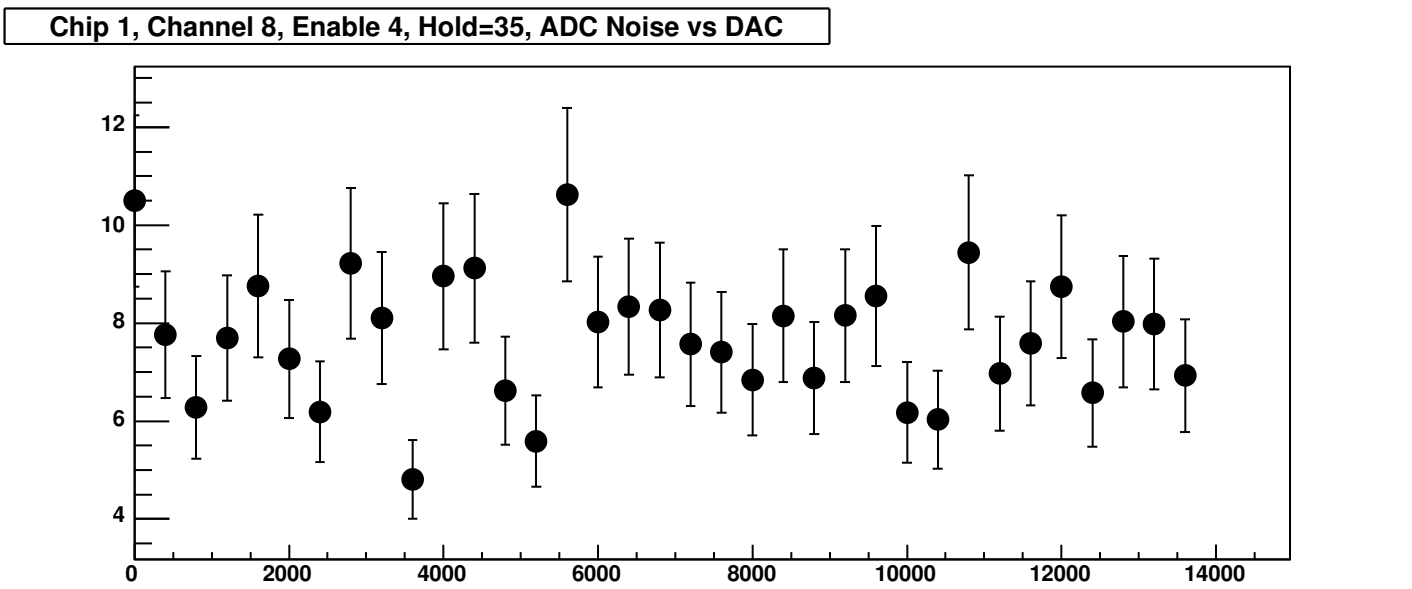

Chip 1, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

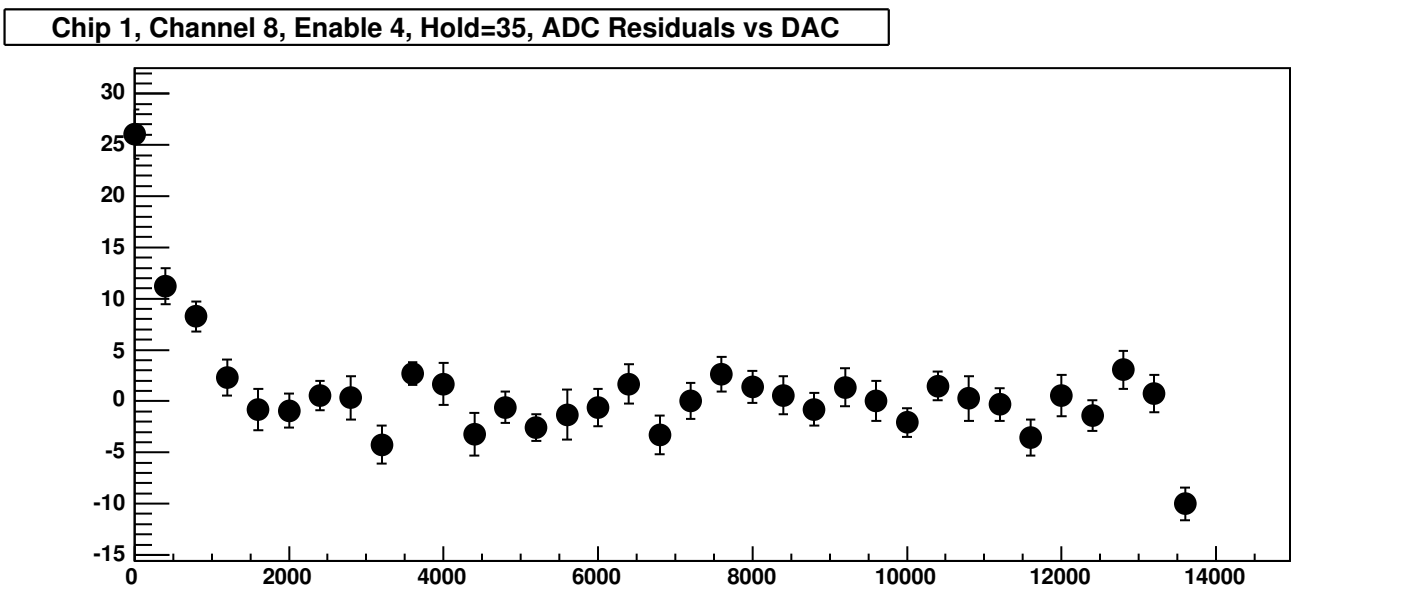

Chip 1, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 71.53 / 23
p0 -686 ± 0.7921
p1 0.02673 ± 0.000121
-300
-350
-400
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

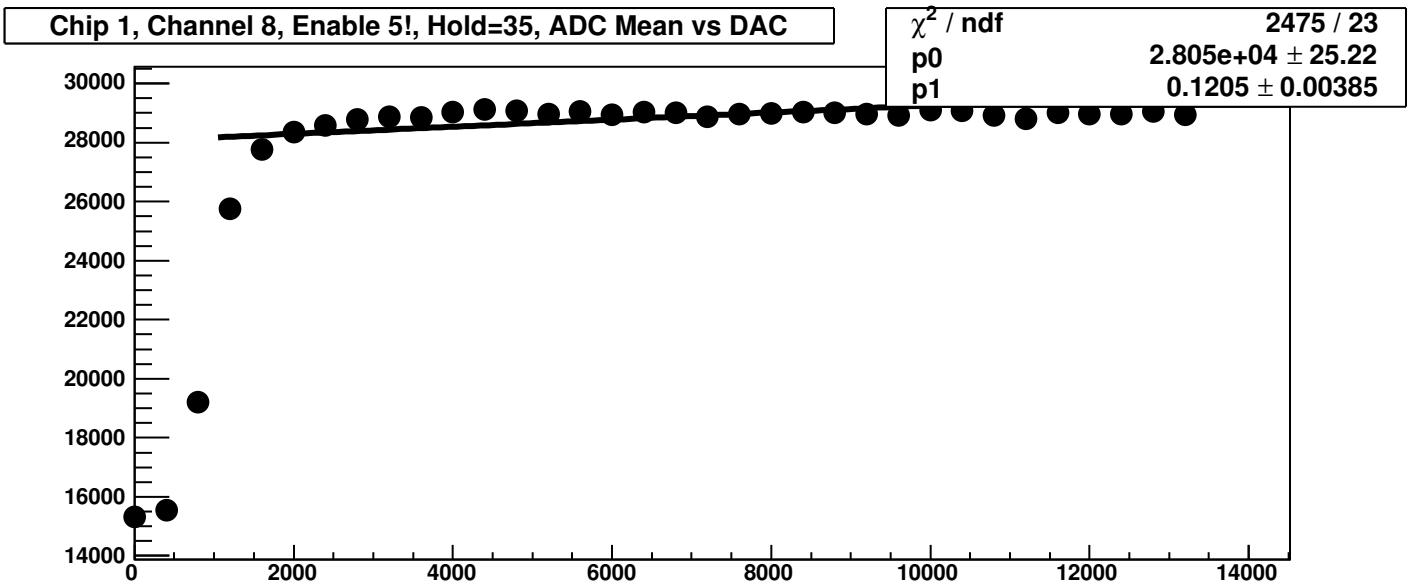

Chip 1, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
14
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

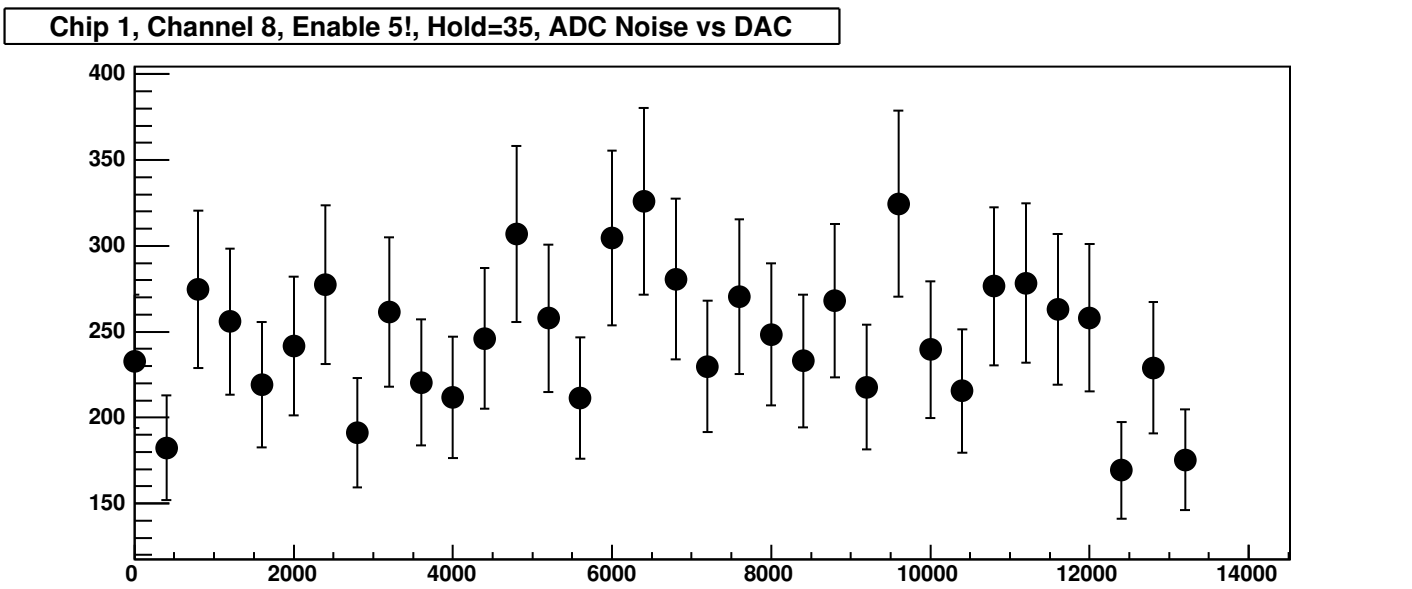

Chip 1, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

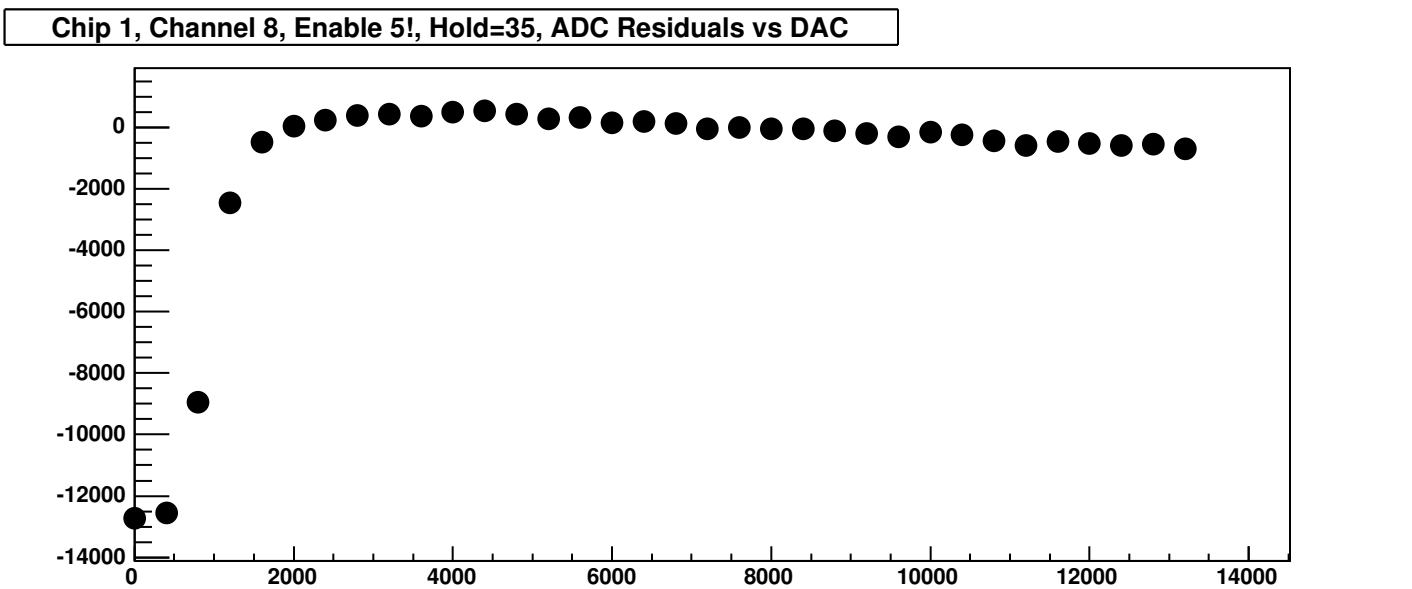

Chip 1, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 32.28 / 23
p0 -682.3 ± 0.7741
p1 0.02581 ± 0.0001166
-350
-400
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

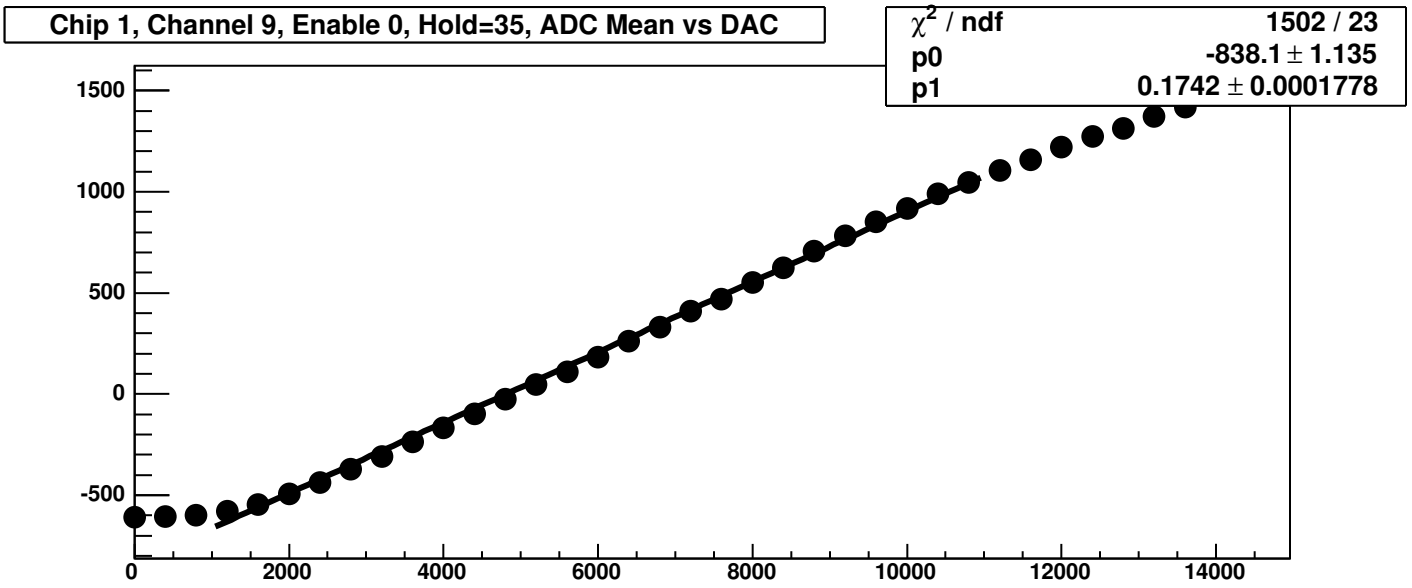

Chip 1, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

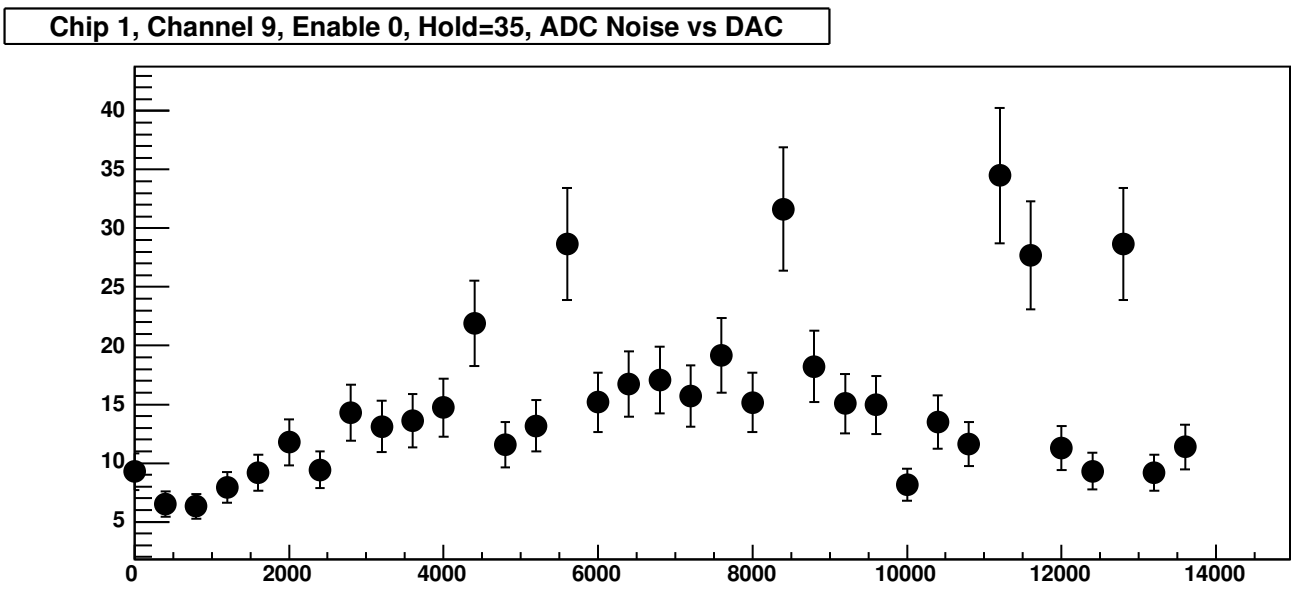

Chip 1, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

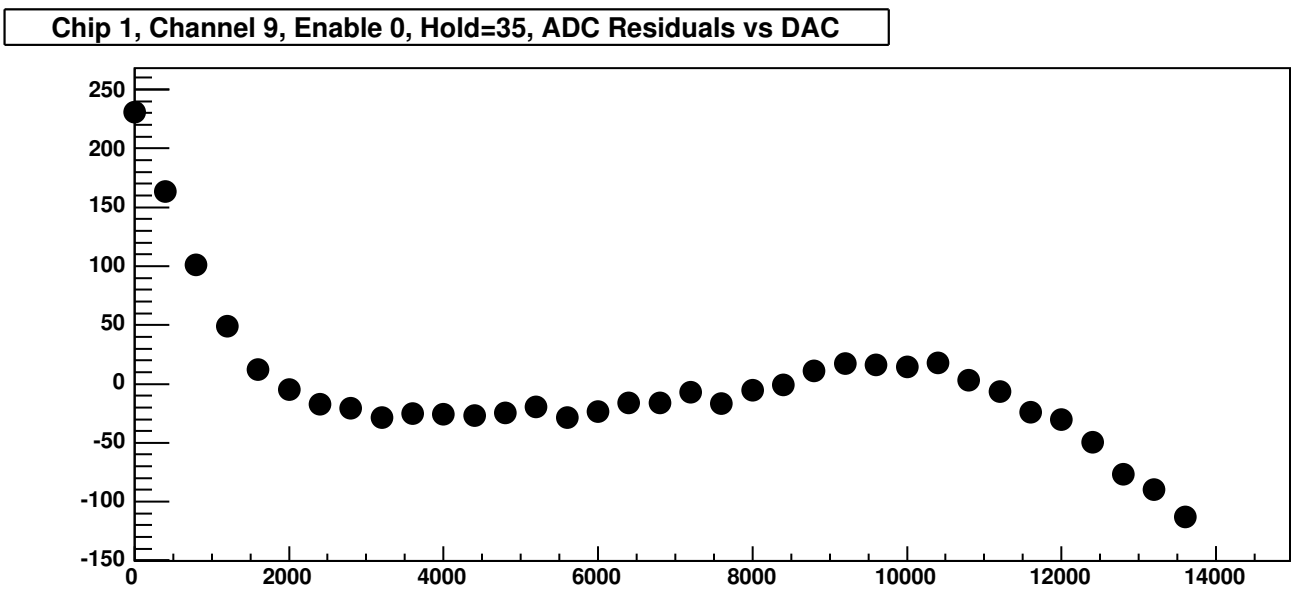

Chip 1, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2475 / 23
p0 2.805e+04 ± 25.22
p1 0.1205 ± 0.00385
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

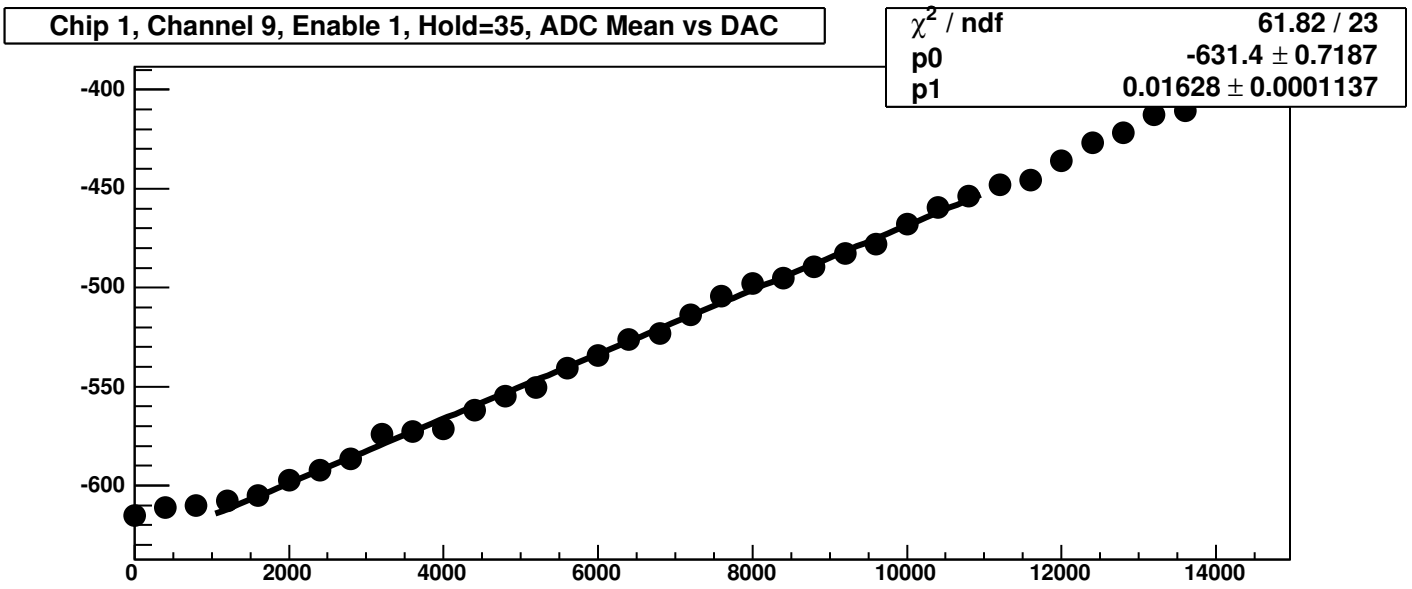

Chip 1, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

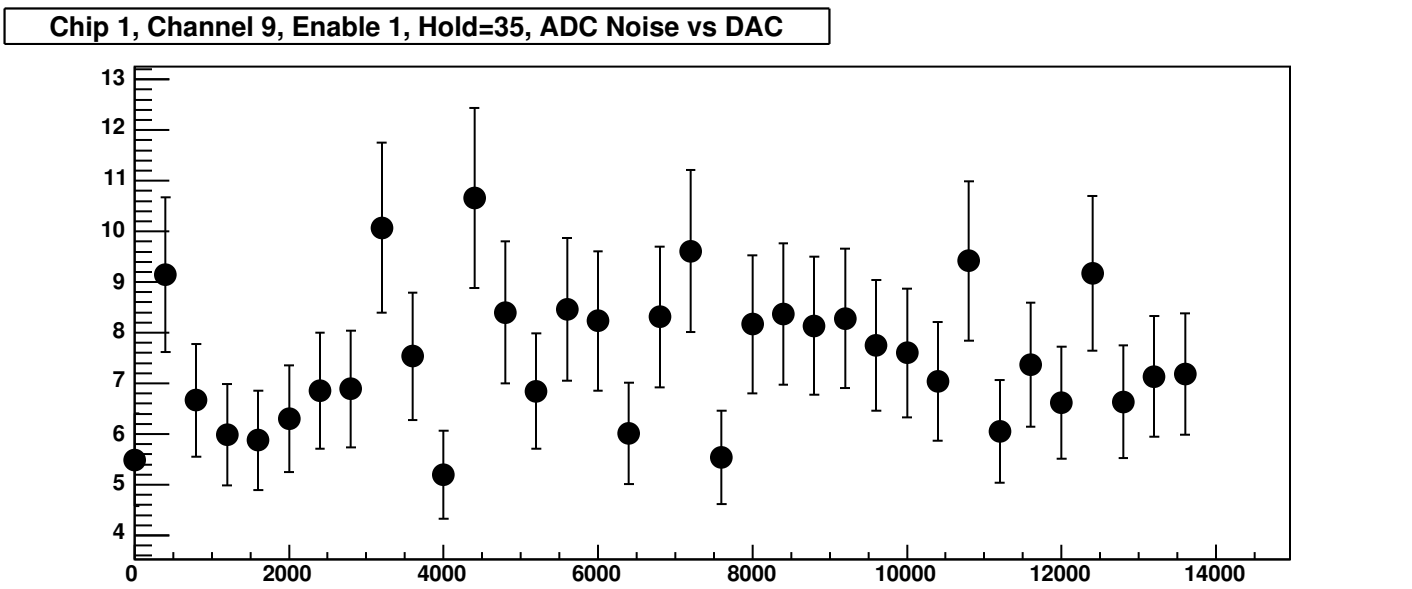

Chip 1, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

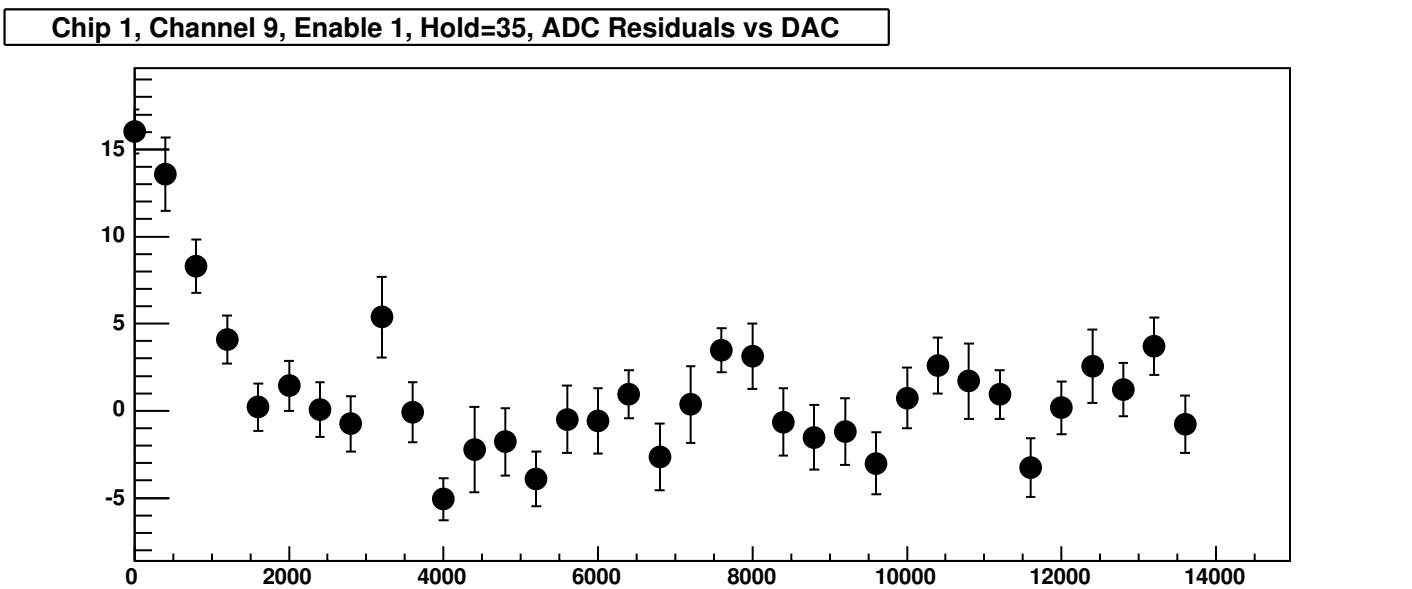

Chip 1, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1502 / 23
p0 -838.1 ± 1.135
p1 0.1742 ± 0.0001778
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

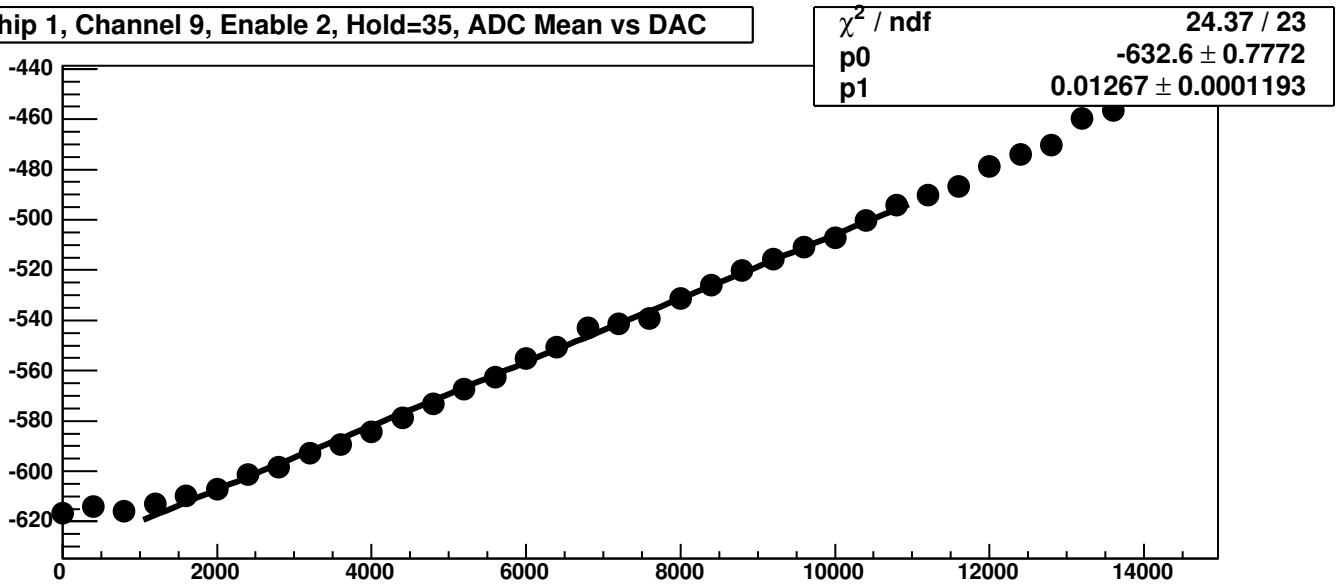

Chip 1, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 61.82 / 23
p0 -631.4 ± 0.7187
p1 0.01628 ± 0.0001137
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

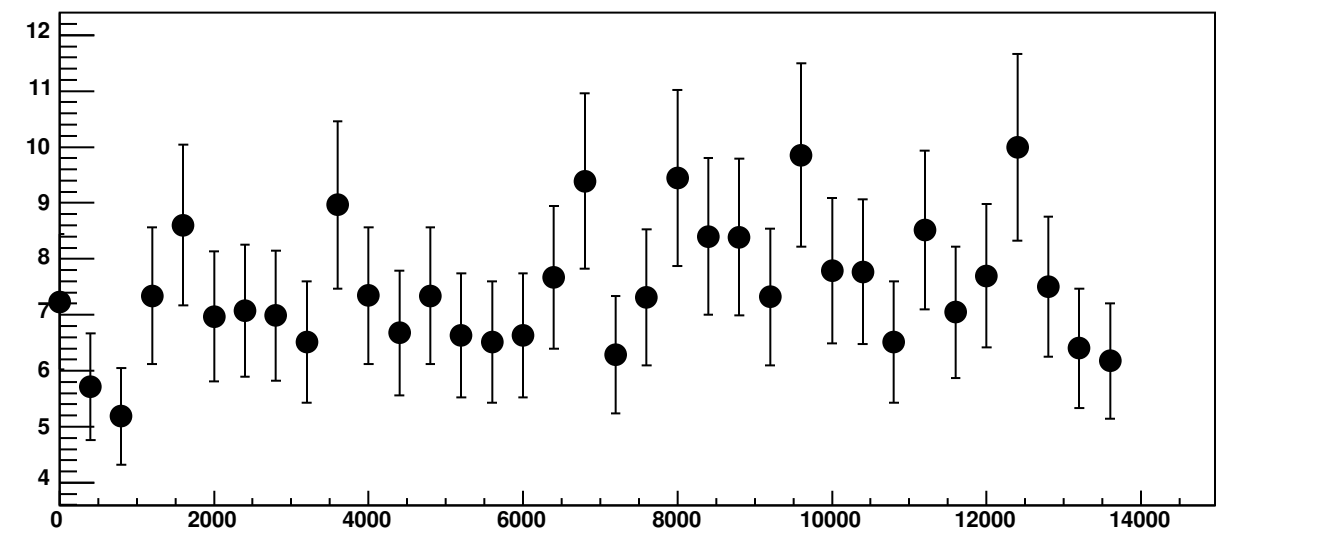

Chip 1, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

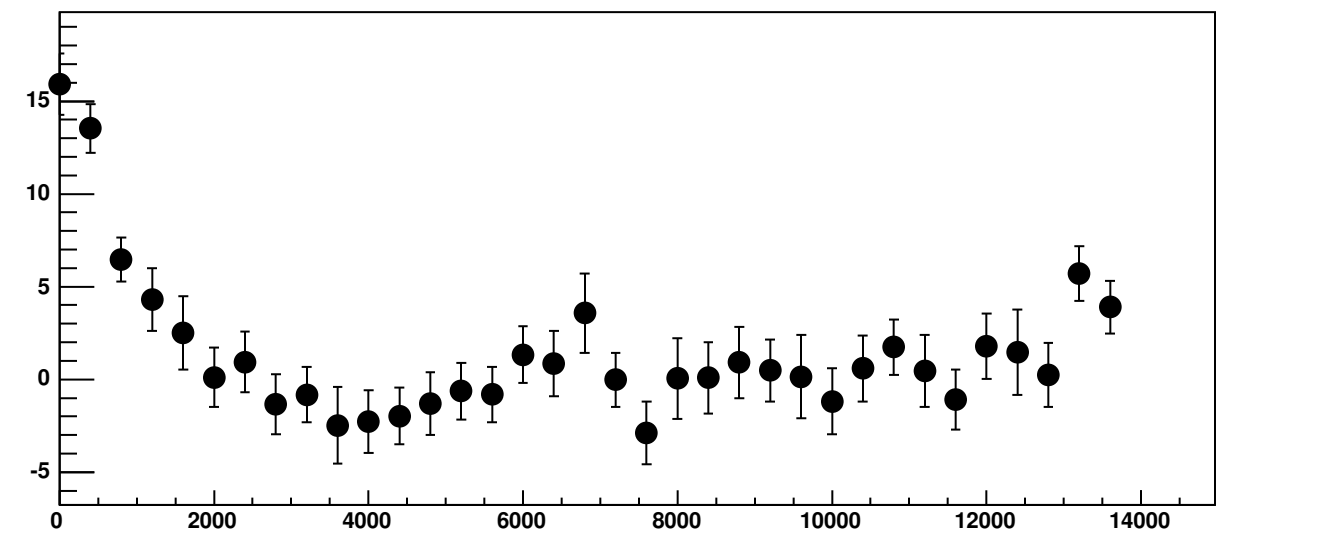
Chip 1, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

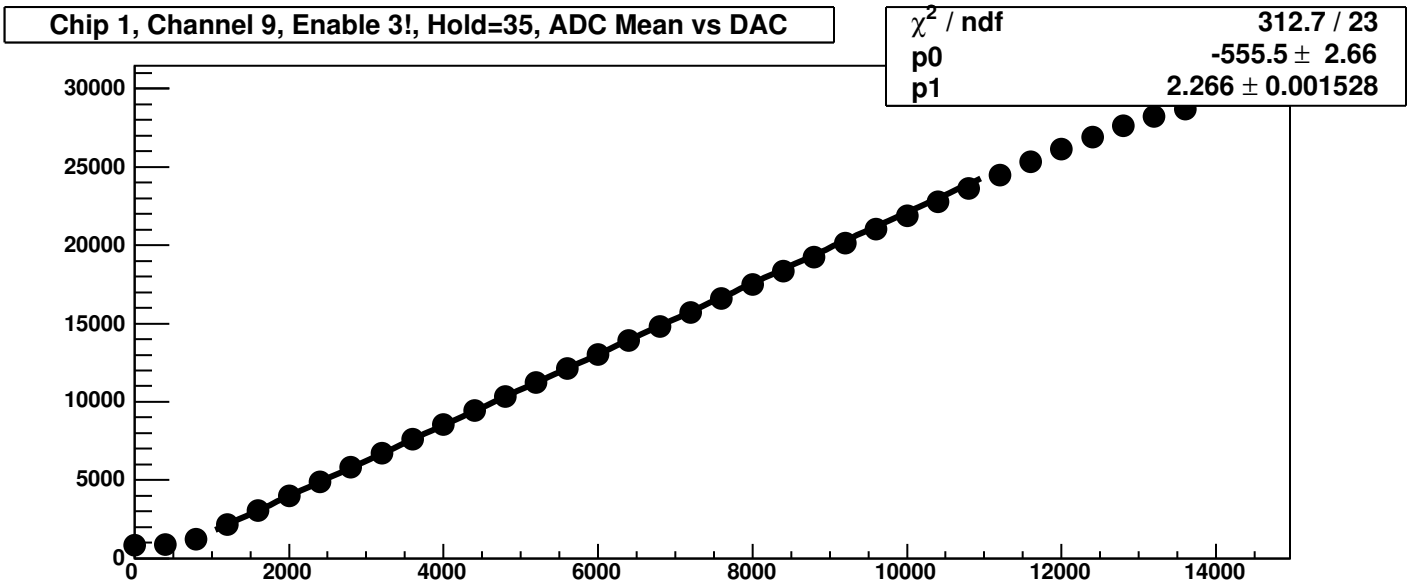


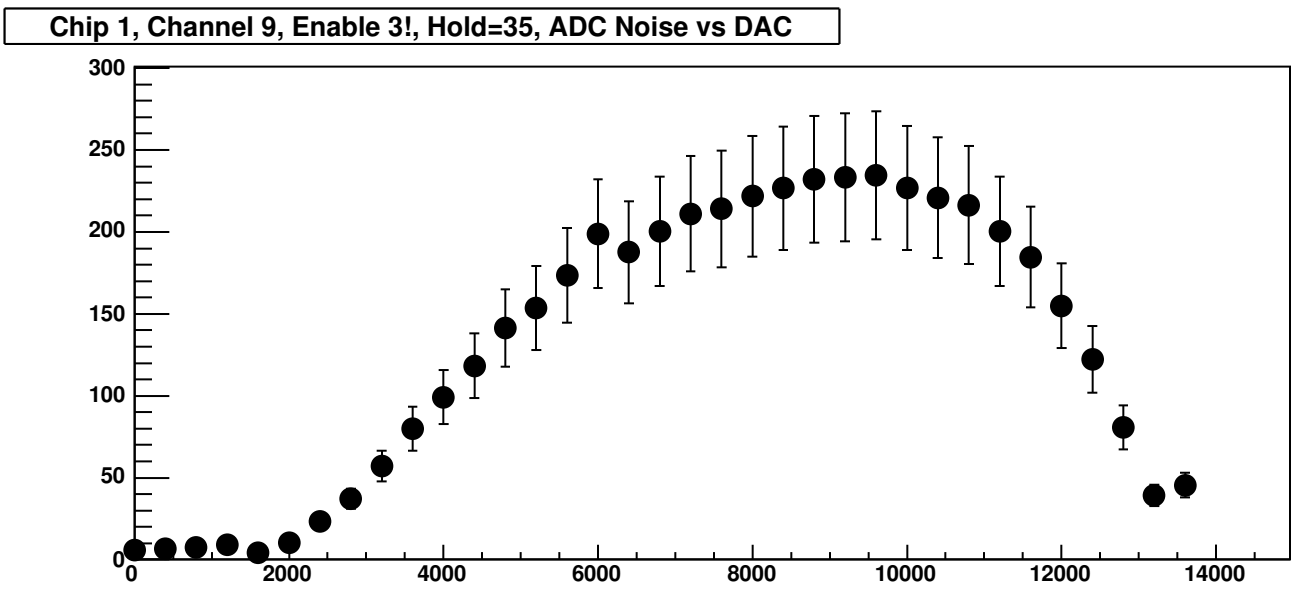
Chip 1, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC

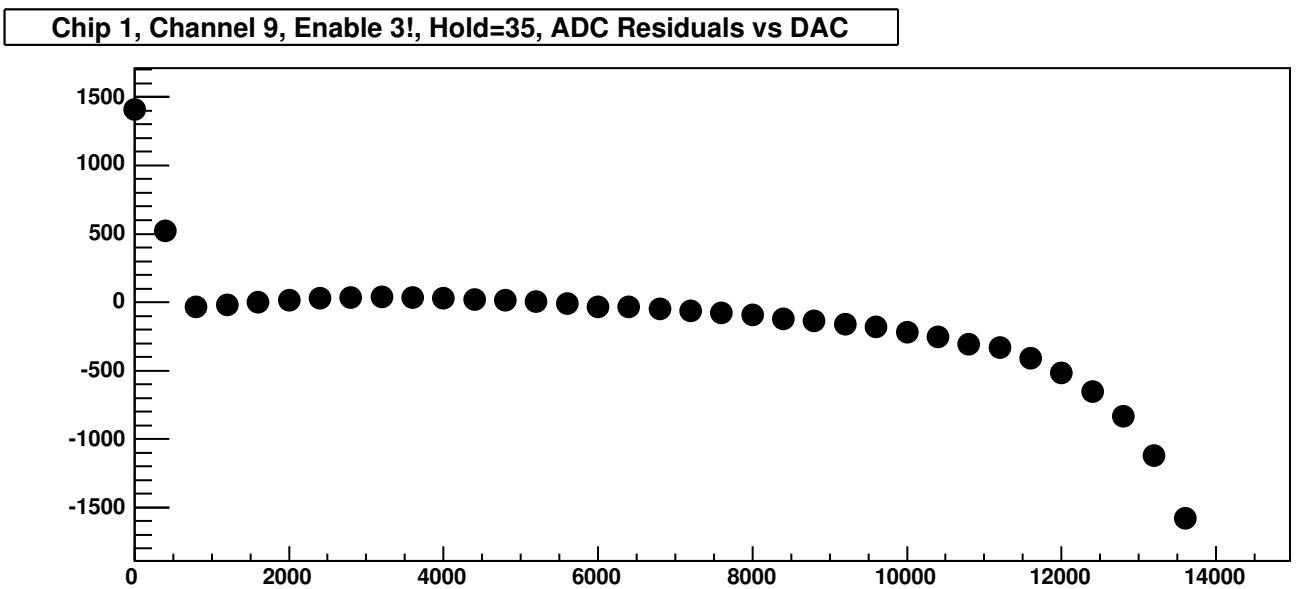


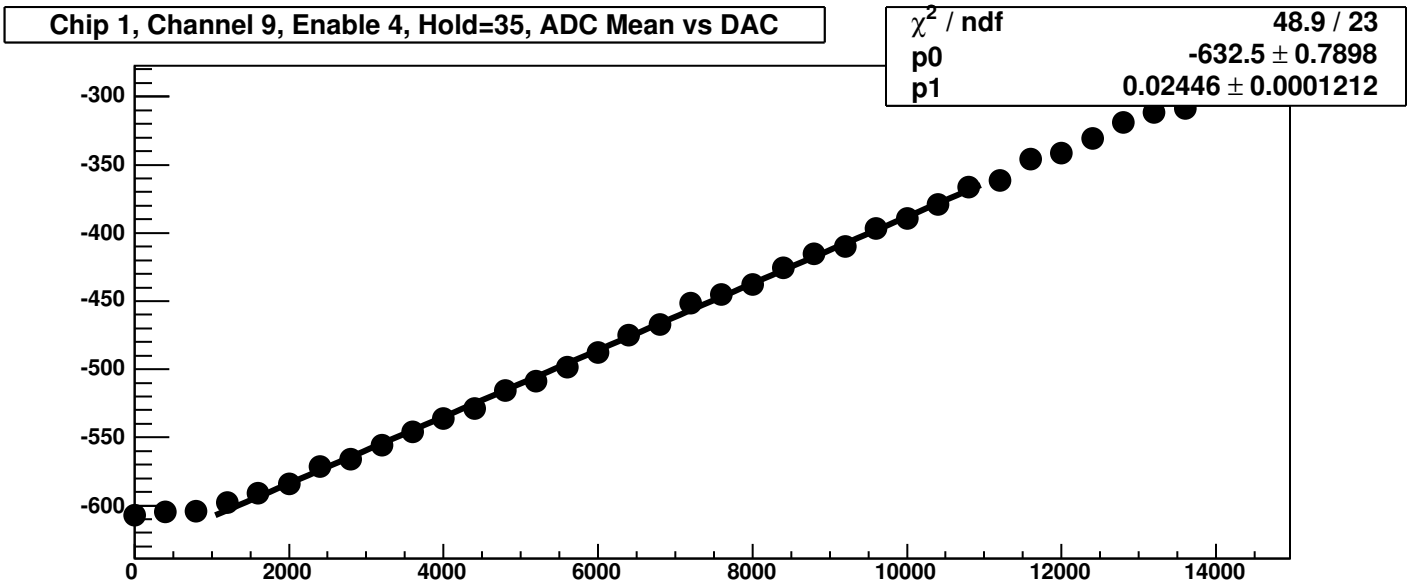
Chip 1, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

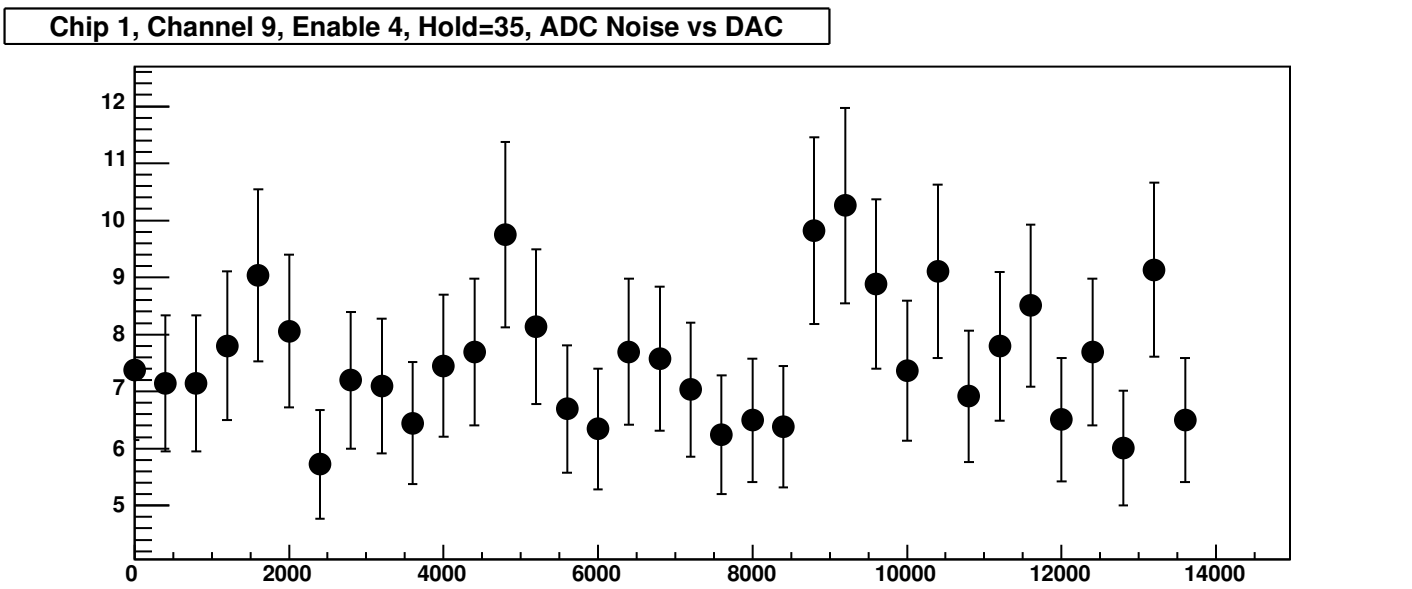


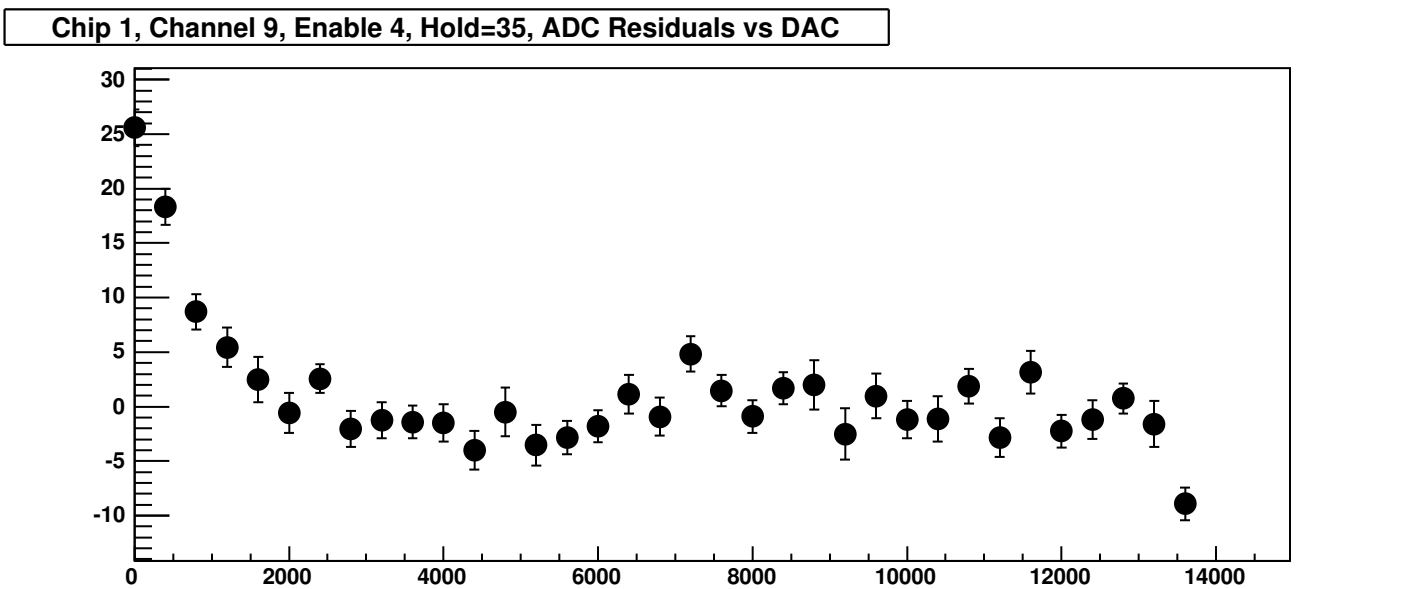

Chip 1, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 312.7 / 23
p0 -555.5 ± 2.66
p1 2.266 ± 0.001528
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

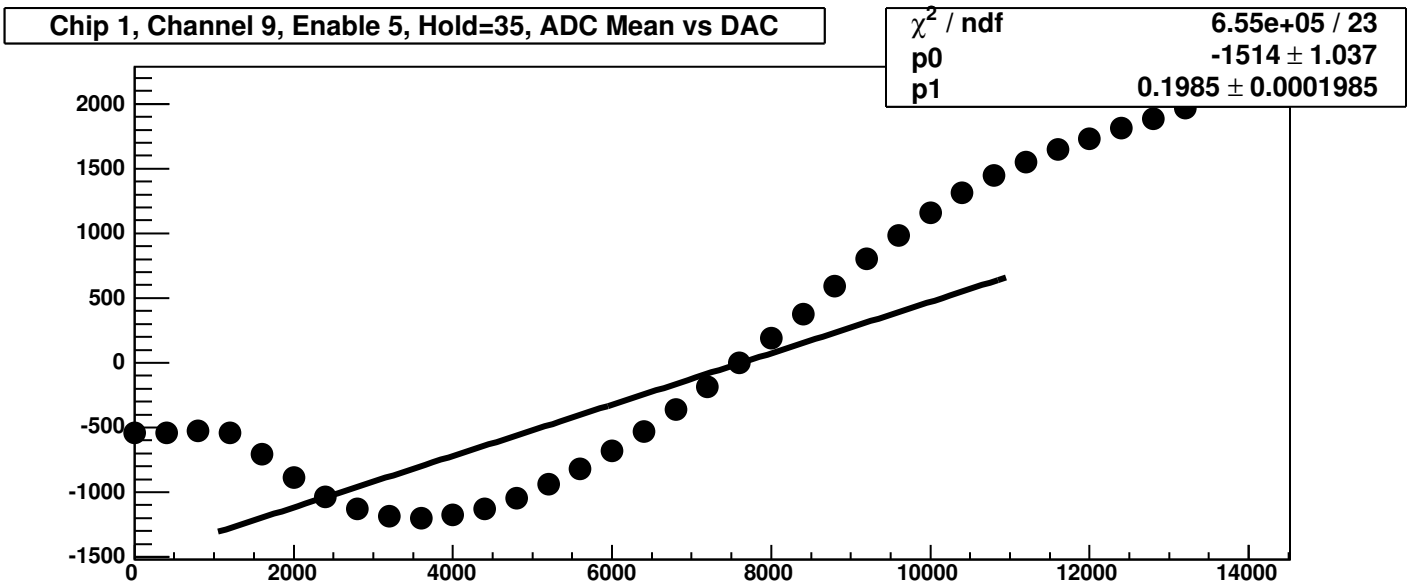

Chip 1, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

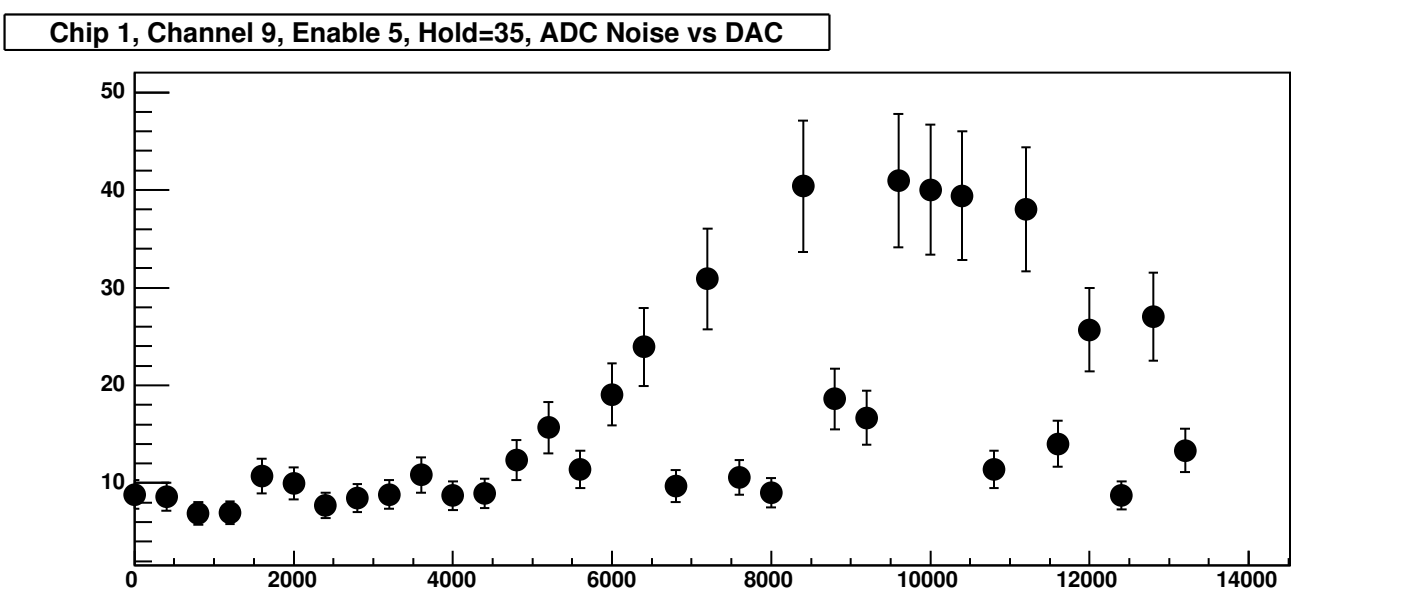

Chip 1, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

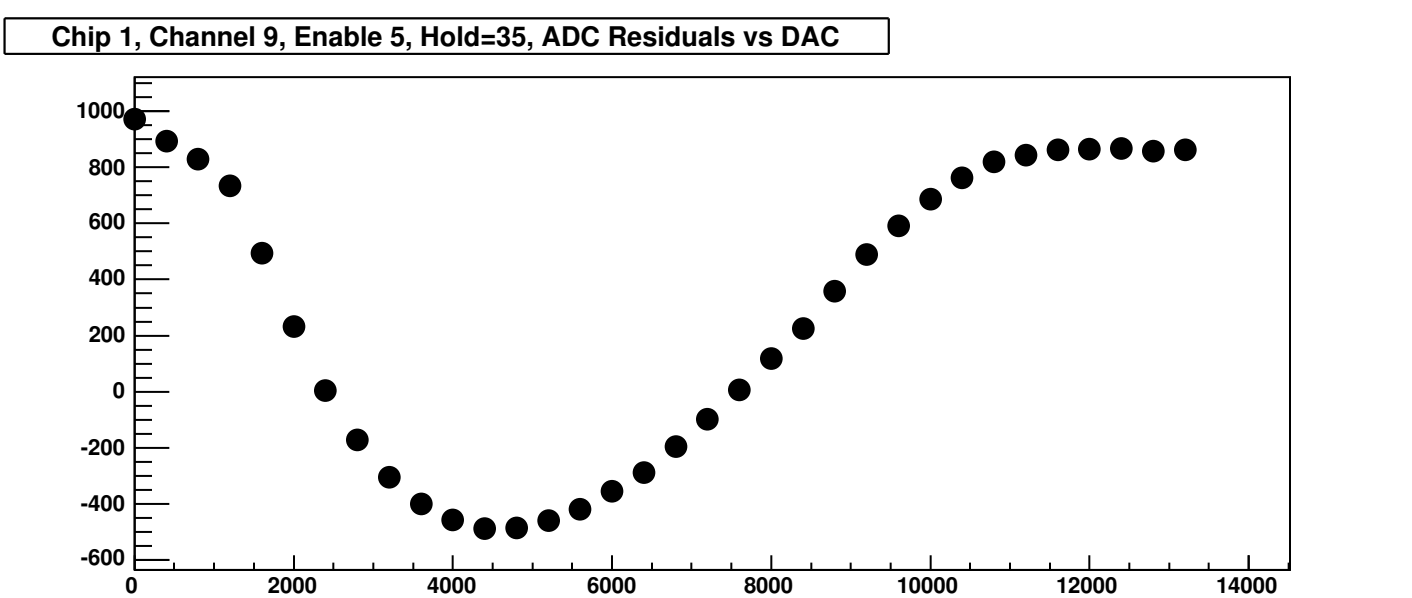

Chip 1, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 48.9 / 23
p0 -632.5 ± 0.7898
p1 0.02446 ± 0.0001212
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

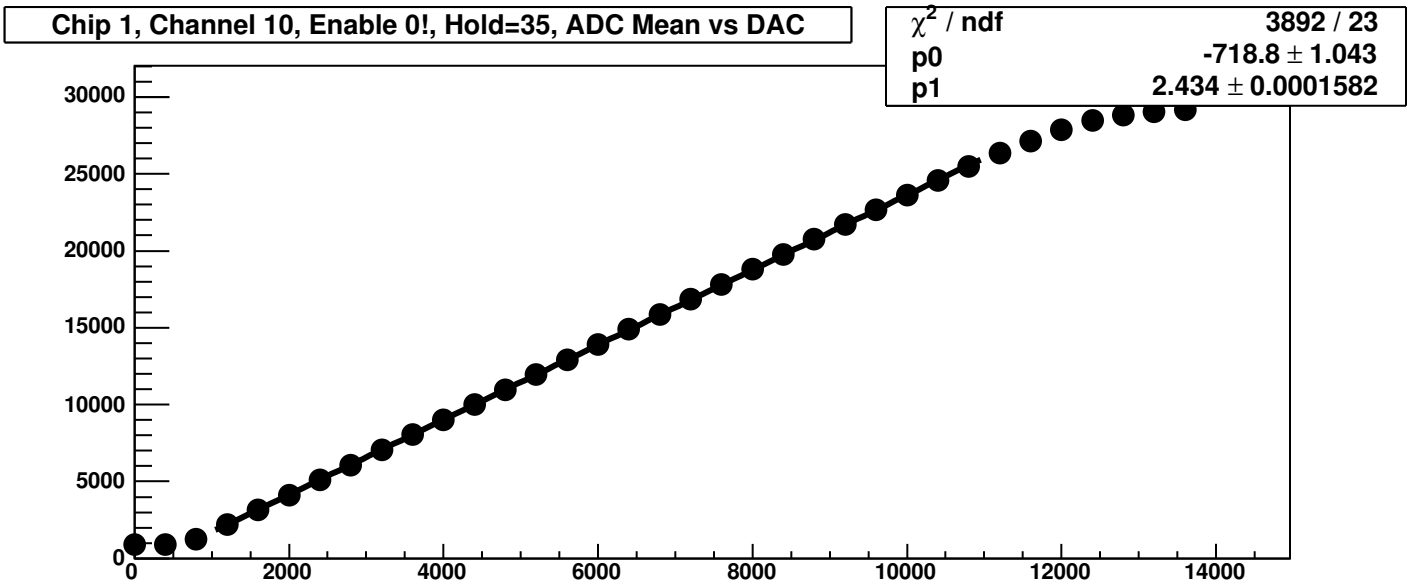

Chip 1, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

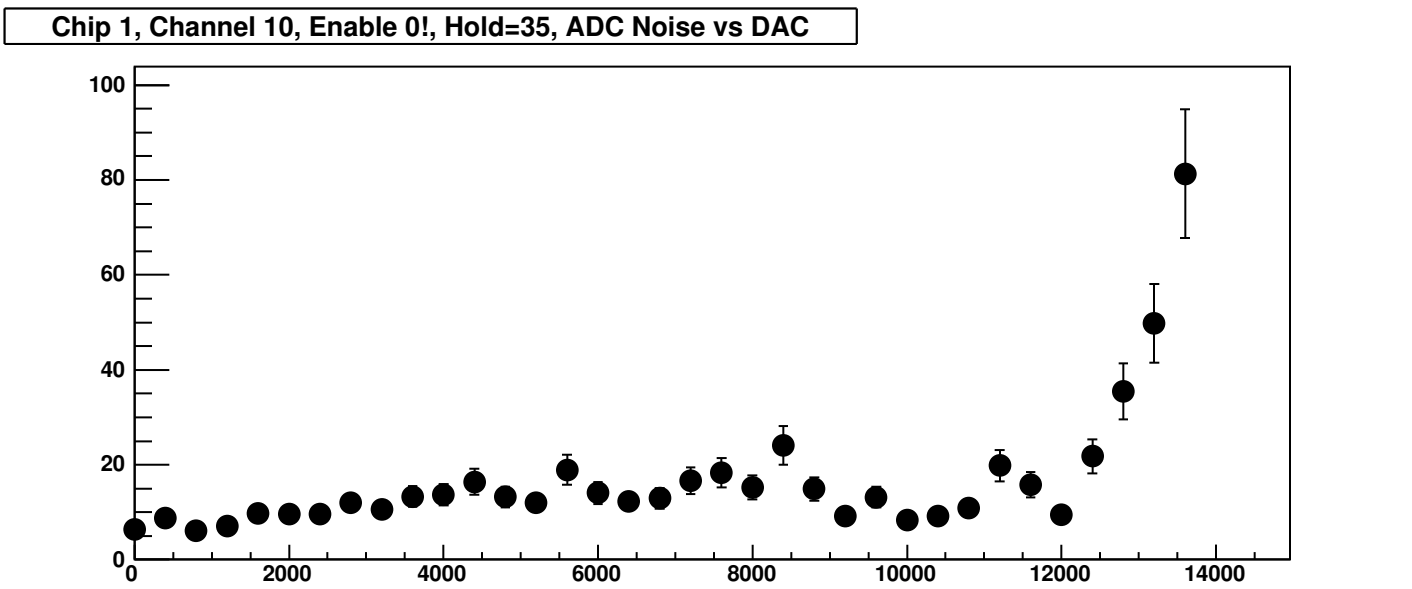

Chip 1, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

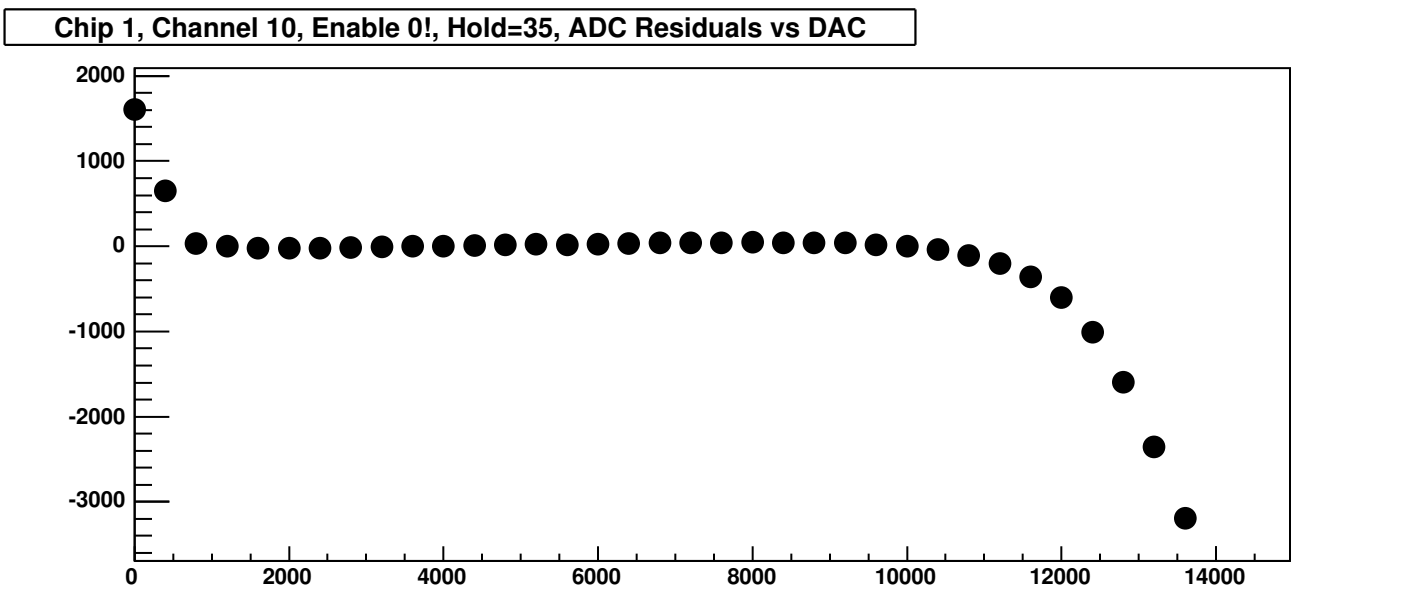

Chip 1, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 6.55e+05 / 23
p0 -1514 ± 1.037
p1 0.1985 ± 0.0001985
2000
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

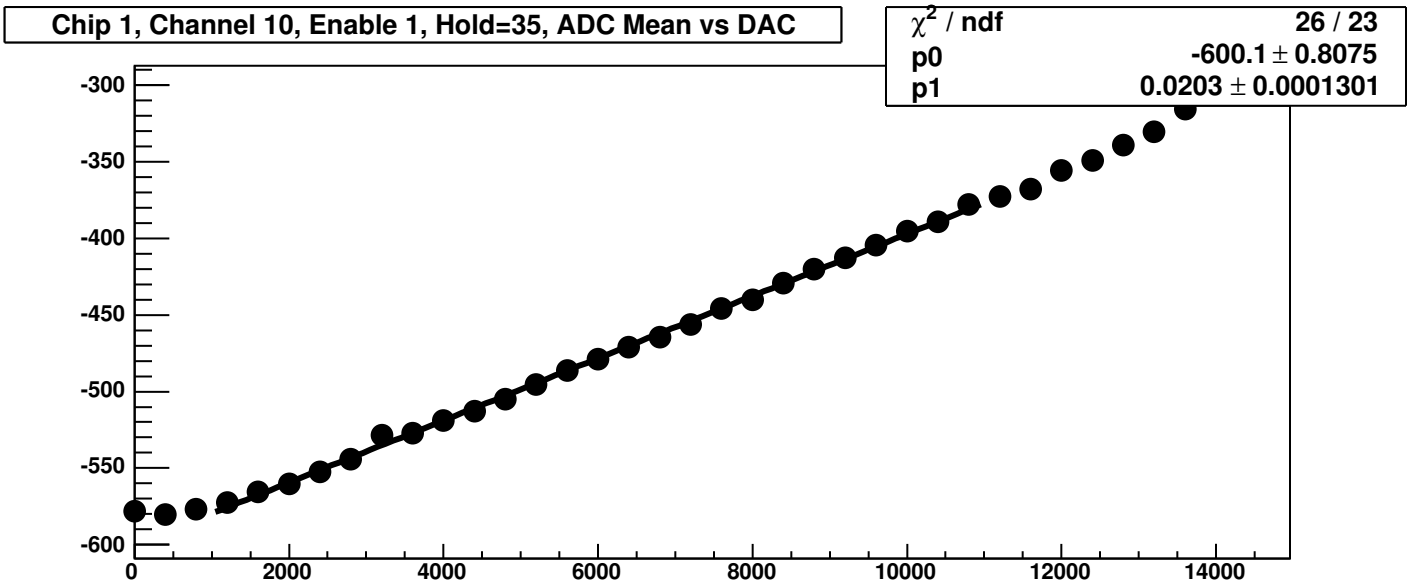

Chip 1, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

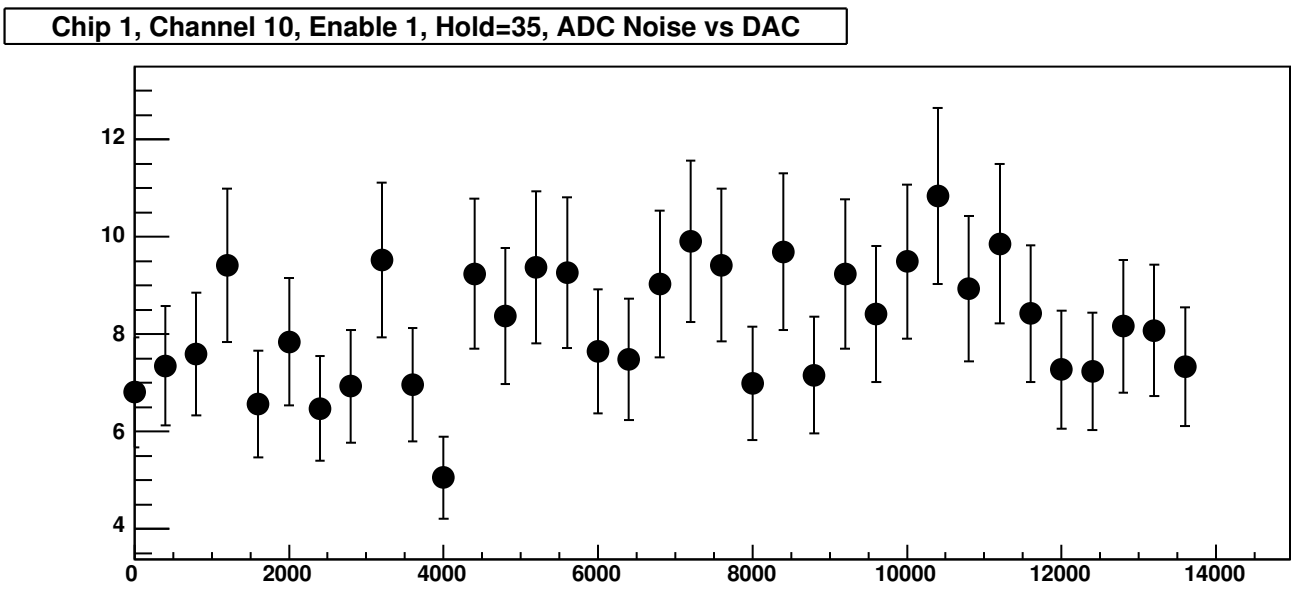

Chip 1, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

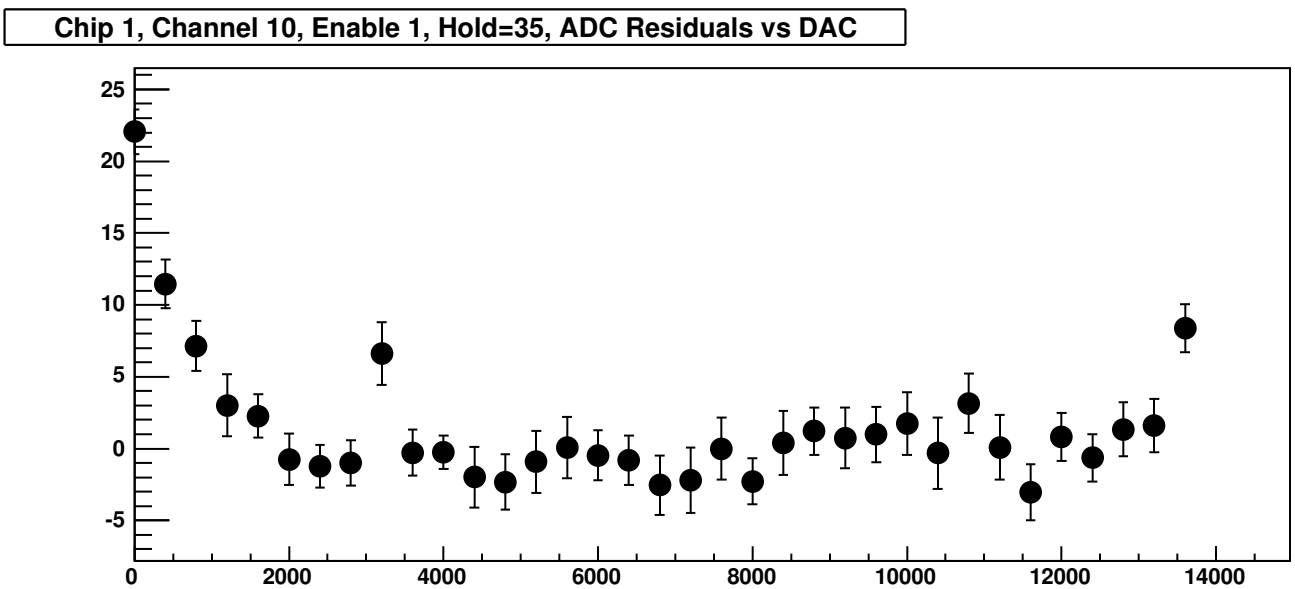

Chip 1, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 3892 / 23
p0 -718.8 ± 1.043
p1 2.434 ± 0.0001582
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

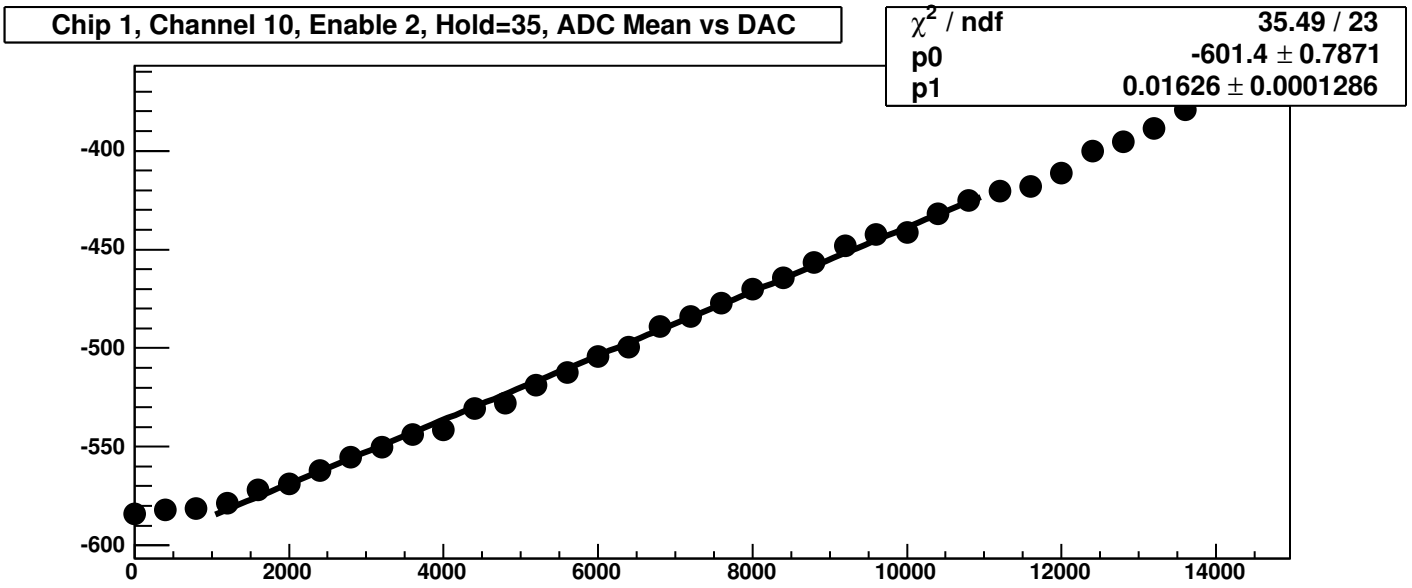

Chip 1, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

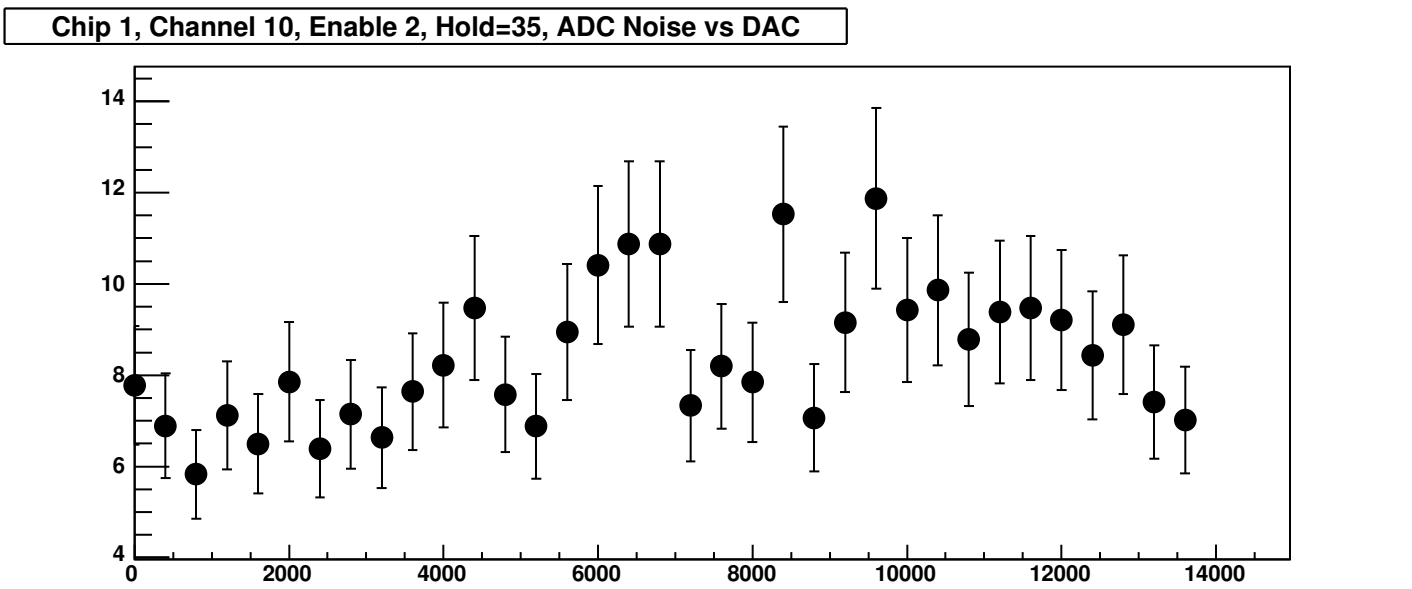

Chip 1, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

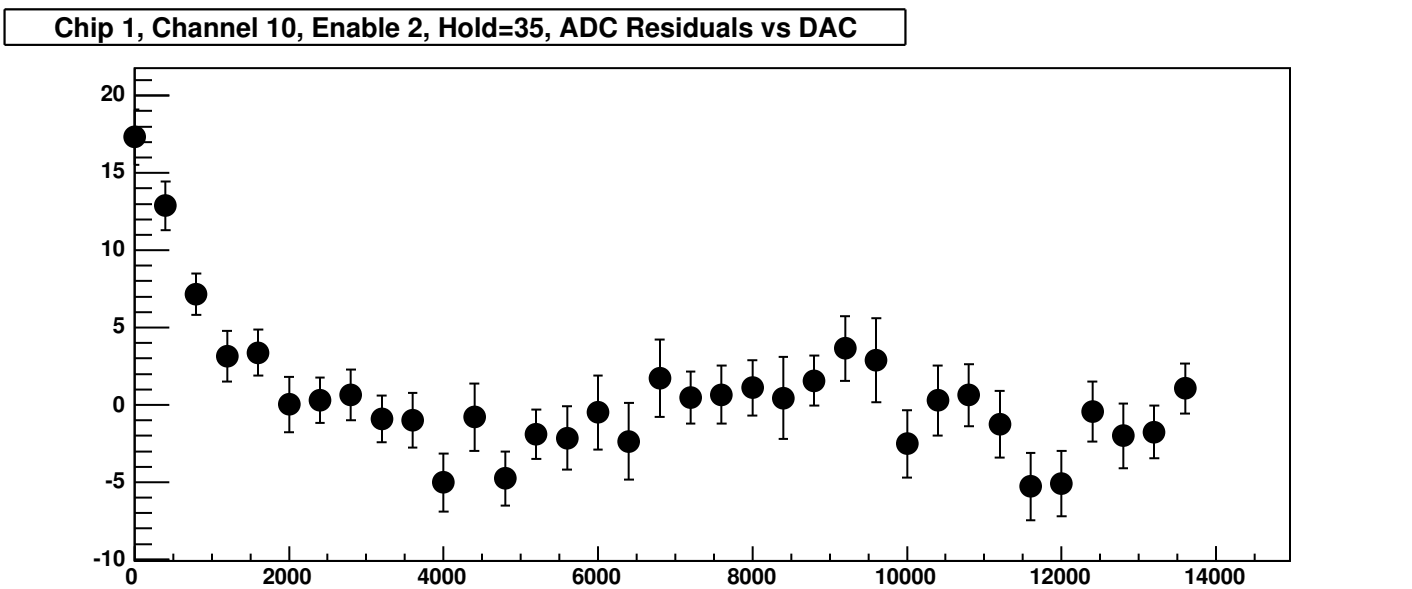

Chip 1, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 26 / 23
p0 -600.1 ± 0.8075
p1 0.0203 ± 0.0001301
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

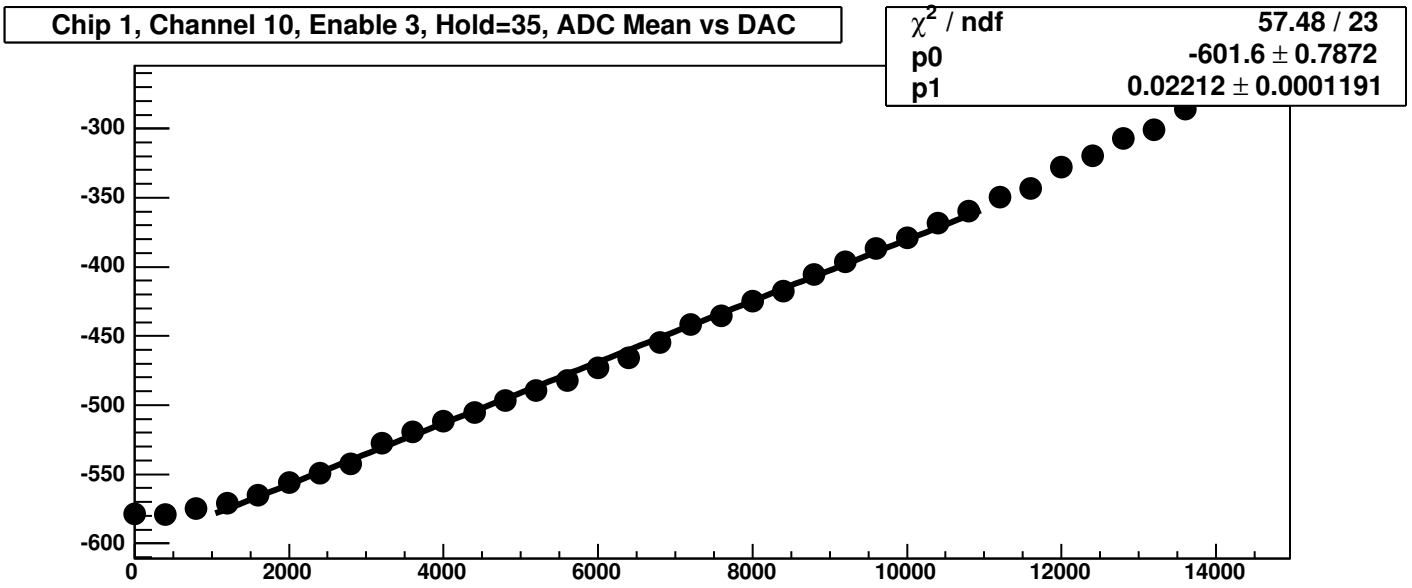

Chip 1, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

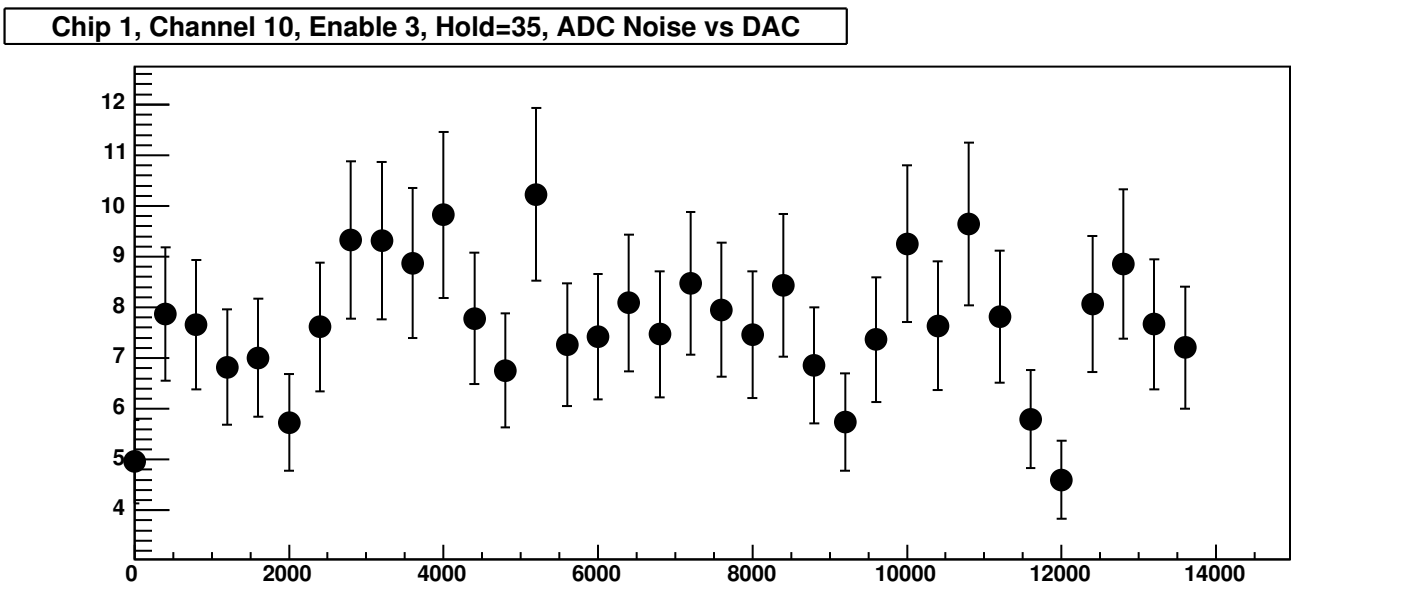

Chip 1, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

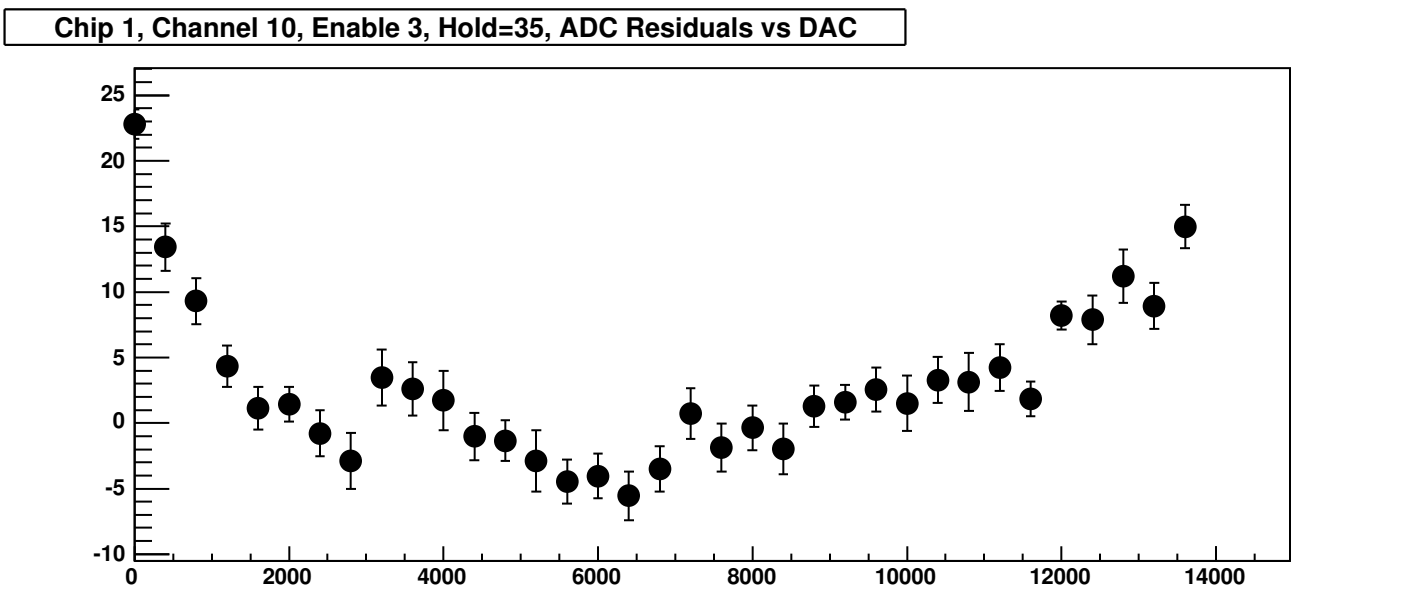

Chip 1, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 35.49 / 23
p0 -601.4 ± 0.7871
p1 0.01626 ± 0.0001286
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

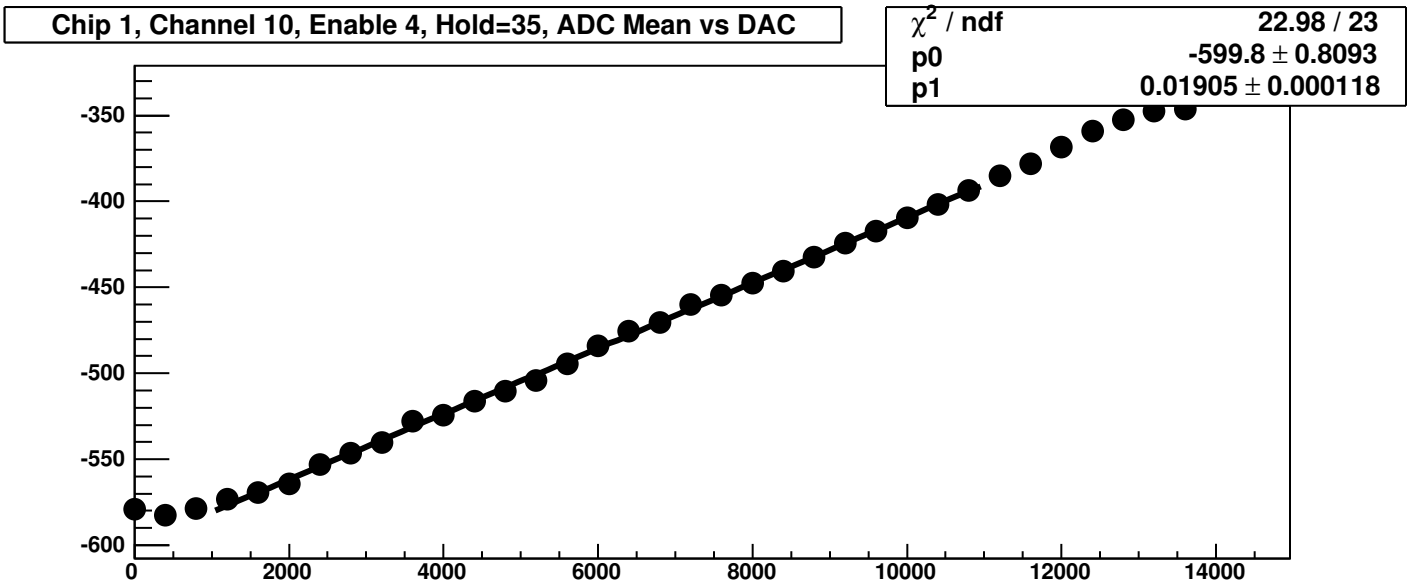

Chip 1, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

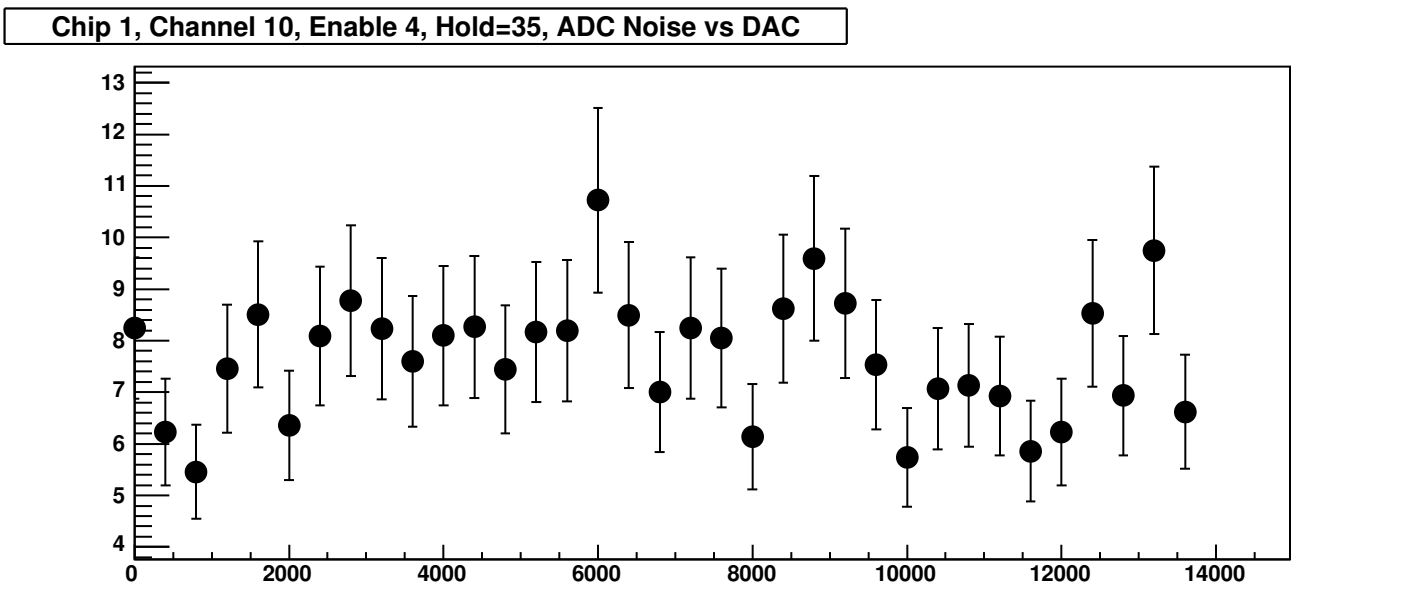

Chip 1, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

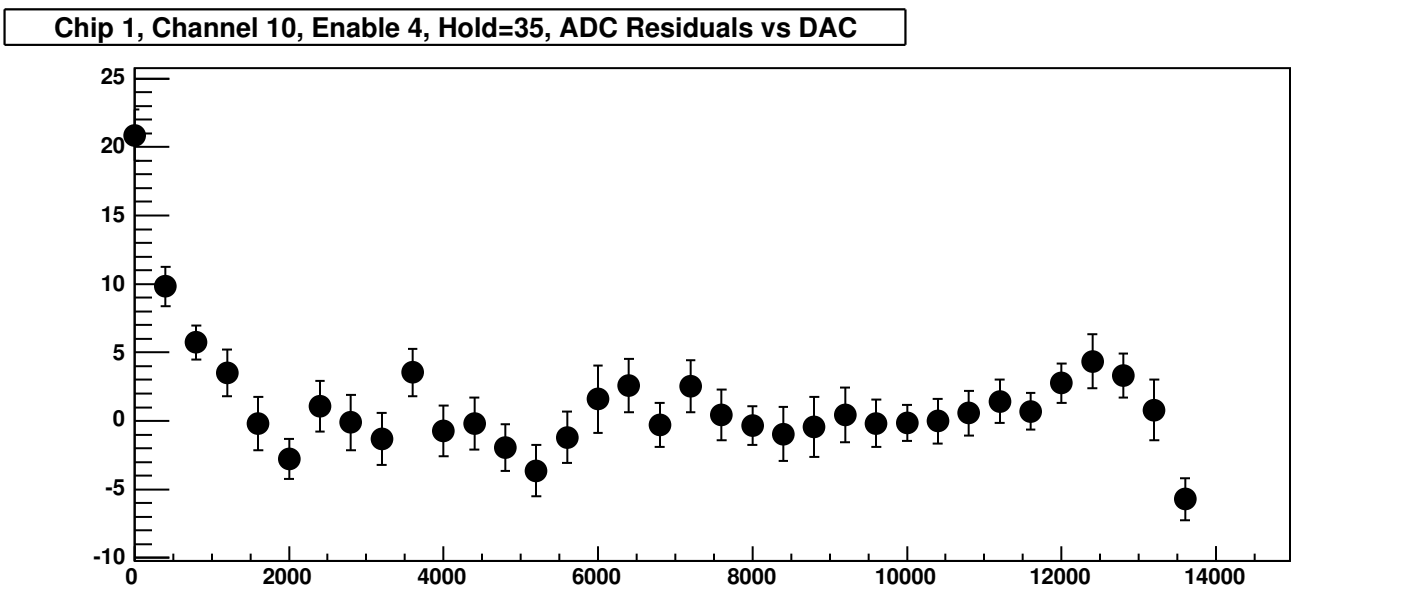

Chip 1, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 57.48 / 23
p0 -601.6 ± 0.7872
p1 0.02212 ± 0.0001191
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

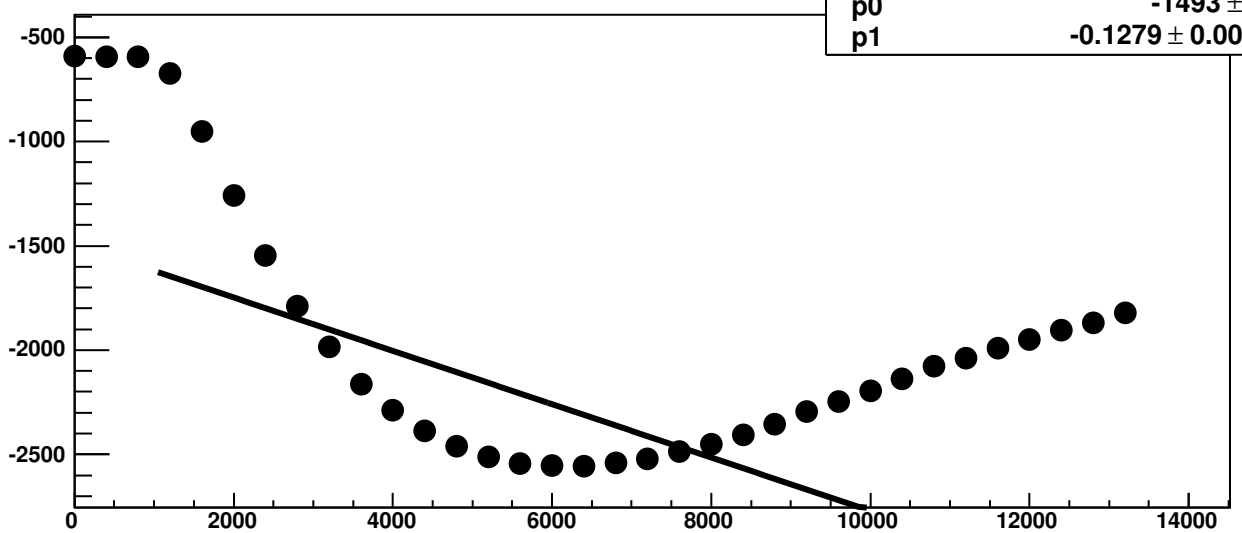

Chip 1, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 22.98 / 23
p0 -599.8 ± 0.8093
p1 0.01905 ± 0.000118
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

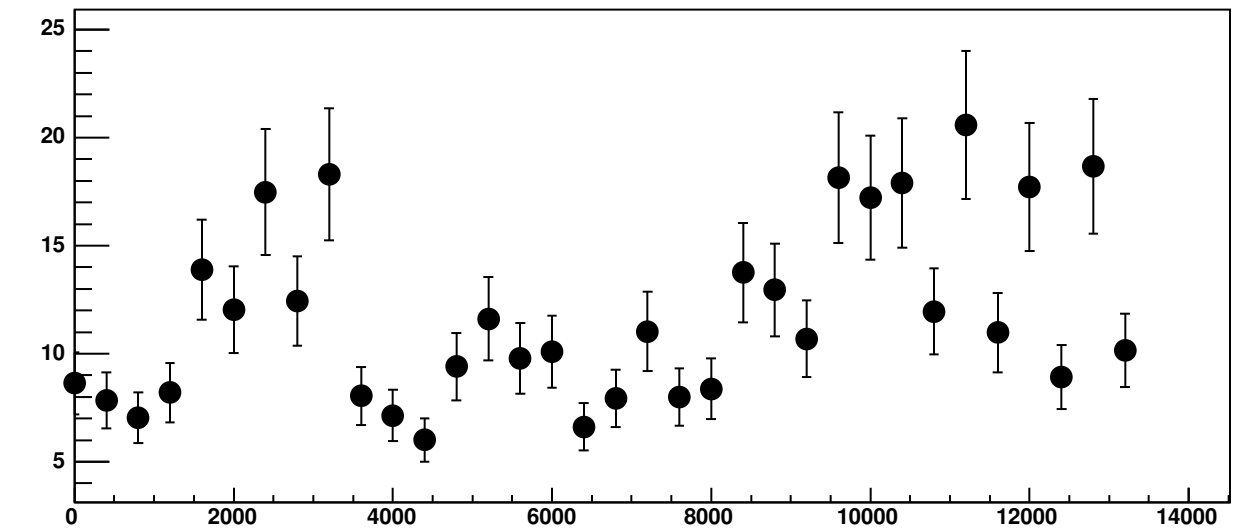

Chip 1, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

Chip 1, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

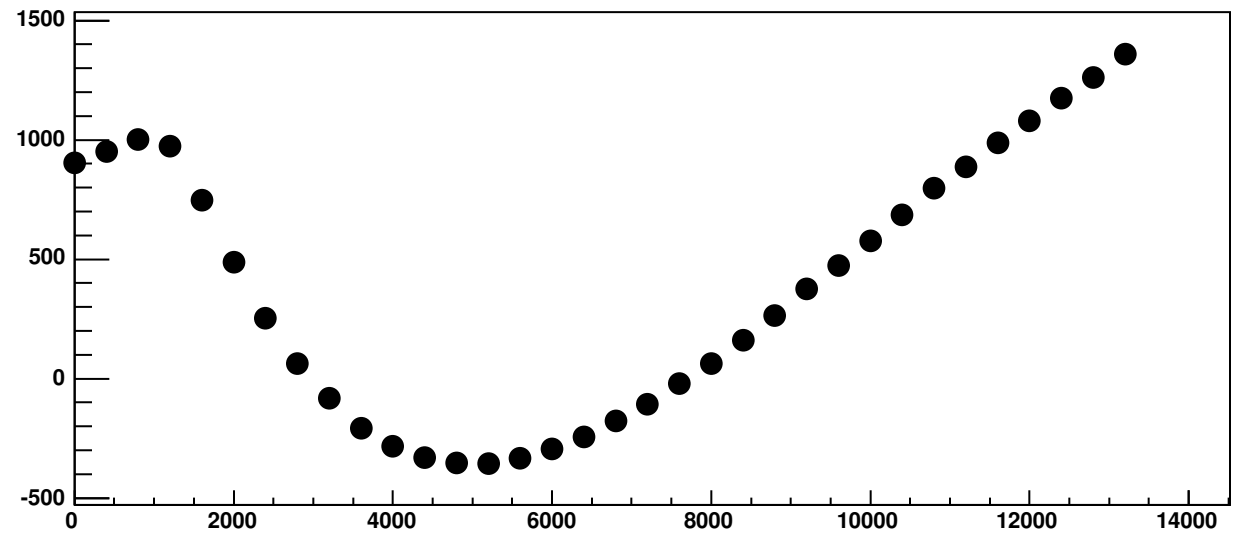
χ^2 / ndf	7.615e+05 / 23
p0	-1493 ± 1.144
p1	-0.1279 ± 0.0001862

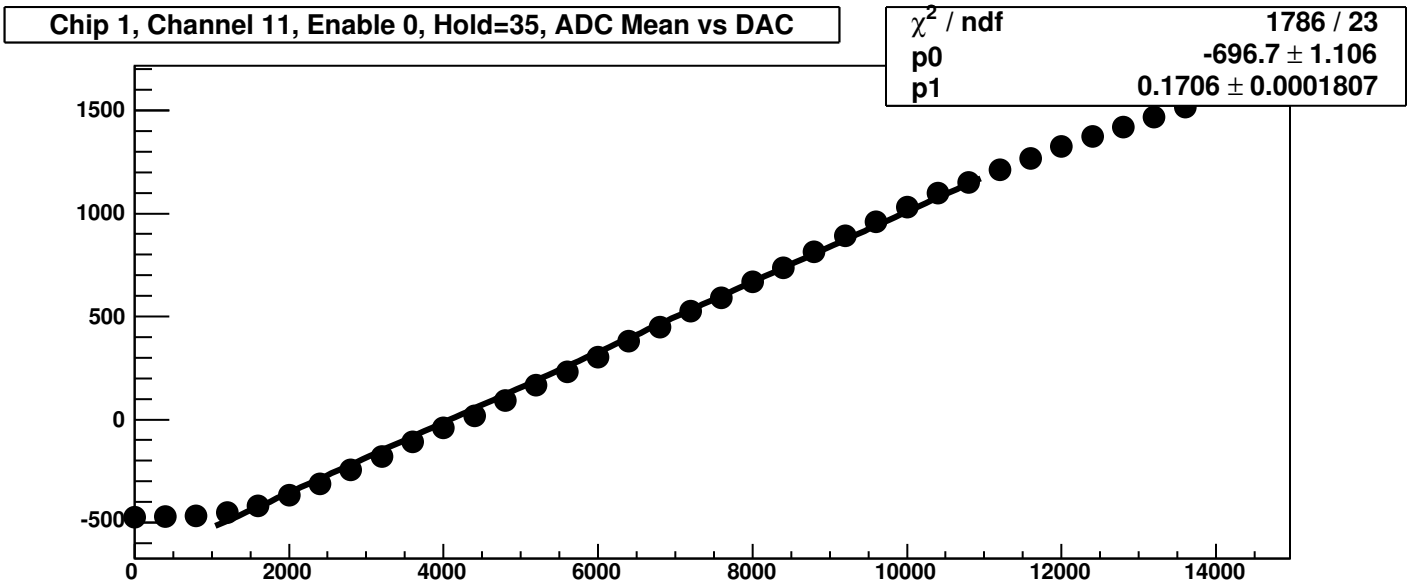


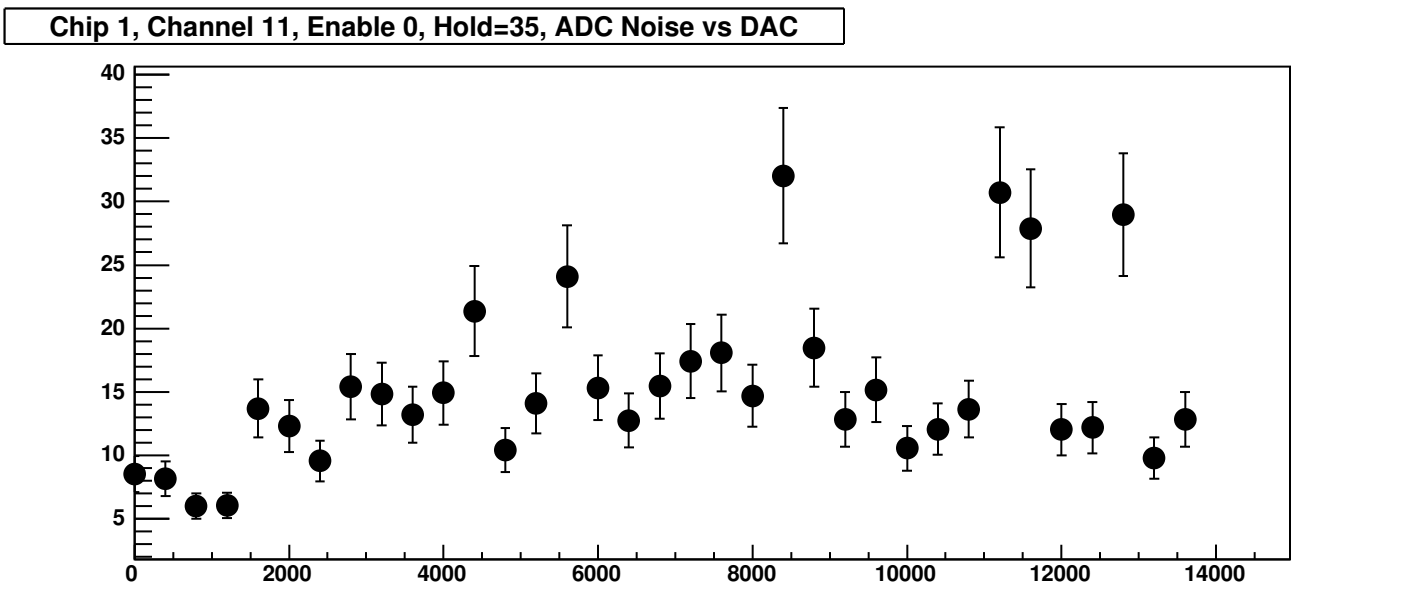
Chip 1, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

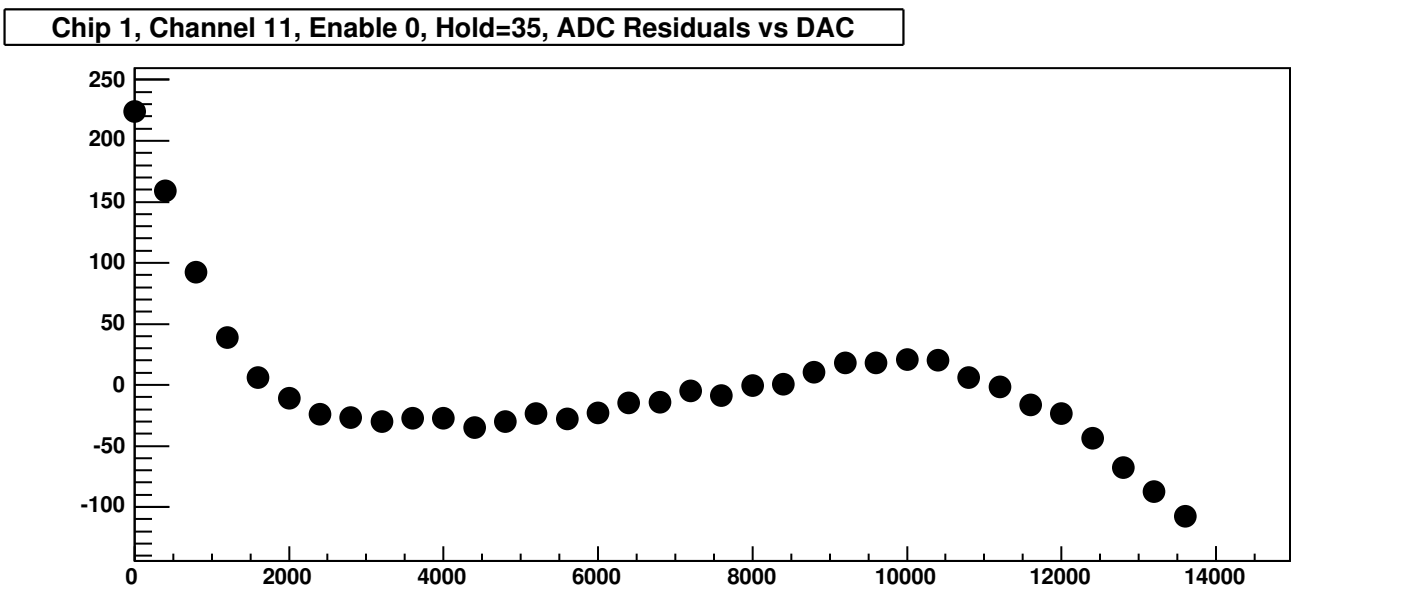


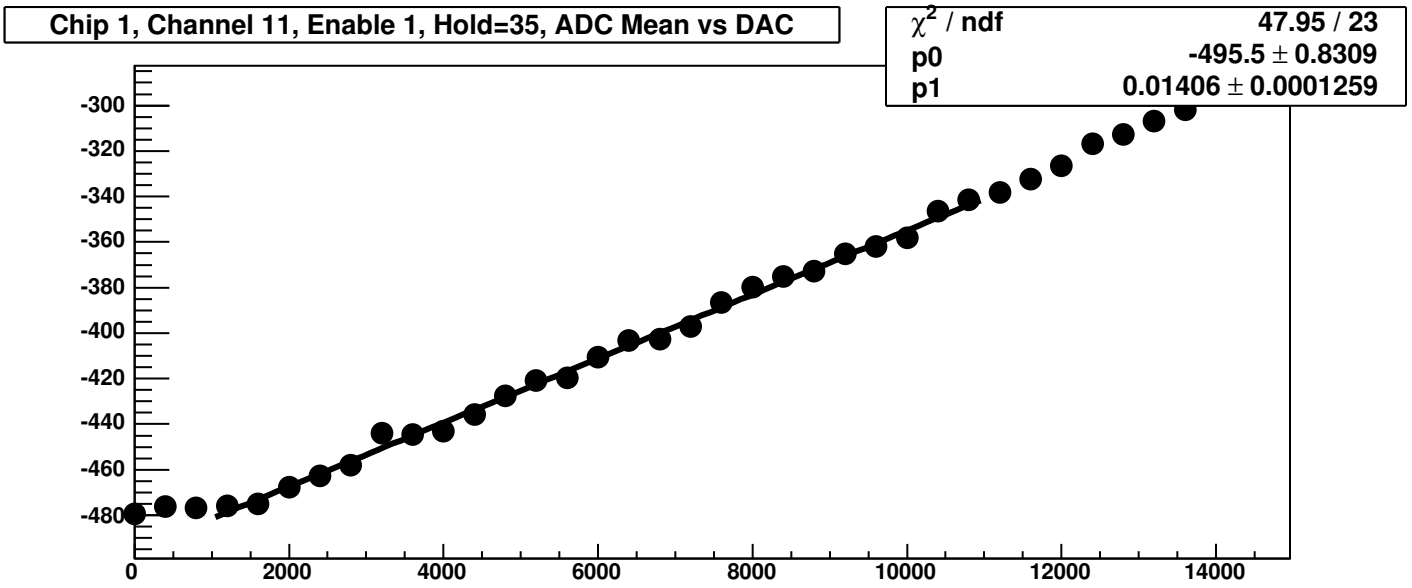
Chip 1, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

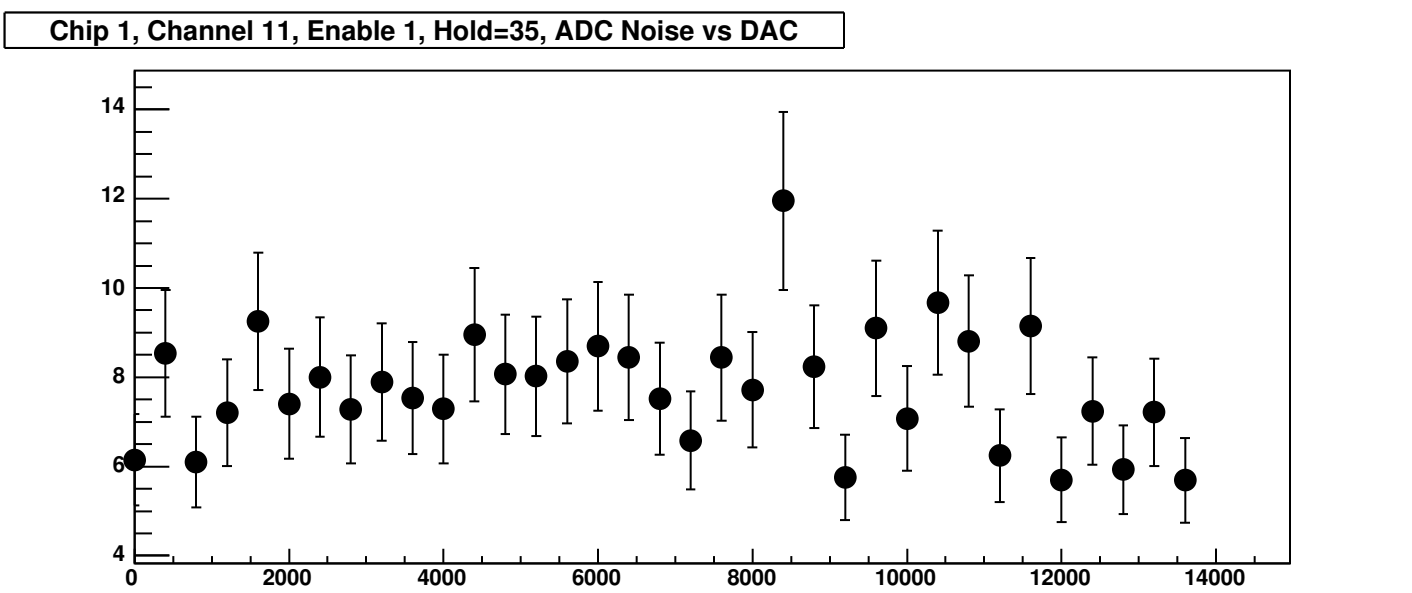


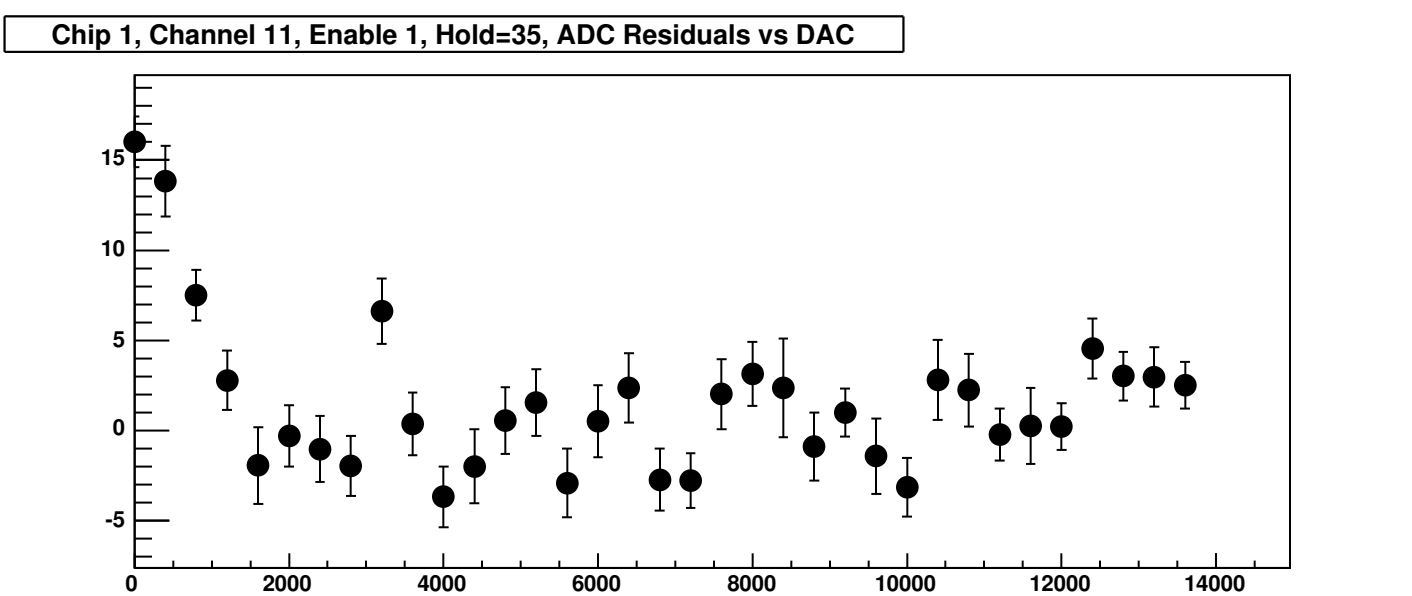

Chip 1, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1786 / 23
p0 -696.7 ± 1.106
p1 0.1706 ± 0.0001807
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

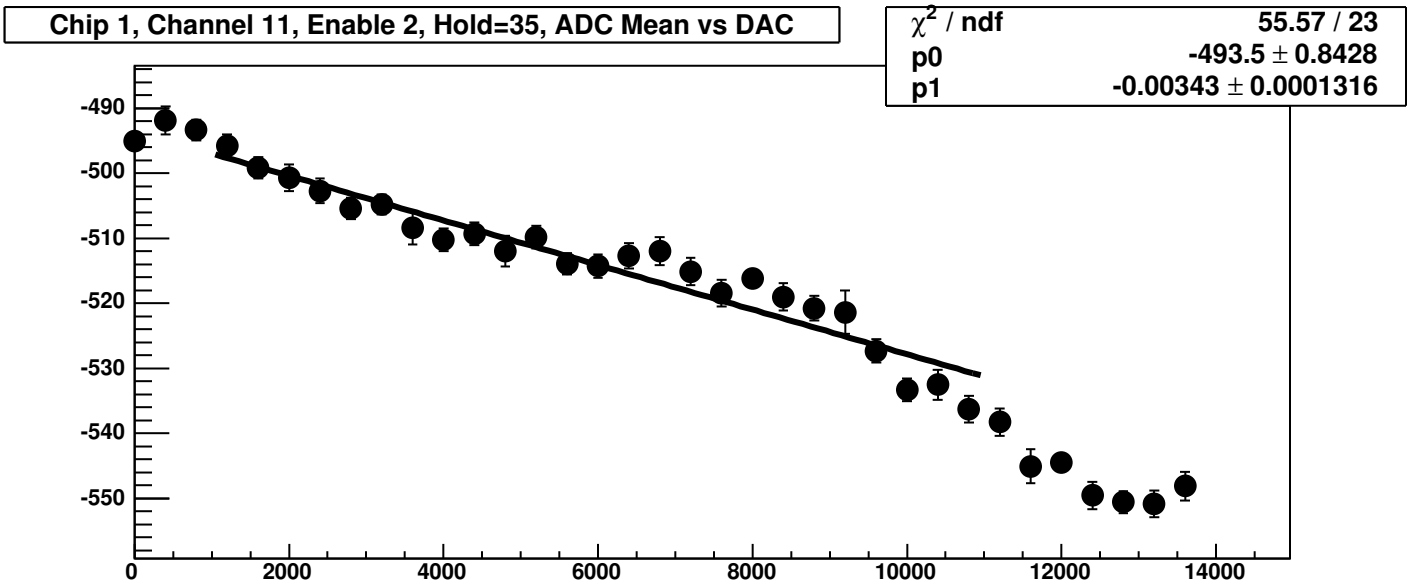

Chip 1, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

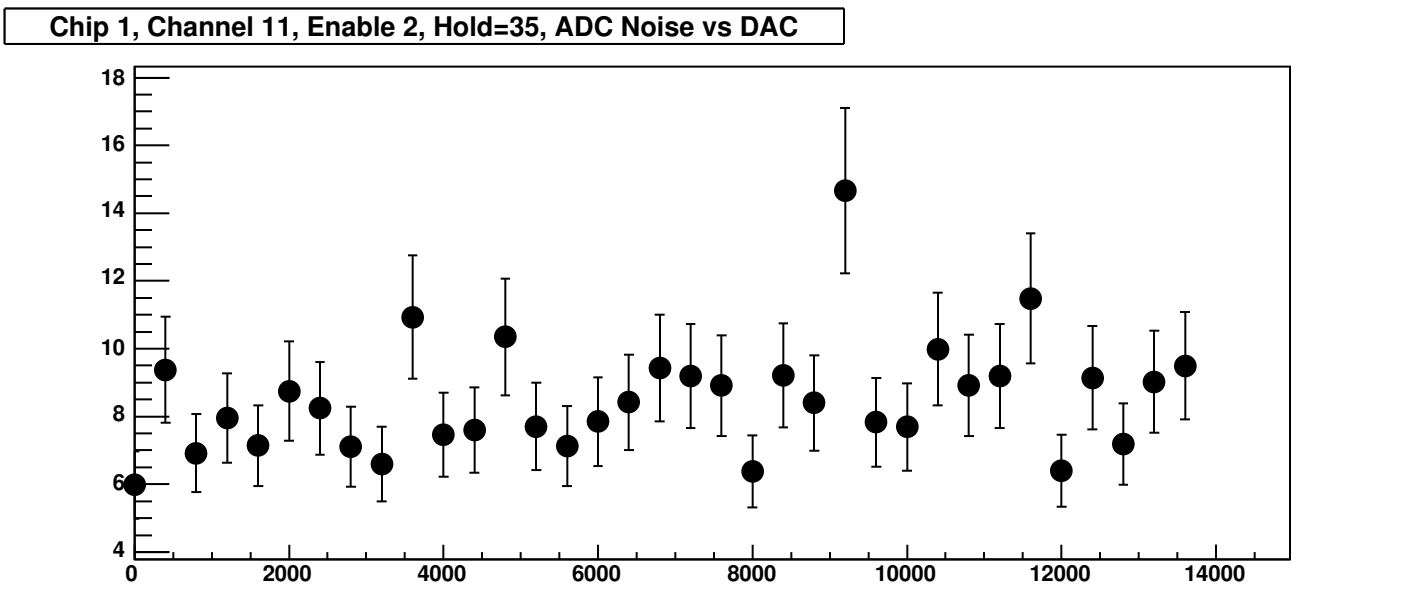

Chip 1, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

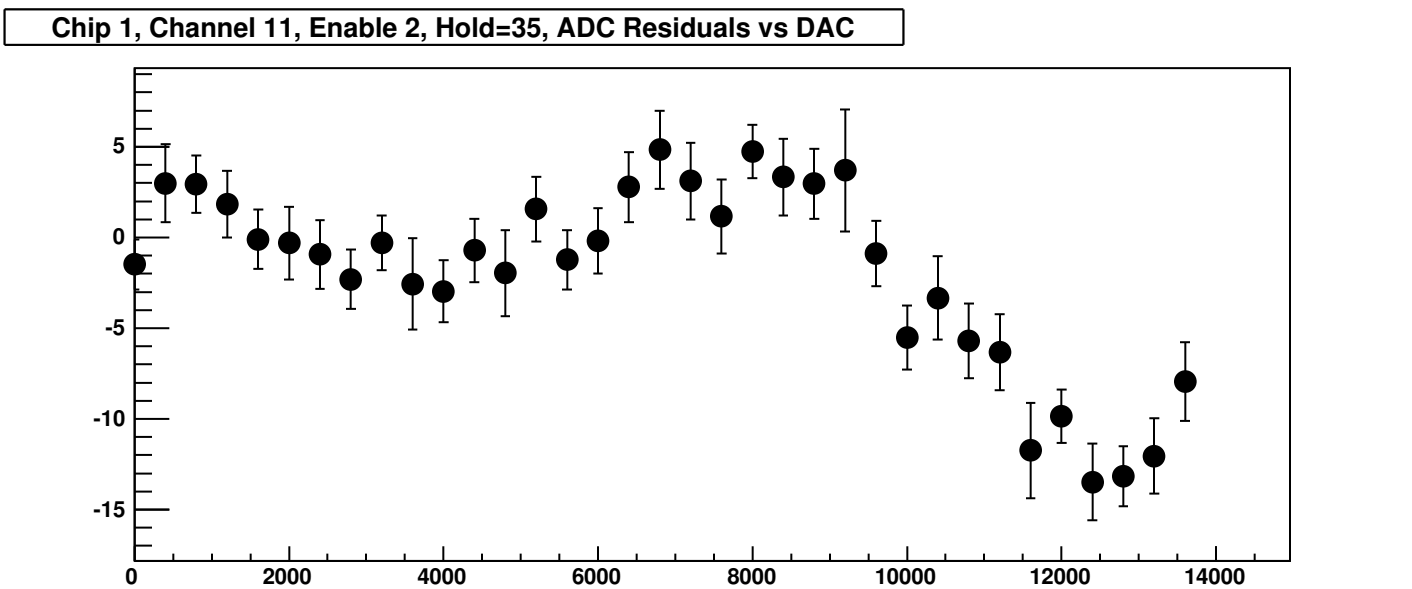

Chip 1, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 47.95 / 23
p0 -495.5 ± 0.8309
p1 0.01406 ± 0.0001259
-300
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
2000
4000
6000
8000
10000
12000
14000

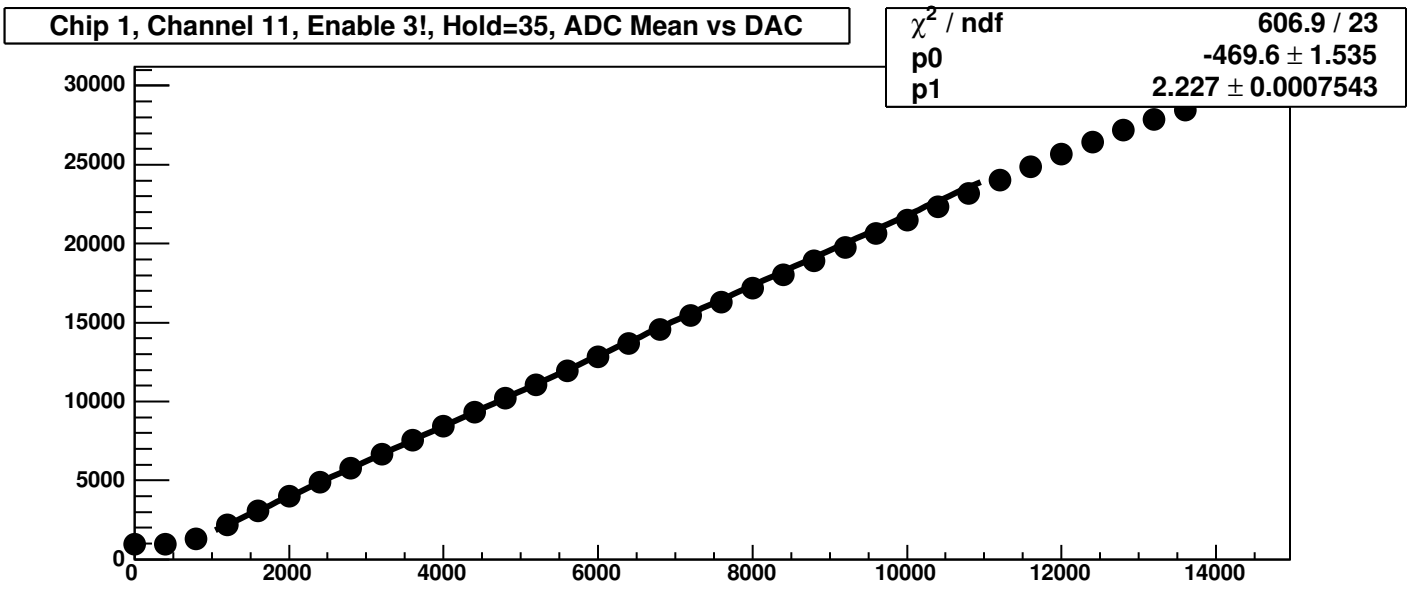

Chip 1, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

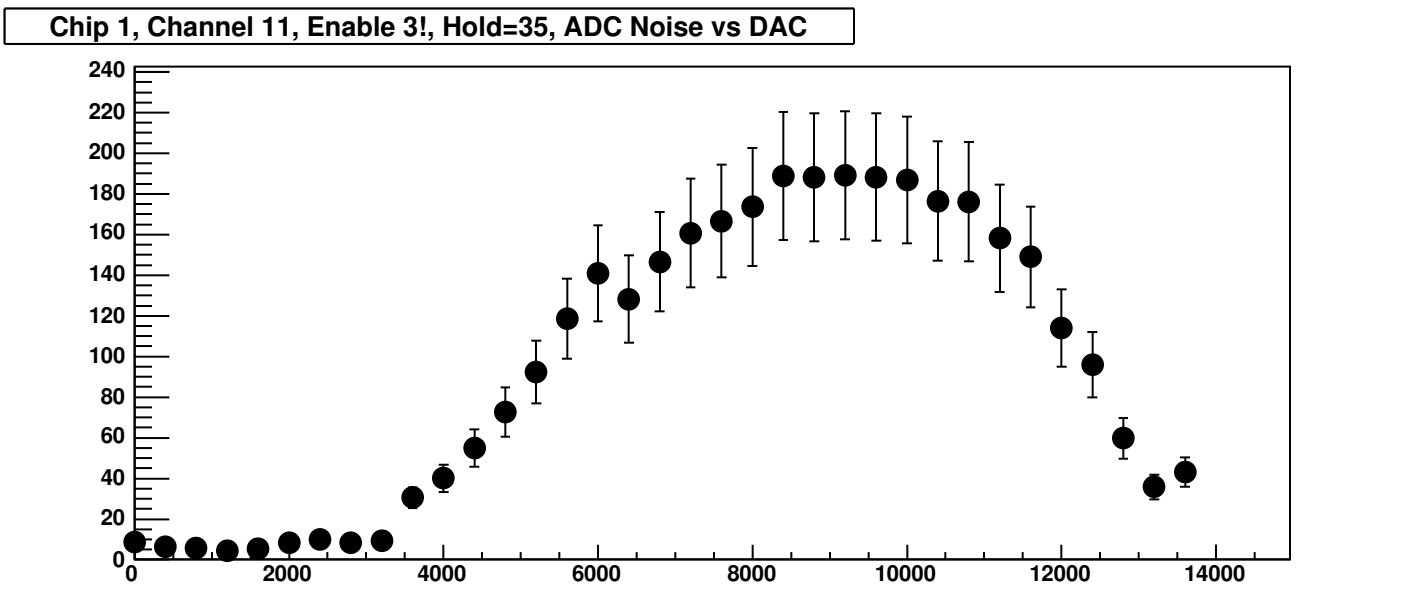

Chip 1, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

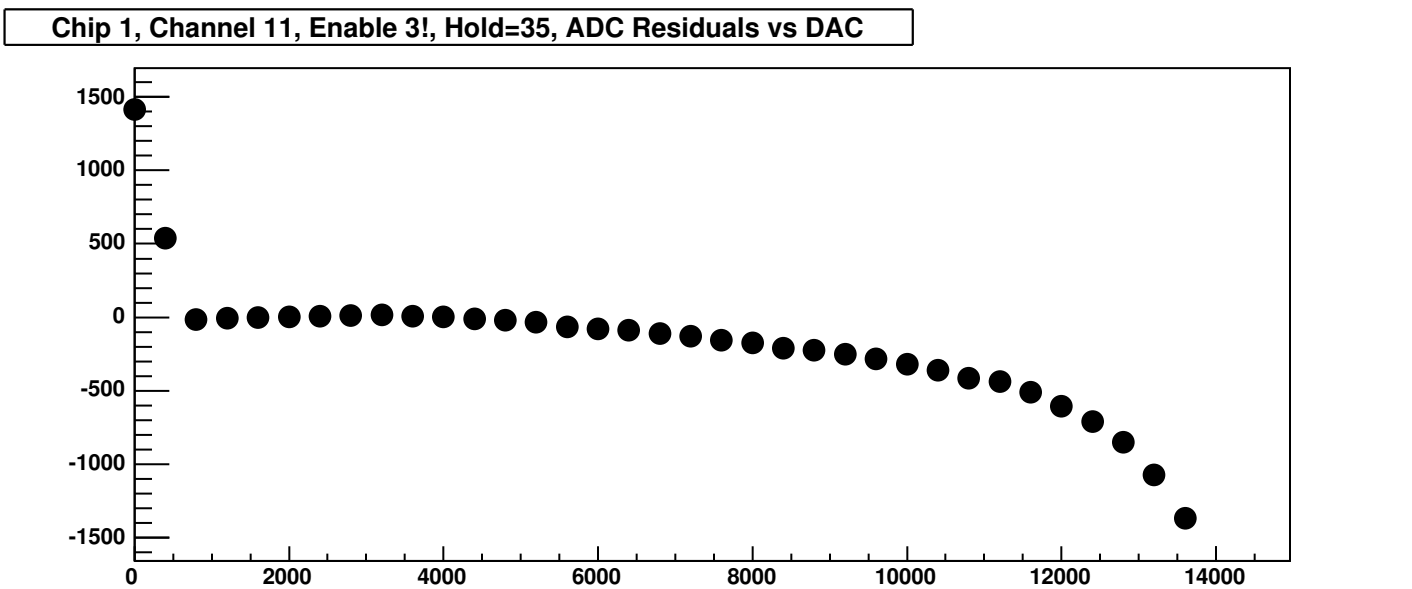

Chip 1, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 55.57 / 23
p0 -493.5 ± 0.8428
p1 -0.00343 ± 0.0001316
-490
-500
-510
-520
-530
-540
-550
0
2000
4000
6000
8000
10000
12000
14000

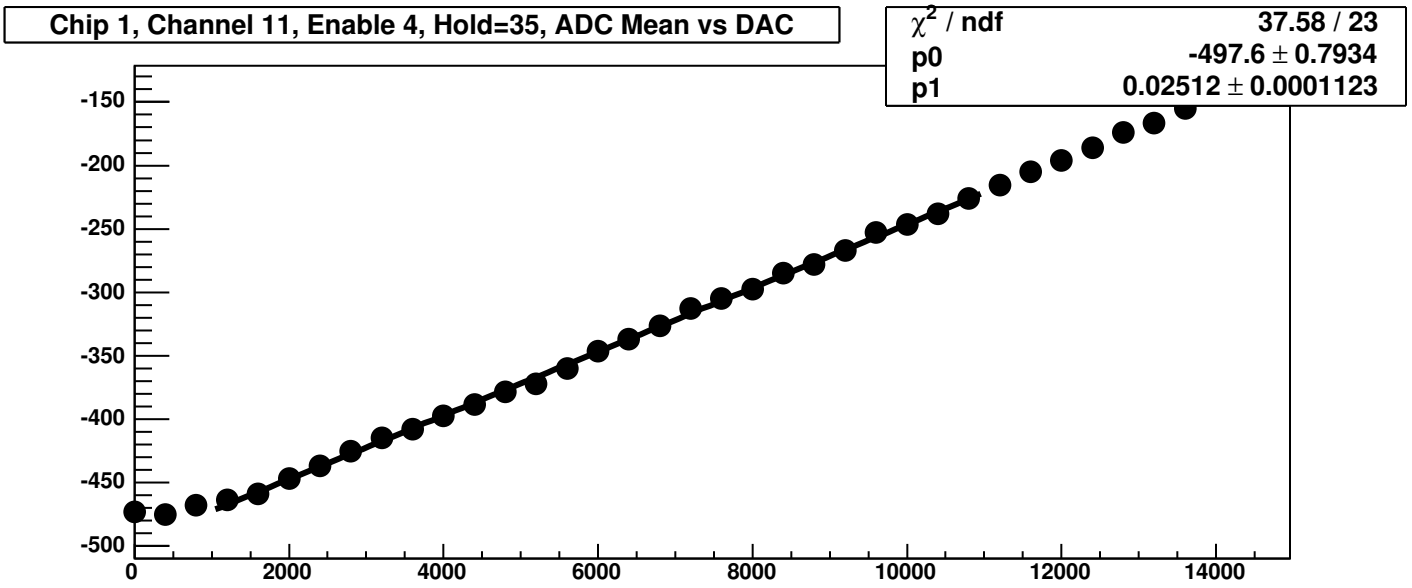

Chip 1, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

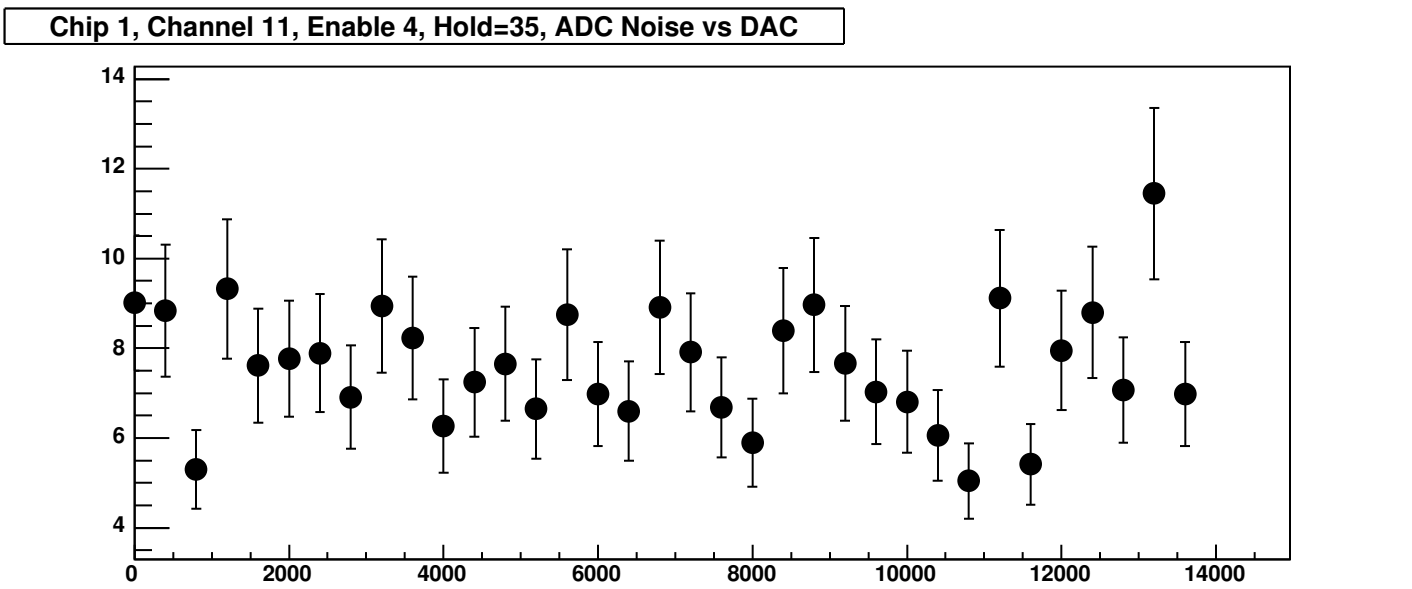

Chip 1, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

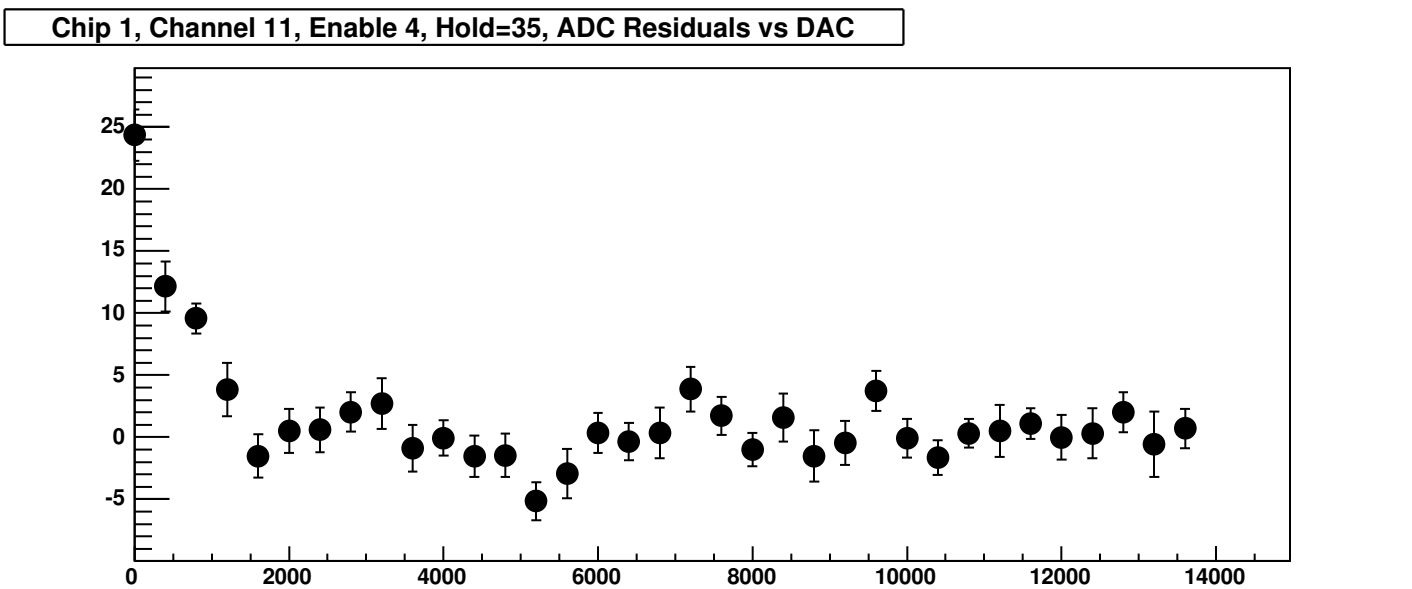

Chip 1, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 606.9 / 23
p0 -469.6 ± 1.535
p1 2.227 ± 0.0007543
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

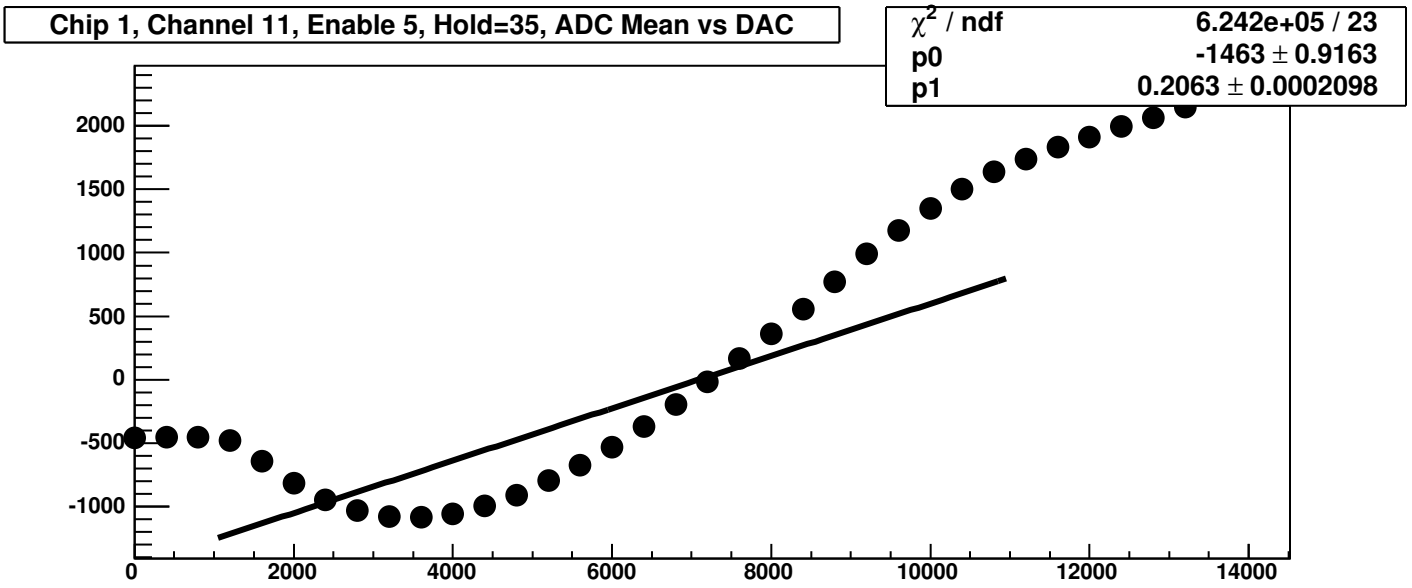

Chip 1, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
240
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

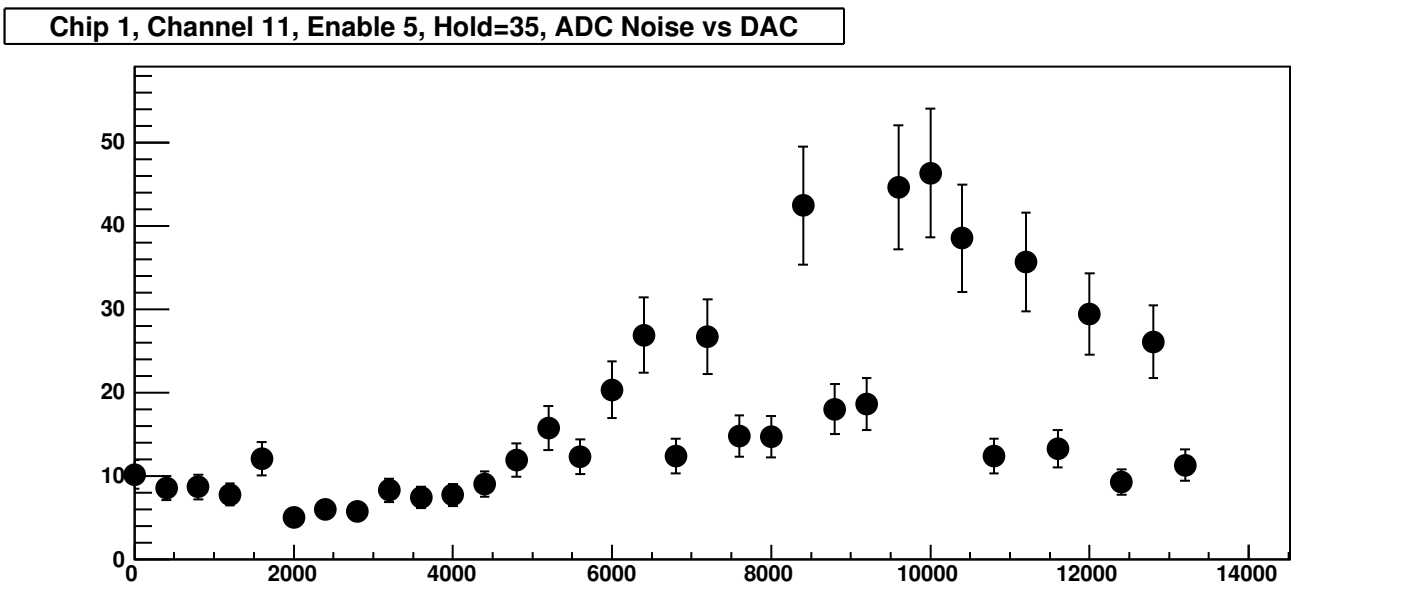

Chip 1, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

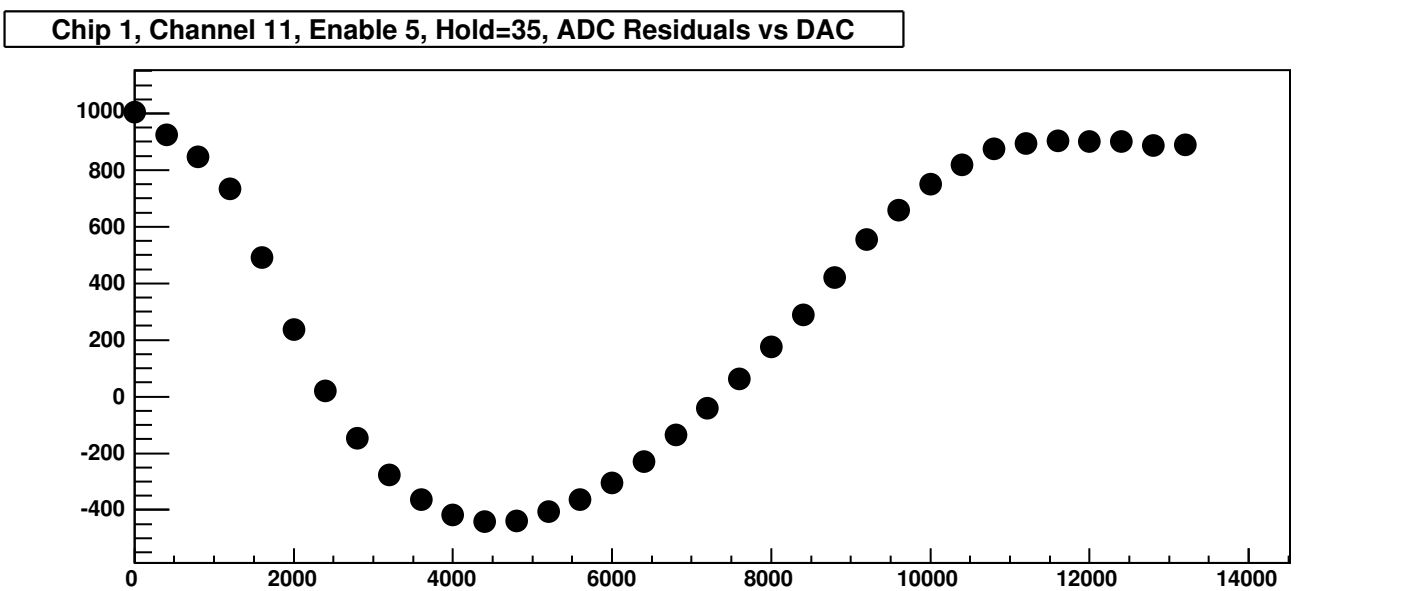

Chip 1, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 37.58 / 23
p0 -497.6 ± 0.7934
p1 0.02512 ± 0.0001123
-150
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

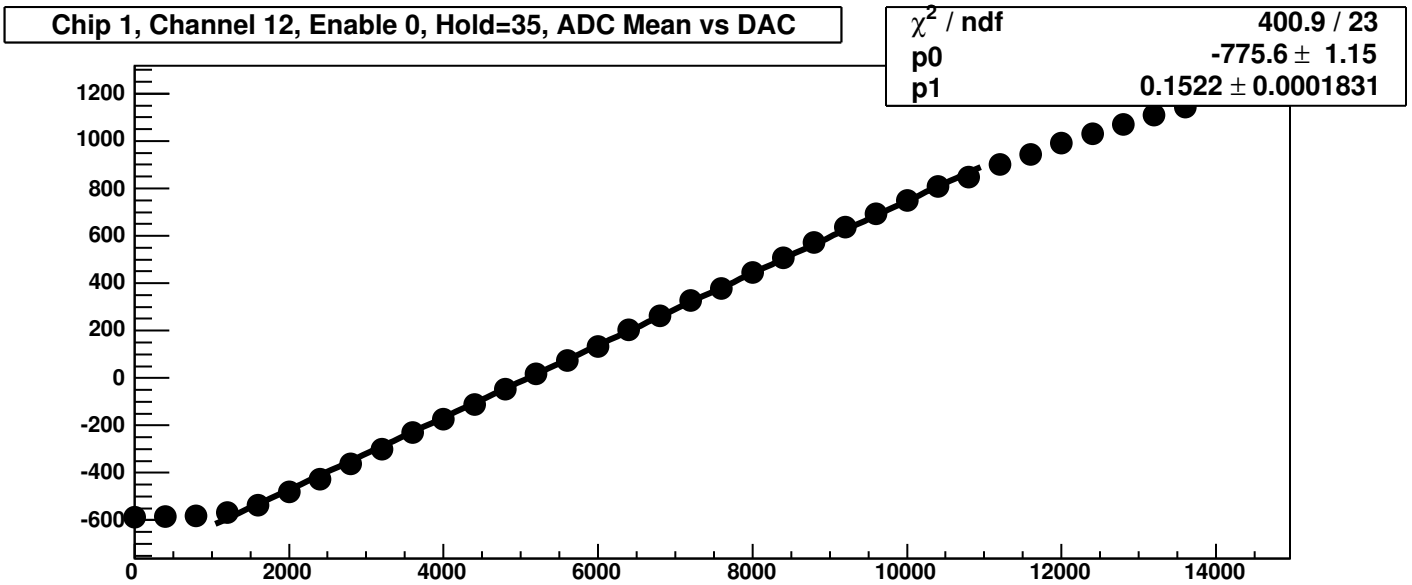

Chip 1, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

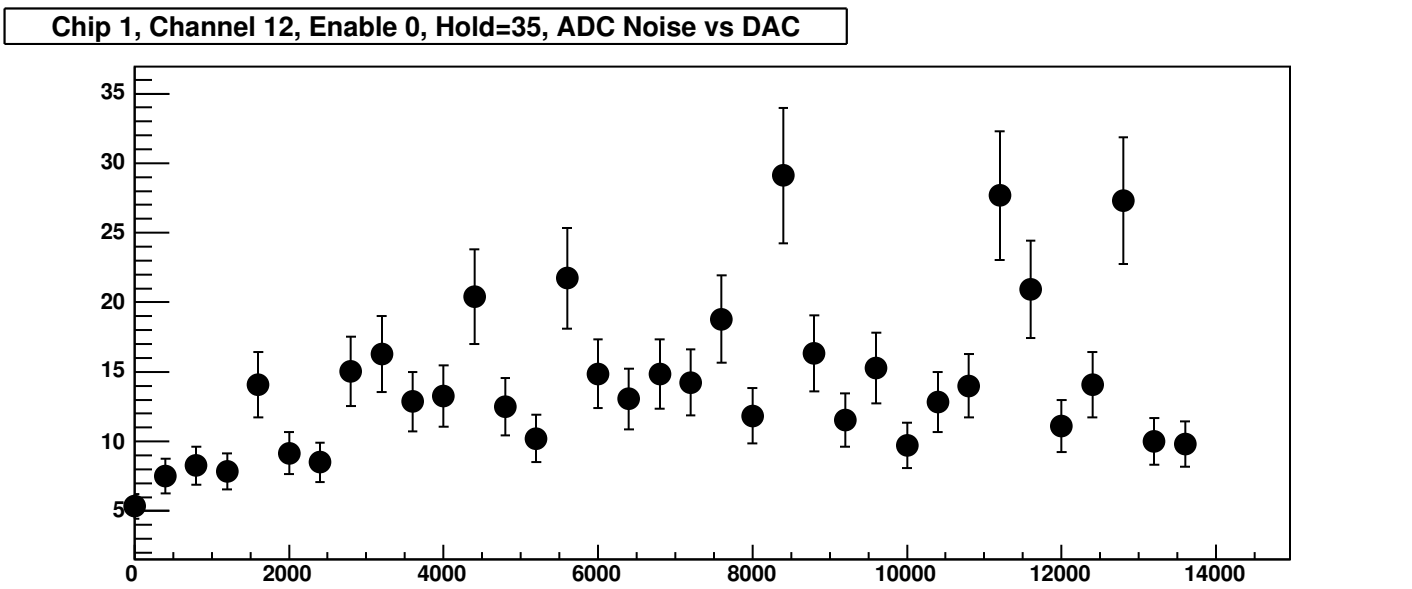

Chip 1, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

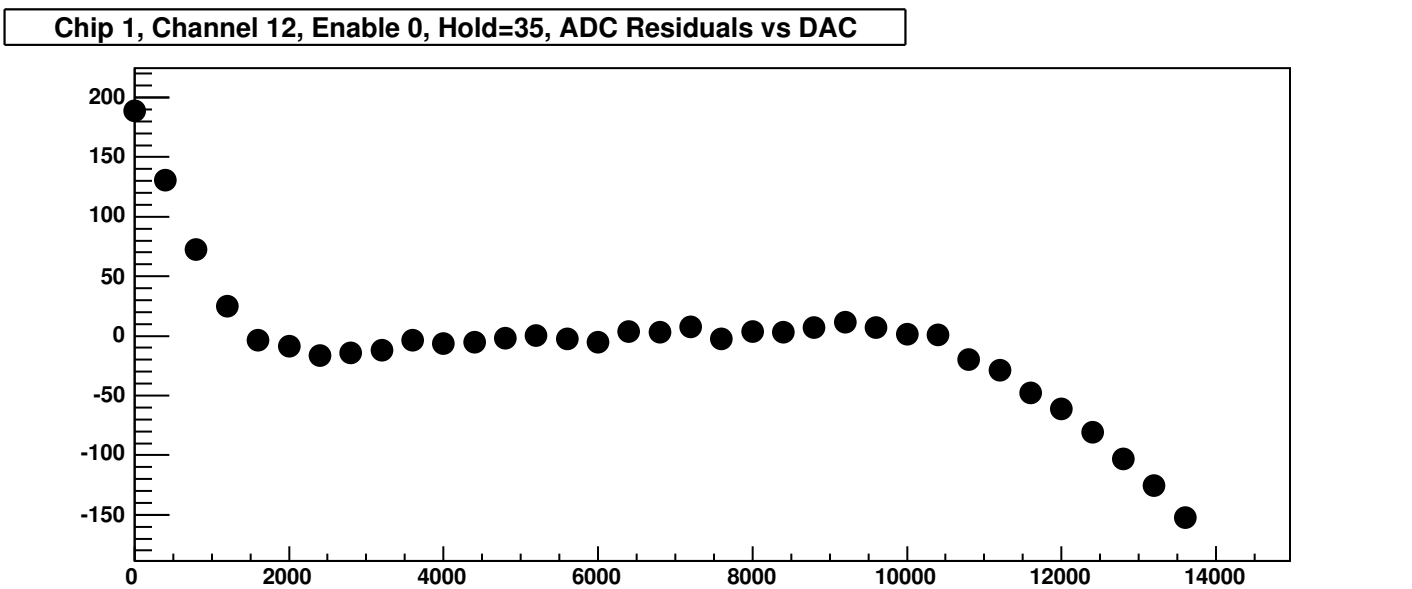

Chip 1, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 6.242e+05 / 23
p0 -1463 ± 0.9163
p1 0.2063 ± 0.0002098
2000
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

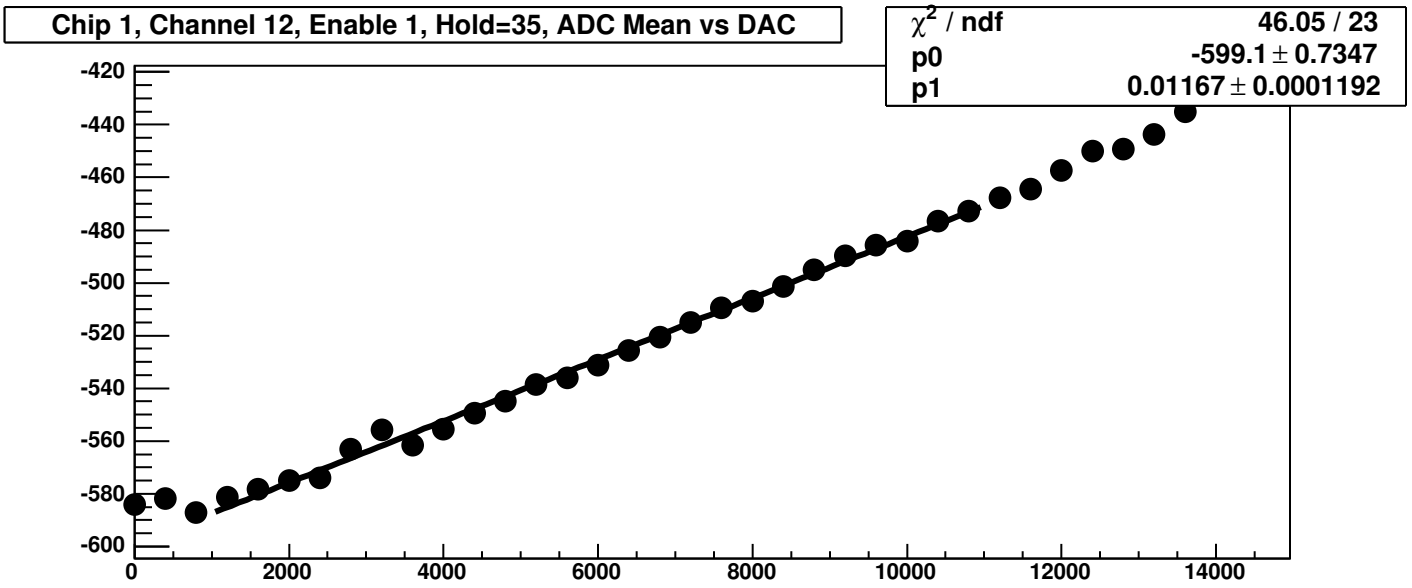

Chip 1, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

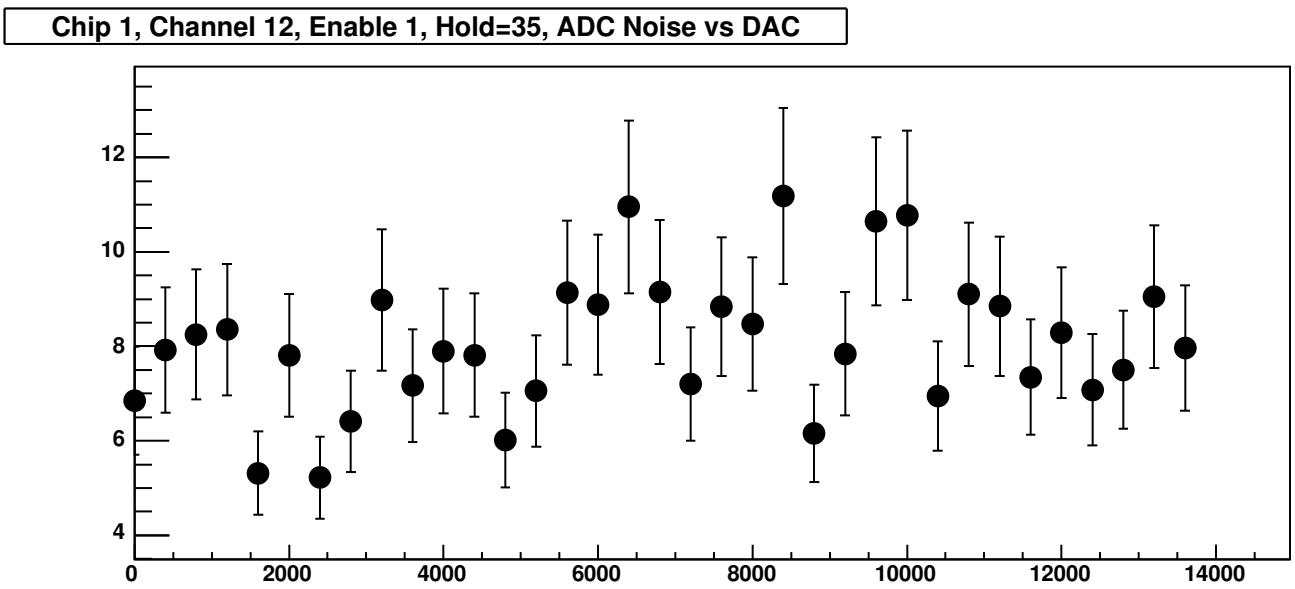

Chip 1, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

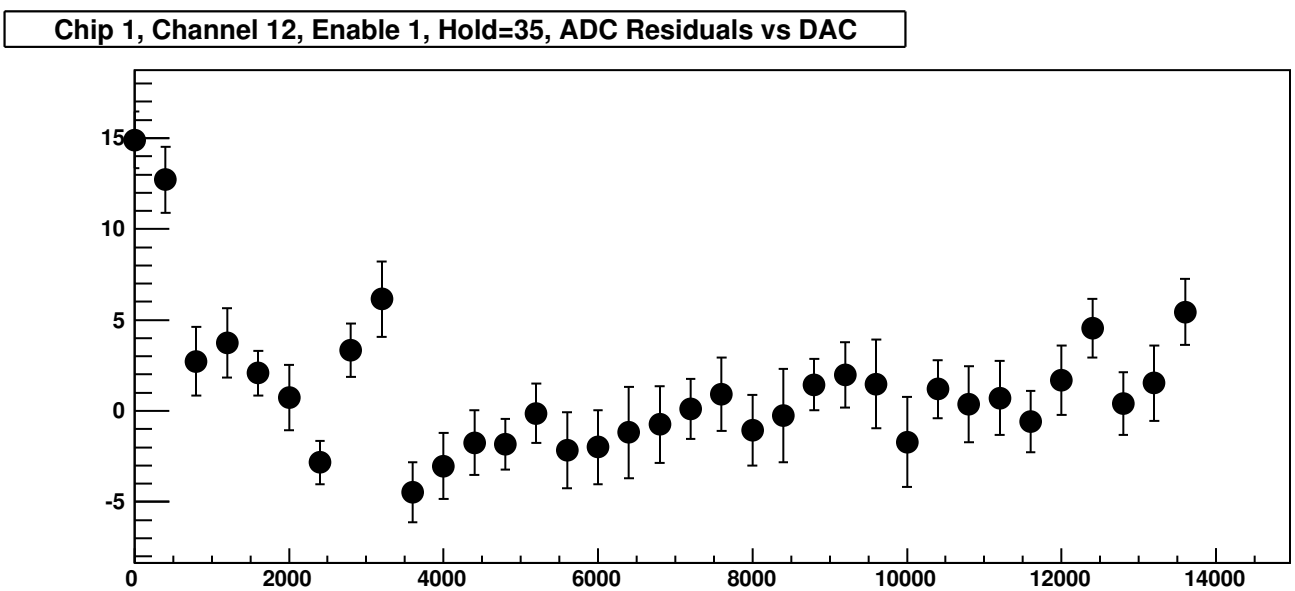

Chip 1, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 400.9 / 23
p0 -775.6 ± 1.15
p1 0.1522 ± 0.0001831
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

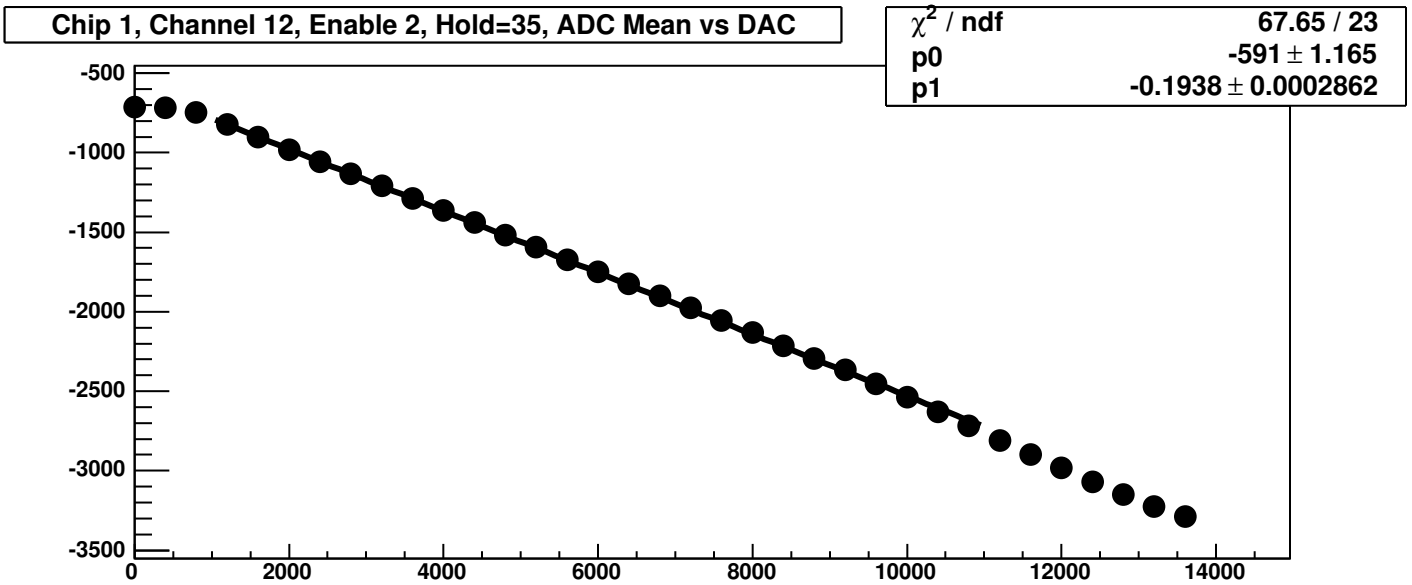

Chip 1, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

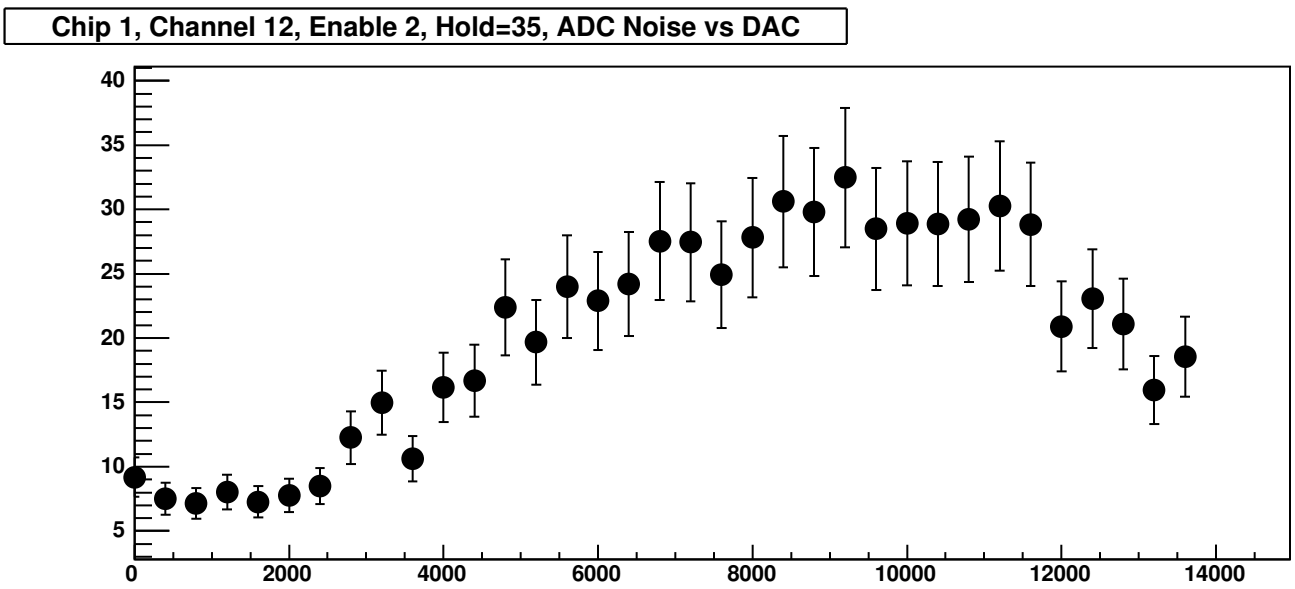

Chip 1, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

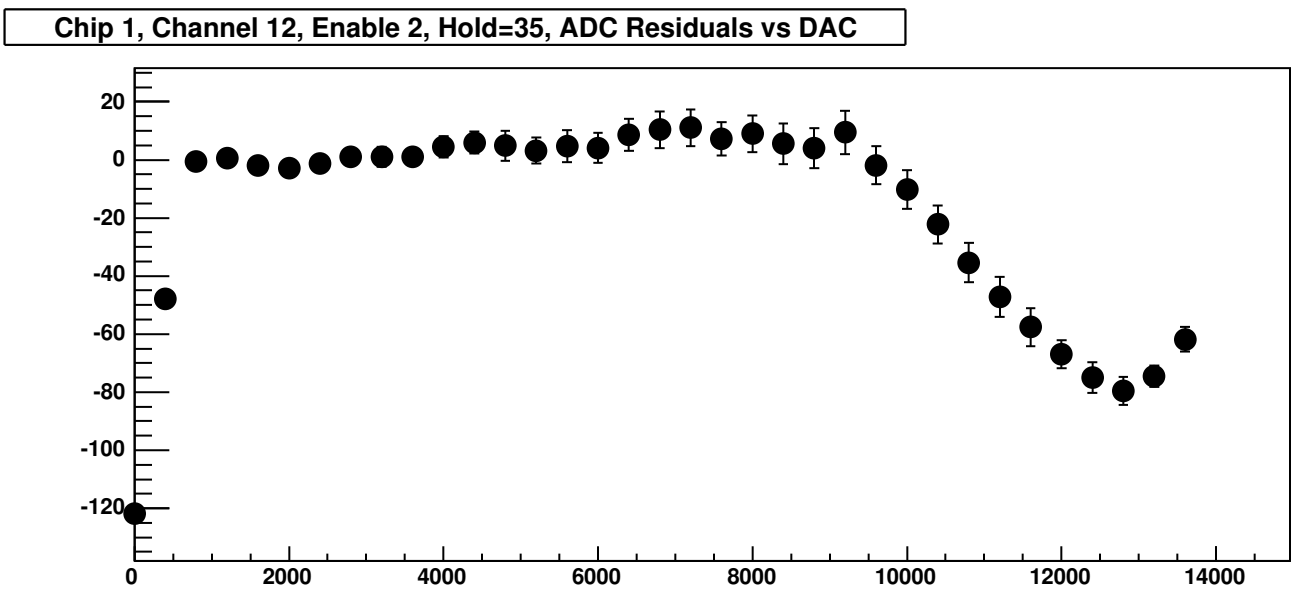

Chip 1, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 46.05 / 23
p0 -599.1 ± 0.7347
p1 0.01167 ± 0.0001192
-420
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
2000
4000
6000
8000
10000
12000
14000

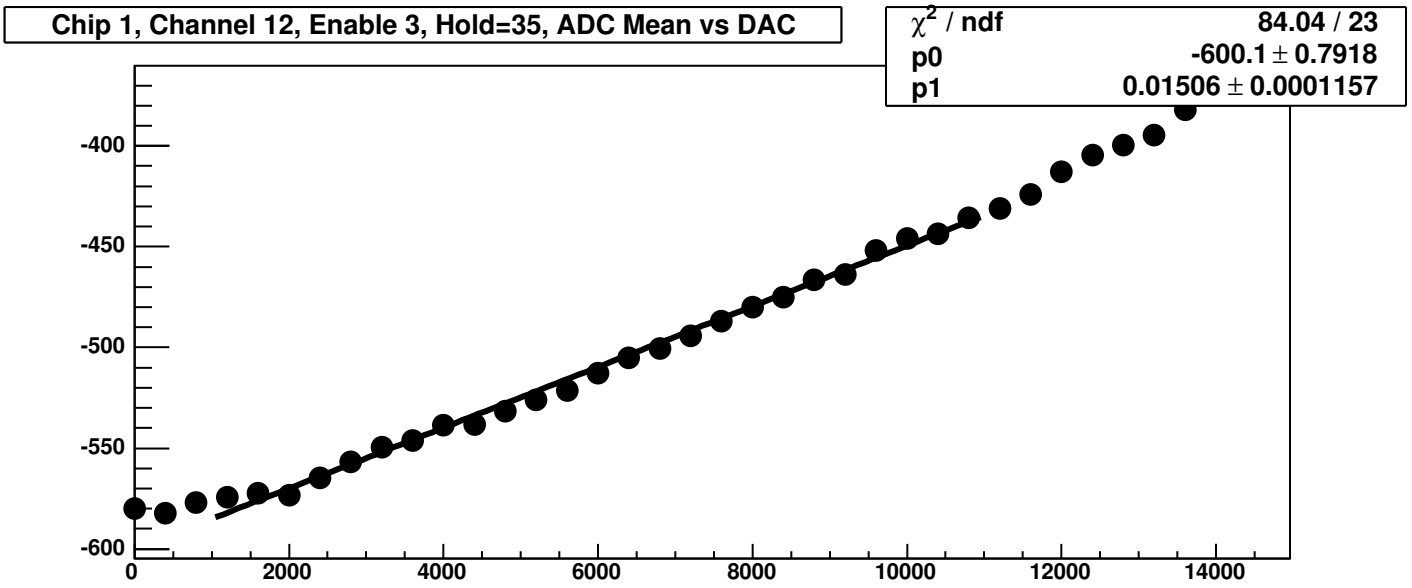

Chip 1, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

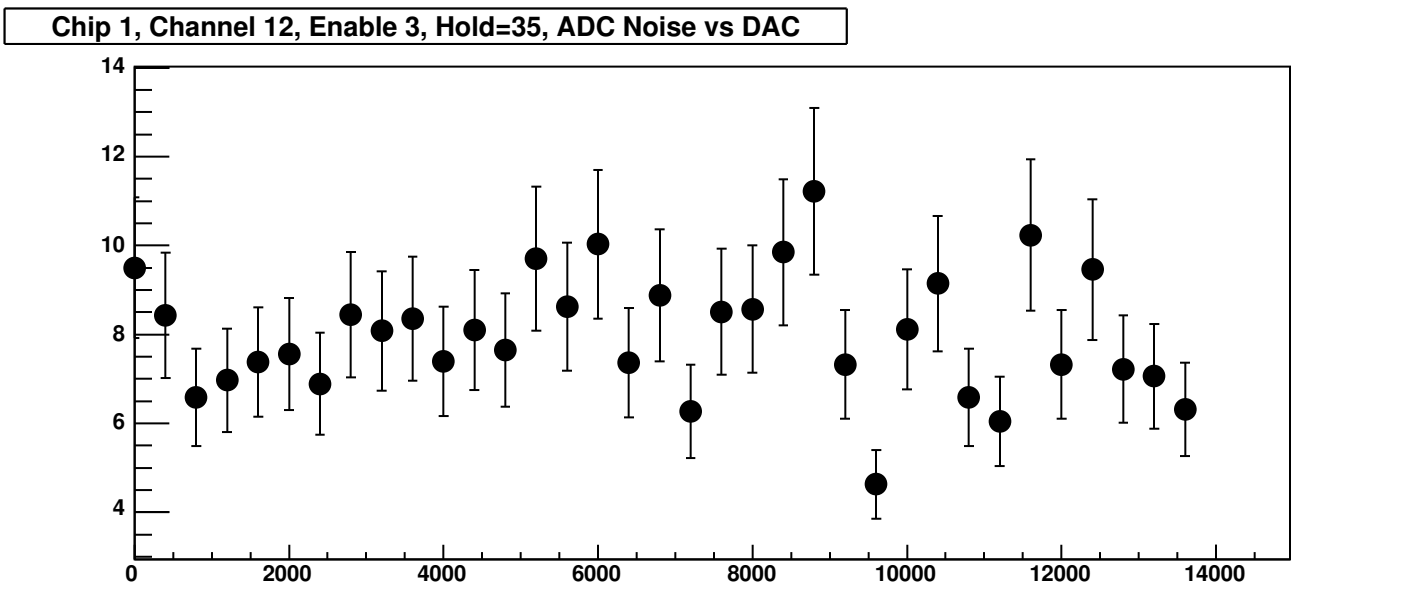

Chip 1, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

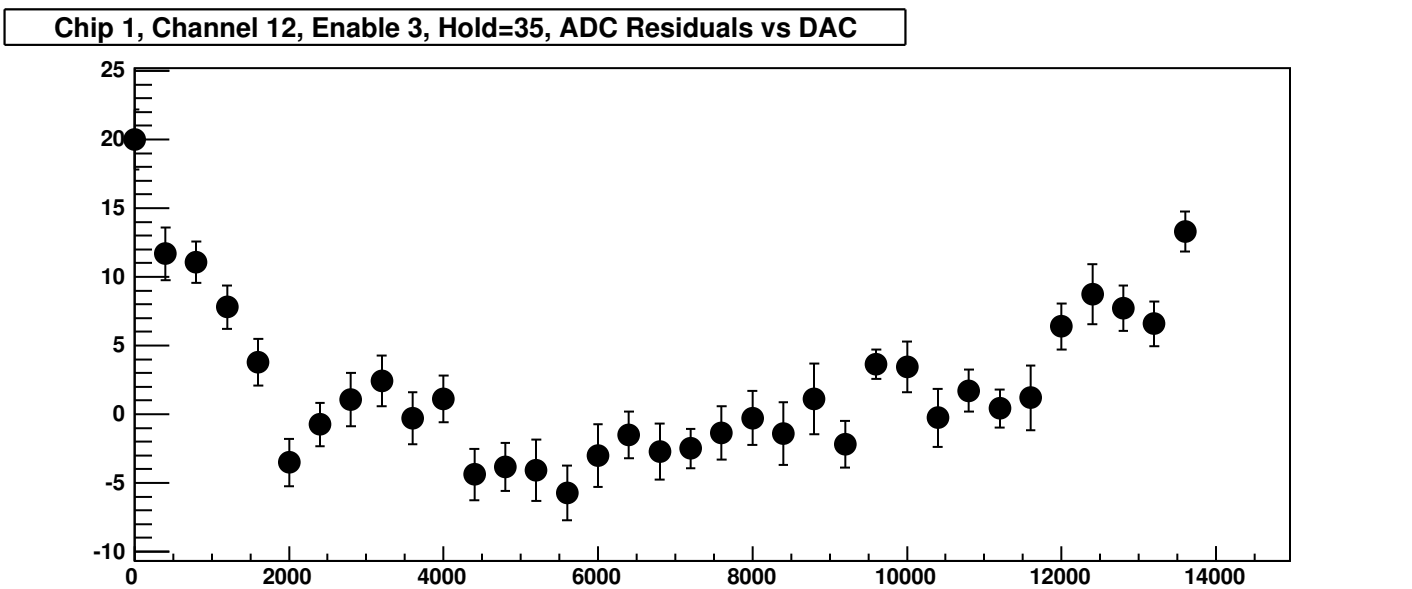

Chip 1, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 67.65 / 23
p0 -591 ± 1.165
p1 -0.1938 ± 0.0002862
-500
-1000
-1500
-2000
-2500
-3000
-3500
0
2000
4000
6000
8000
10000
12000
14000

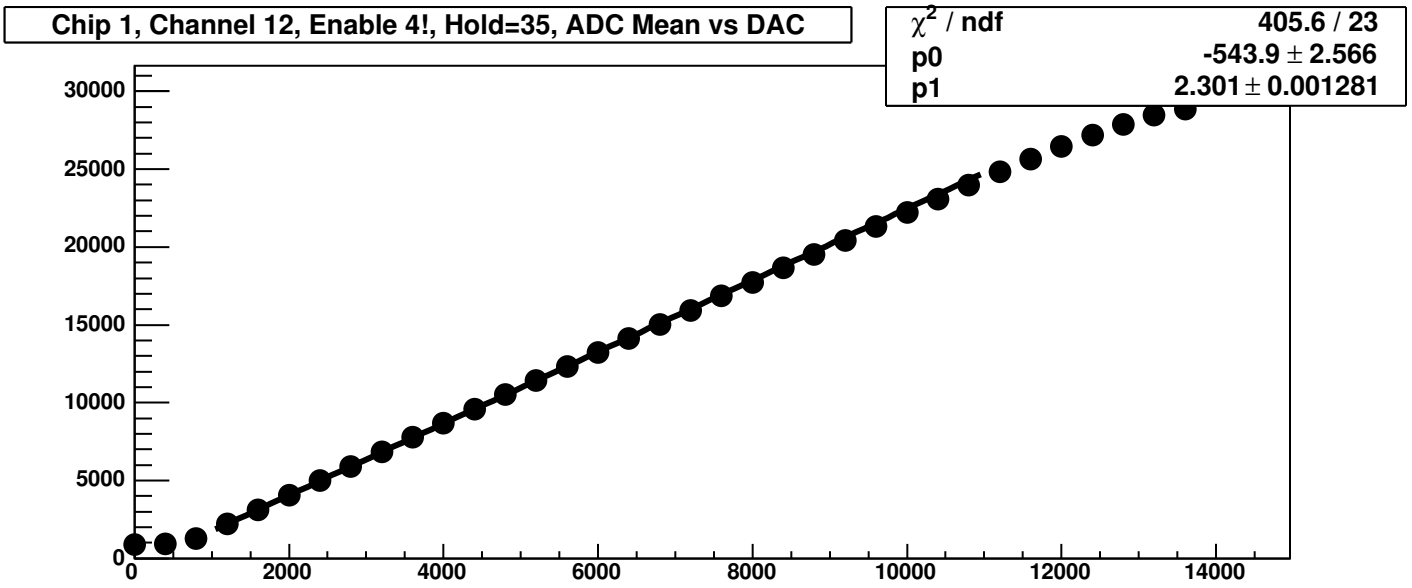

Chip 1, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

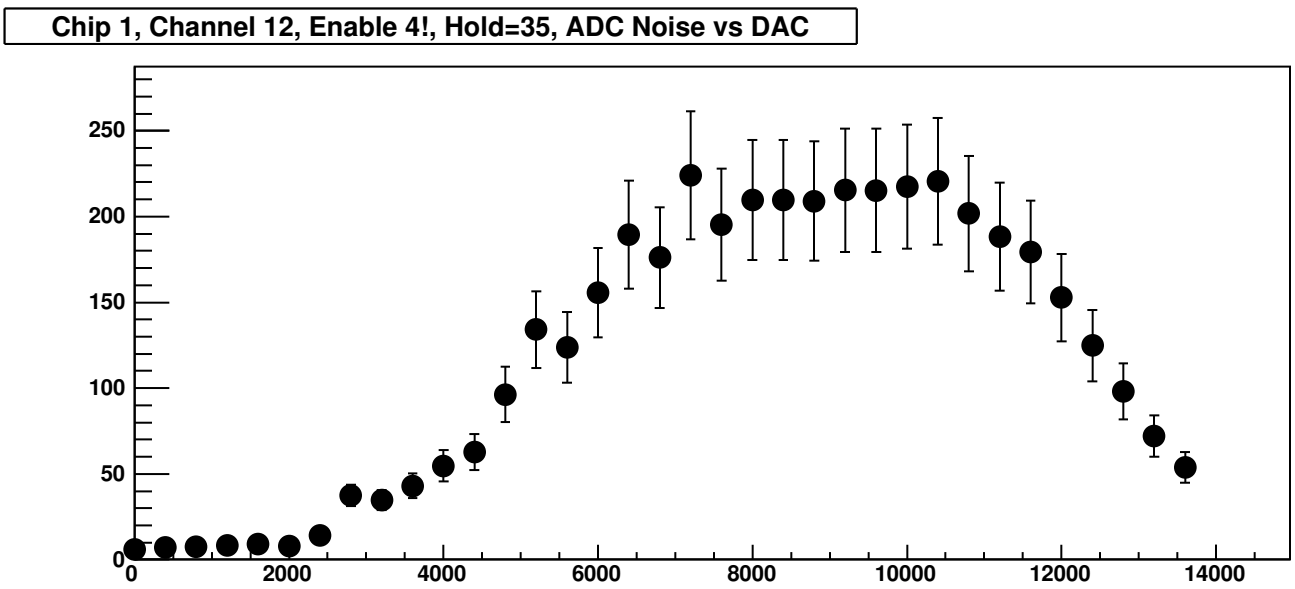

Chip 1, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
2000
4000
6000
8000
10000
12000
14000

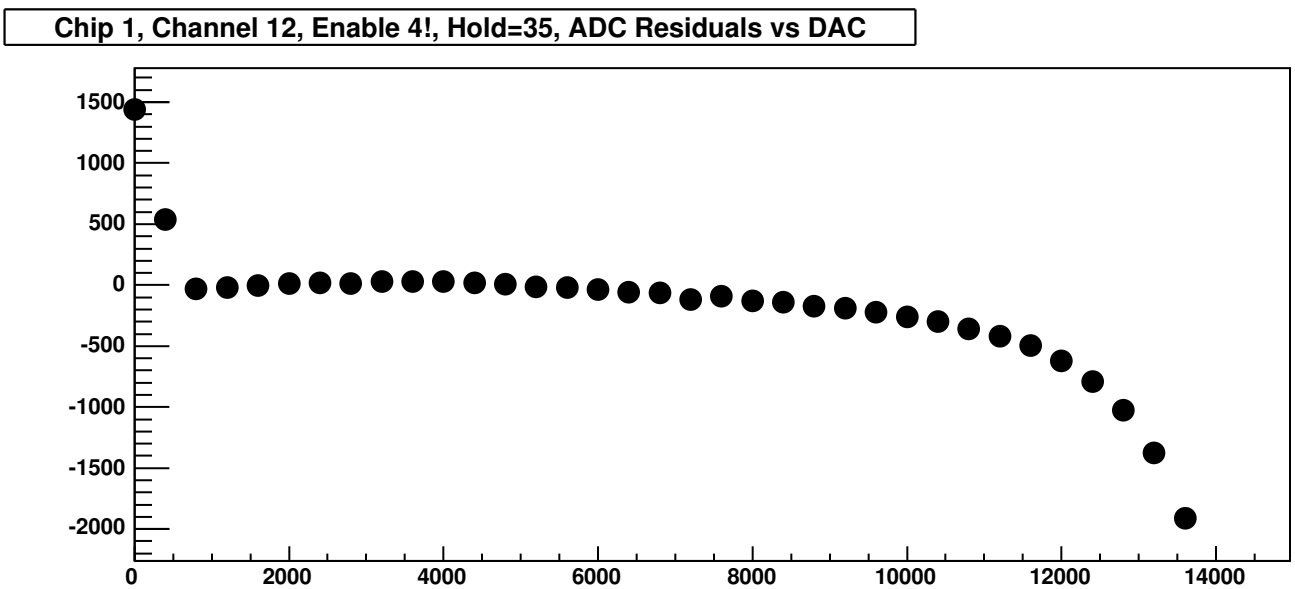

Chip 1, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 84.04 / 23
p0 -600.1 ± 0.7918
p1 0.01506 ± 0.0001157
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

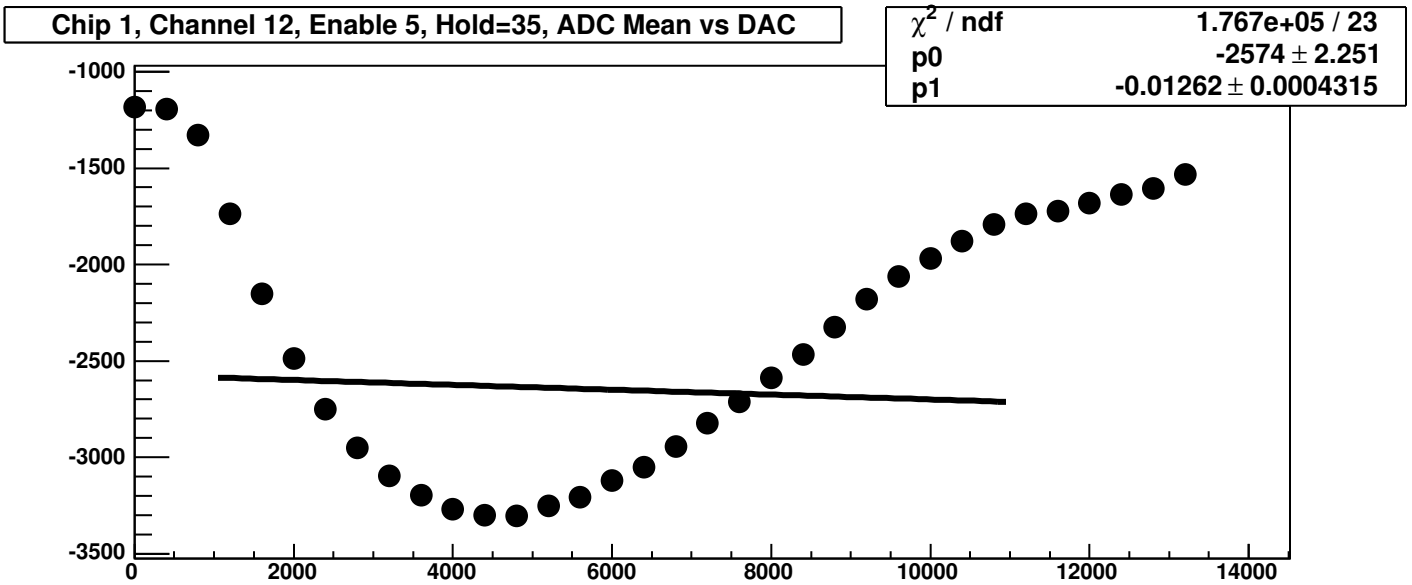

Chip 1, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

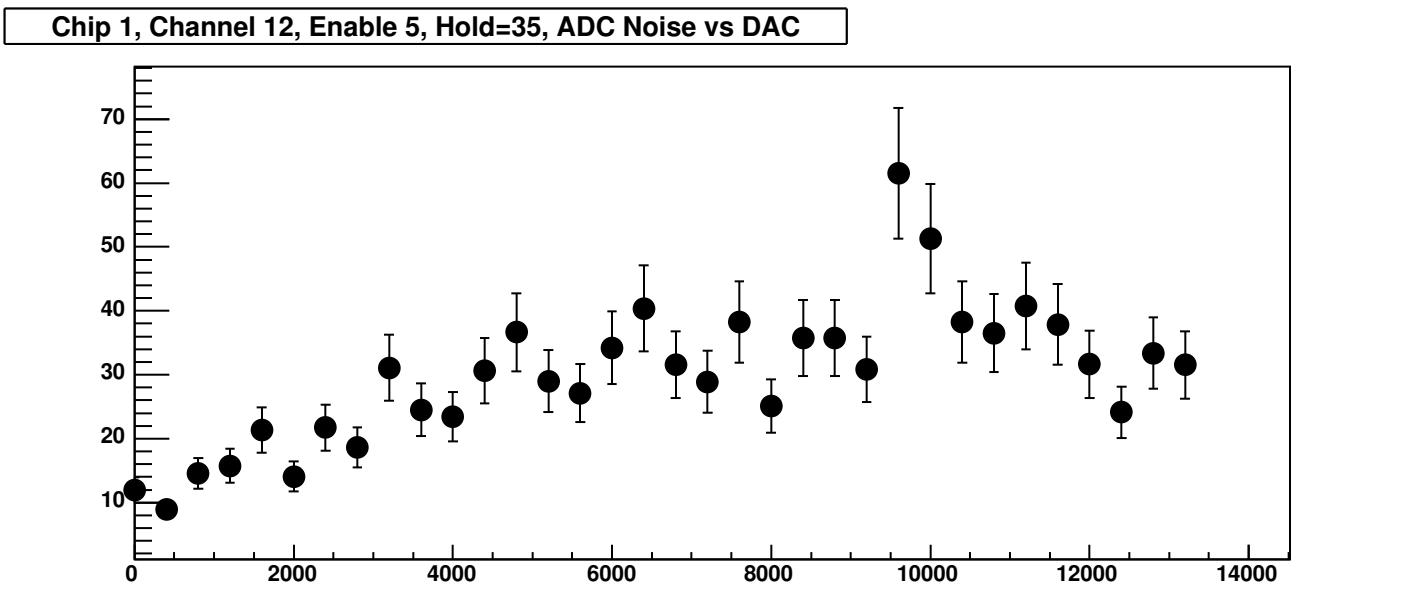

Chip 1, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

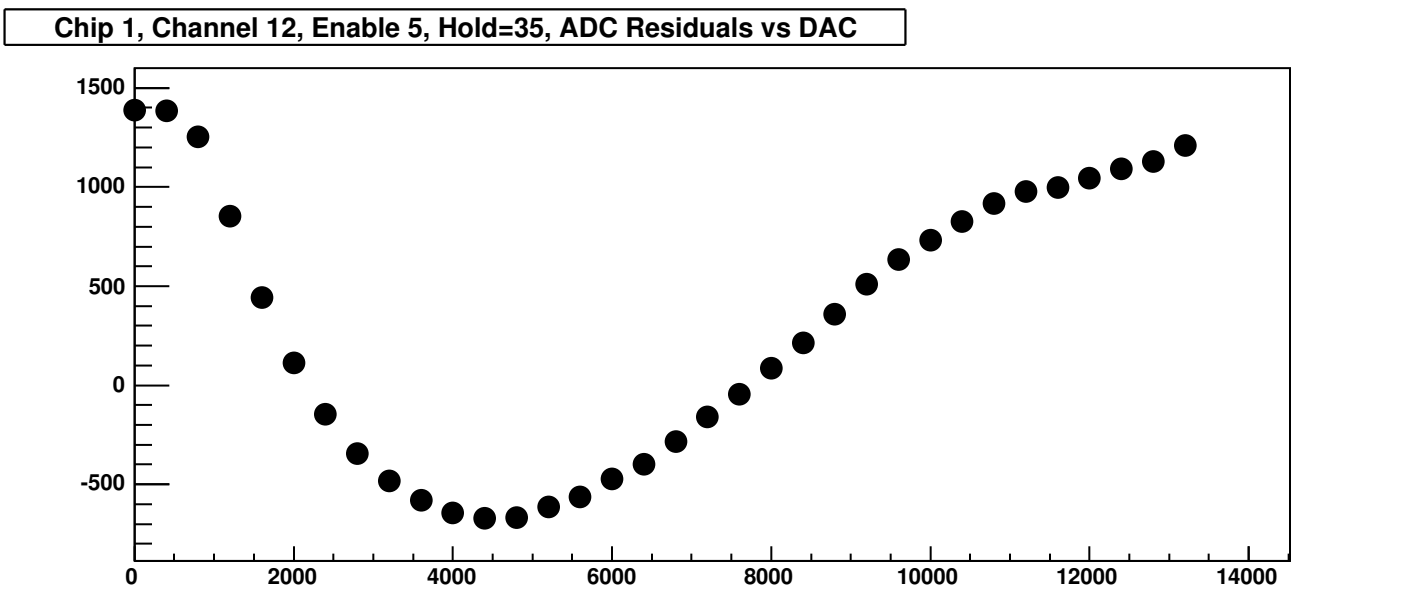

Chip 1, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 405.6 / 23
p0 -543.9 ± 2.566
p1 2.301 ± 0.001281
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

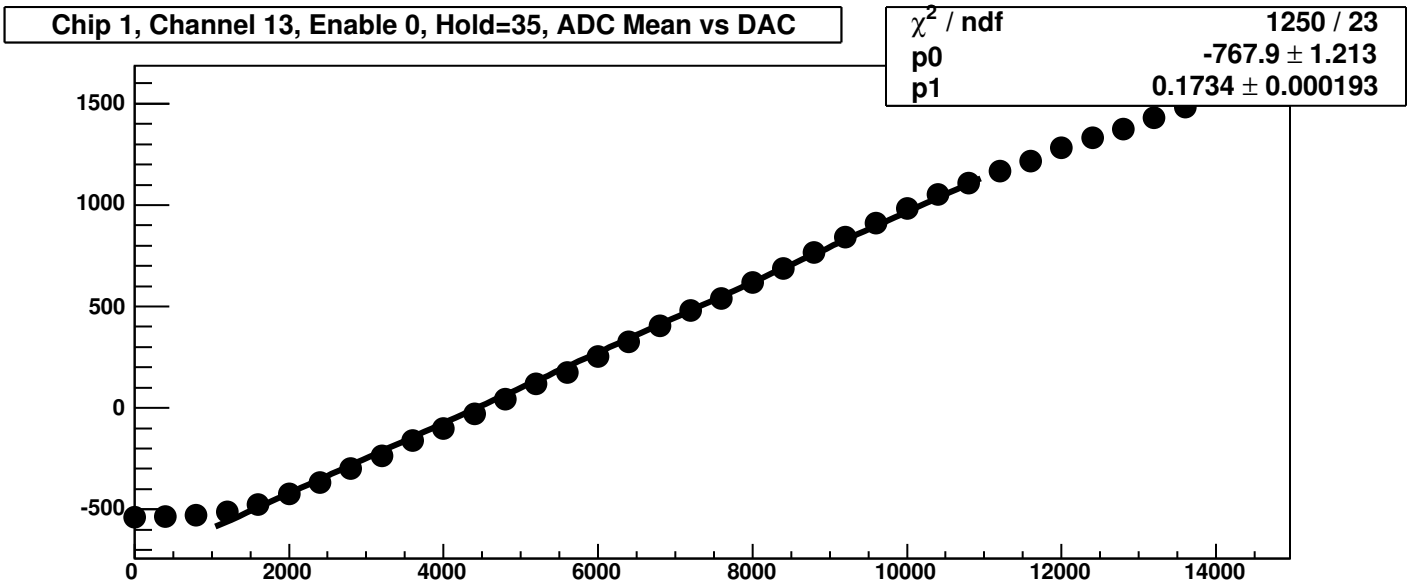

Chip 1, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

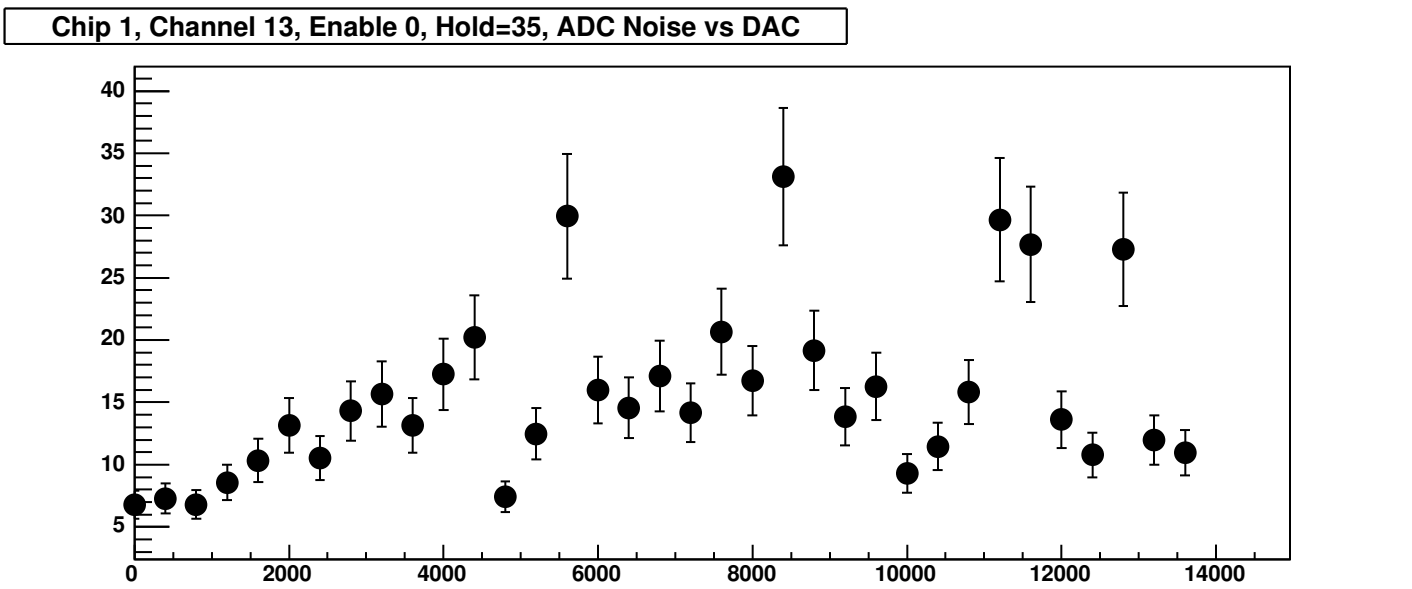

Chip 1, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

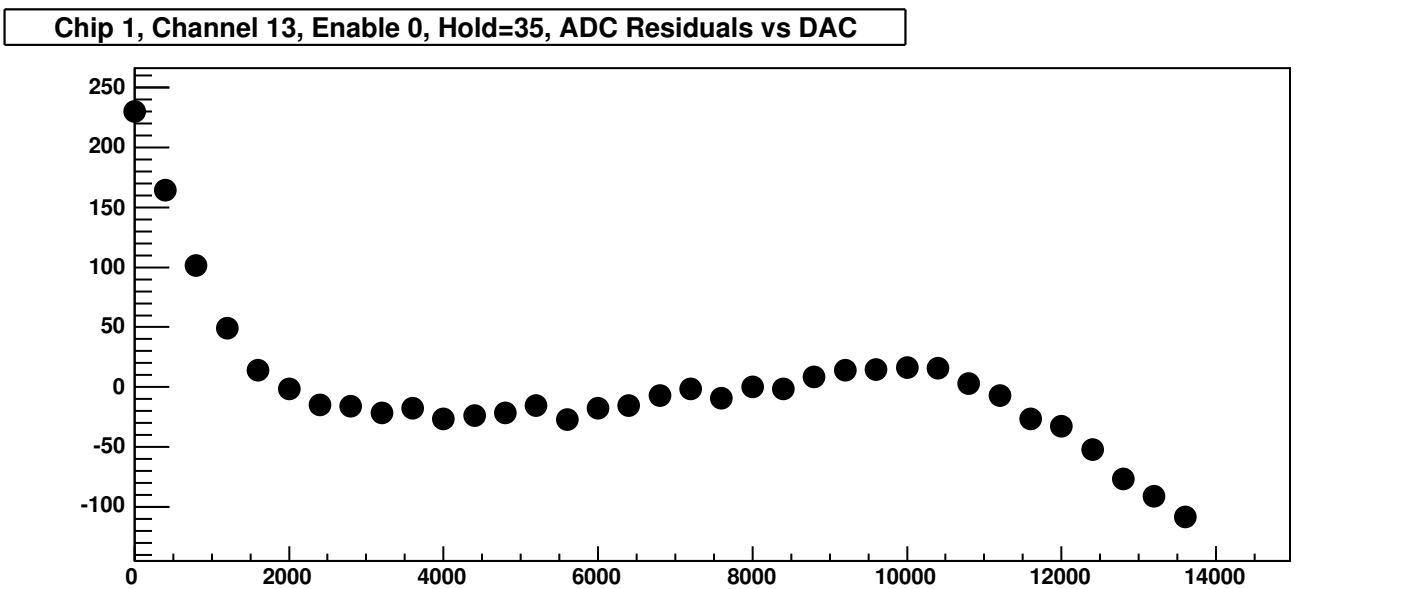

Chip 1, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.767e+05 / 23
p0 -2574 ± 2.251
p1 -0.01262 ± 0.0004315
-1000
-1500
-2000
-2500
-3000
-3500
0
2000
4000
6000
8000
10000
12000
14000

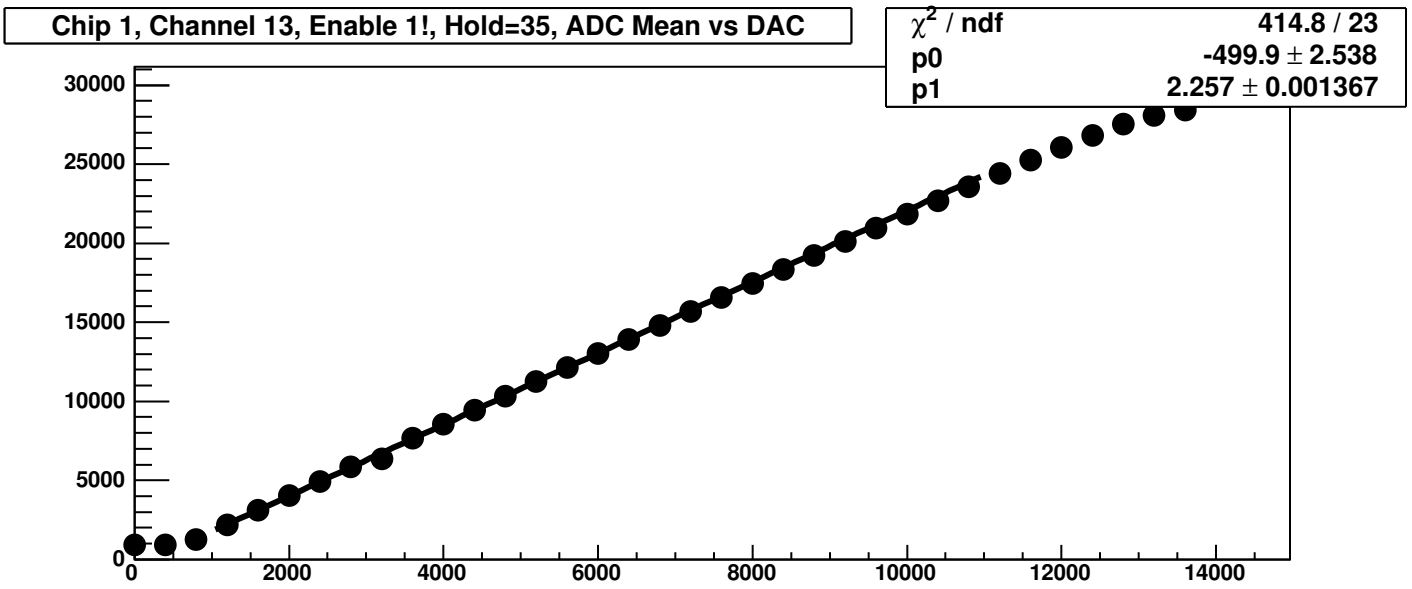

Chip 1, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

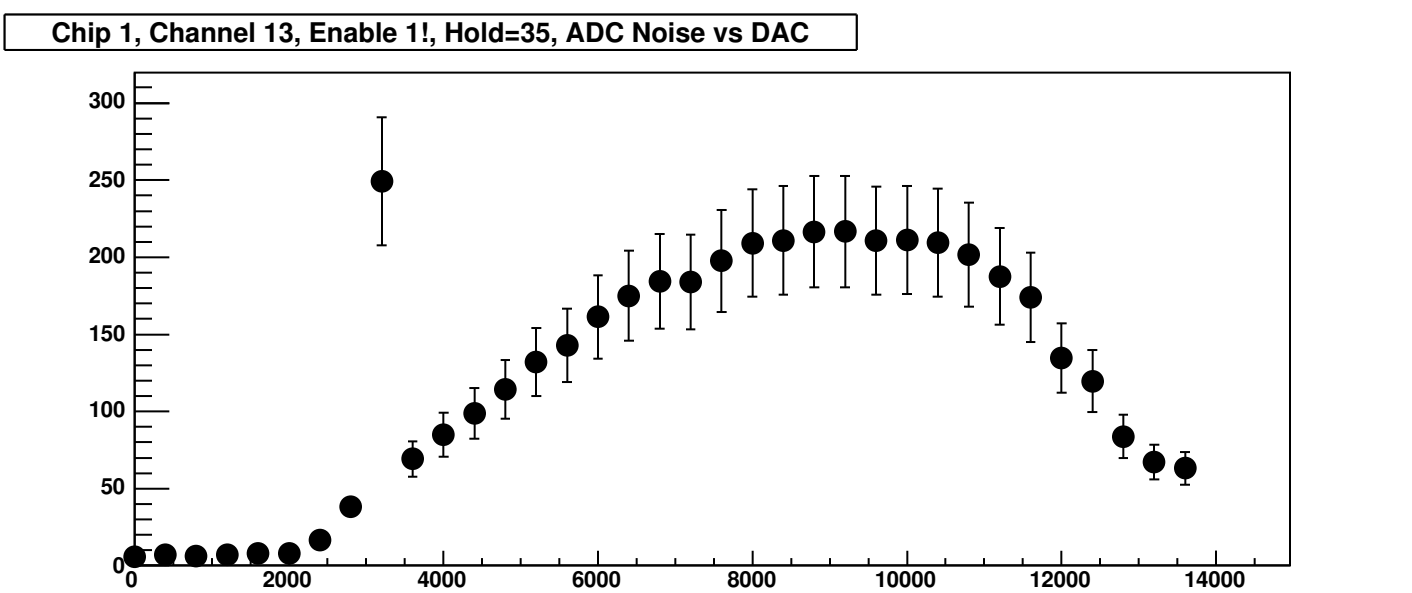

Chip 1, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

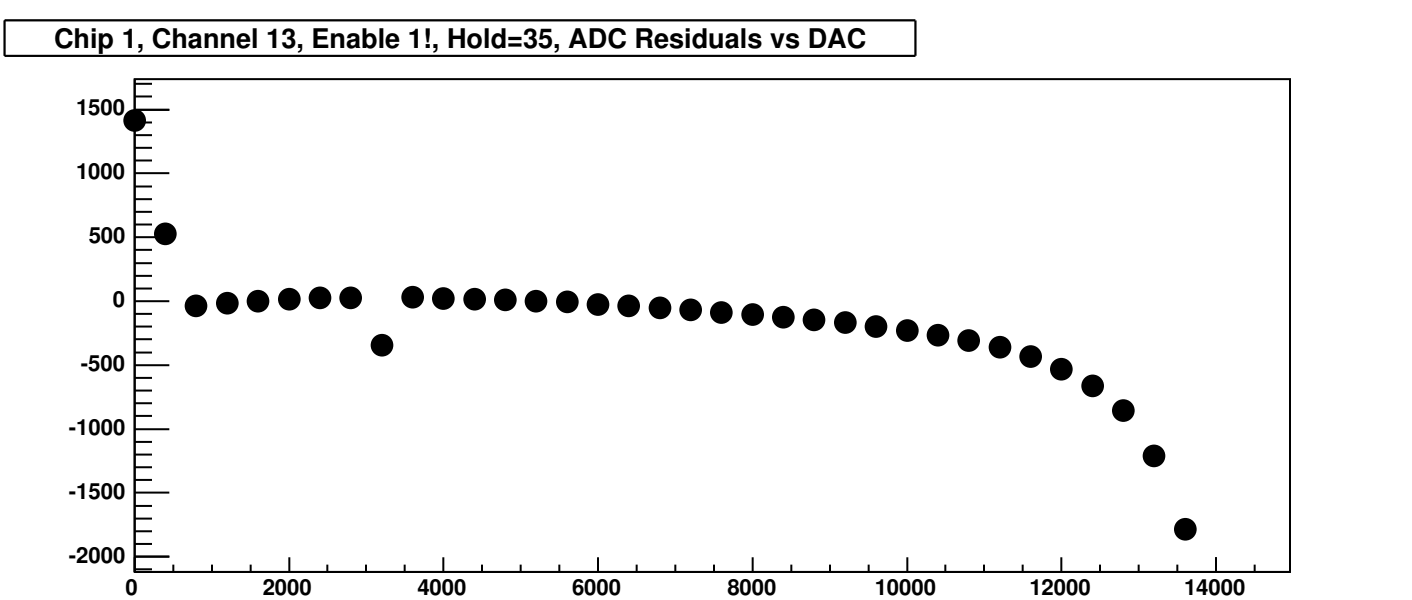

Chip 1, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1250 / 23
p0 -767.9 ± 1.213
p1 0.1734 ± 0.000193
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

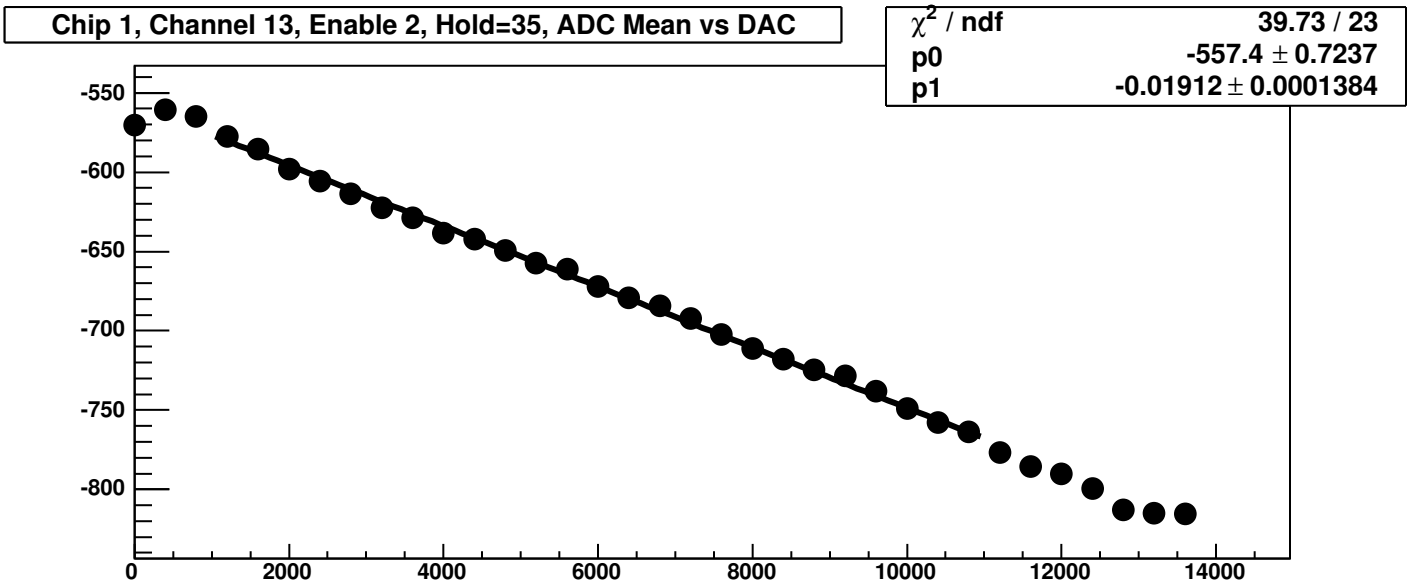

Chip 1, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

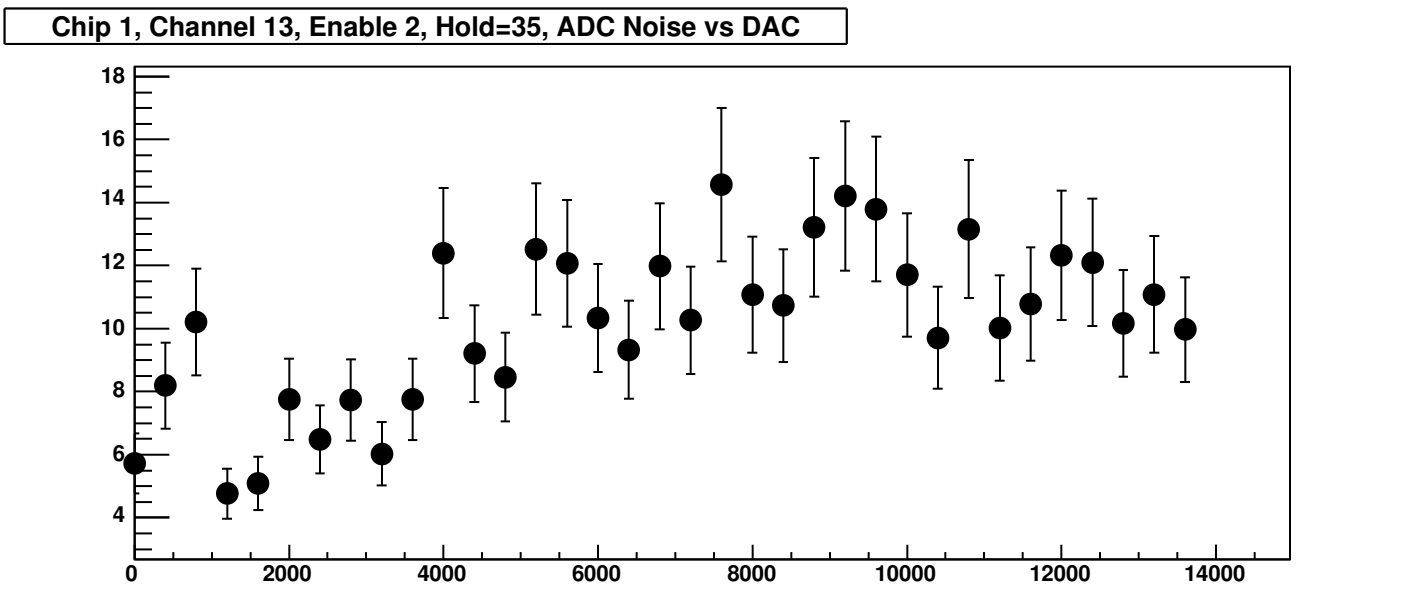

Chip 1, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

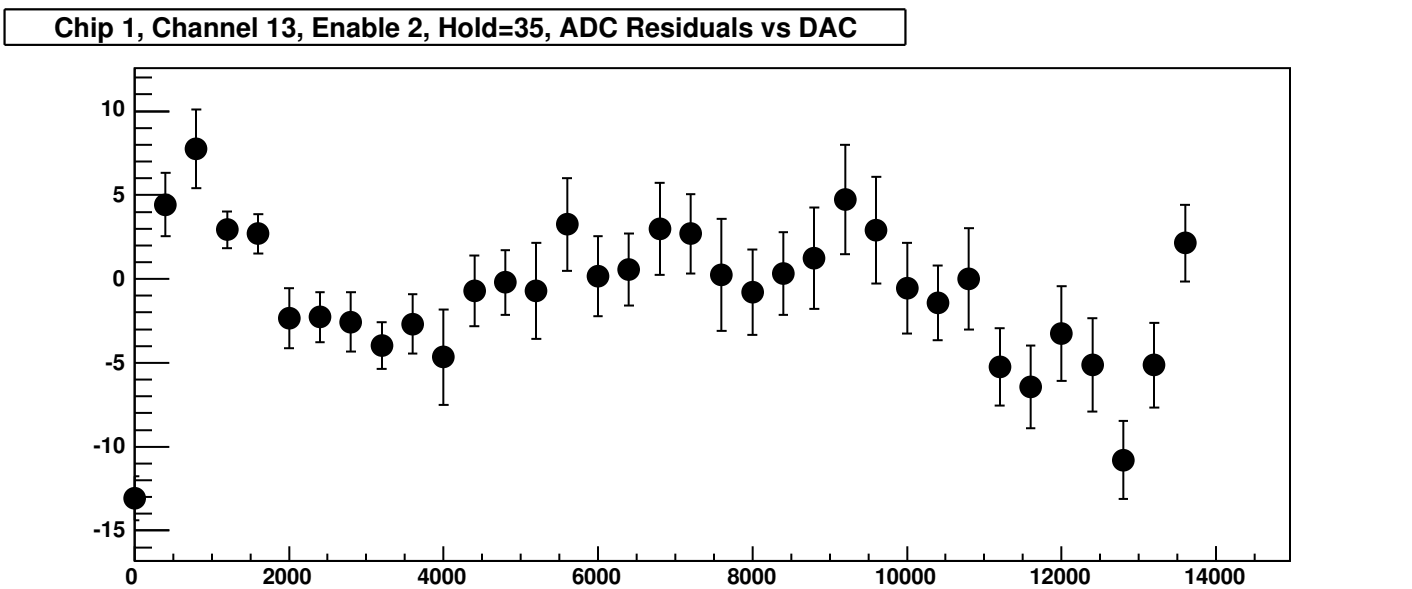

Chip 1, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 414.8 / 23
p0 -499.9 ± 2.538
p1 2.257 ± 0.001367
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

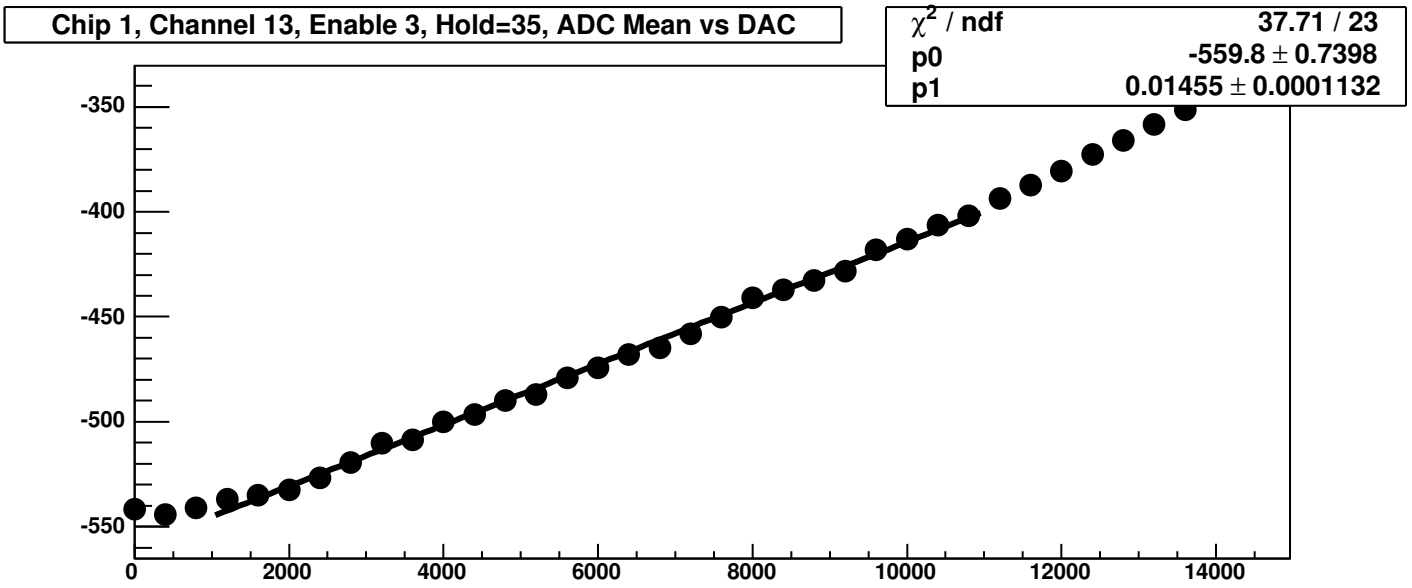

Chip 1, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

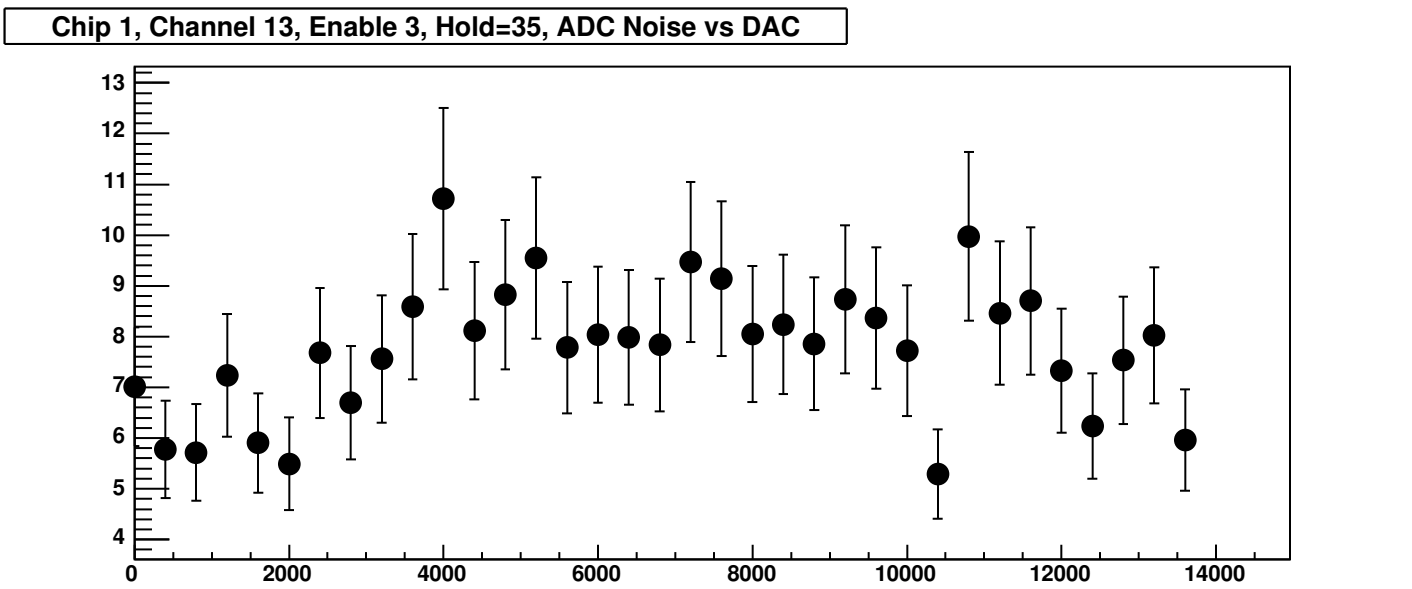

Chip 1, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

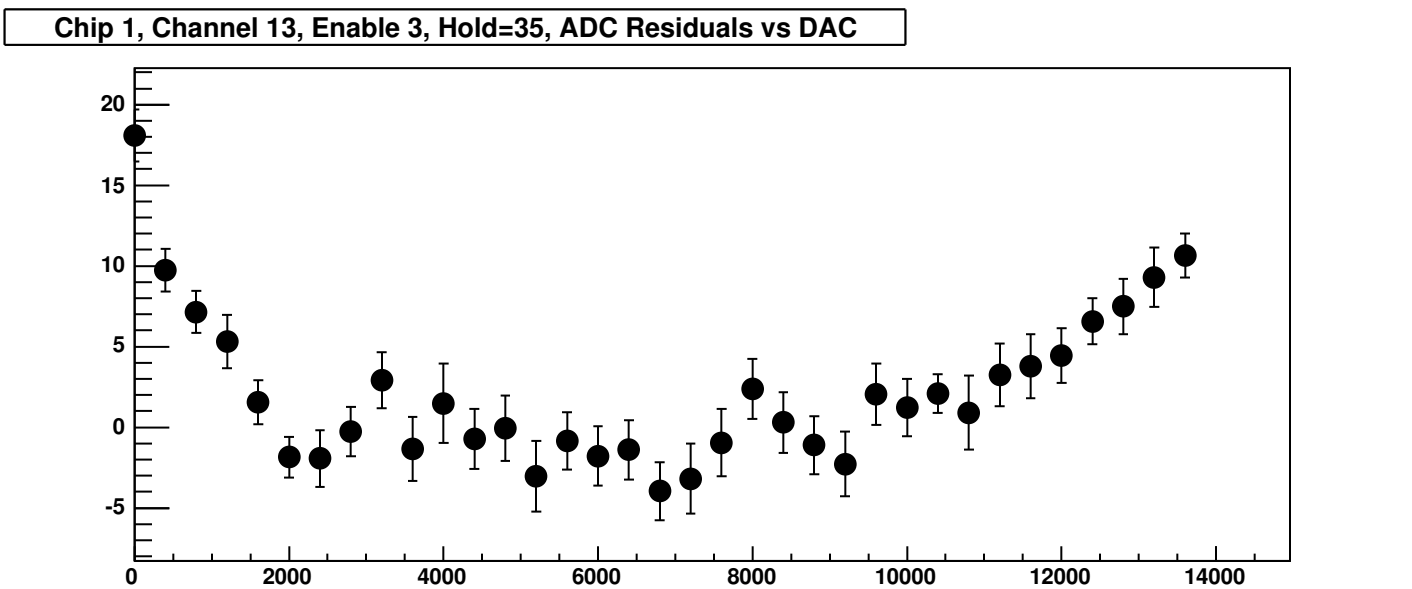

Chip 1, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 39.73 / 23
p0 -557.4 ± 0.7237
p1 -0.01912 ± 0.0001384
-550
-600
-650
-700
-750
-800
0
2000
4000
6000
8000
10000
12000
14000

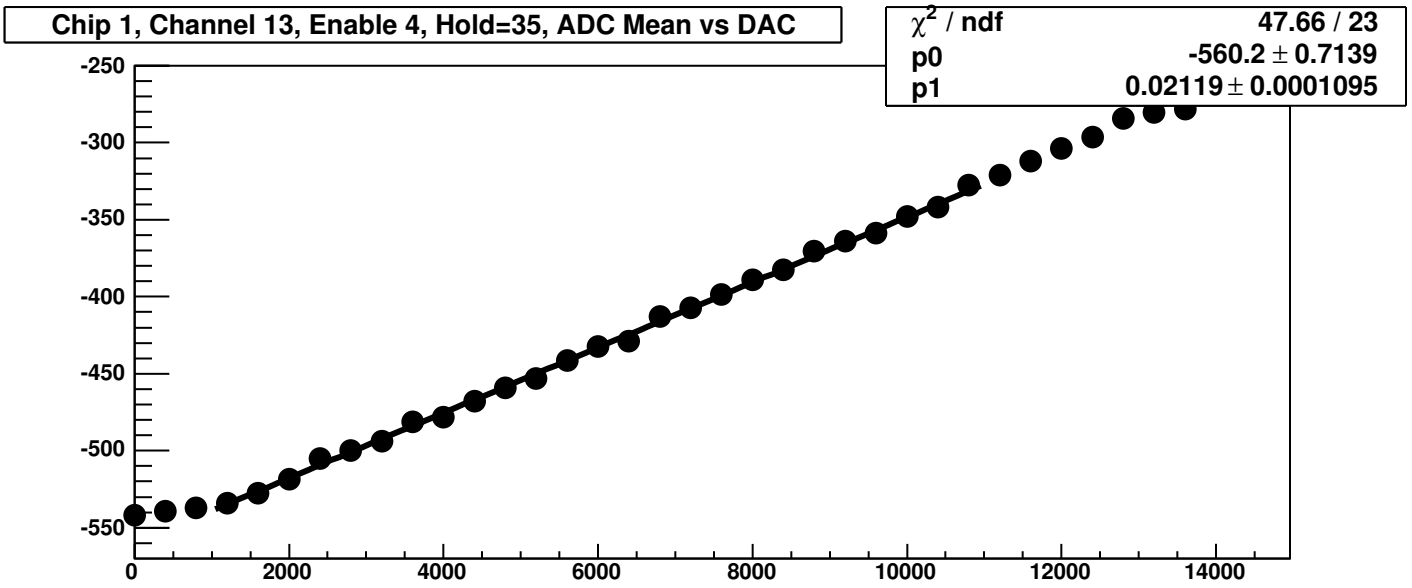

Chip 1, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

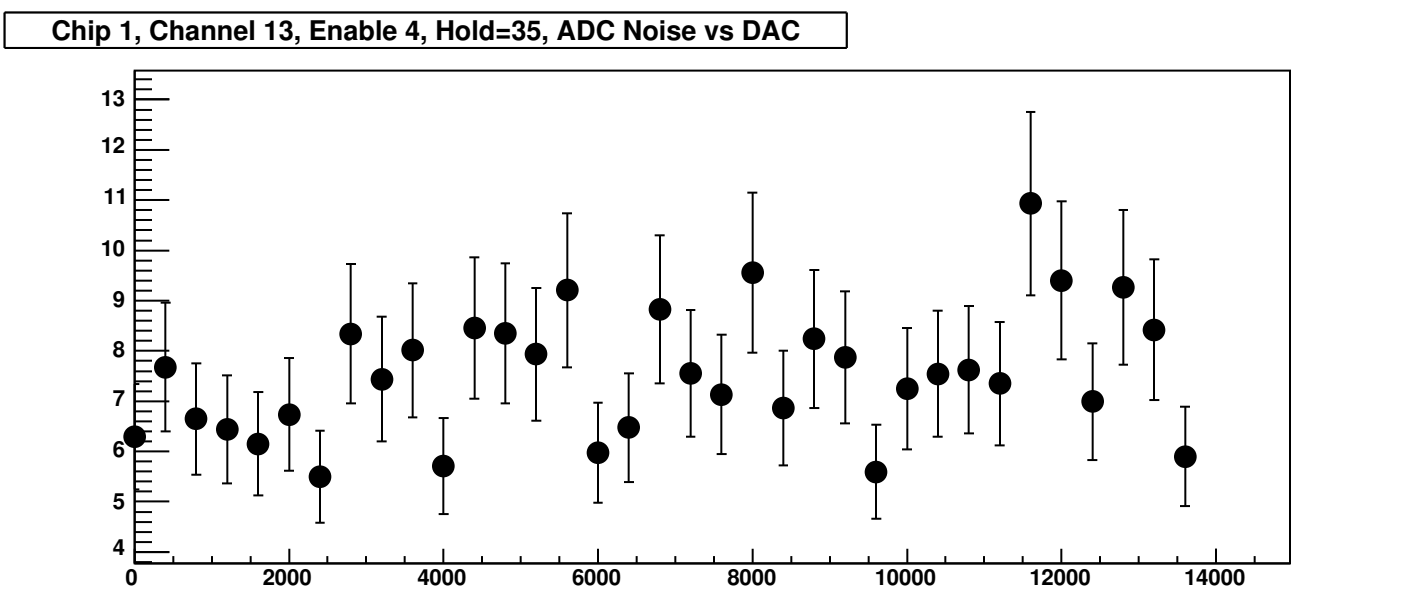

Chip 1, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

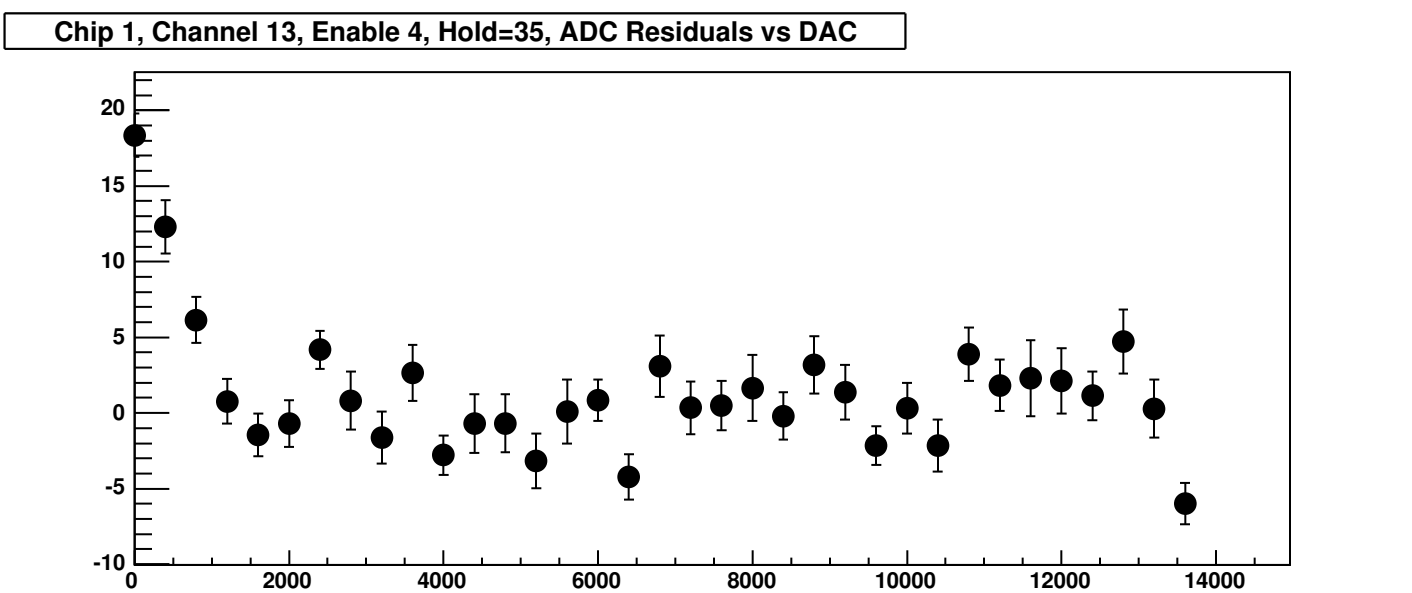

Chip 1, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 37.71 / 23
p0 -559.8 ± 0.7398
p1 0.01455 ± 0.0001132
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

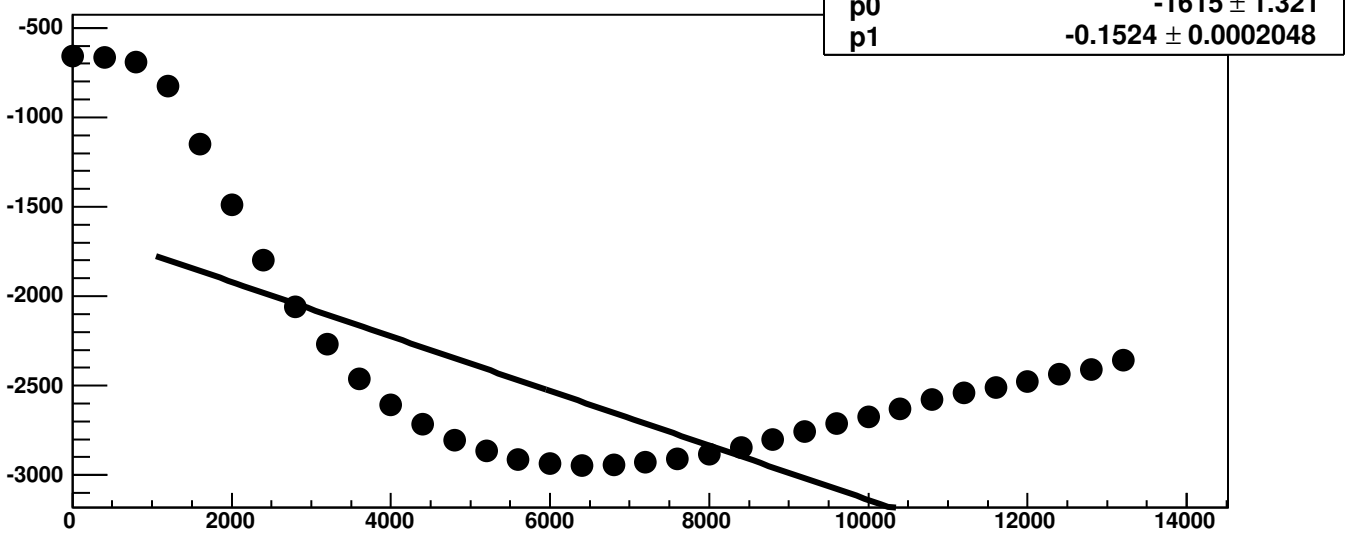

Chip 1, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 47.66 / 23
p0 -560.2 ± 0.7139
p1 0.02119 ± 0.0001095
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

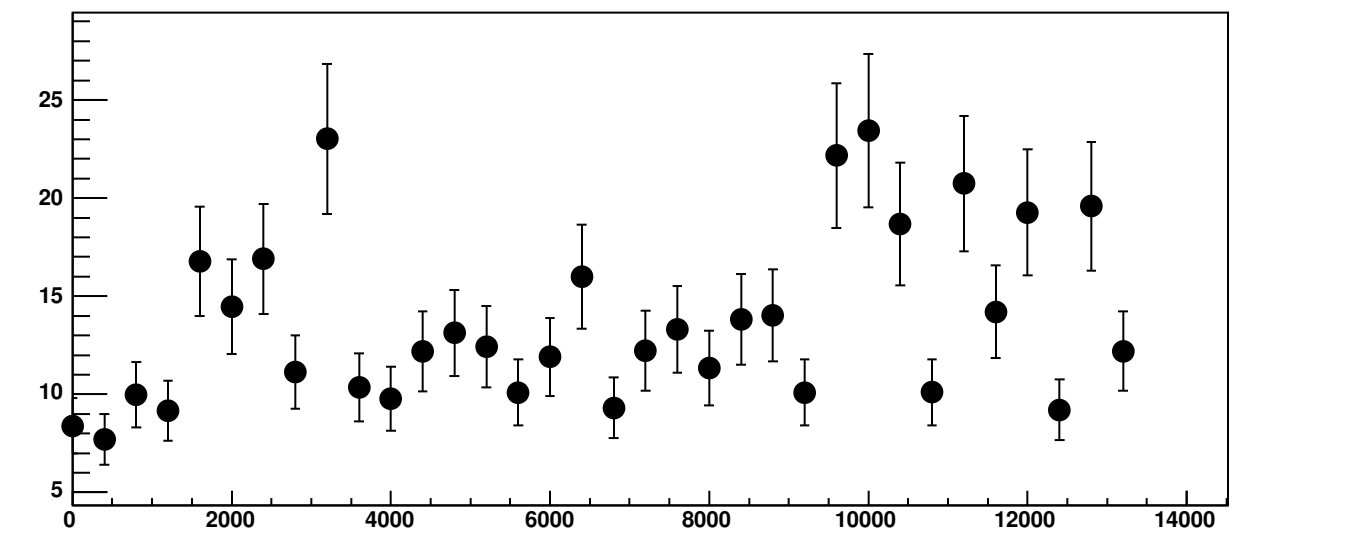

Chip 1, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

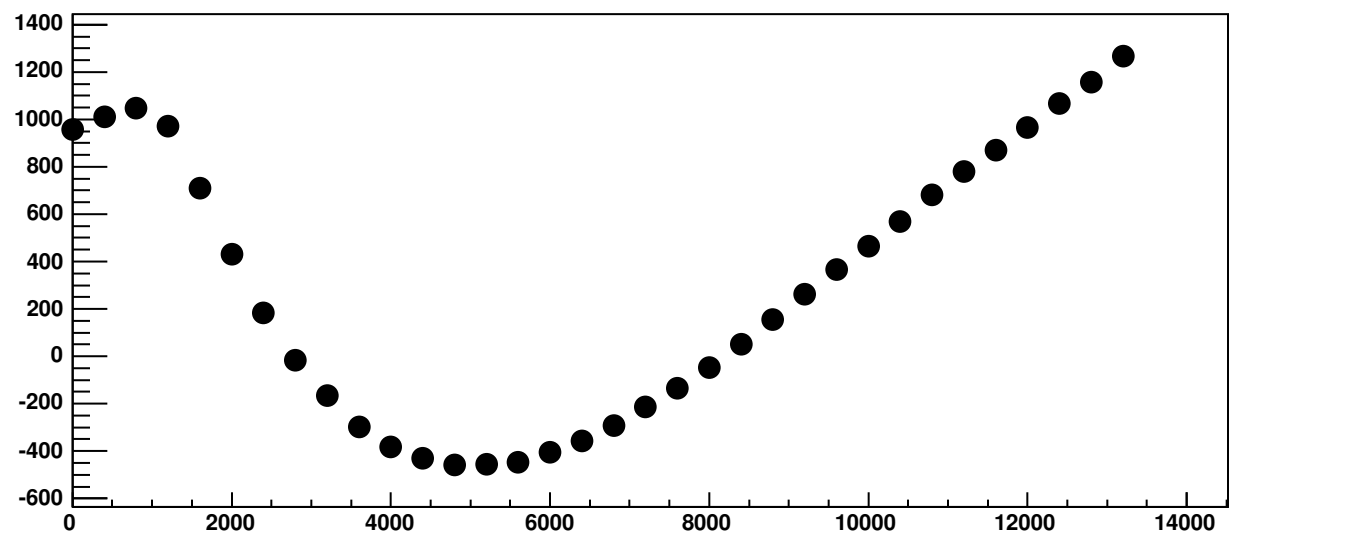
Chip 1, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

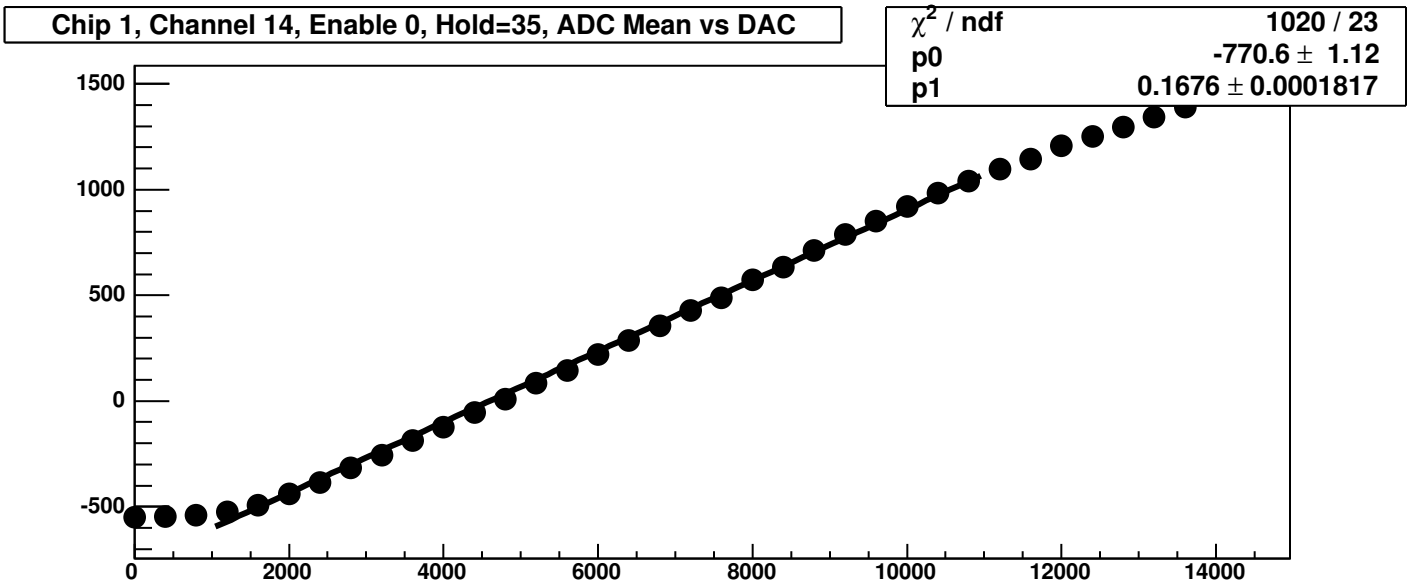


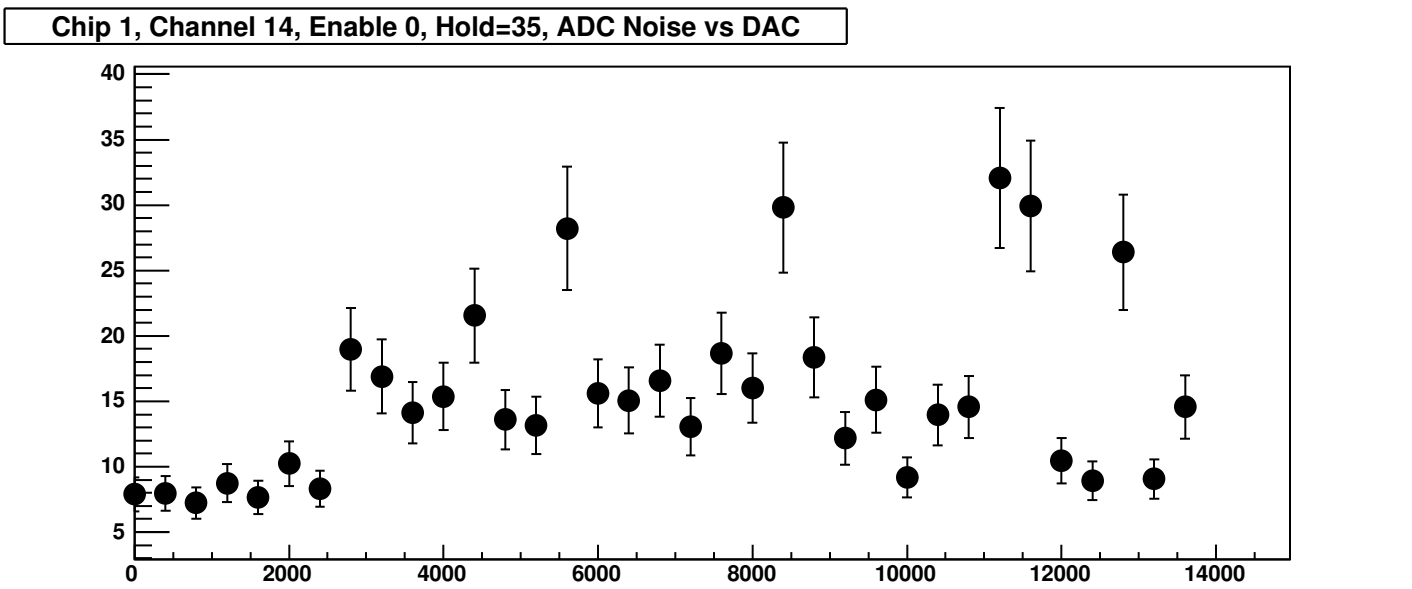
Chip 1, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

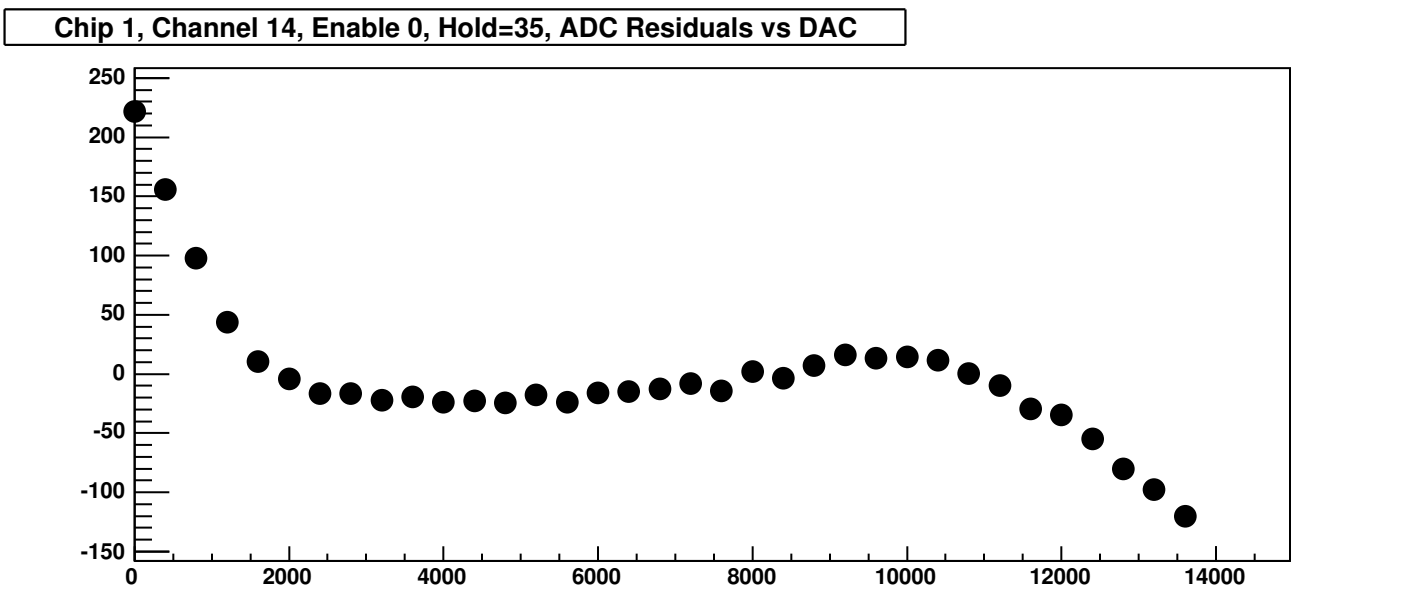


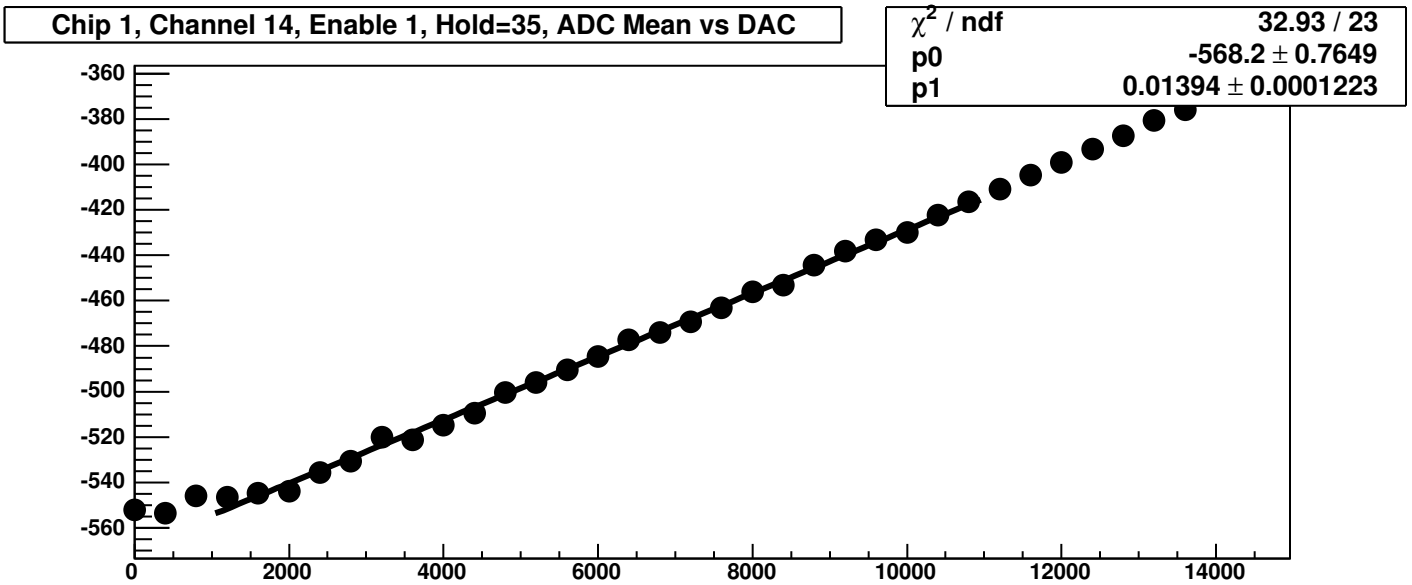
Chip 1, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

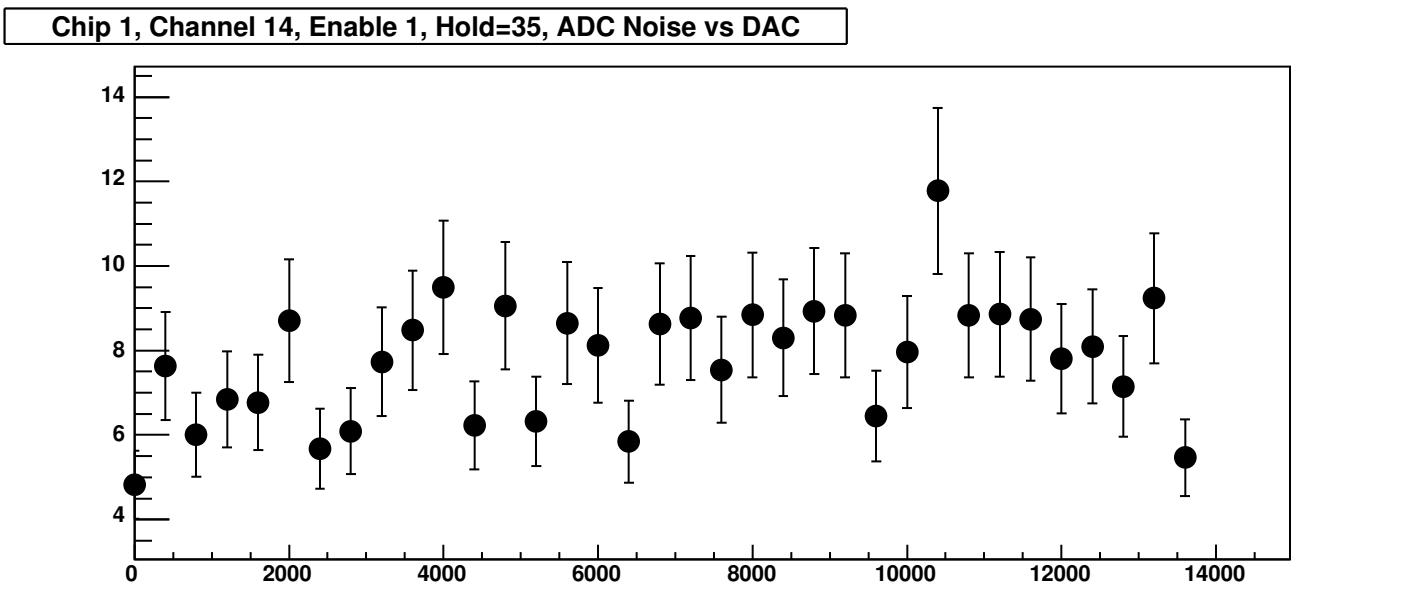


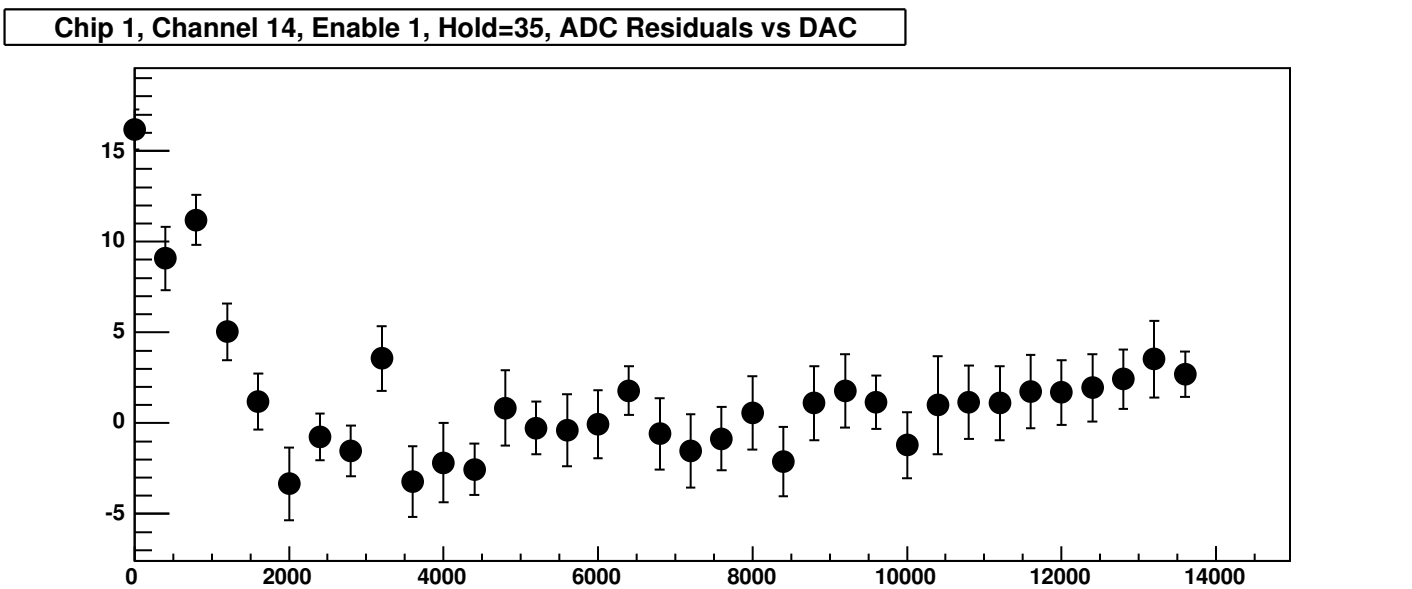

Chip 1, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1020 / 23
p0 -770.6 ± 1.12
p1 0.1676 ± 0.0001817
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

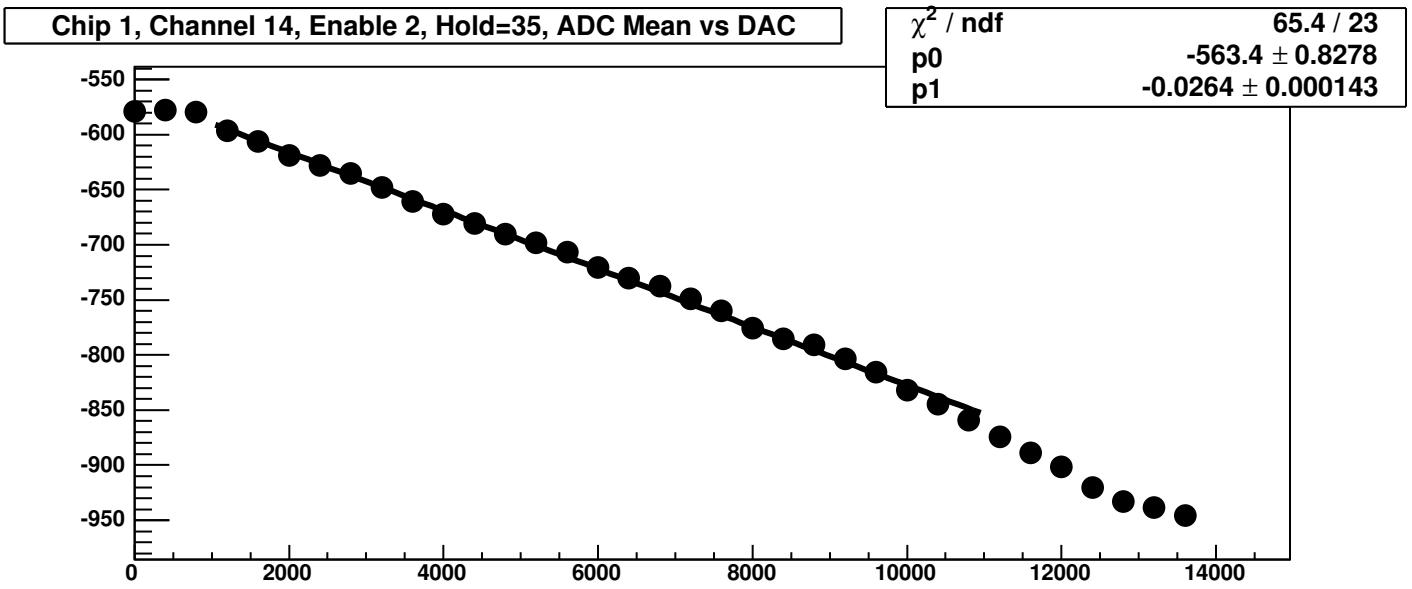

Chip 1, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

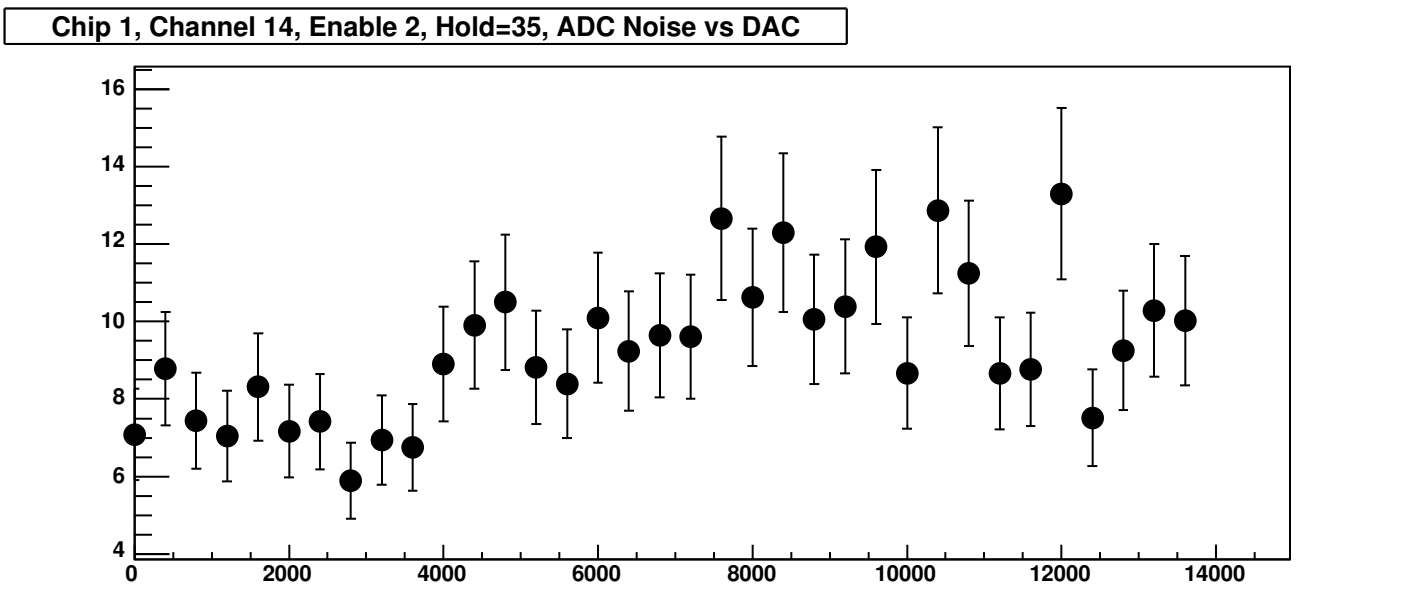

Chip 1, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

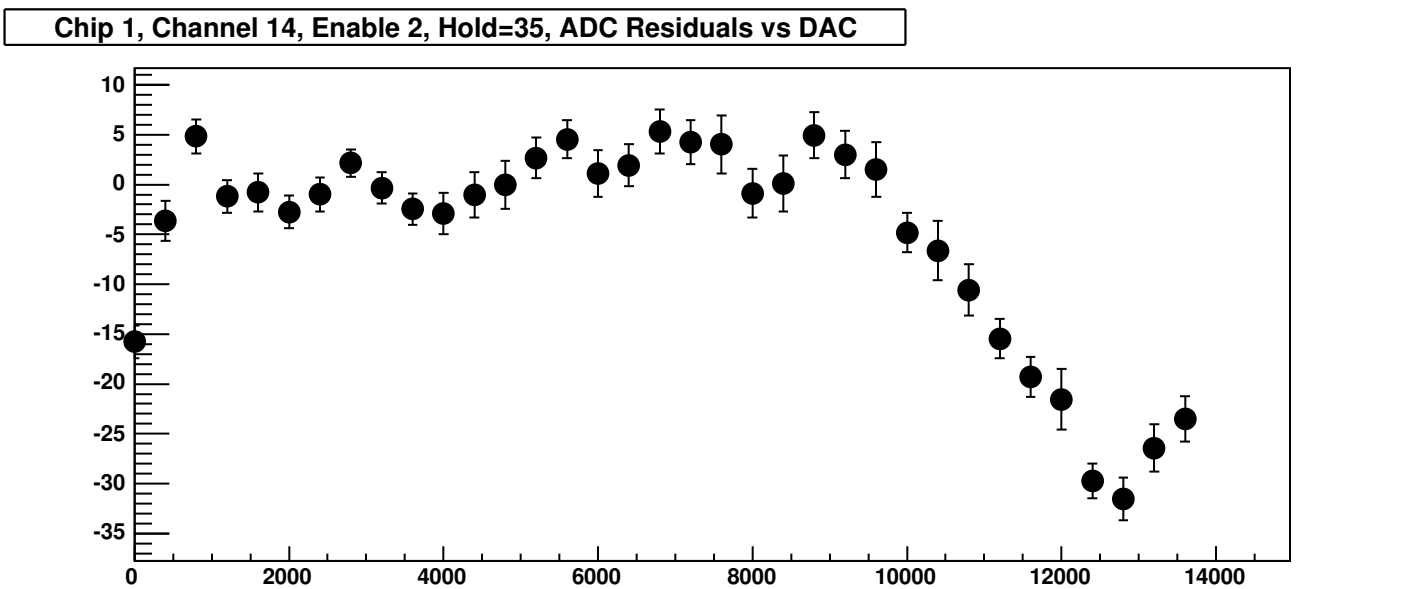

Chip 1, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.93 / 23
p0 -568.2 ± 0.7649
p1 0.01394 ± 0.0001223
-360
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
0
2000
4000
6000
8000
10000
12000
14000

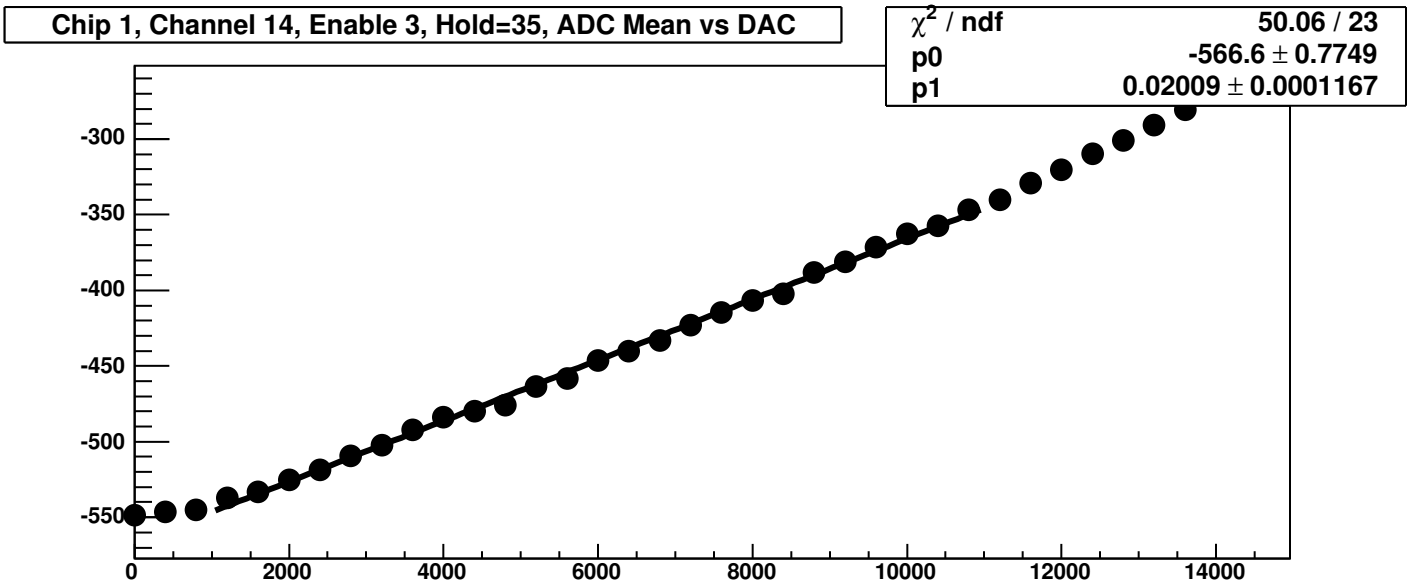

Chip 1, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

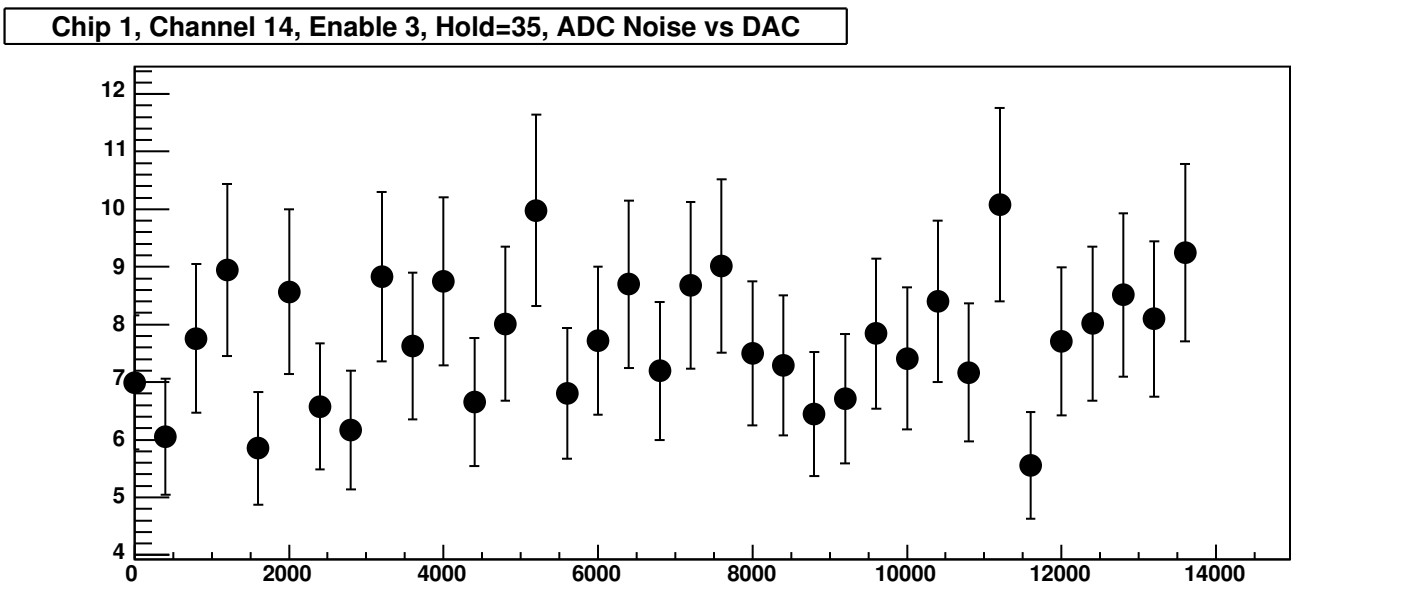

Chip 1, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

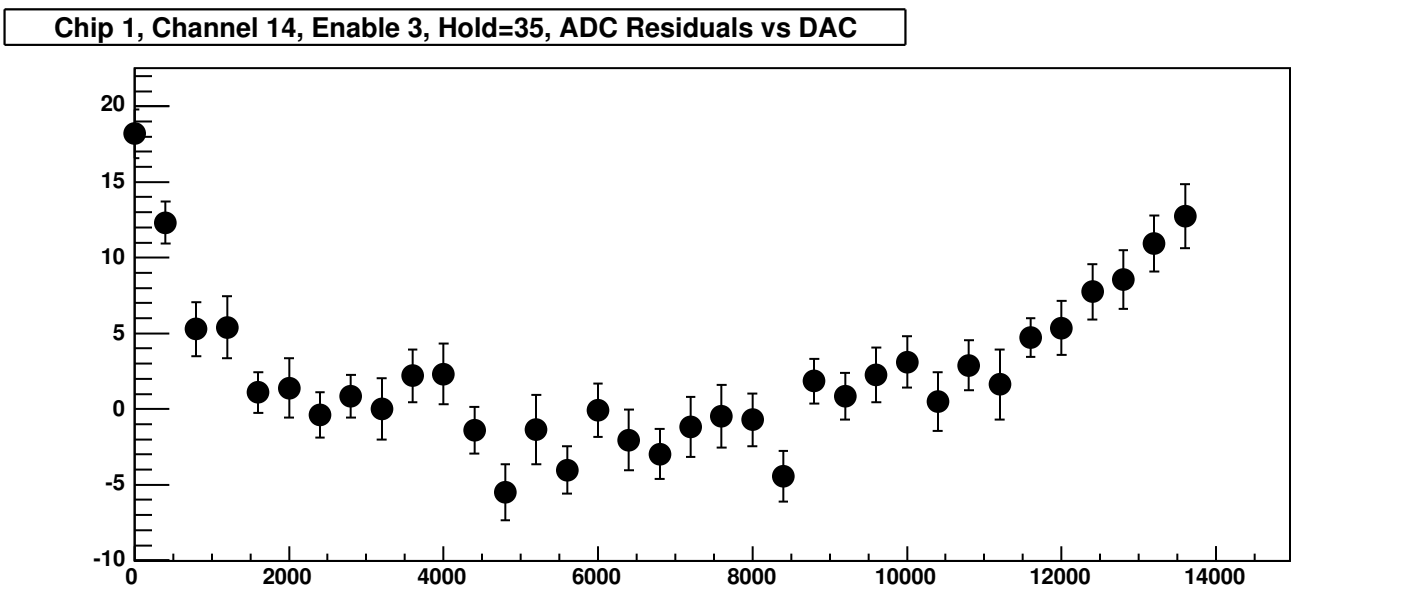

Chip 1, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 65.4 / 23
p0 -563.4 ± 0.8278
p1 -0.0264 ± 0.000143
-550
-600
-650
-700
-750
-800
-850
-900
-950
0
2000
4000
6000
8000
10000
12000
14000

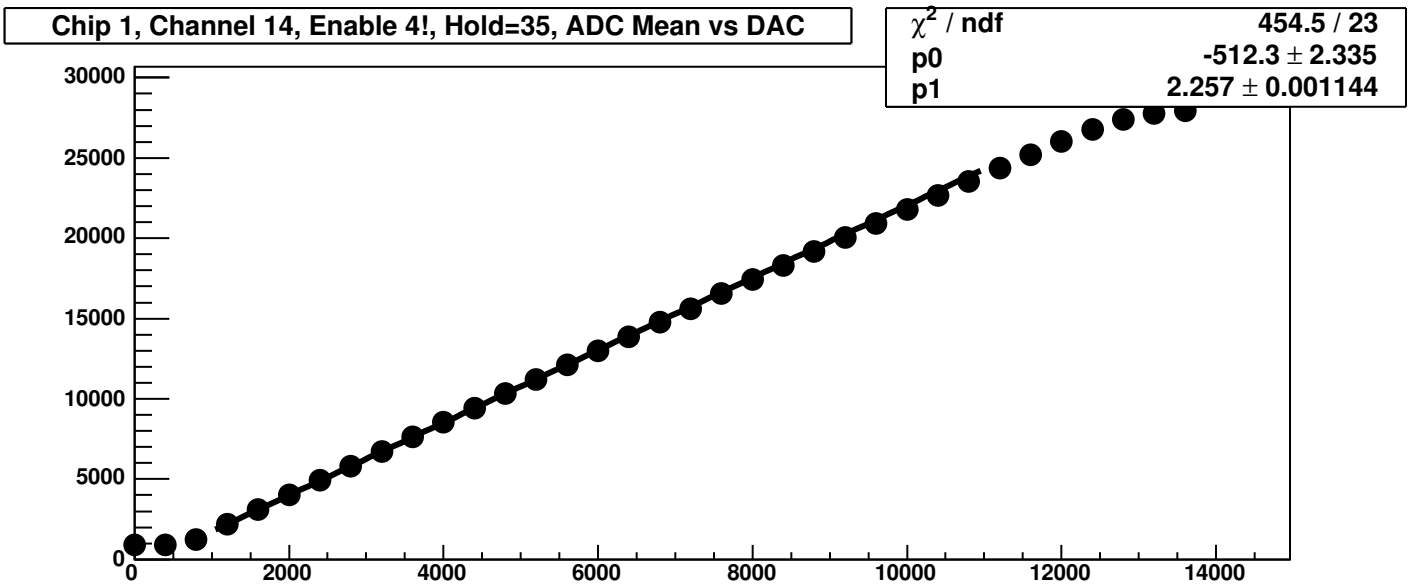

Chip 1, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

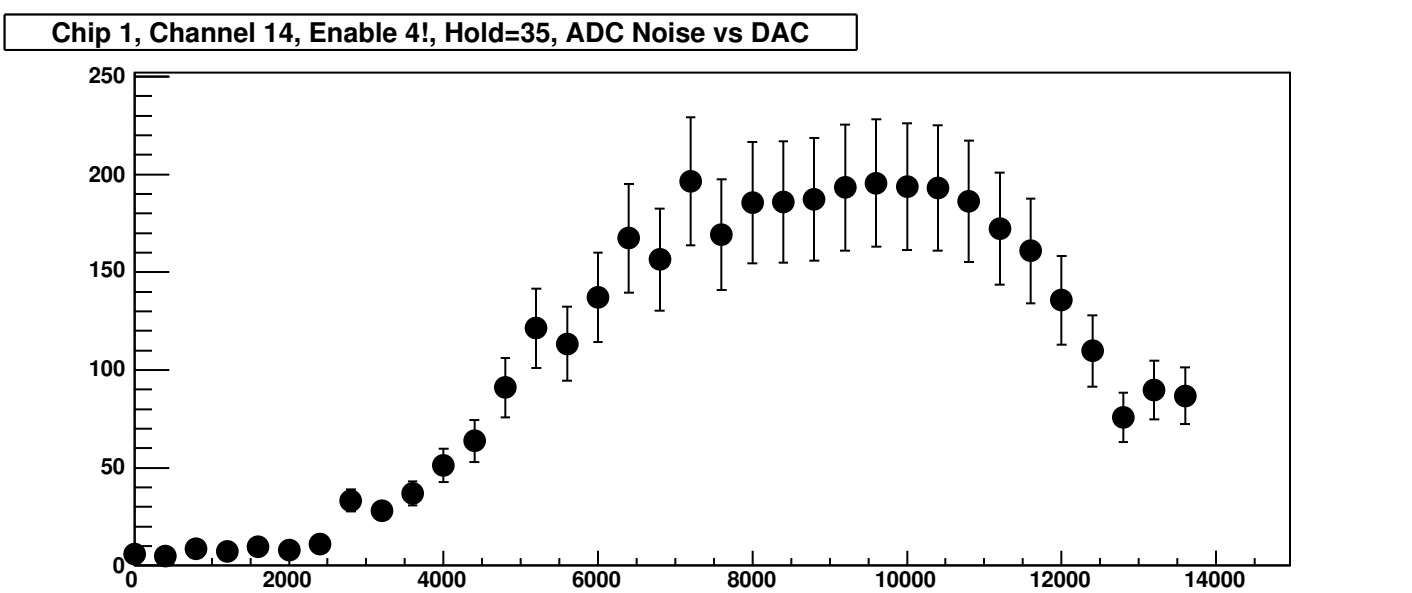

Chip 1, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
-35
0
2000
4000
6000
8000
10000
12000
14000

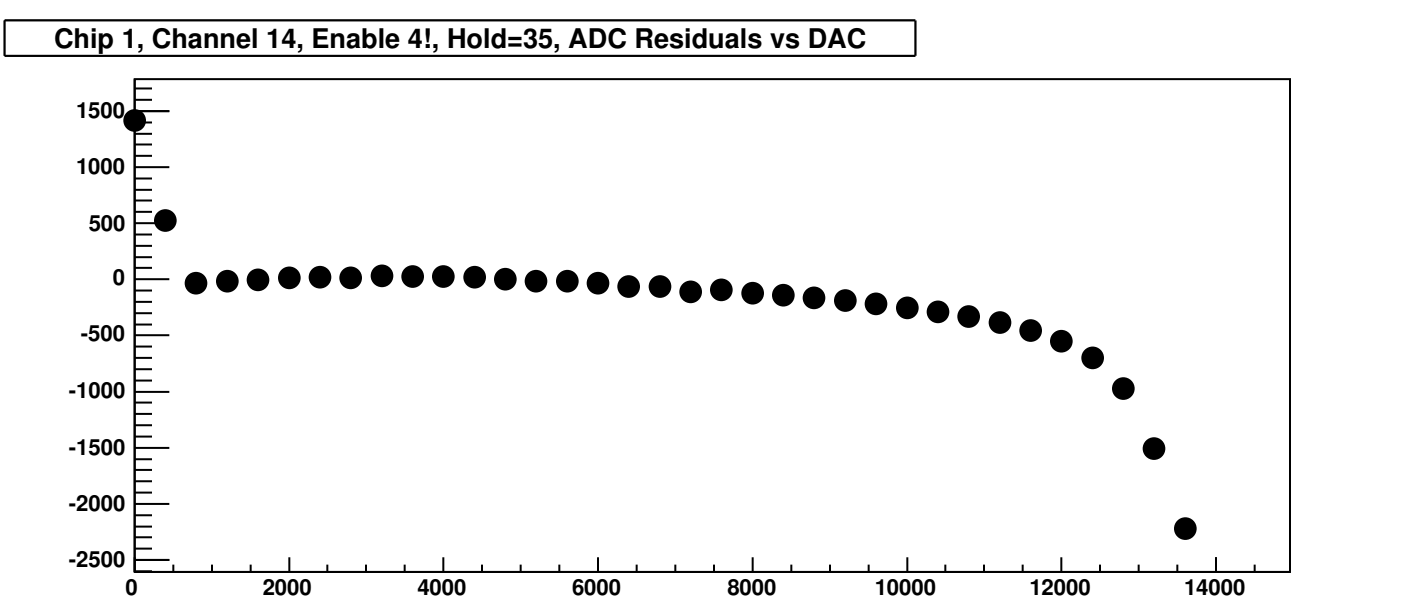

Chip 1, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 50.06 / 23
p0 -566.6 ± 0.7749
p1 0.02009 ± 0.0001167
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

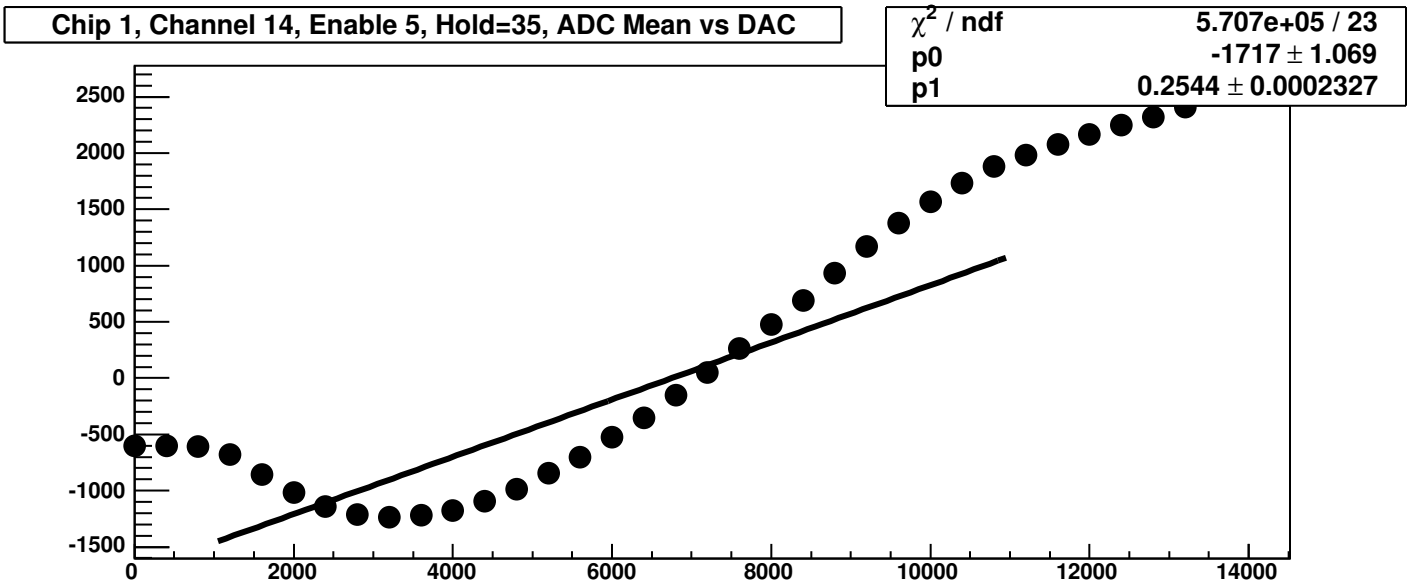

Chip 1, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

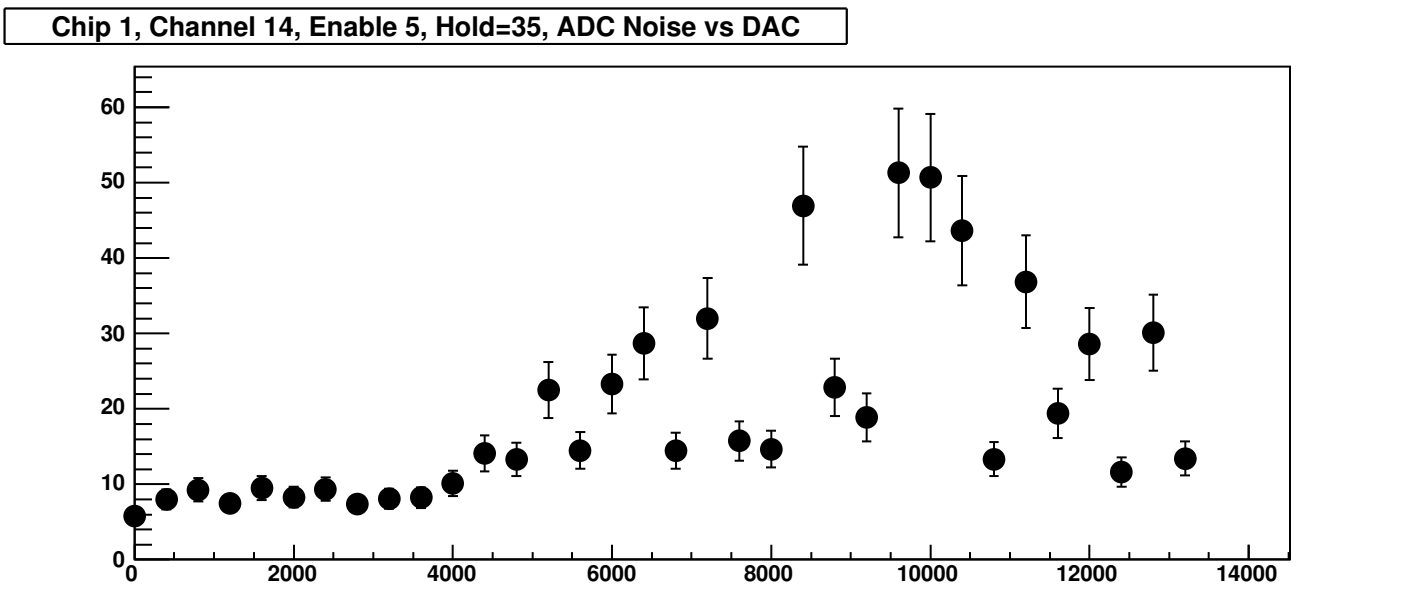

Chip 1, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

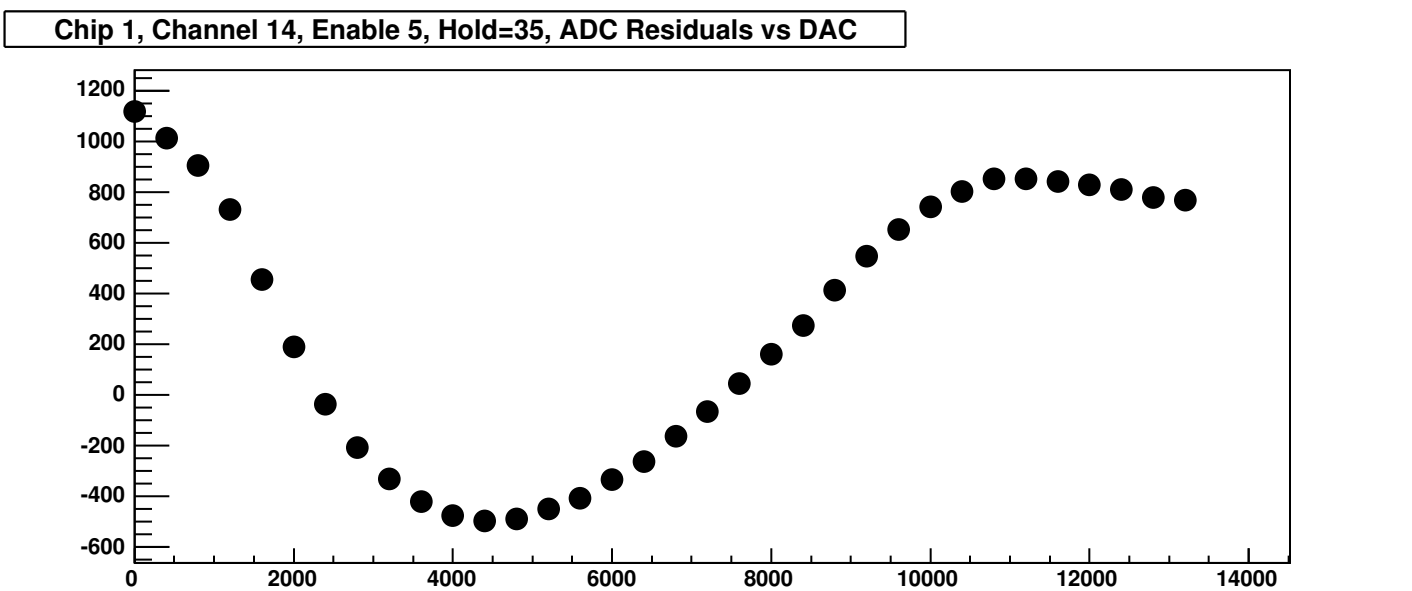

Chip 1, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 454.5 / 23
p0 -512.3 ± 2.335
p1 2.257 ± 0.001144
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

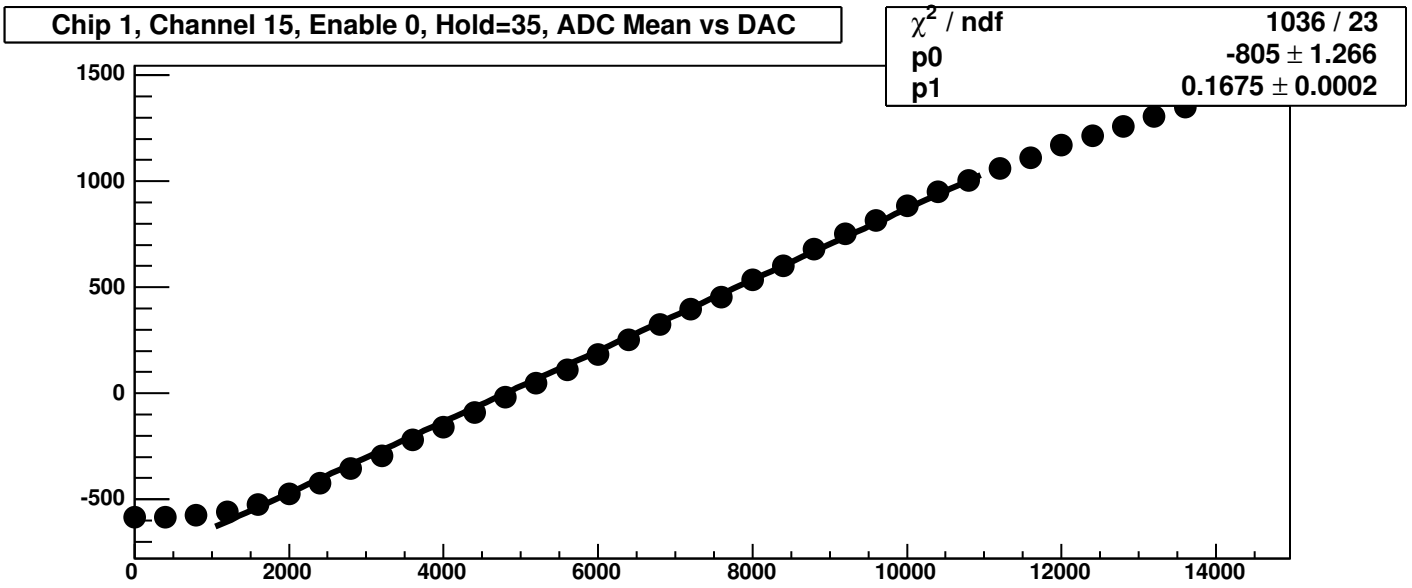

Chip 1, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

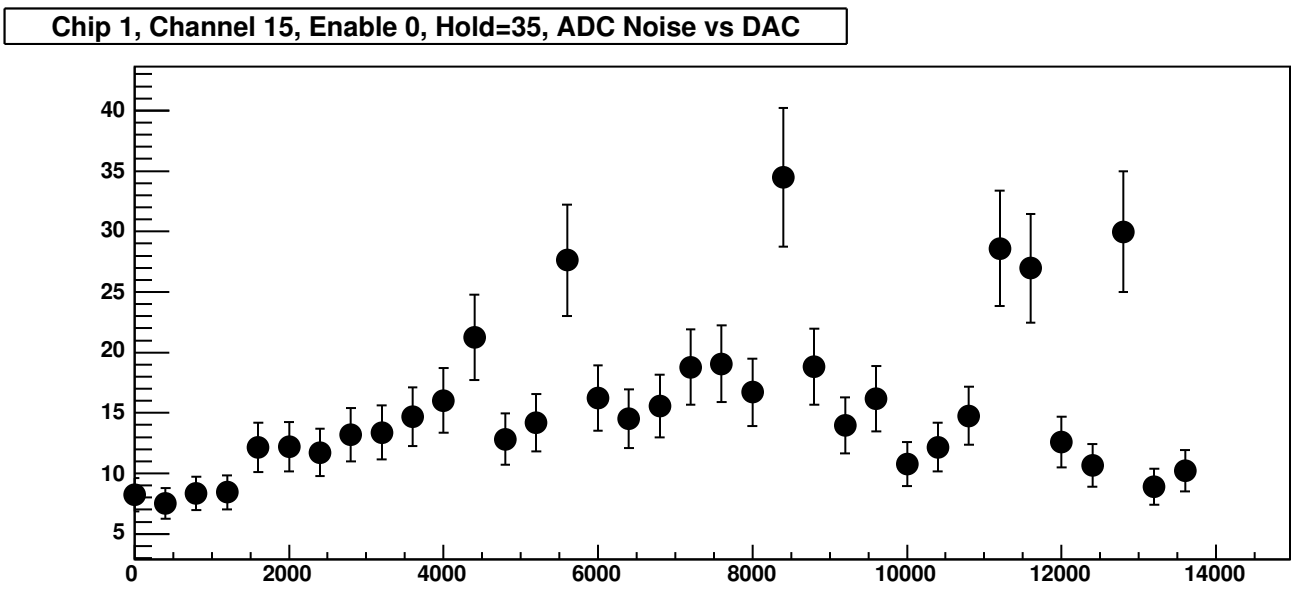

Chip 1, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

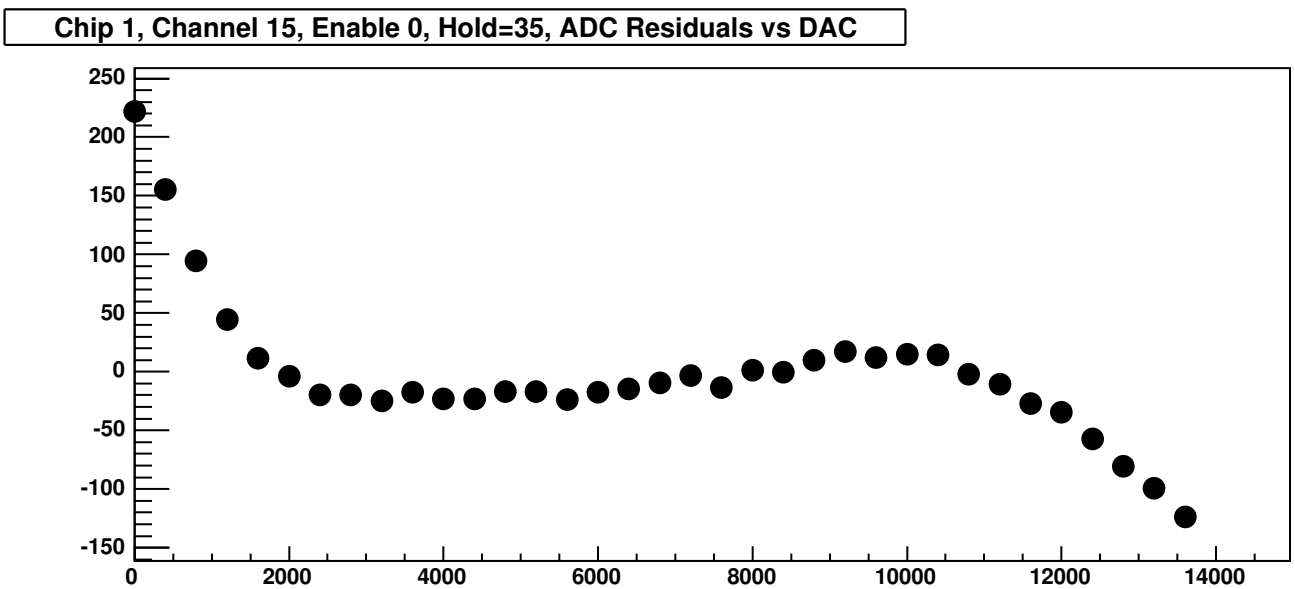

Chip 1, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5.707e+05 / 23
p0 -1717 ± 1.069
p1 0.2544 ± 0.0002327
2500
2000
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

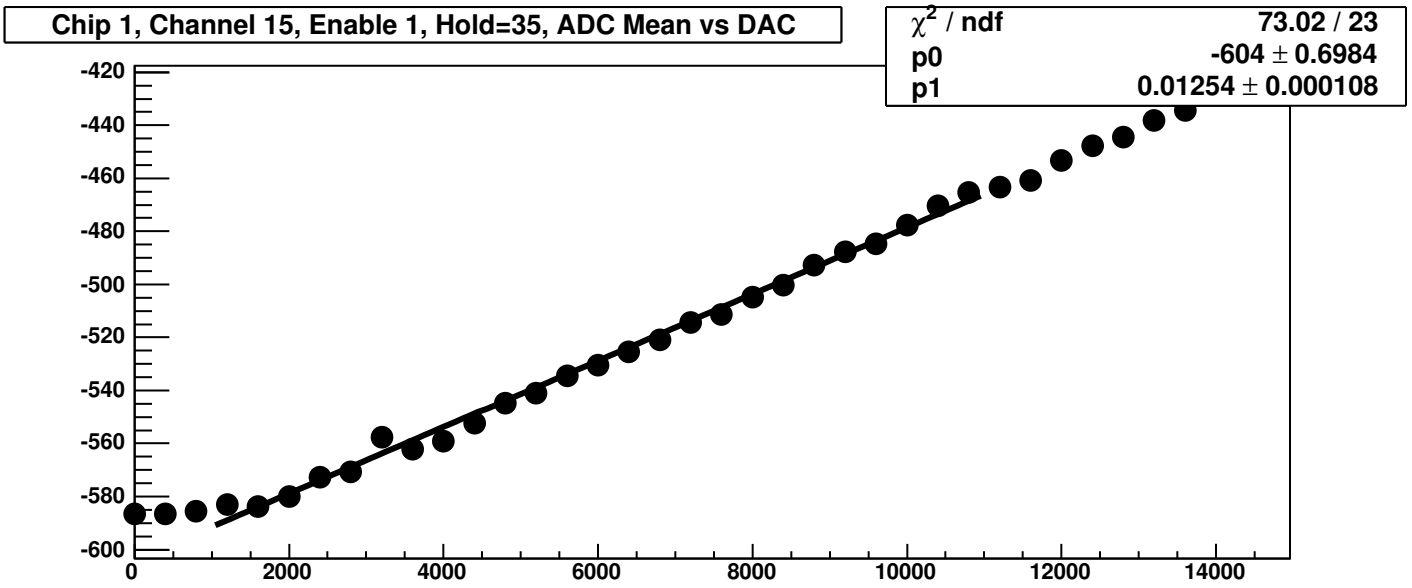

Chip 1, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

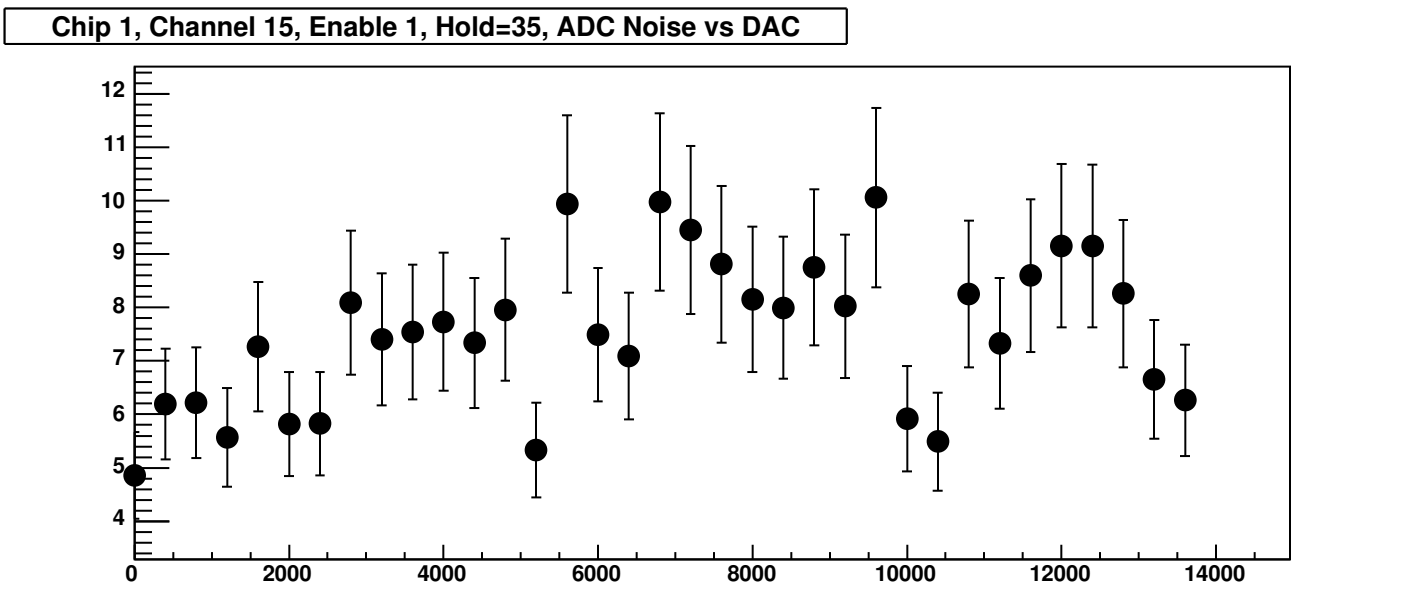

Chip 1, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

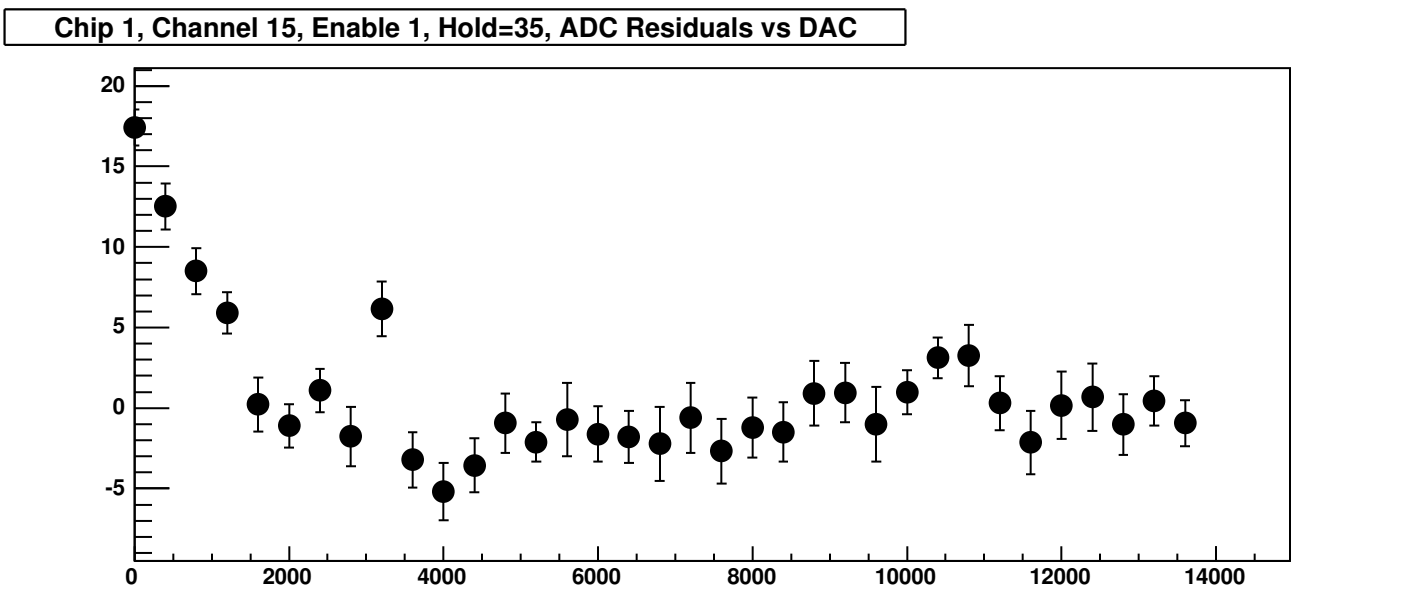

Chip 1, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1036 / 23
p0 -805 ± 1.266
p1 0.1675 ± 0.0002
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

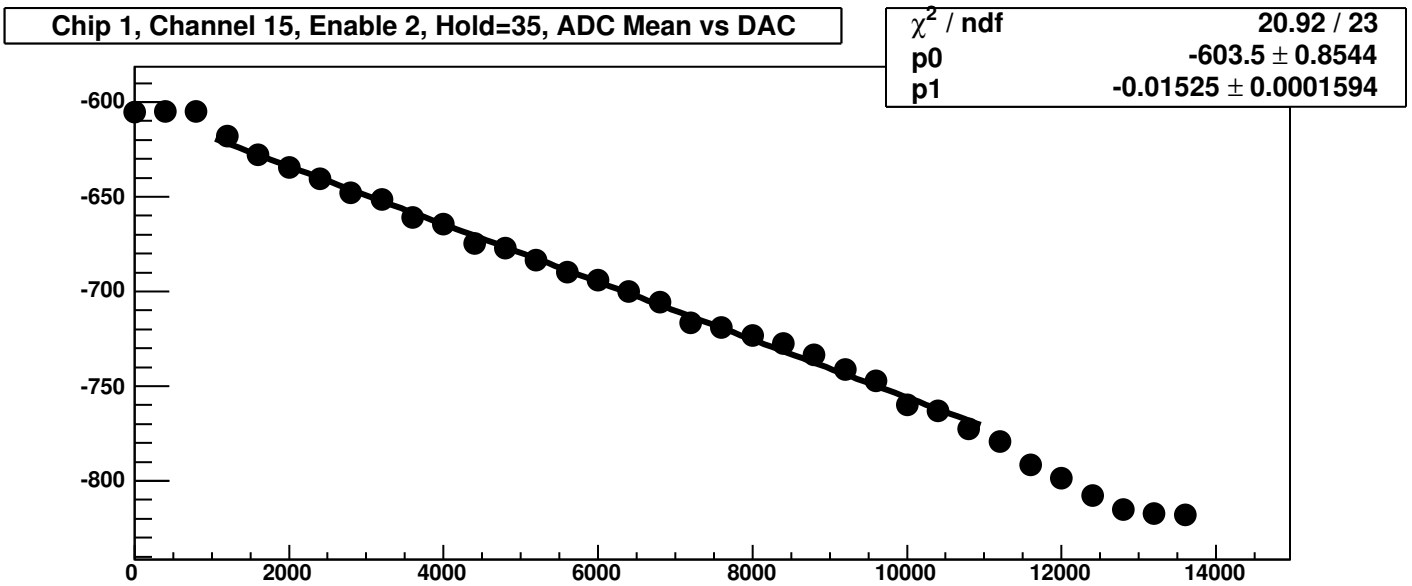

Chip 1, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

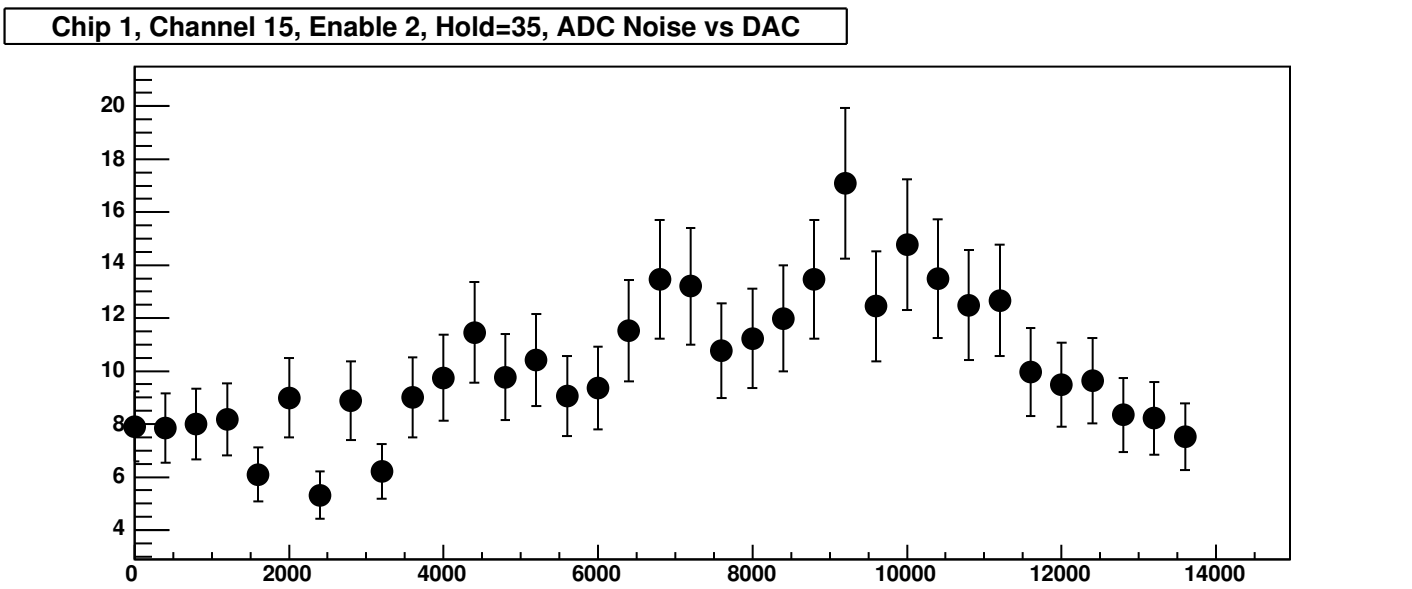

Chip 1, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

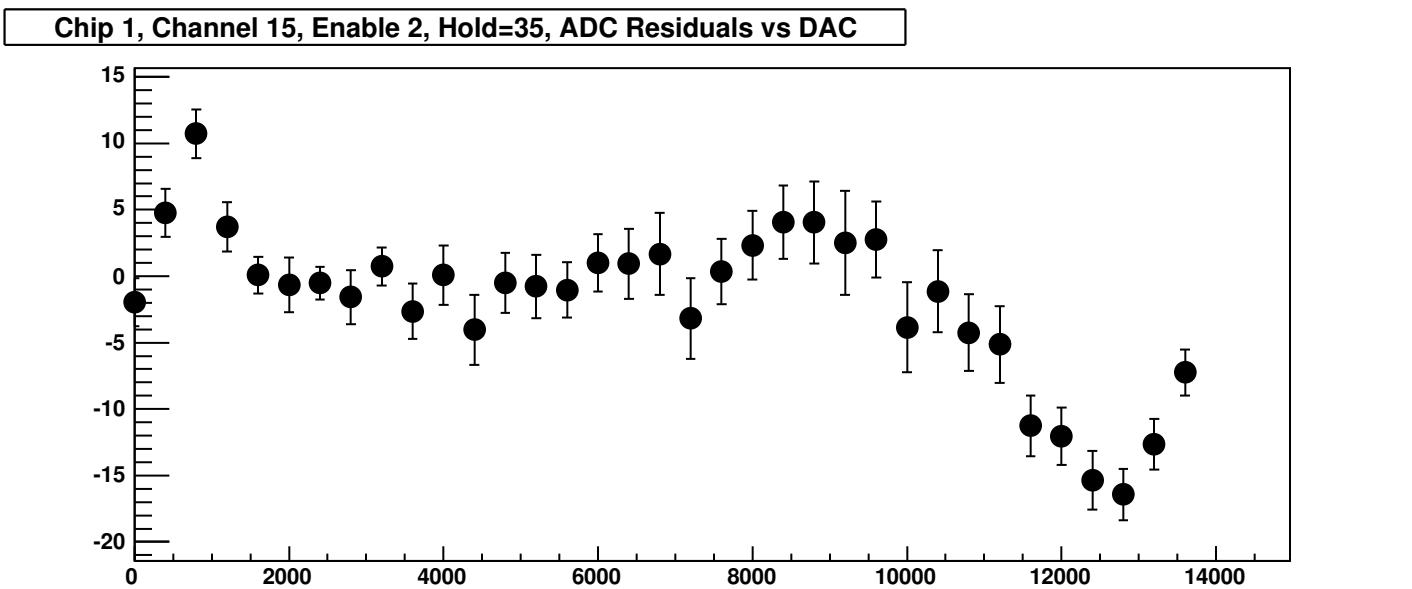

Chip 1, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 73.02 / 23
p0 -604 ± 0.6984
p1 0.01254 ± 0.000108
-420
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
2000
4000
6000
8000
10000
12000
14000

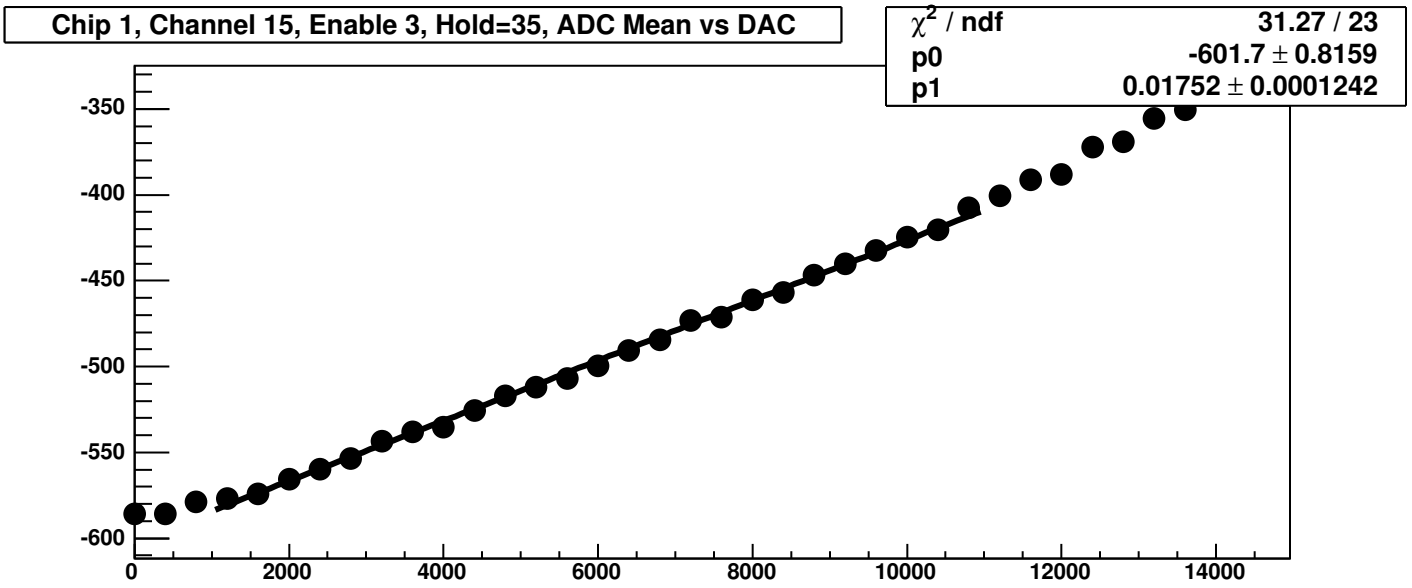

Chip 1, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

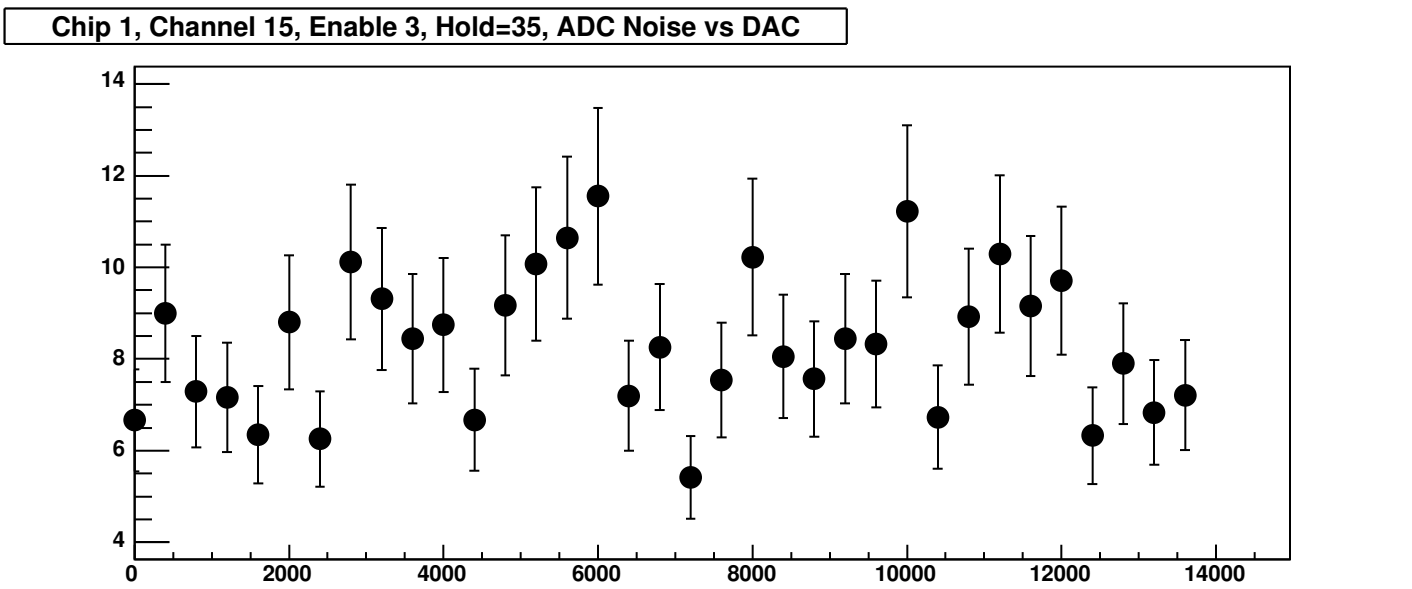

Chip 1, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

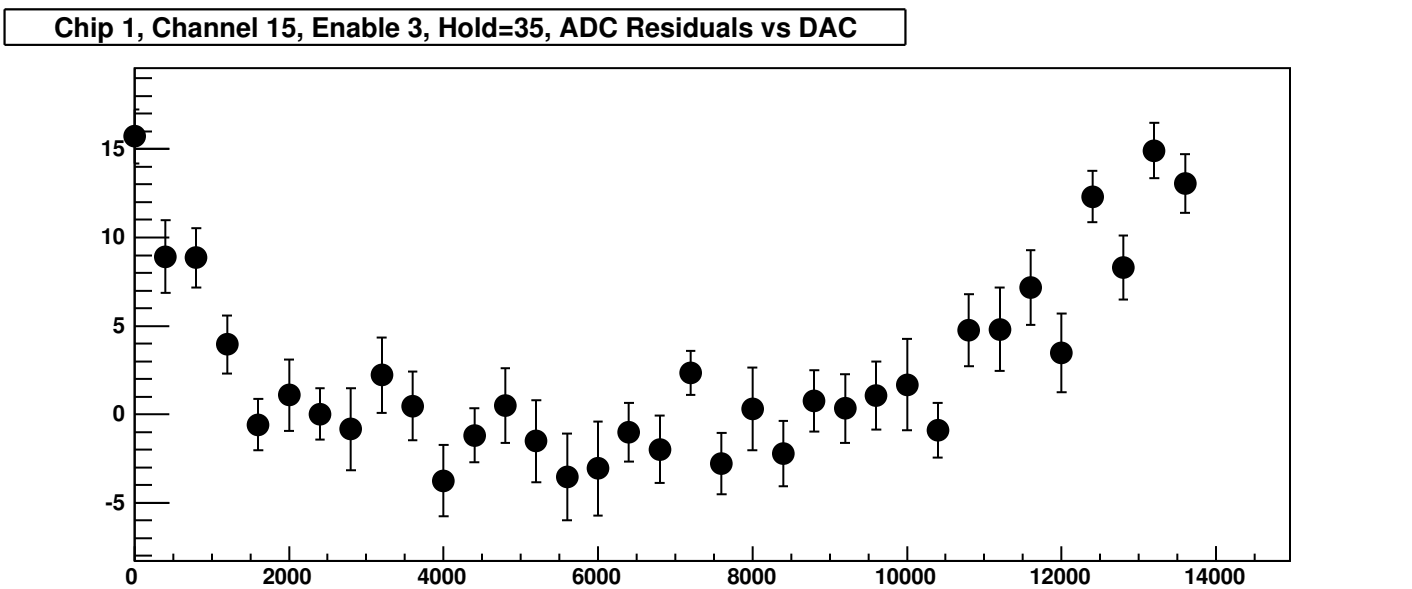

Chip 1, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 20.92 / 23
p0 -603.5 ± 0.8544
p1 -0.01525 ± 0.0001594
-600
-650
-700
-750
-800
0
2000
4000
6000
8000
10000
12000
14000

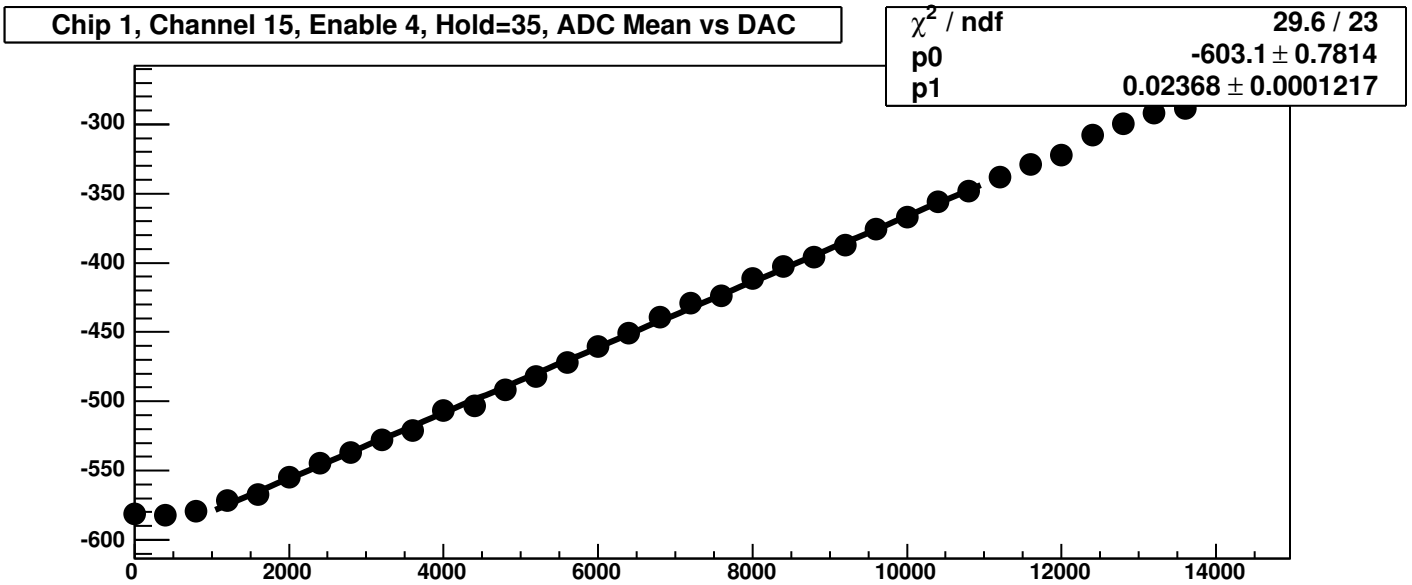

Chip 1, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

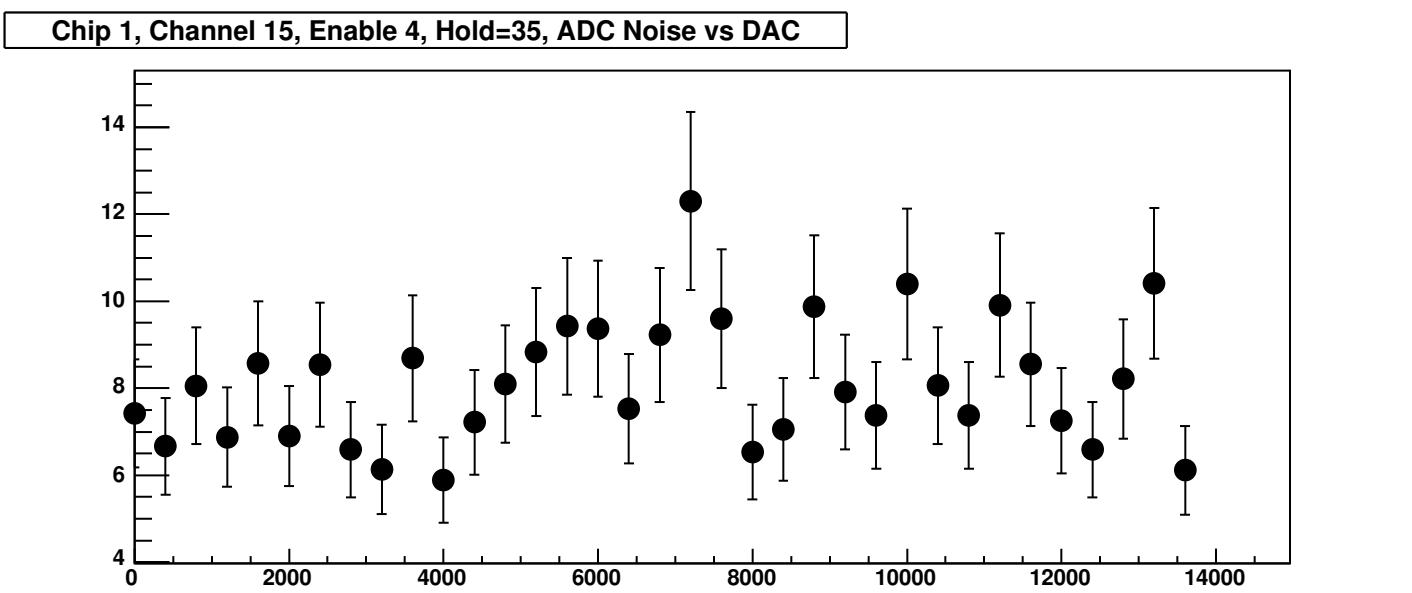

Chip 1, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

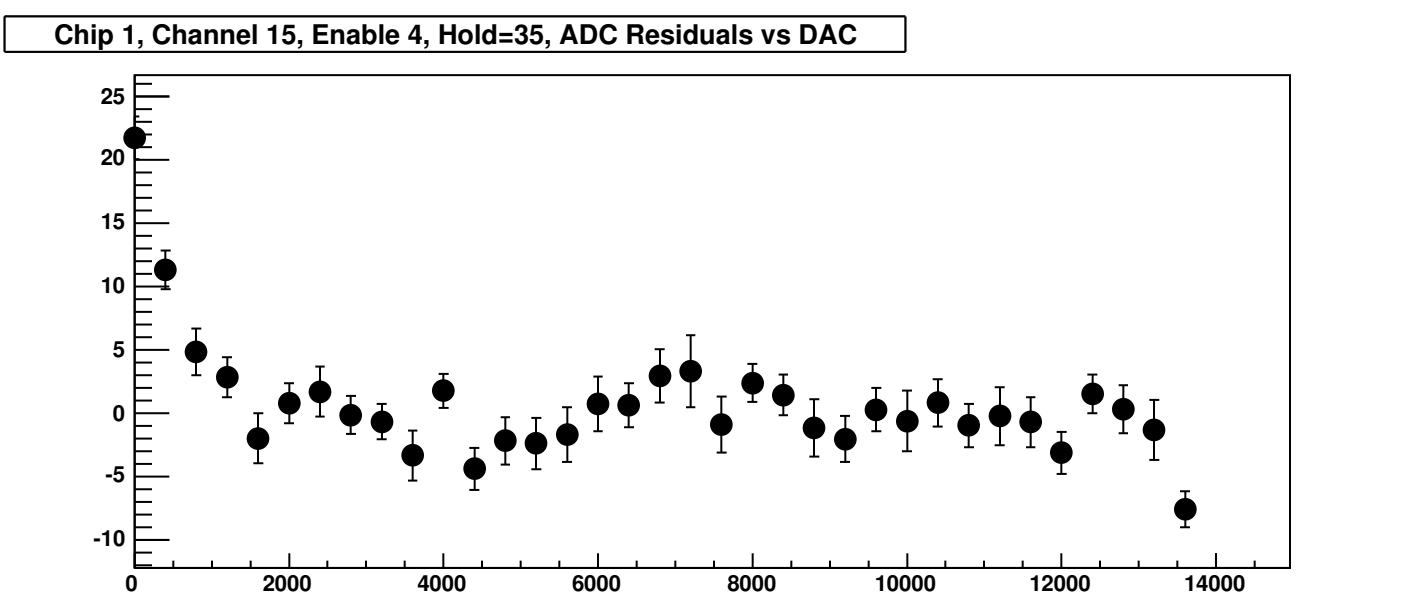

Chip 1, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.27 / 23
p0 -601.7 ± 0.8159
p1 0.01752 ± 0.0001242
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

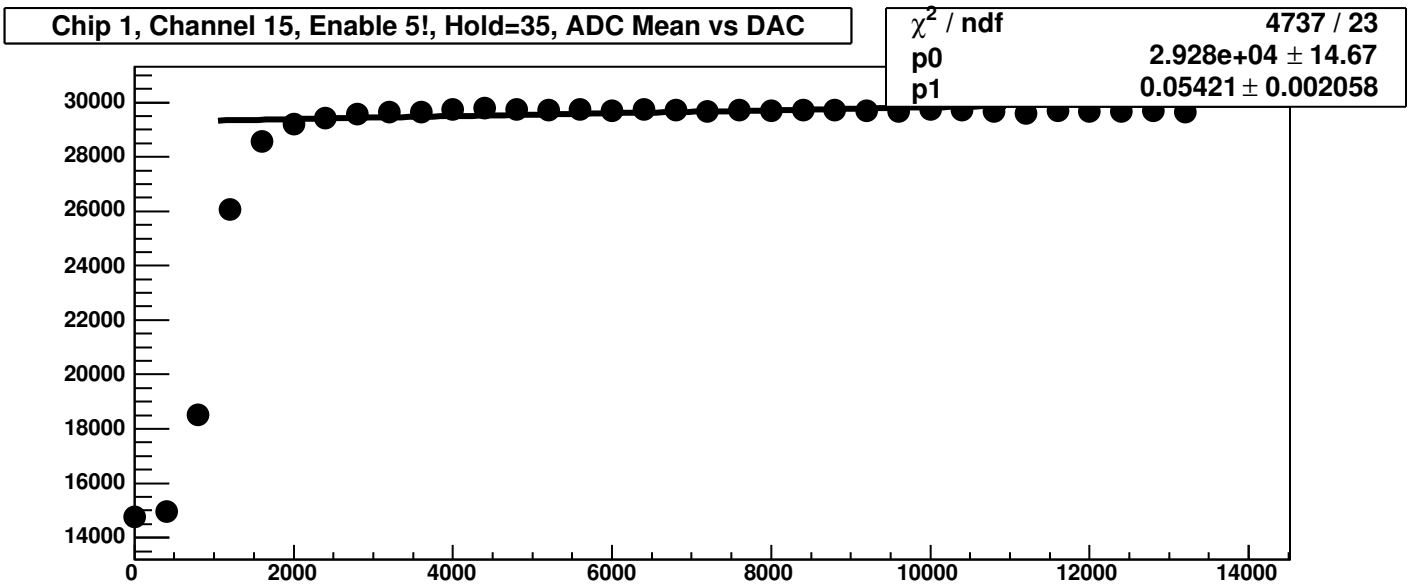

Chip 1, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

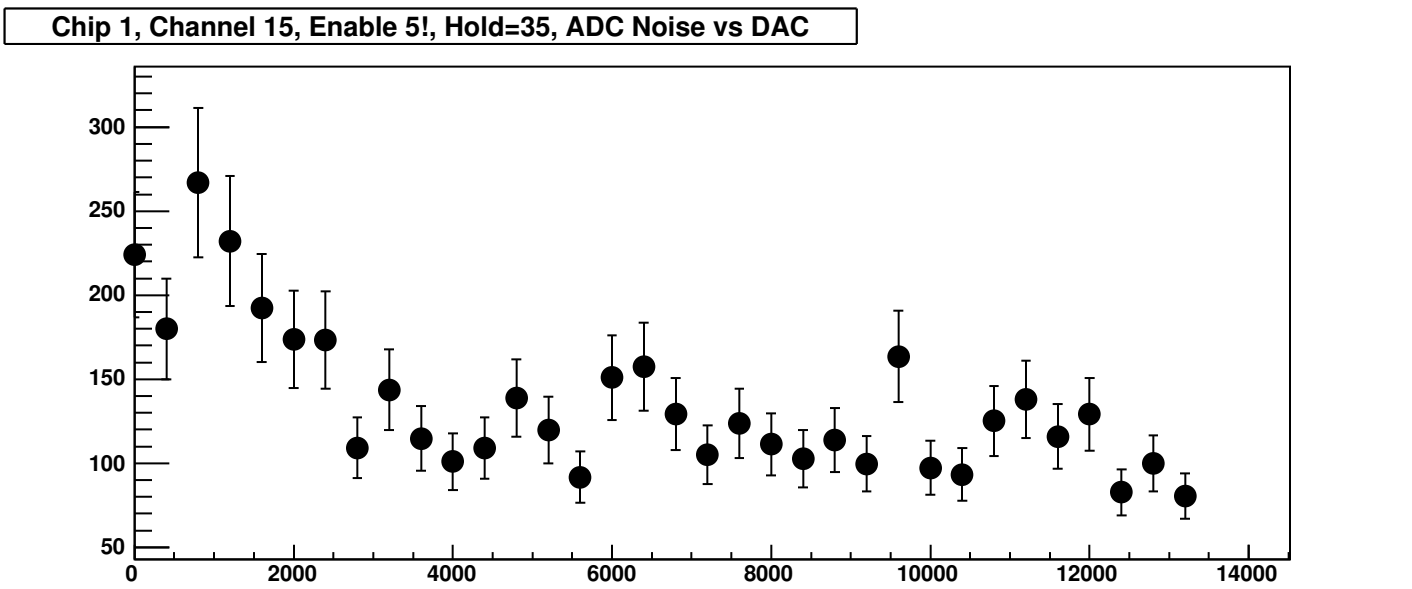

Chip 1, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

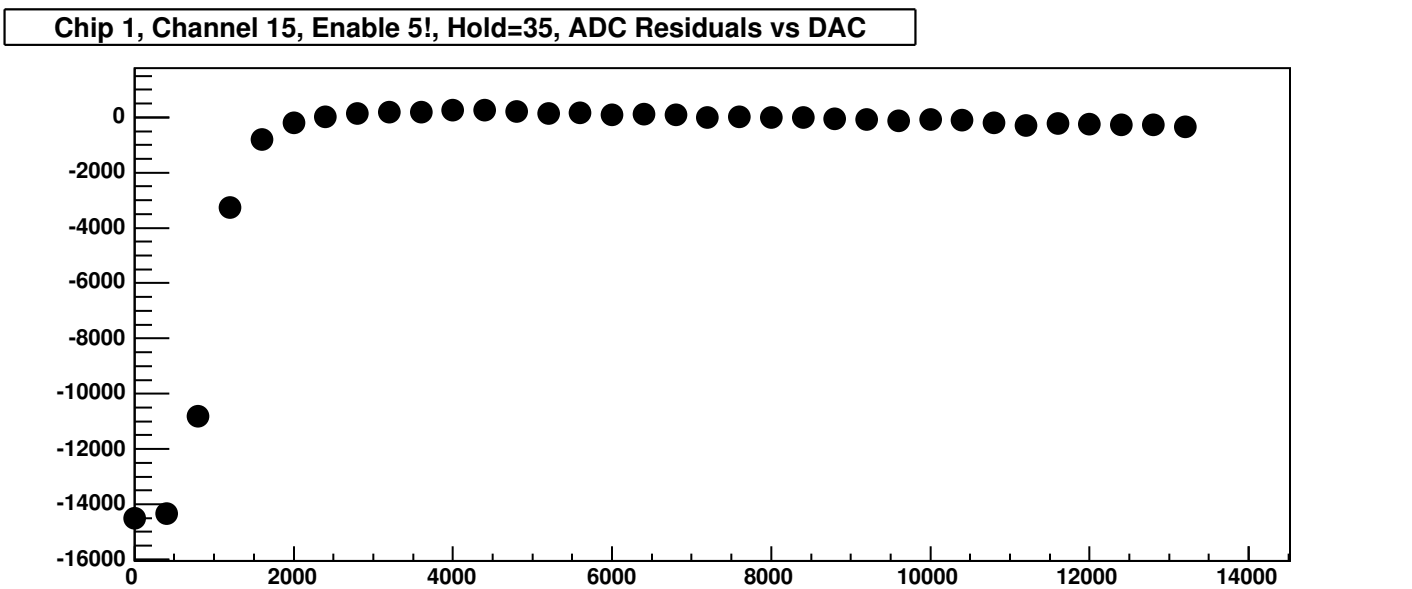

Chip 1, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.6 / 23
p0 -603.1 ± 0.7814
p1 0.02368 ± 0.0001217
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

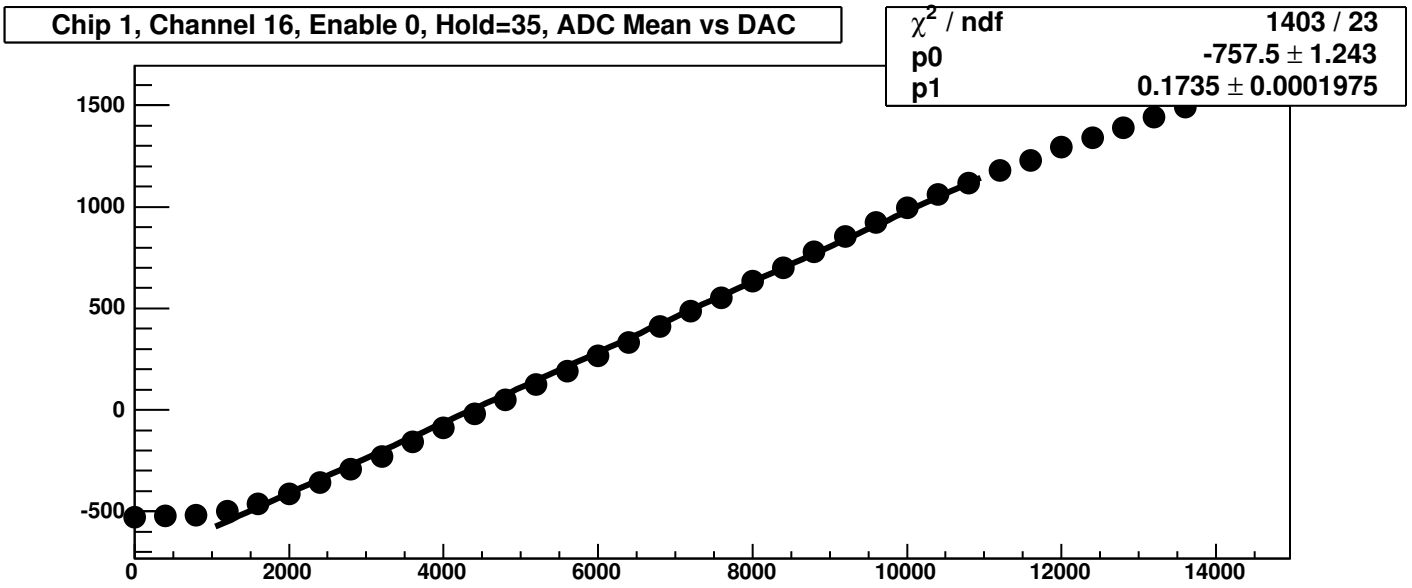

Chip 1, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

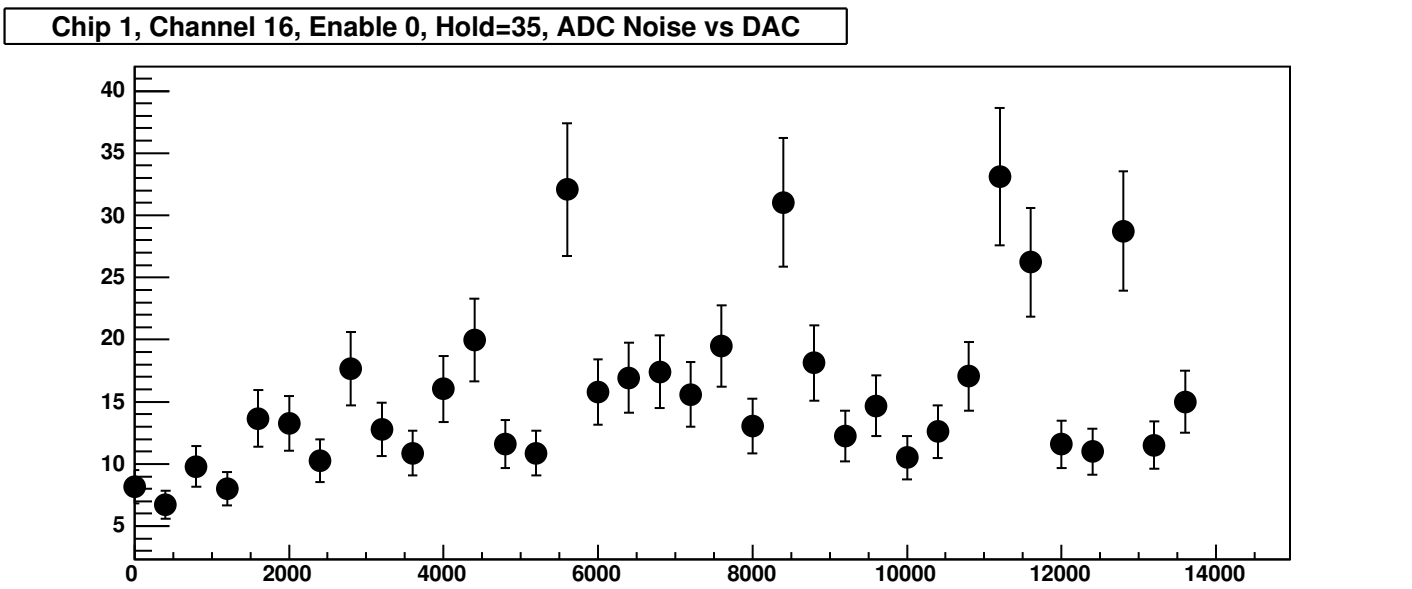

Chip 1, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

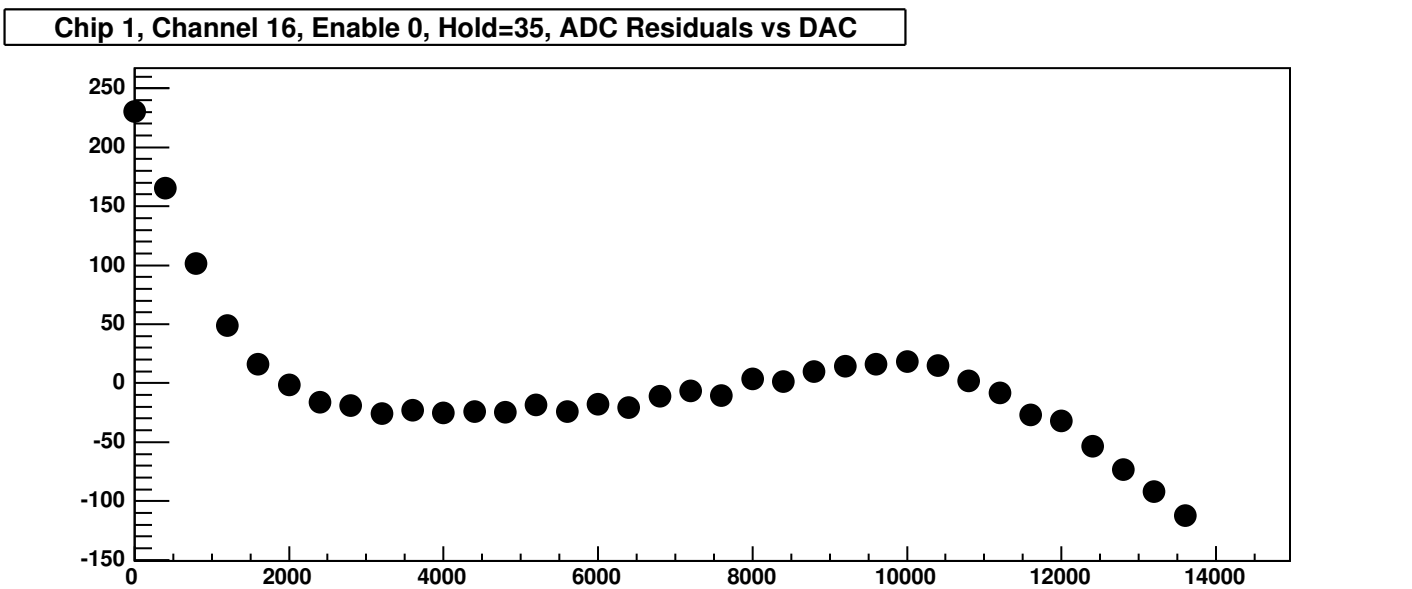

Chip 1, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4737 / 23
p0 2.928e+04 ± 14.67
p1 0.05421 ± 0.002058
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

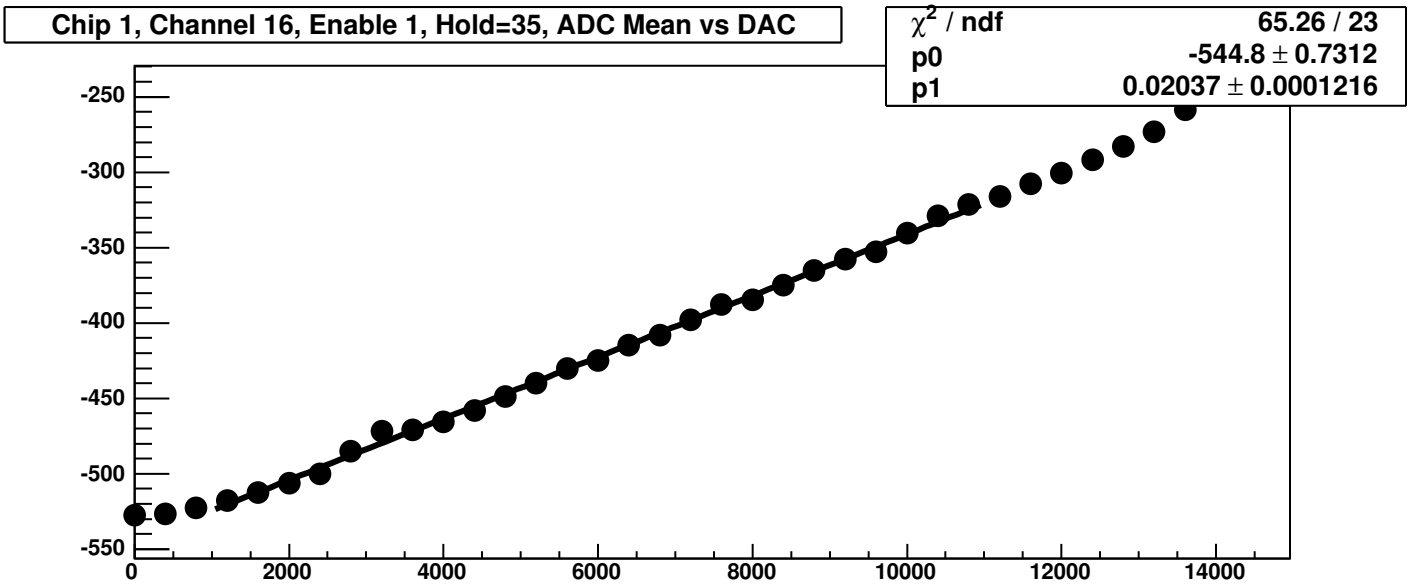

Chip 1, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

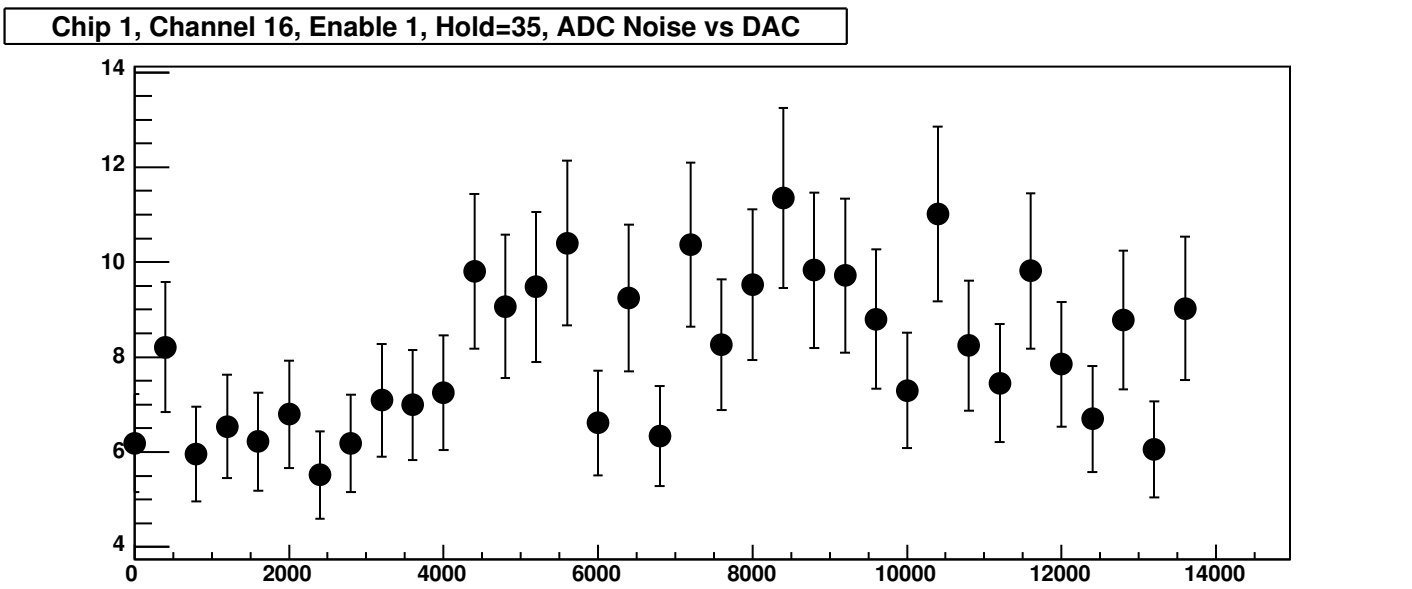

Chip 1, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

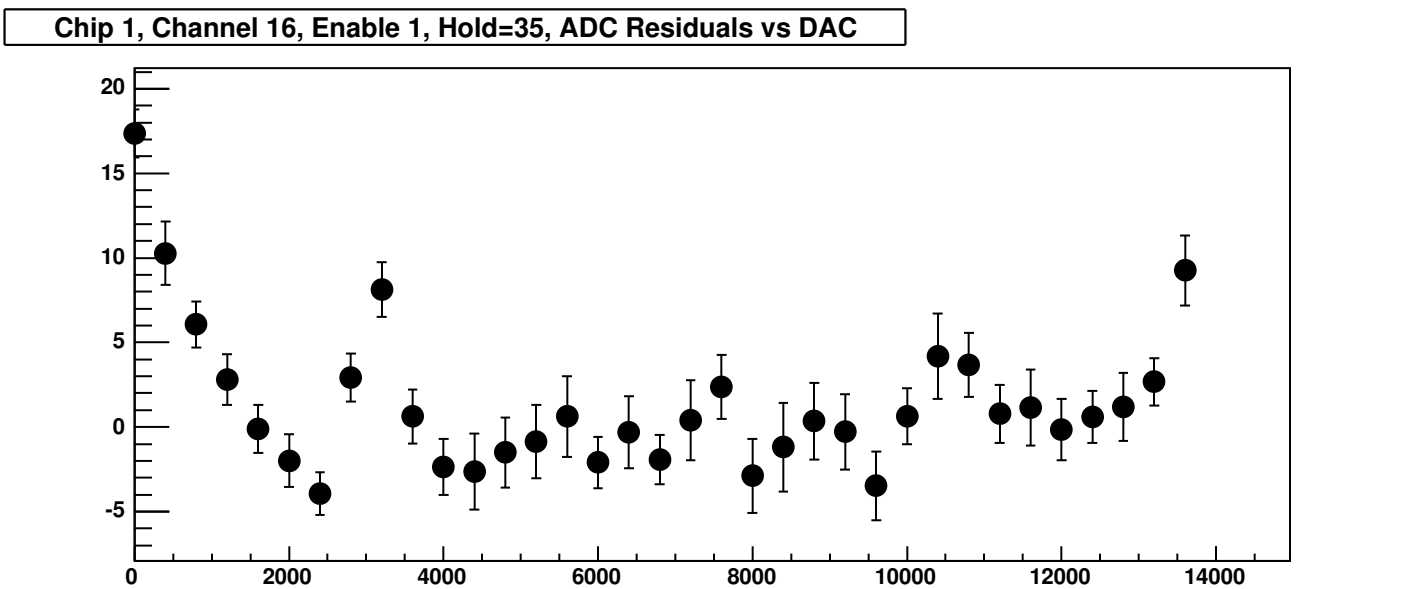

Chip 1, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1403 / 23
p0 -757.5 ± 1.243
p1 0.1735 ± 0.0001975
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

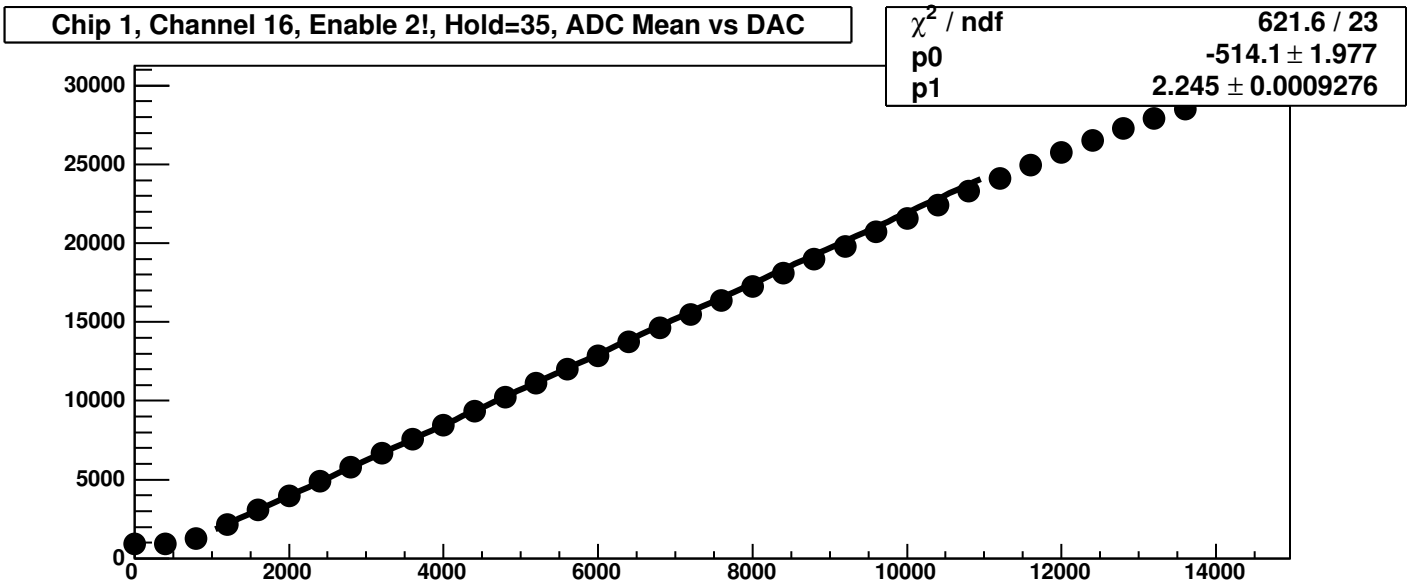

Chip 1, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

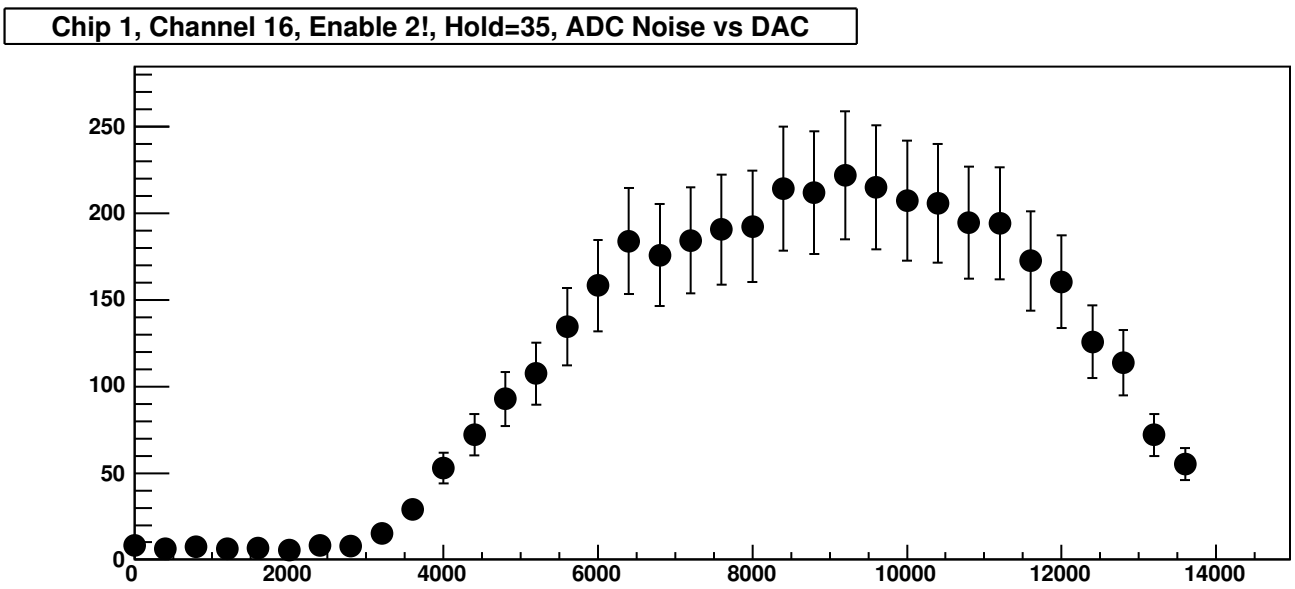

Chip 1, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

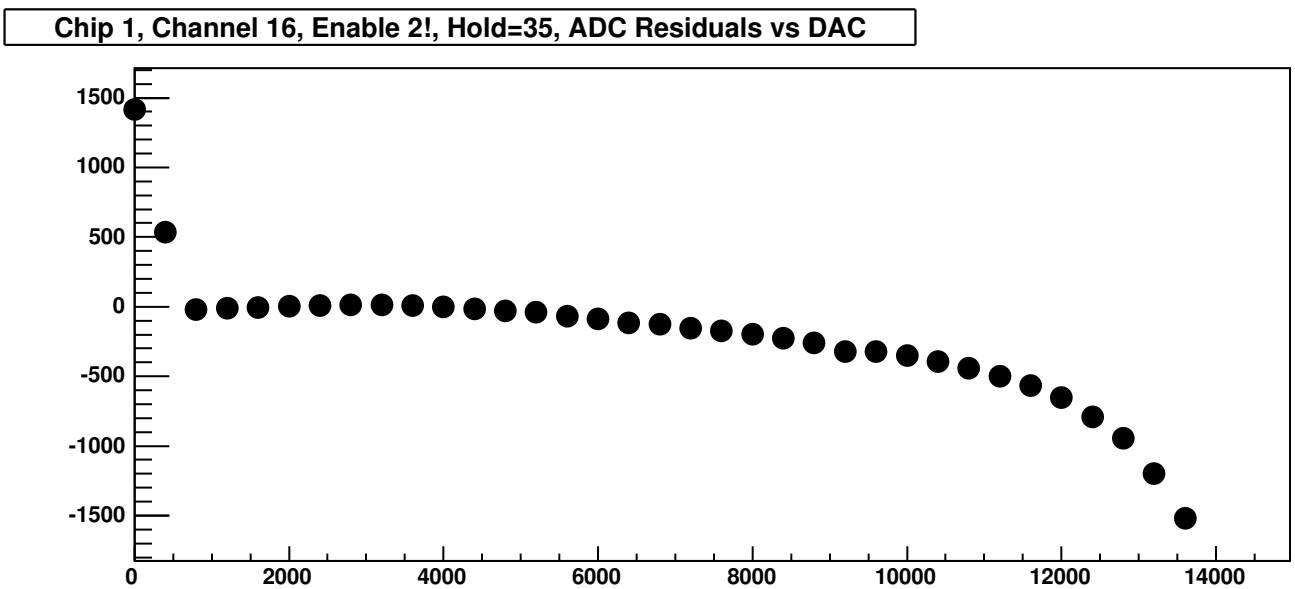

Chip 1, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 65.26 / 23
p0 -544.8 ± 0.7312
p1 0.02037 ± 0.0001216
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

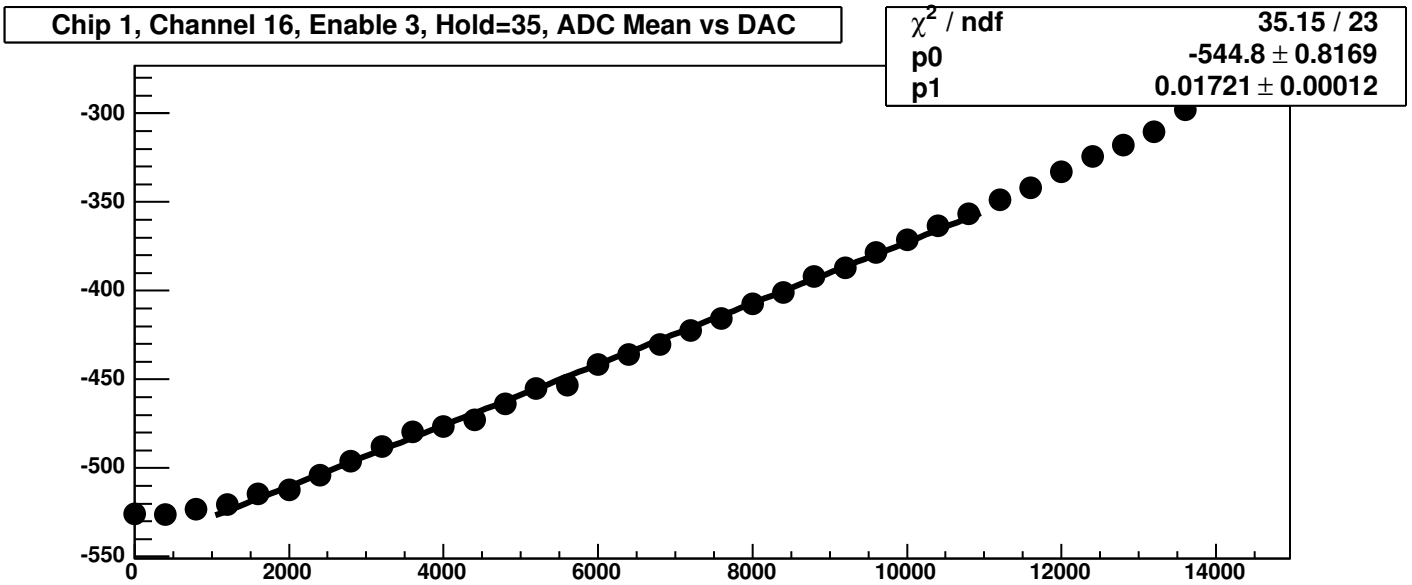

Chip 1, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

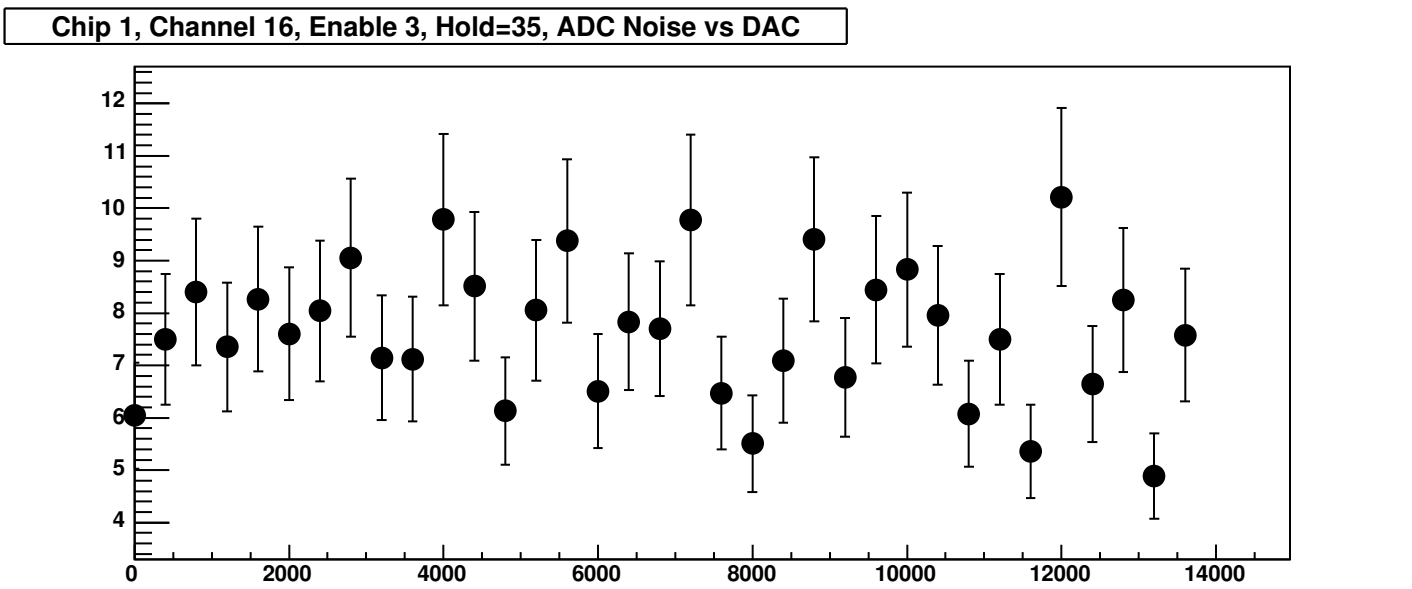

Chip 1, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

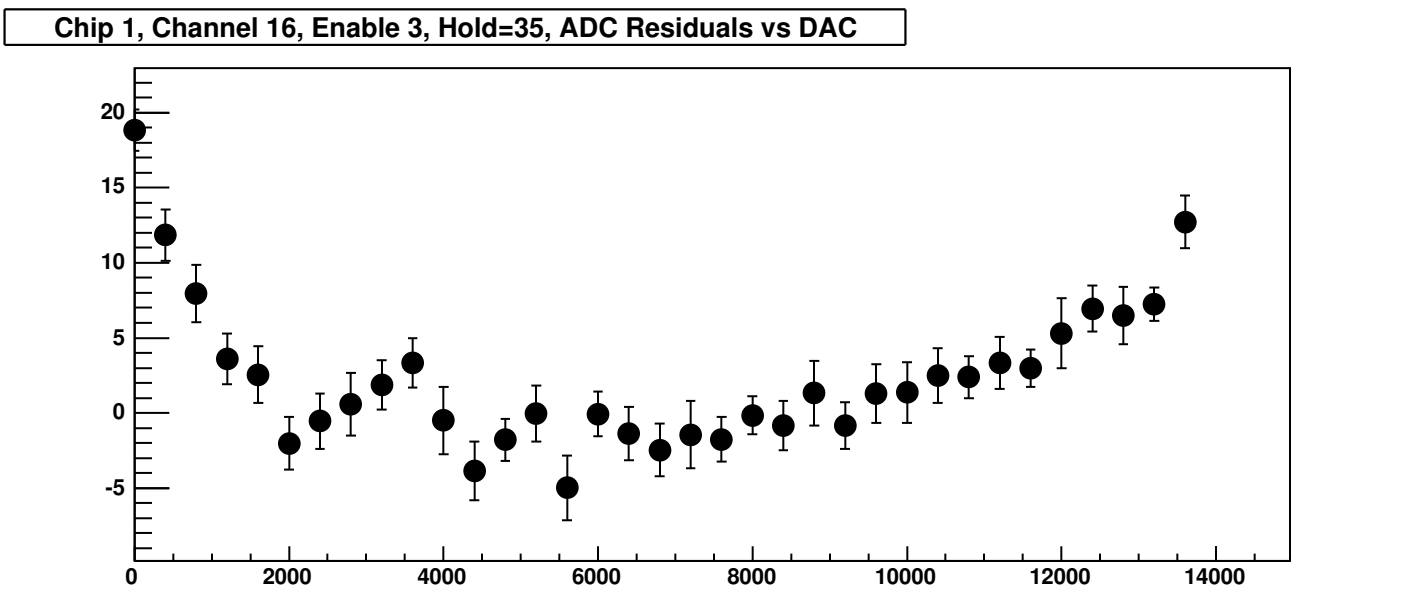

Chip 1, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 621.6 / 23
p0 -514.1 ± 1.977
p1 2.245 ± 0.0009276
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

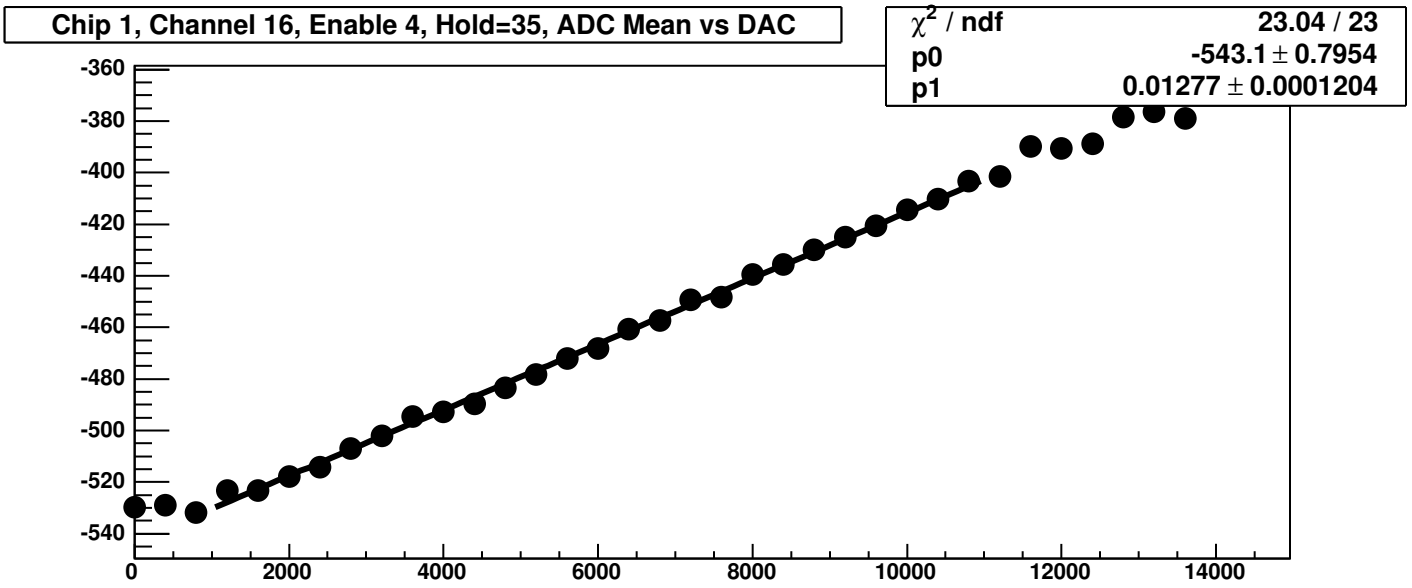

Chip 1, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

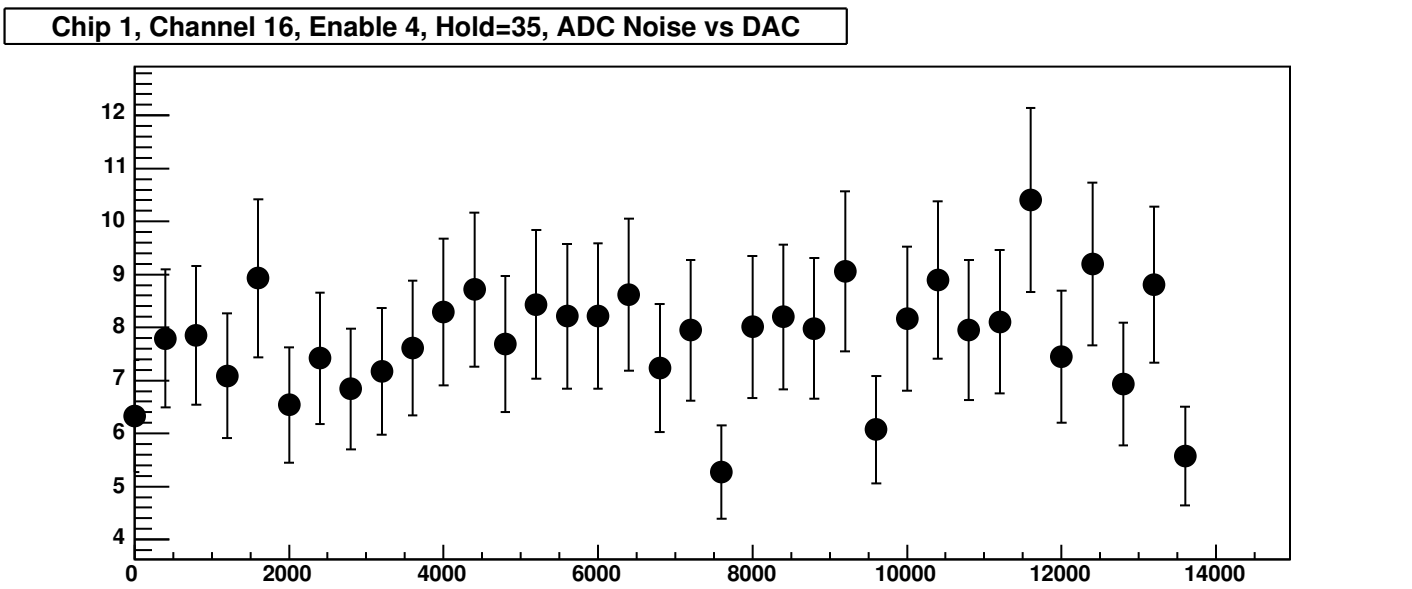

Chip 1, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

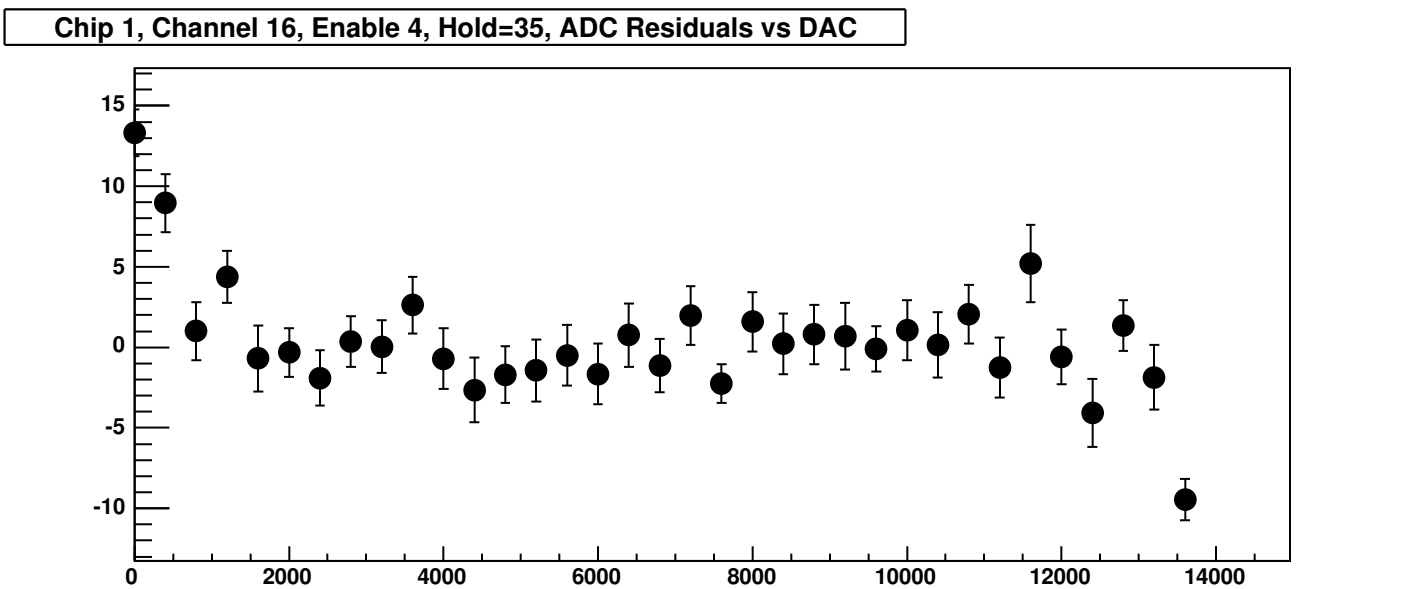

Chip 1, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 35.15 / 23
p0 -544.8 ± 0.8169
p1 0.01721 ± 0.00012
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

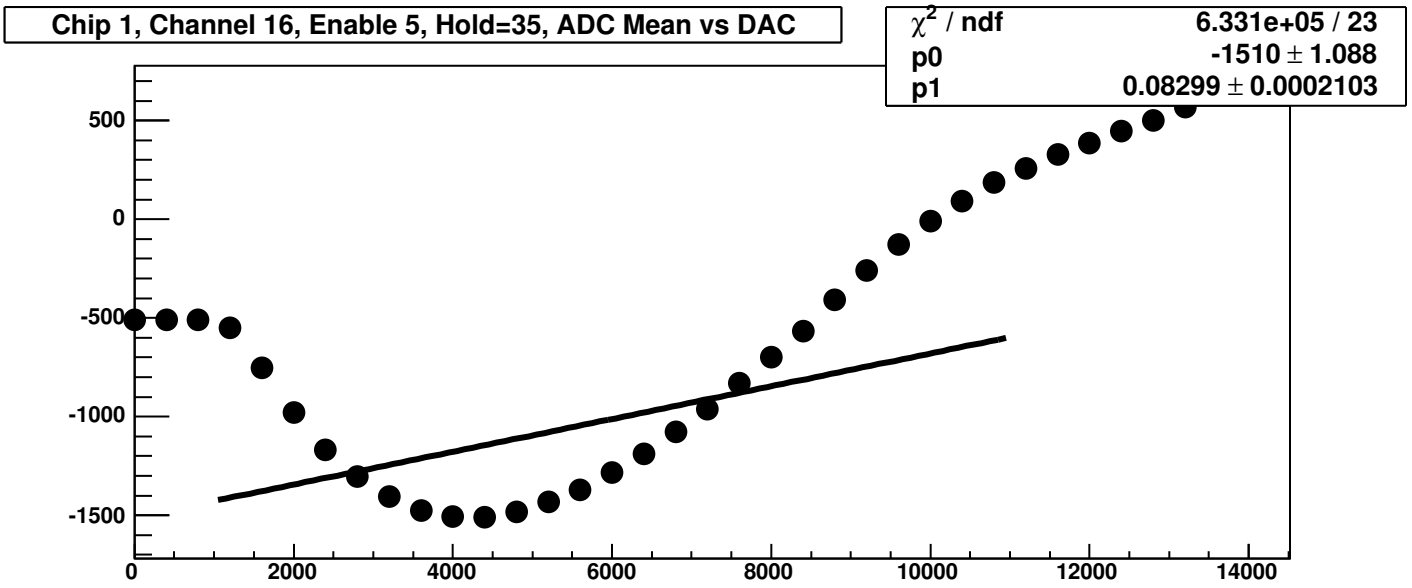

Chip 1, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

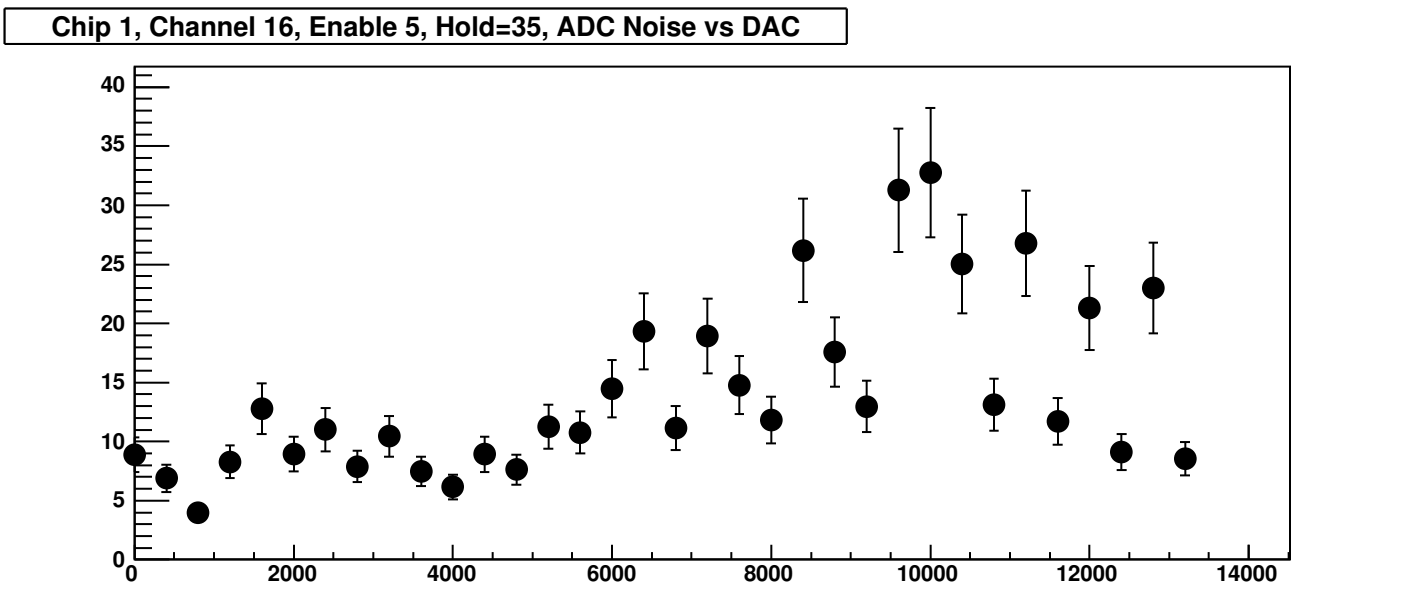

Chip 1, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

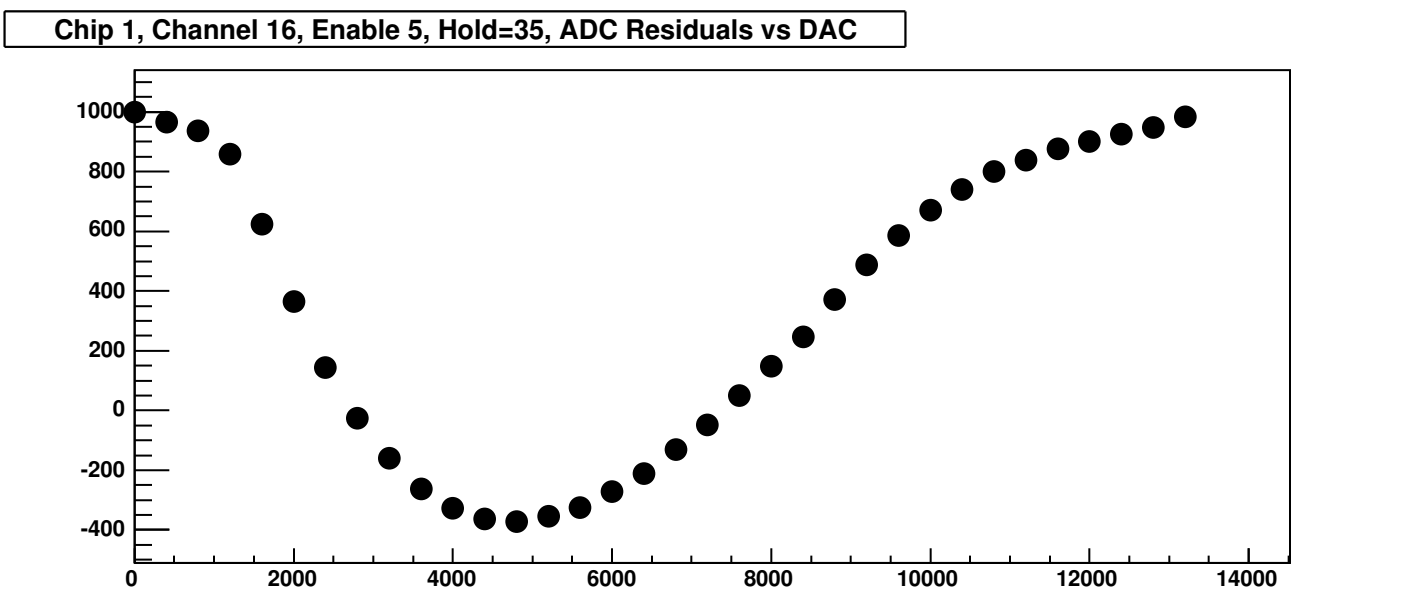

Chip 1, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 23.04 / 23
p0 -543.1 ± 0.7954
p1 0.01277 ± 0.0001204
-360
-380
-400
-420
-440
-460
-480
-500
-520
-540
0
2000
4000
6000
8000
10000
12000
14000

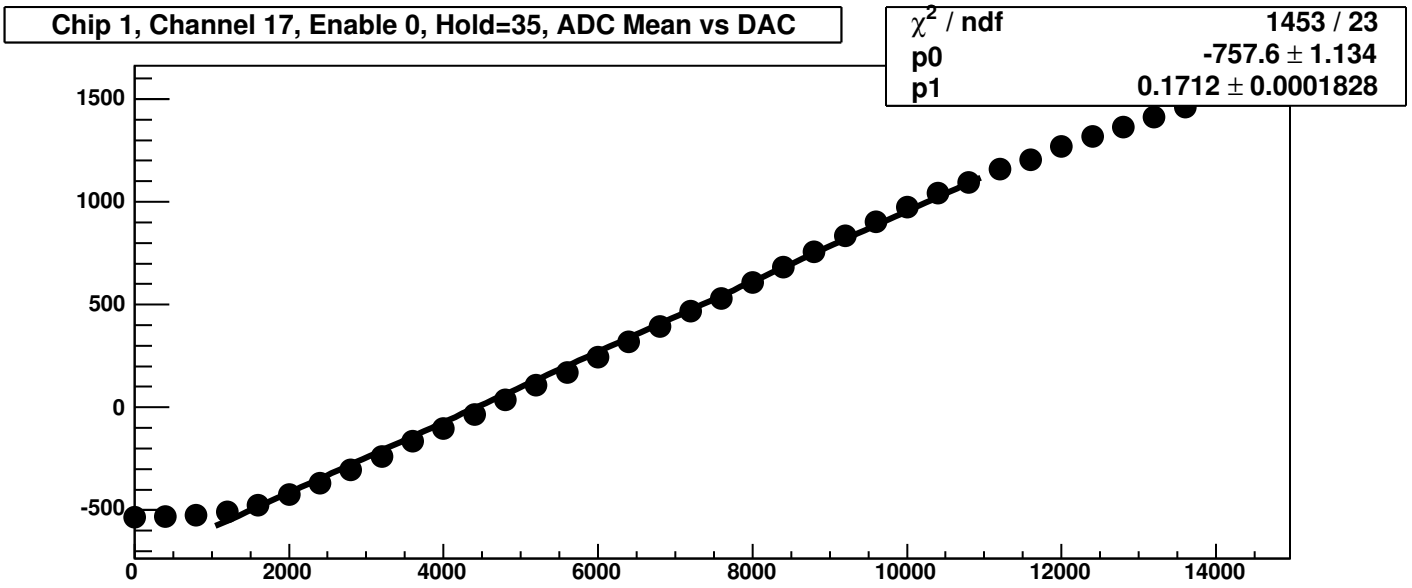

Chip 1, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

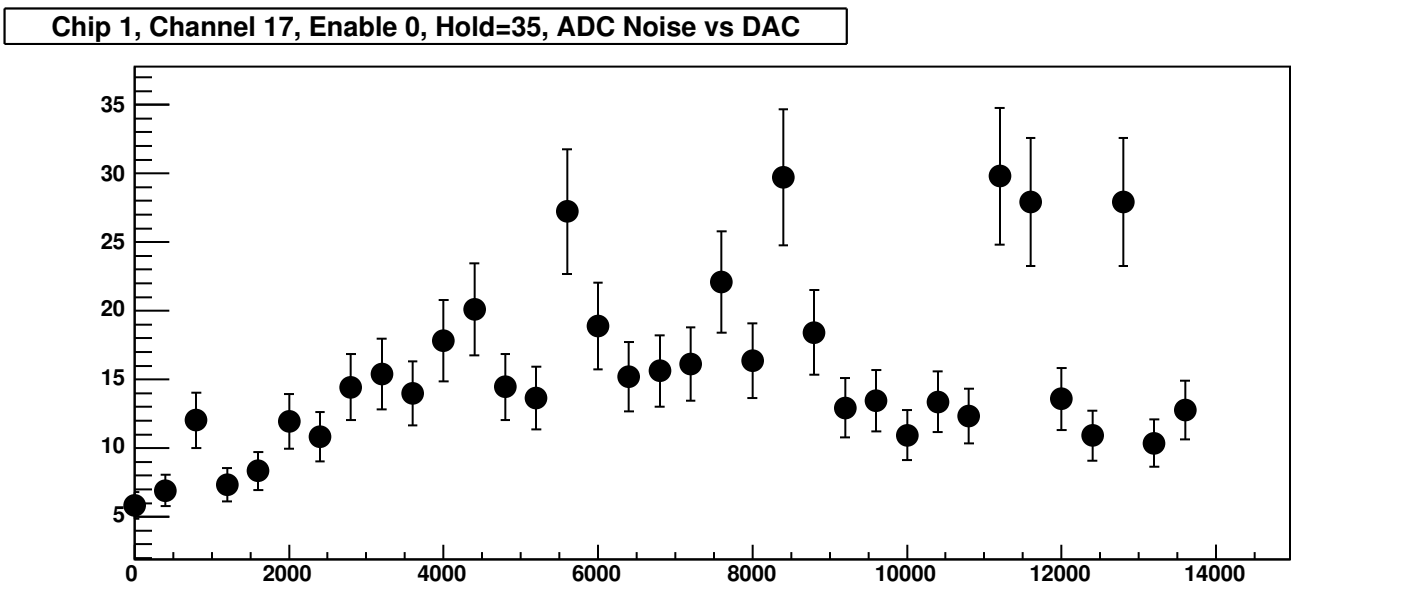

Chip 1, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

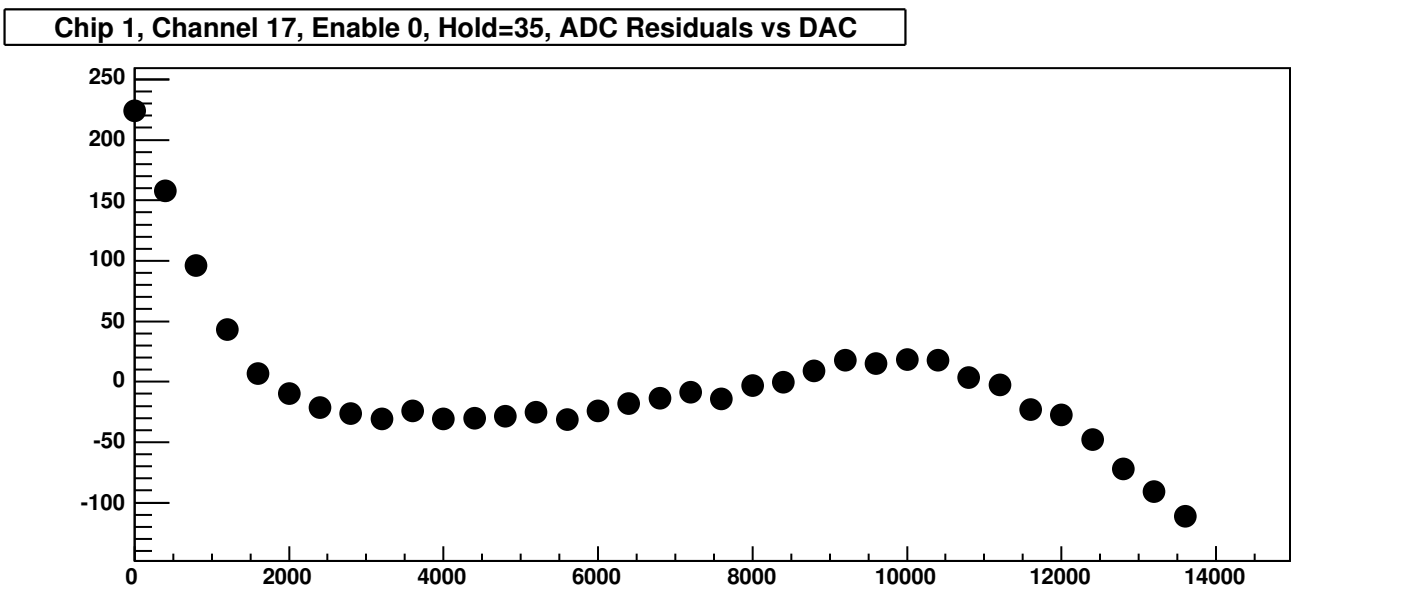

Chip 1, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 6.331e+05 / 23
p0 -1510 ± 1.088
p1 0.08299 ± 0.0002103
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

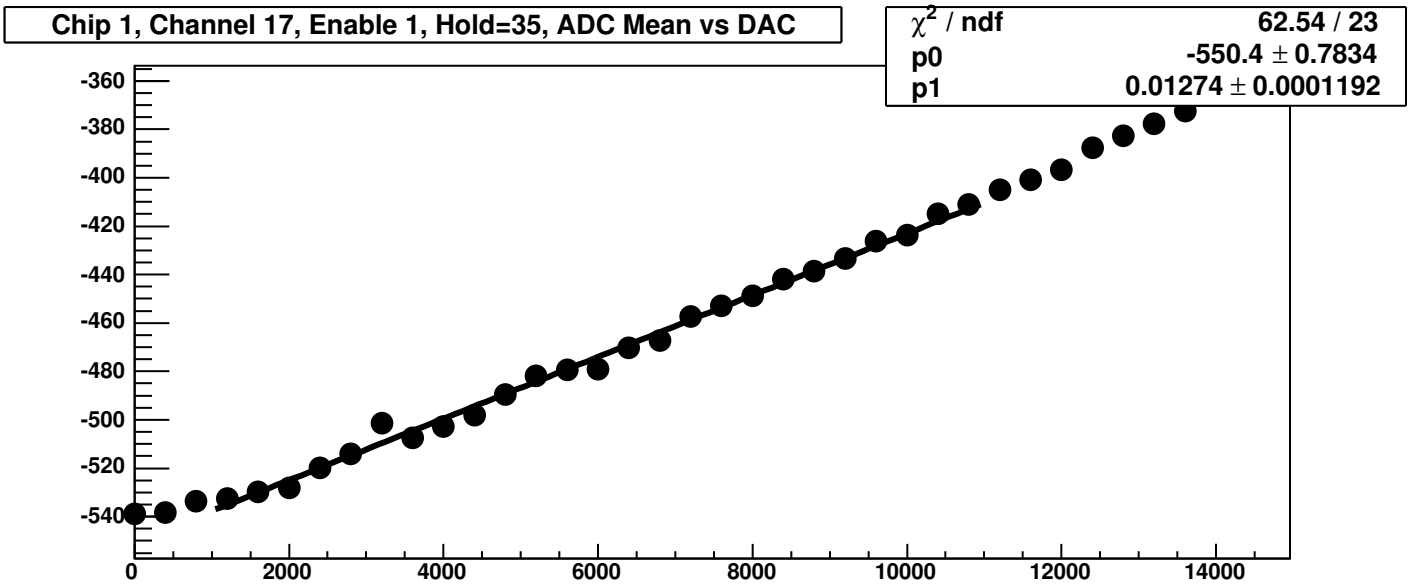

Chip 1, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
0
2000
4000
6000
8000
10000
12000
14000

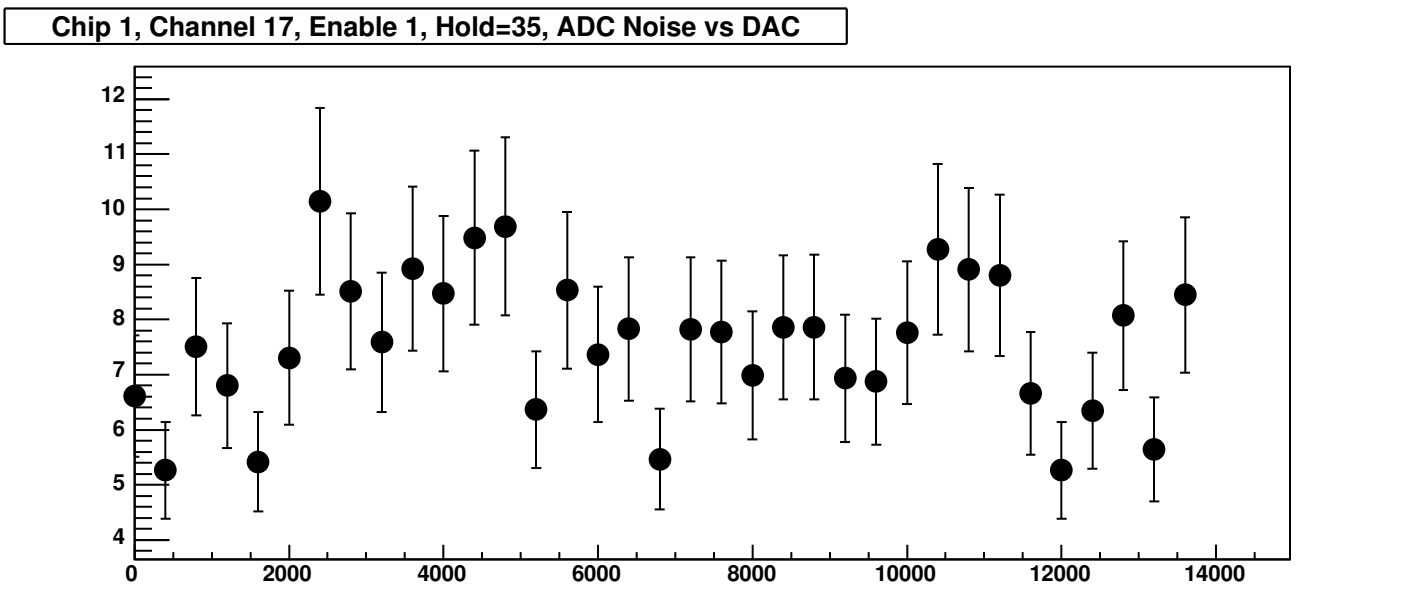

Chip 1, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

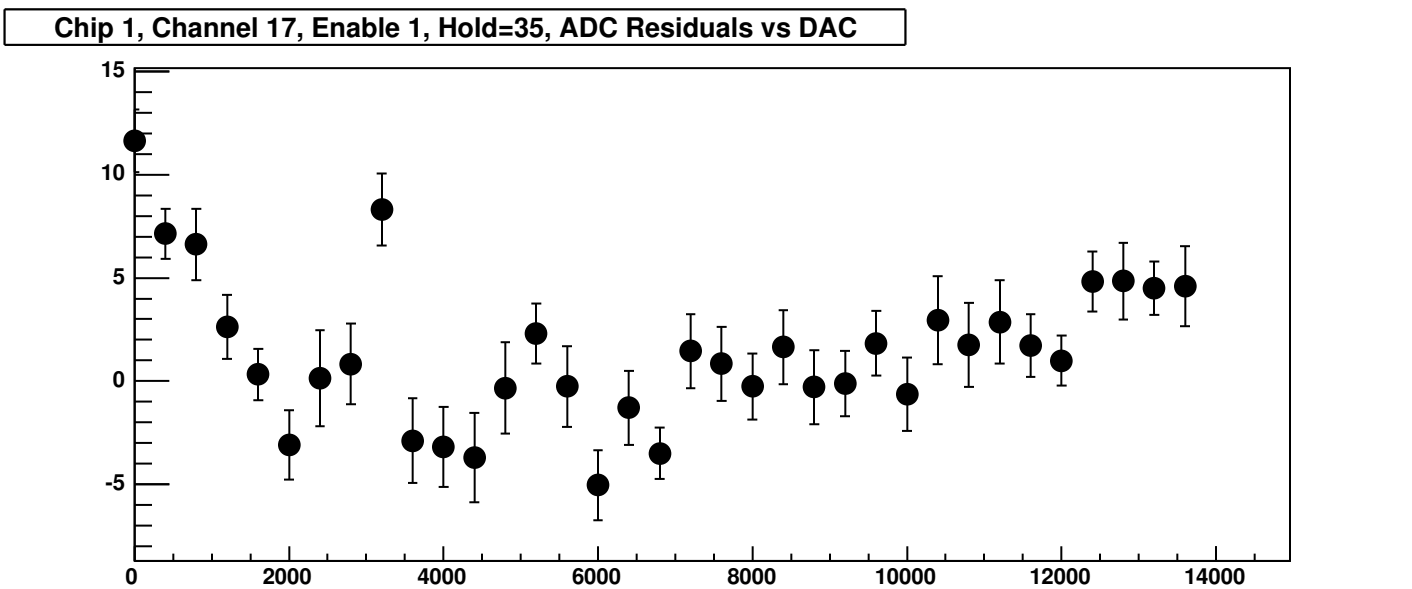

Chip 1, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1453 / 23
p0 -757.6 ± 1.134
p1 0.1712 ± 0.0001828
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

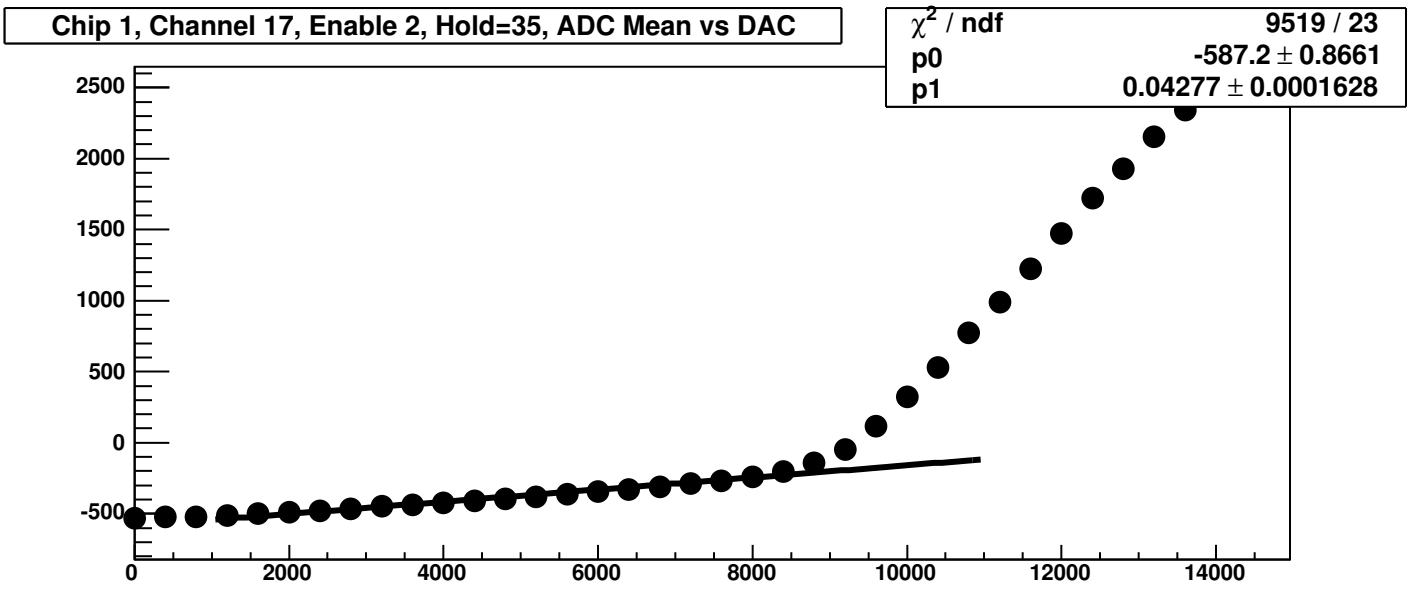

Chip 1, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

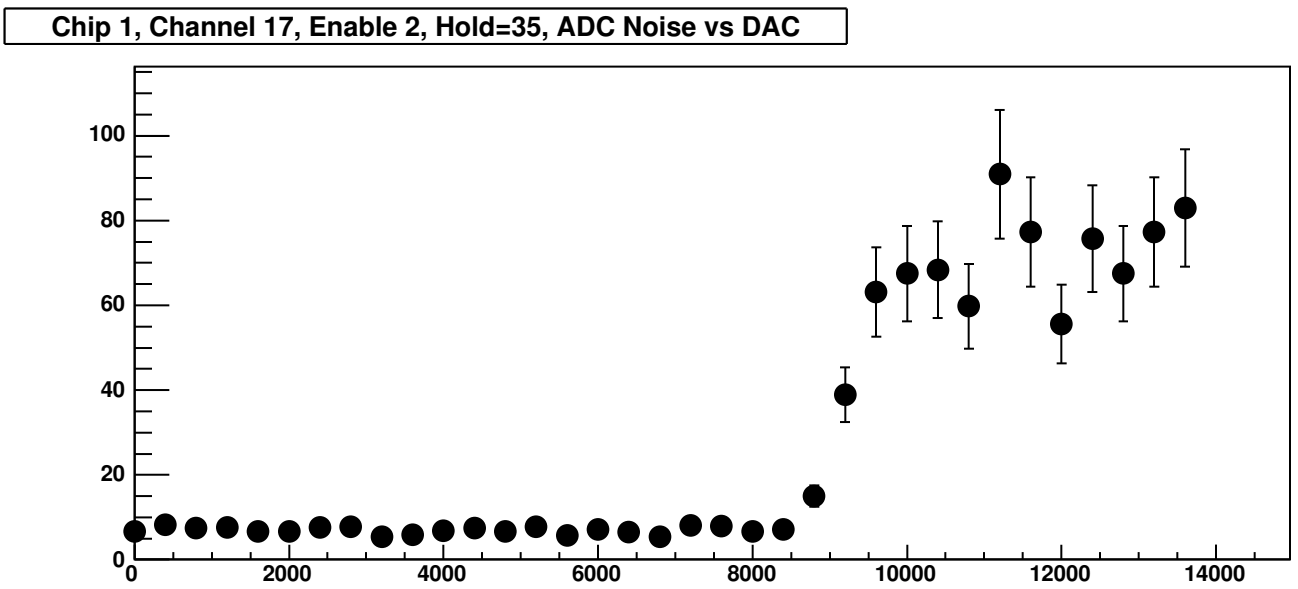

Chip 1, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

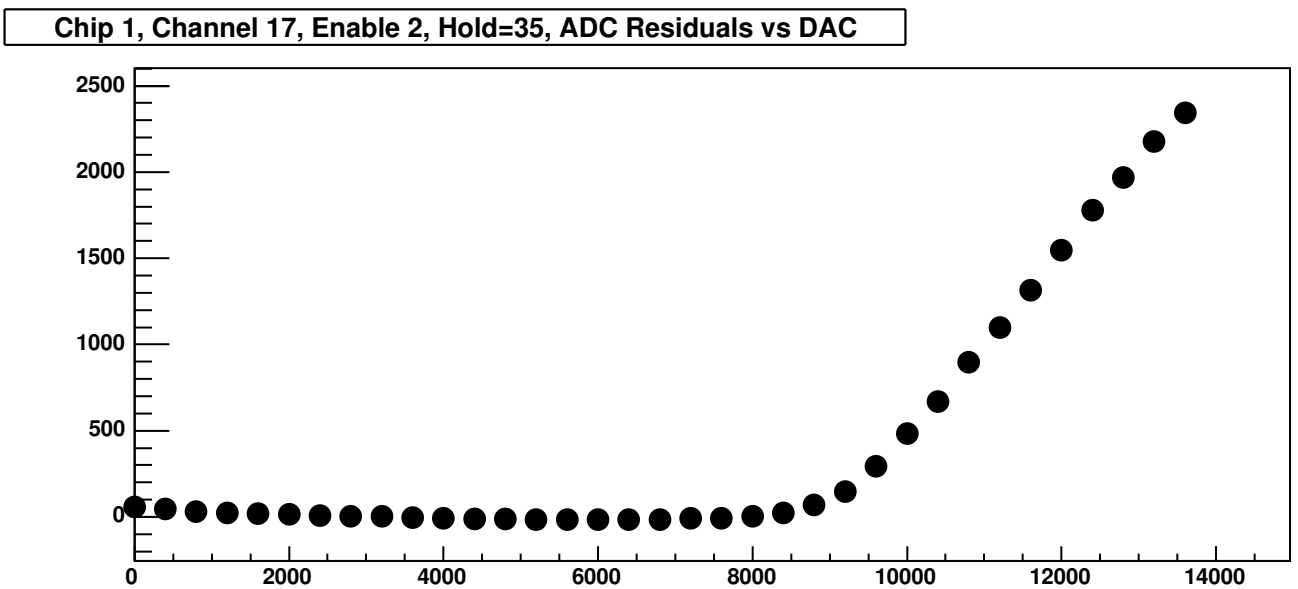

Chip 1, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 62.54 / 23
p0 -550.4 ± 0.7834
p1 0.01274 ± 0.0001192
-360
-380
-400
-420
-440
-460
-480
-500
-520
-540
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

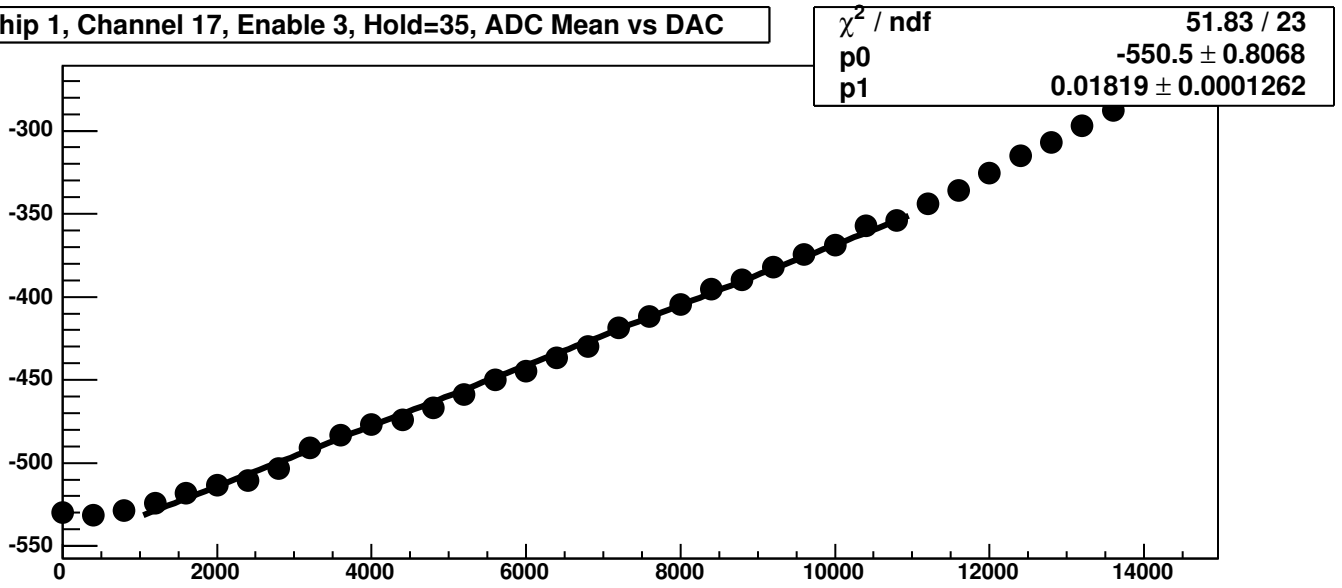

Chip 1, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 9519 / 23
p0 -587.2 ± 0.8661
p1 0.04277 ± 0.0001628
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

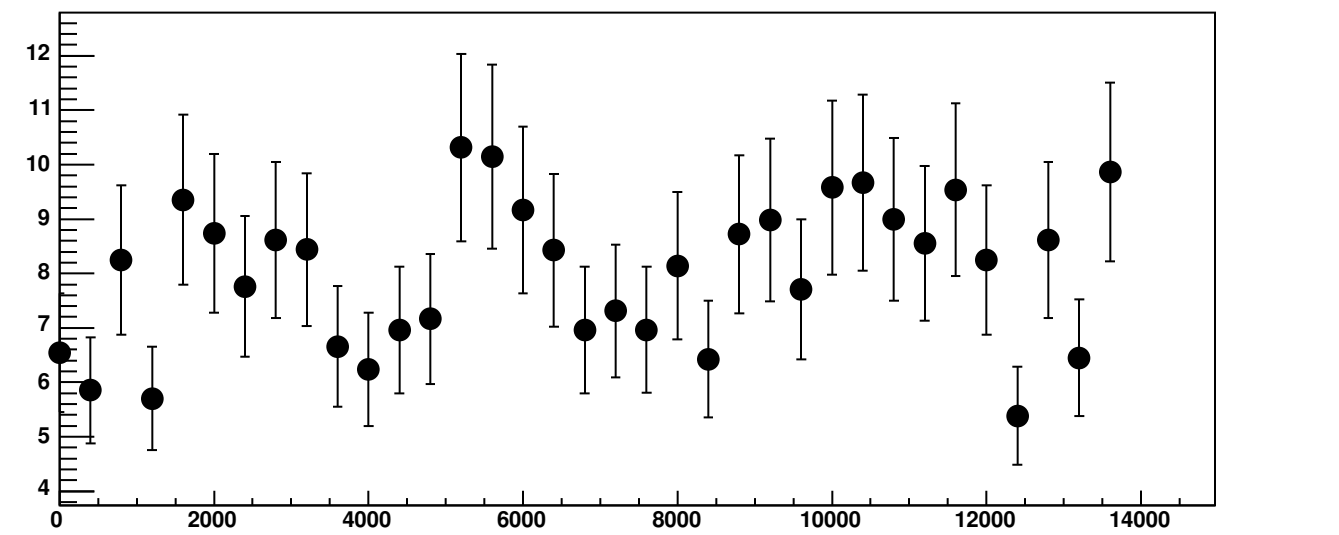

Chip 1, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

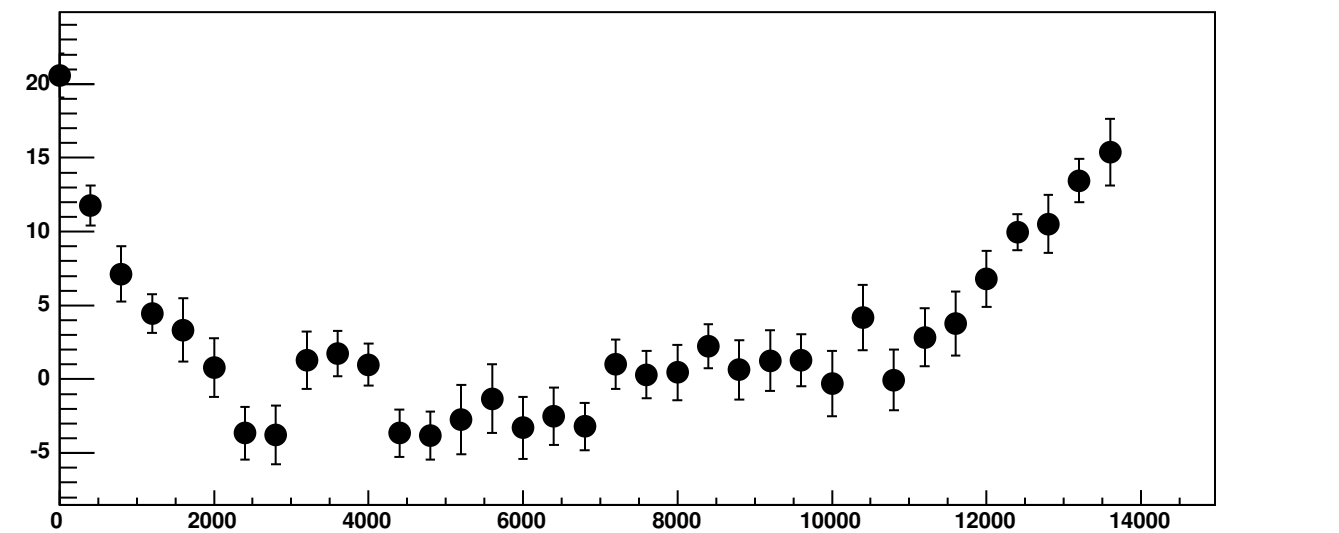
Chip 1, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

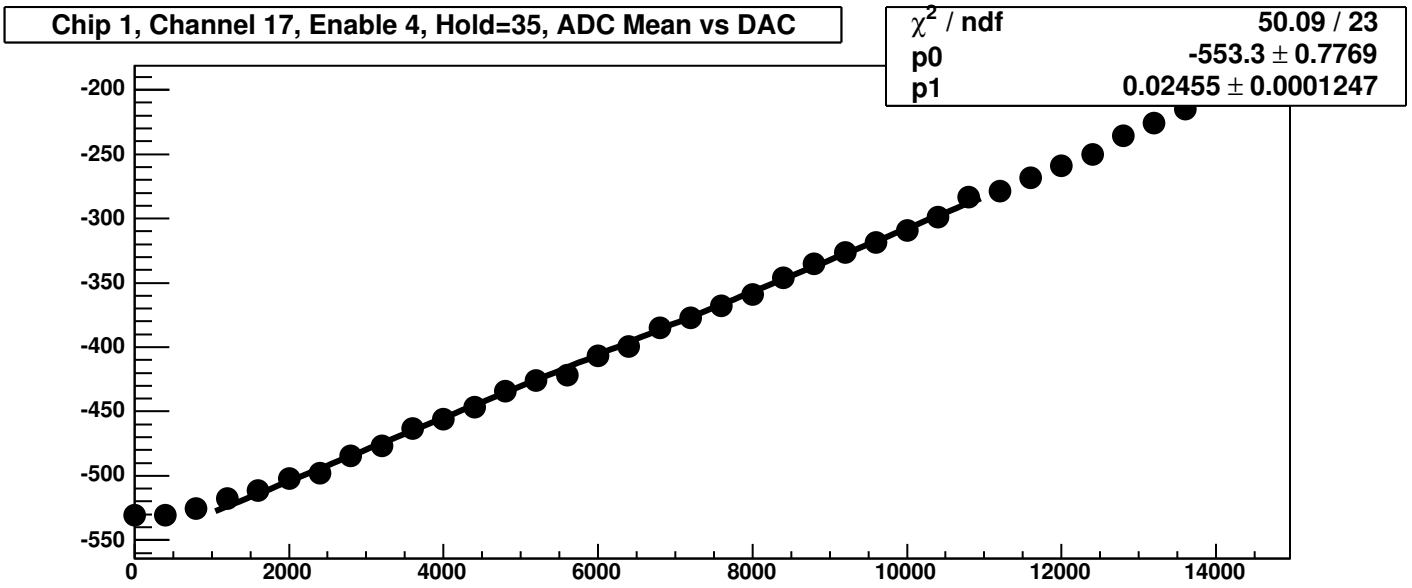


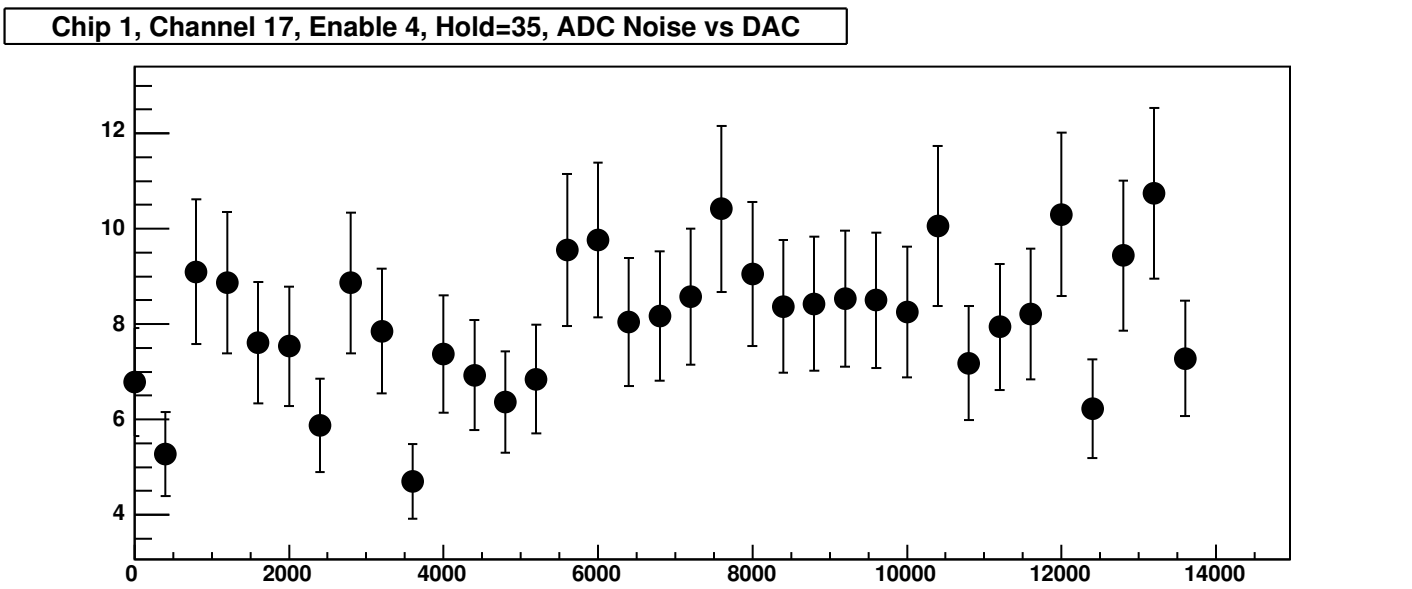
Chip 1, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC

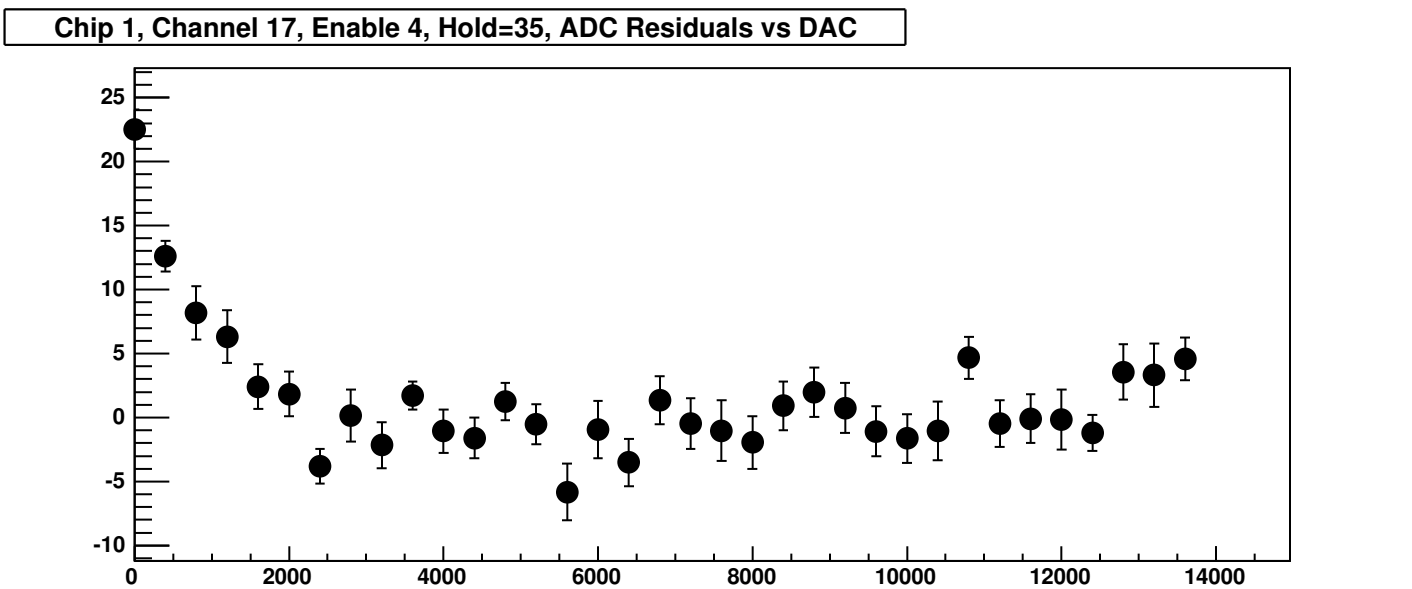


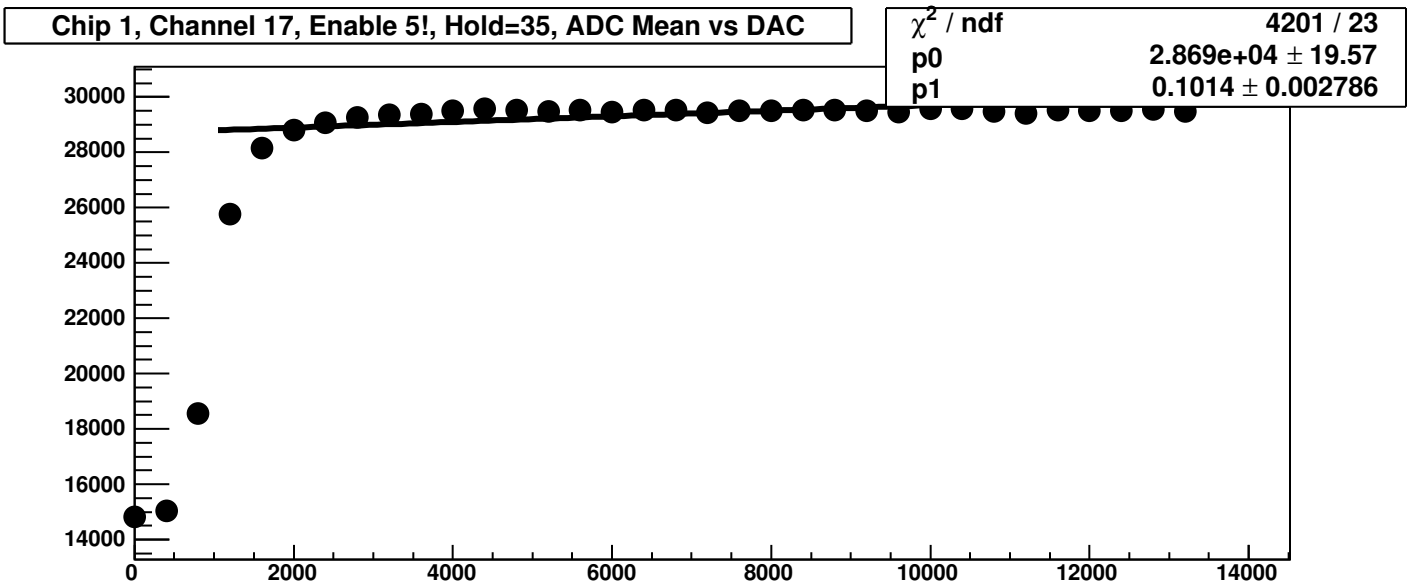
Chip 1, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

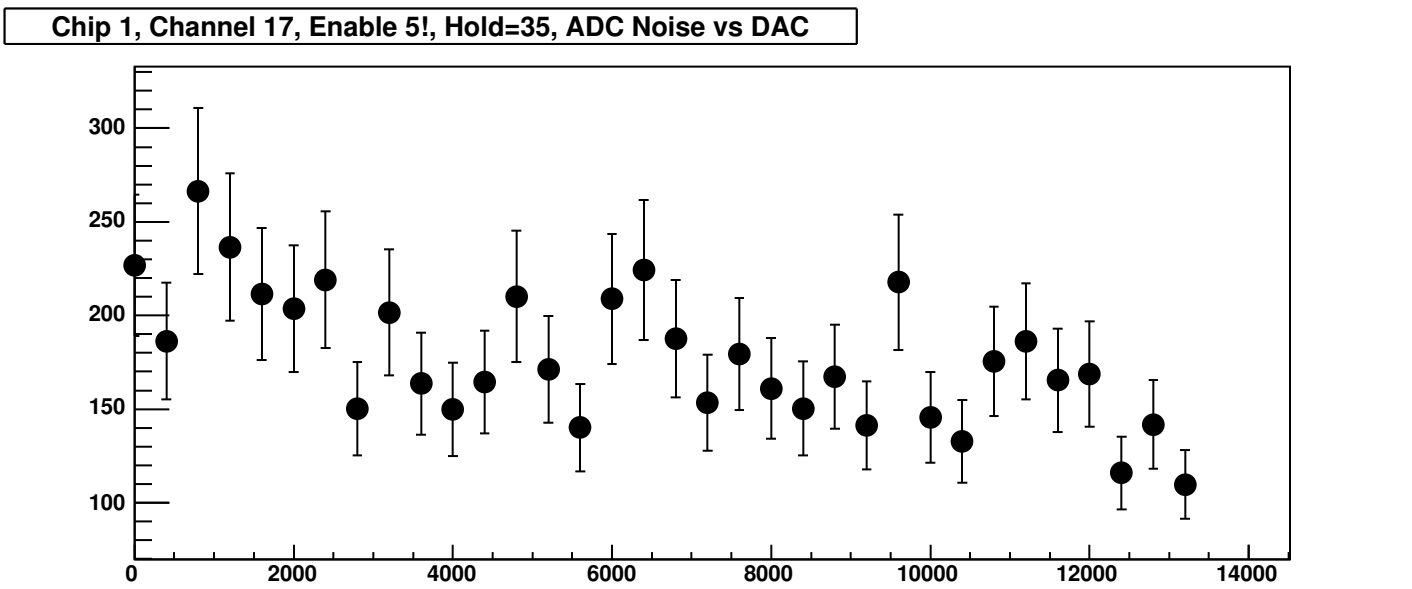


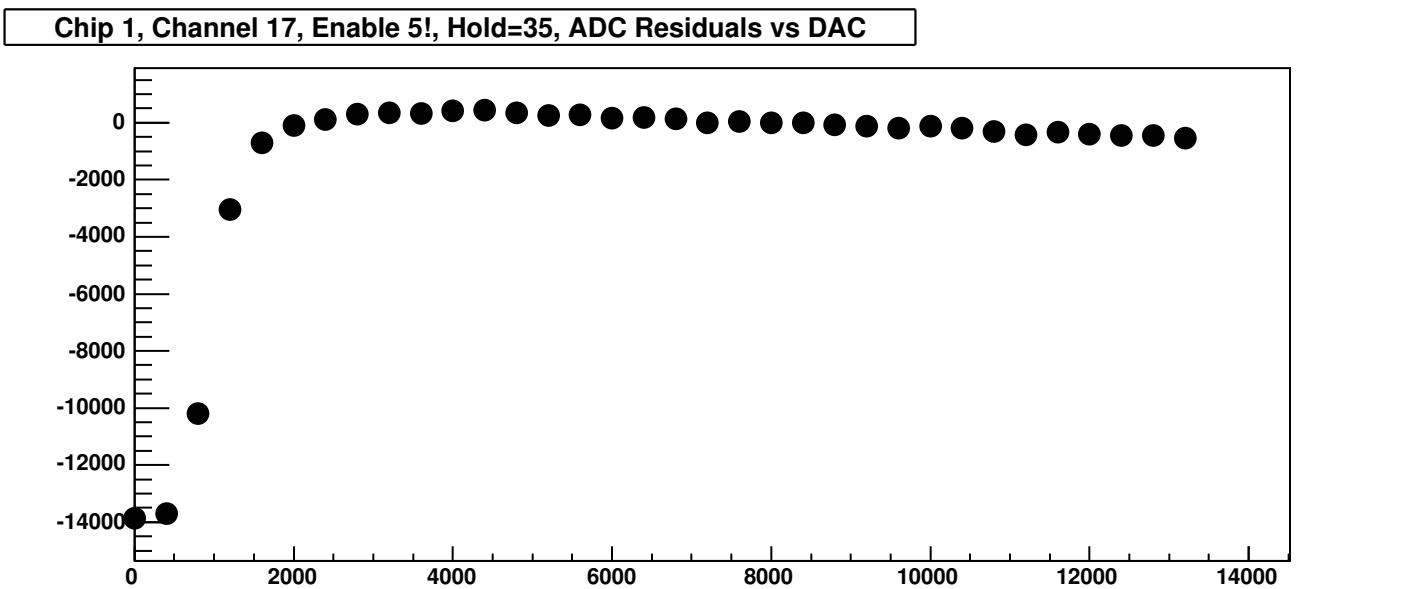

Chip 1, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 50.09 / 23
p0 -553.3 ± 0.7769
p1 0.02455 ± 0.0001247
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

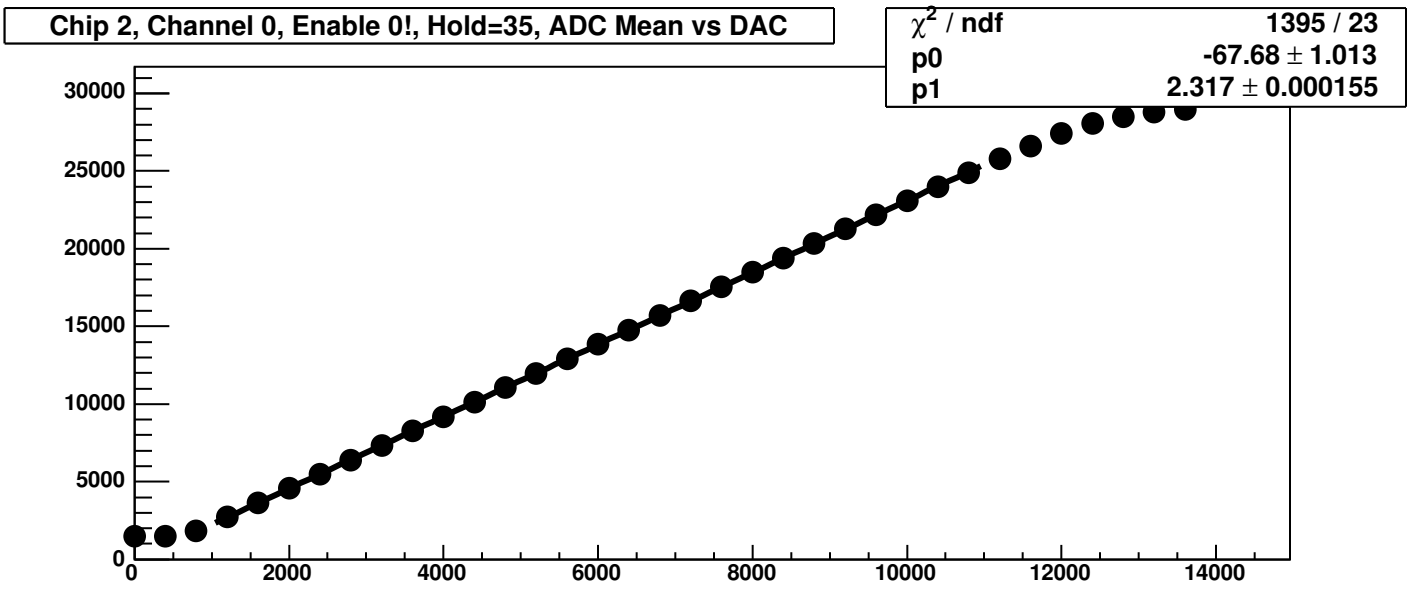

Chip 1, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

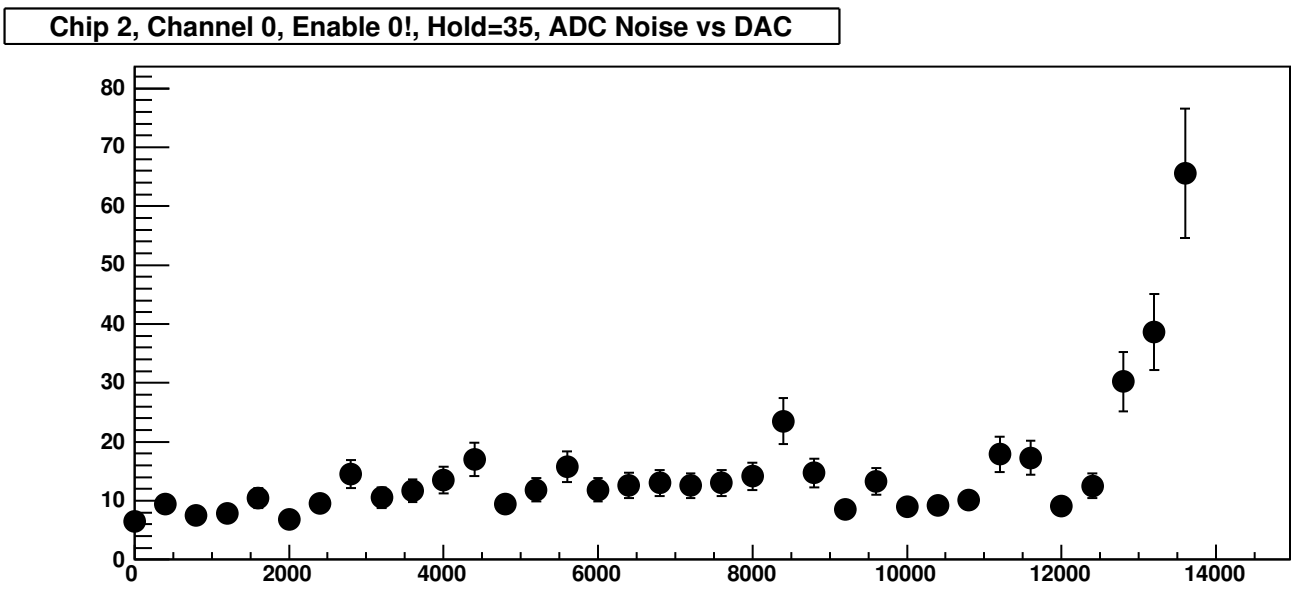

Chip 1, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

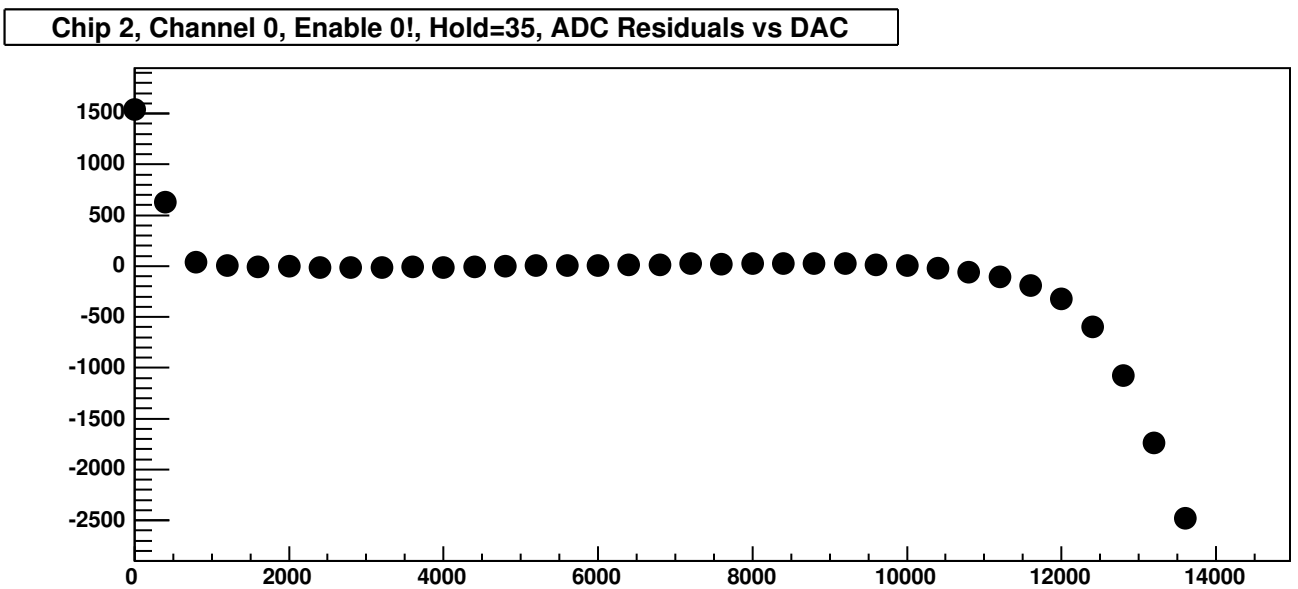

Chip 1, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4201 / 23
p0 2.869e+04 ± 19.57
p1 0.1014 ± 0.002786
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

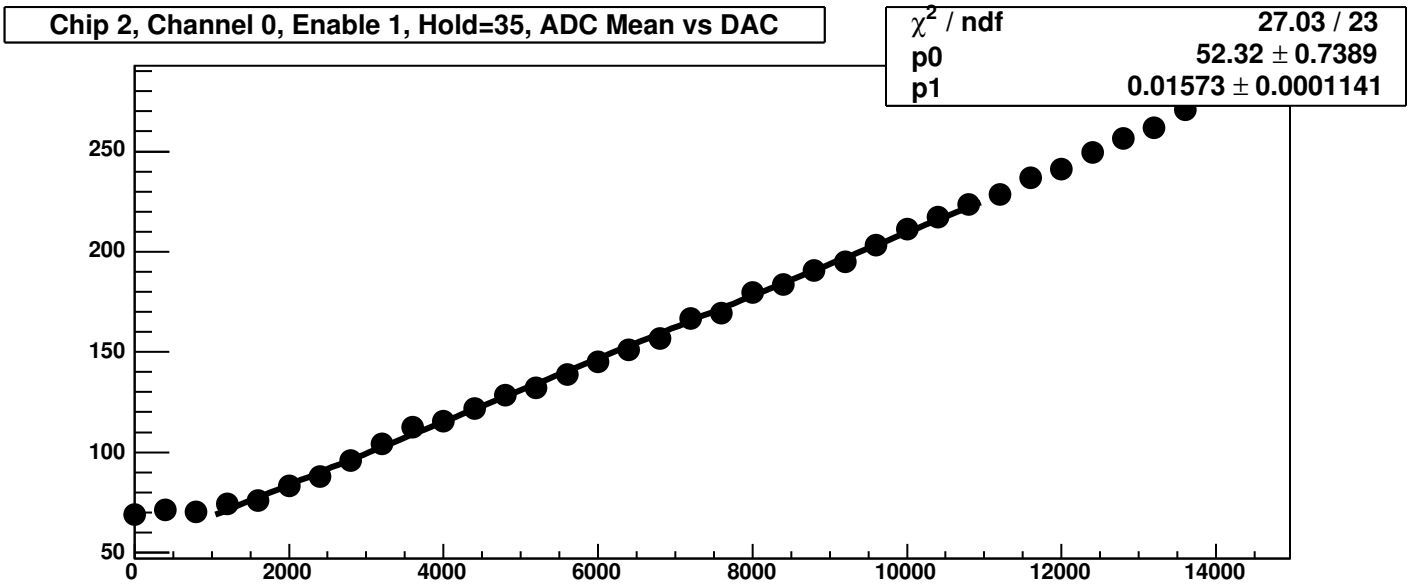

Chip 1, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

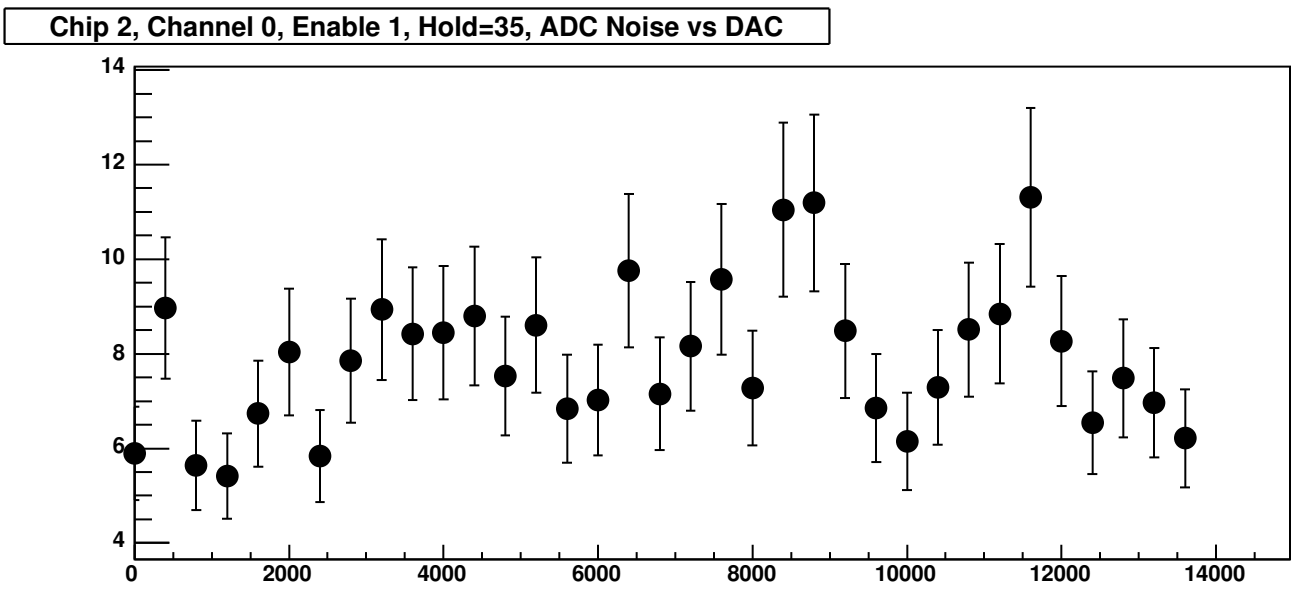

Chip 1, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

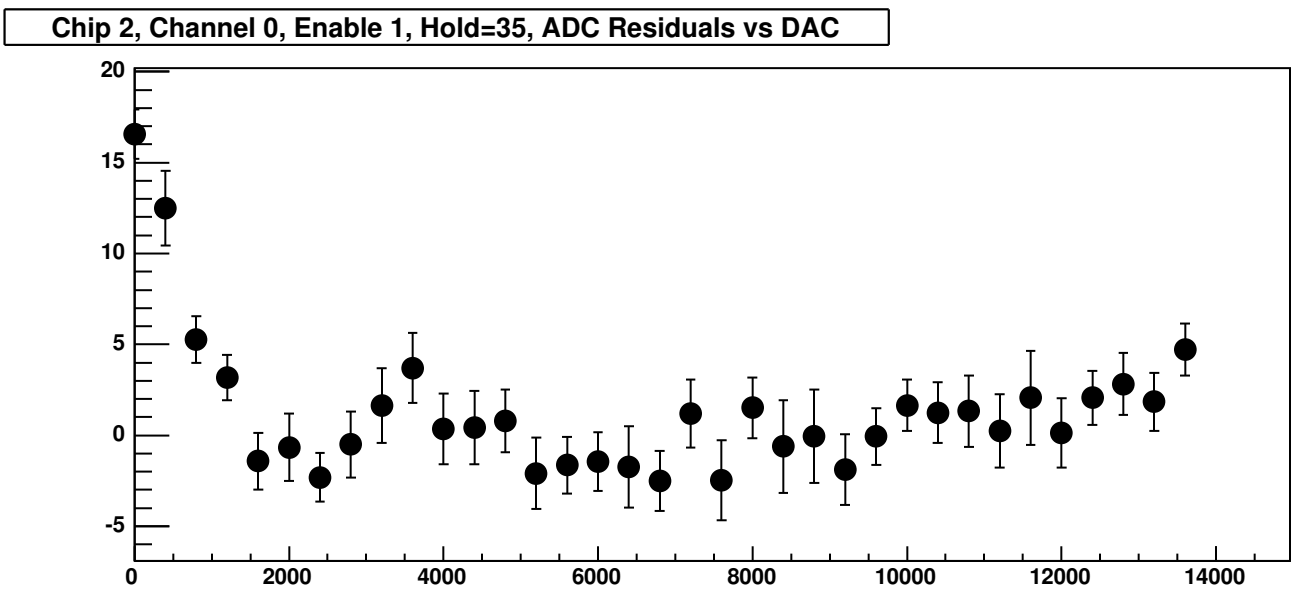

Chip 2, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1395 / 23
p0 -67.68 ± 1.013
p1 2.317 ± 0.000155
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

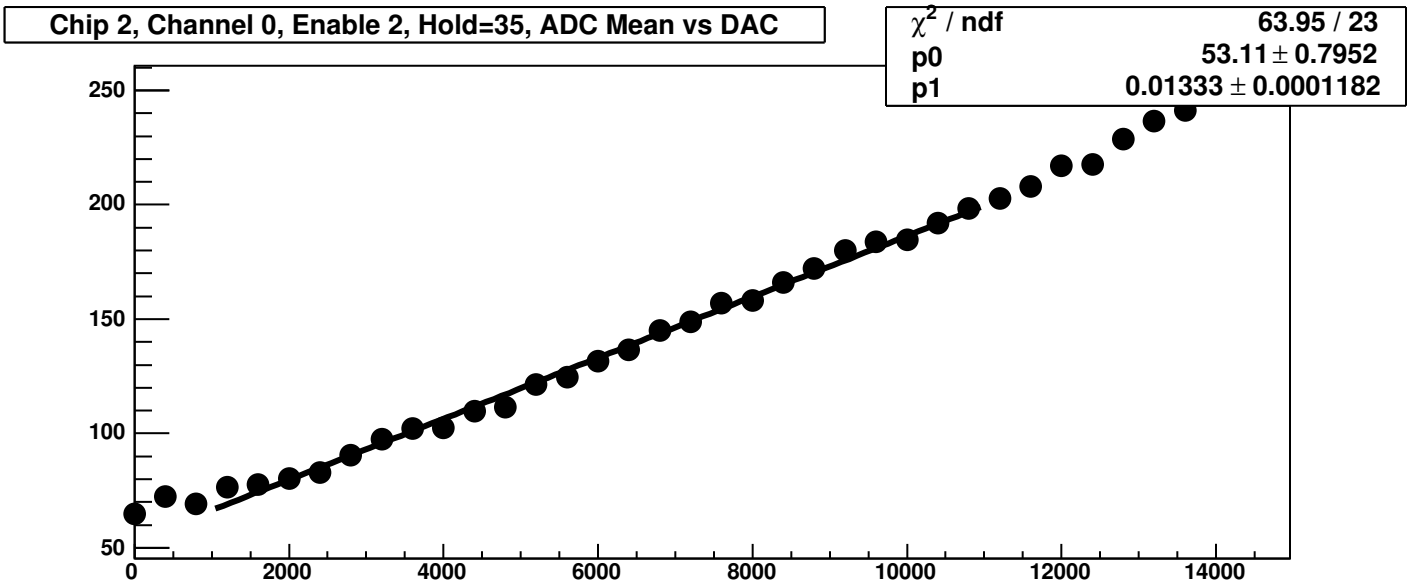

Chip 2, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

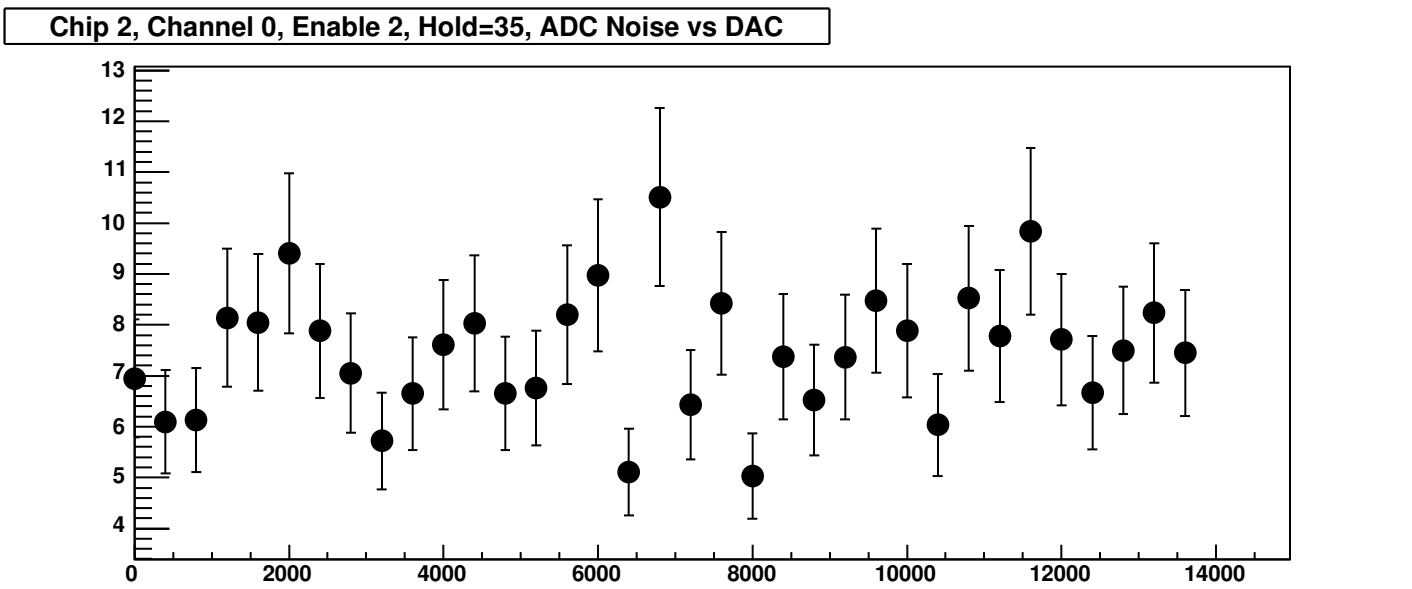

Chip 2, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

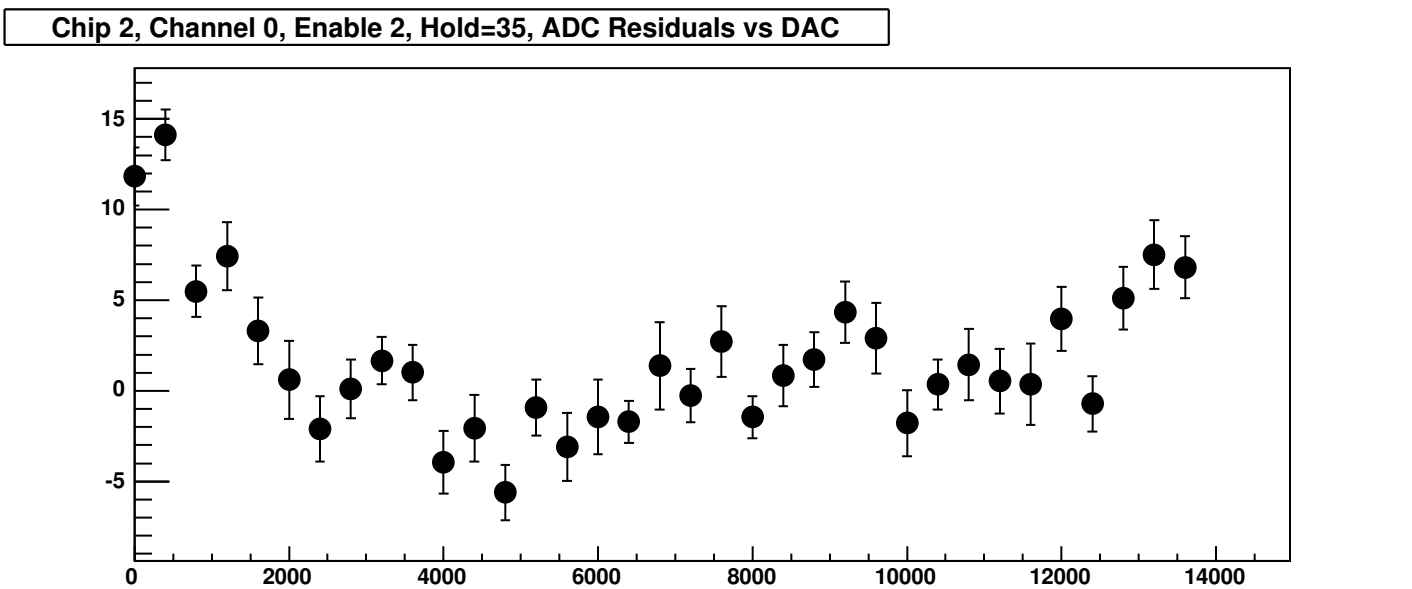

Chip 2, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 27.03 / 23
p0 52.32 ± 0.7389
p1 0.01573 ± 0.0001141
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

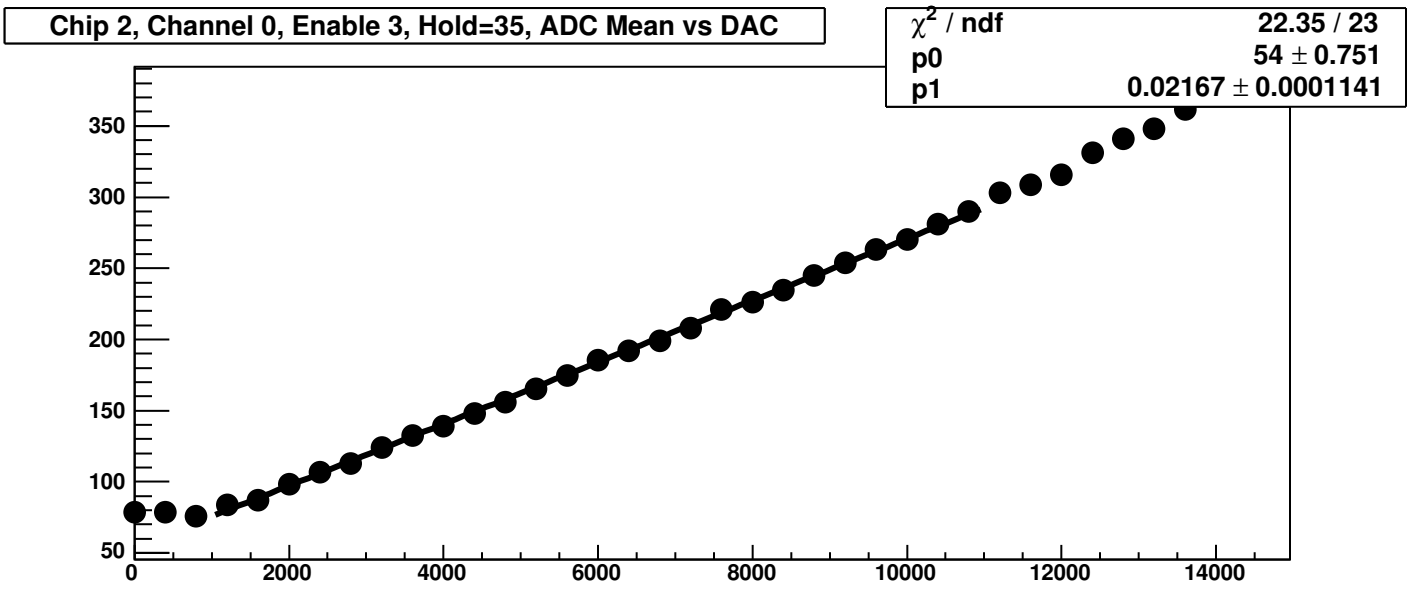

Chip 2, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

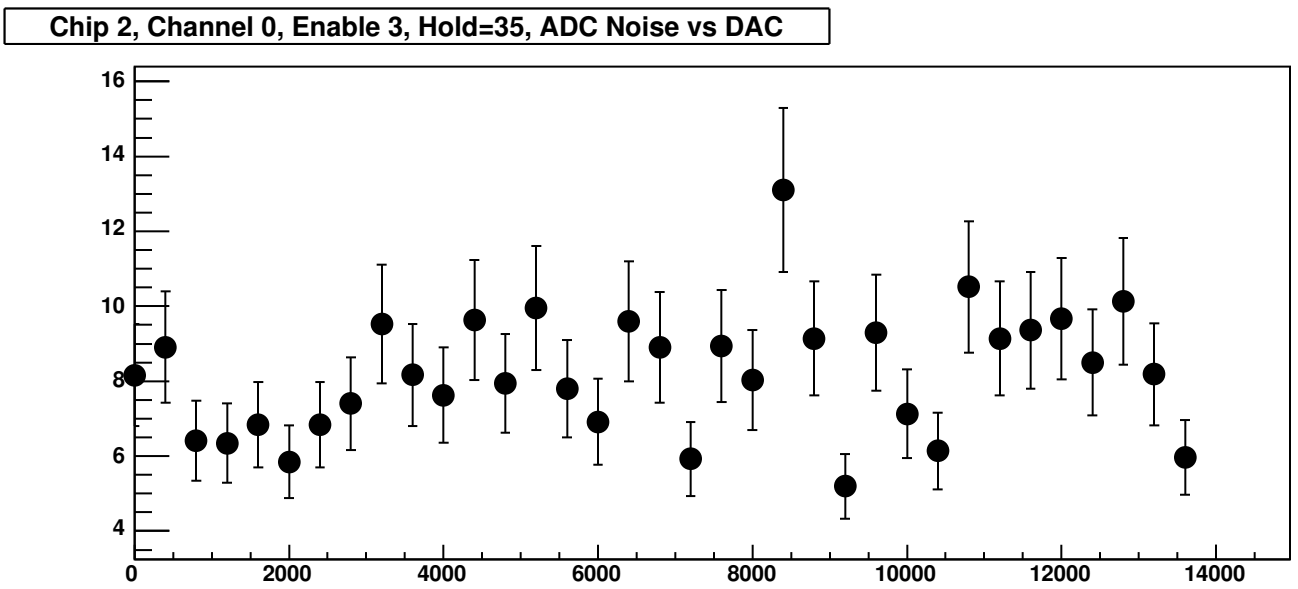

Chip 2, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

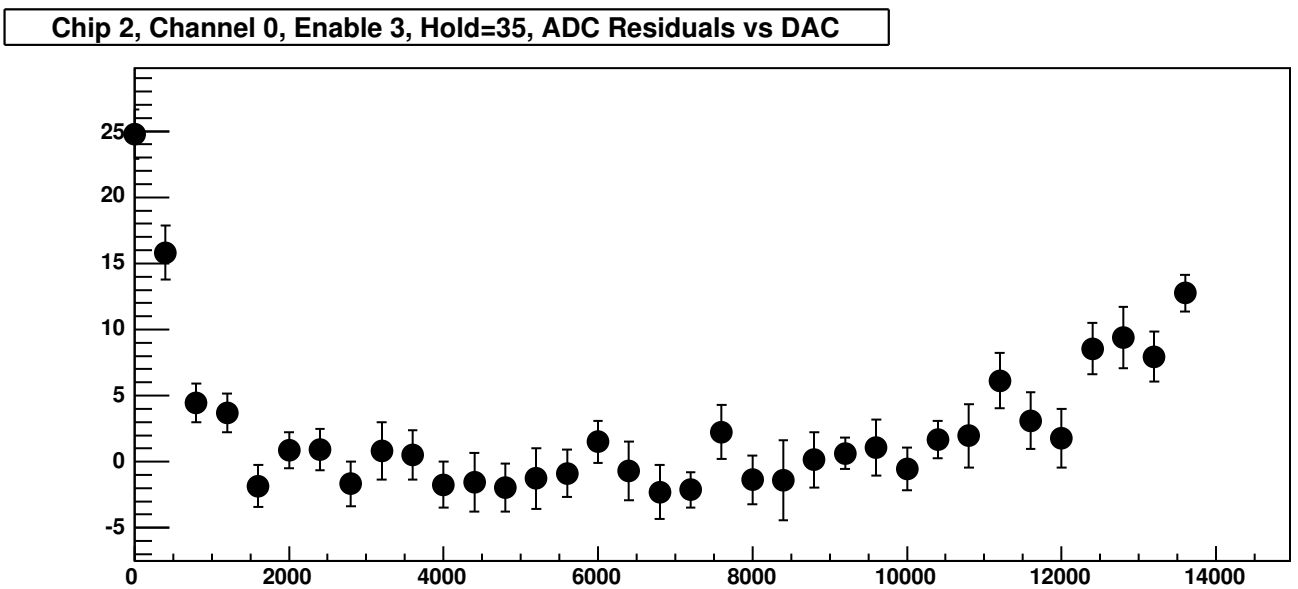

Chip 2, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 63.95 / 23
p0 53.11 ± 0.7952
p1 0.01333 ± 0.0001182
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

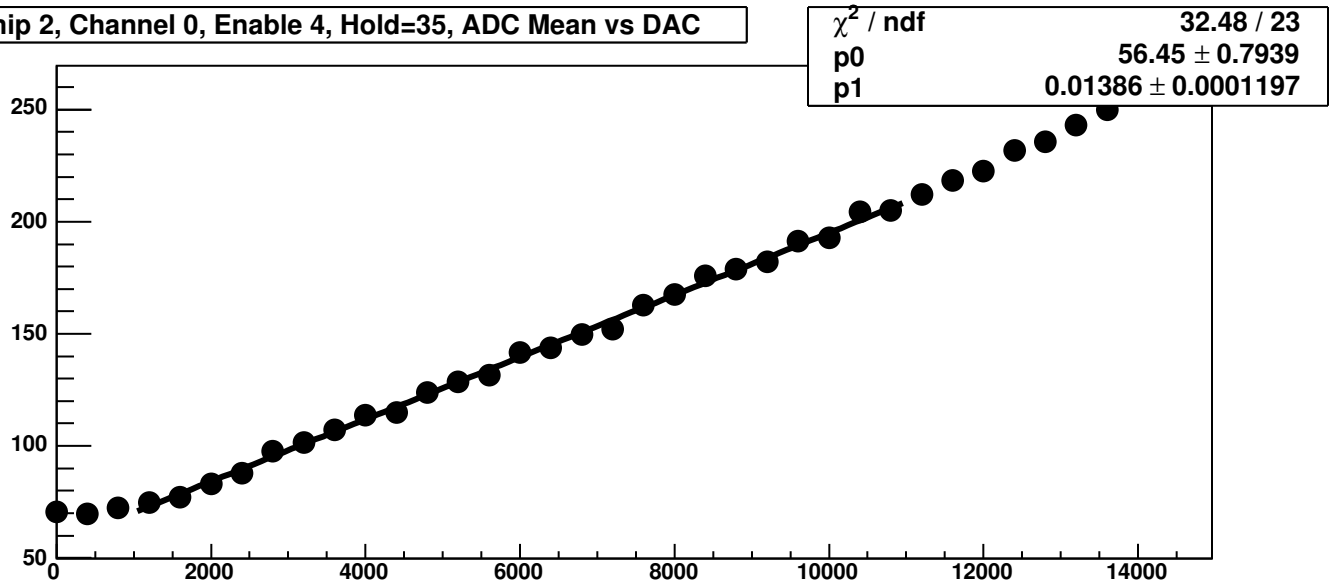

Chip 2, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 22.35 / 23
p0 54 ± 0.751
p1 0.02167 ± 0.0001141
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

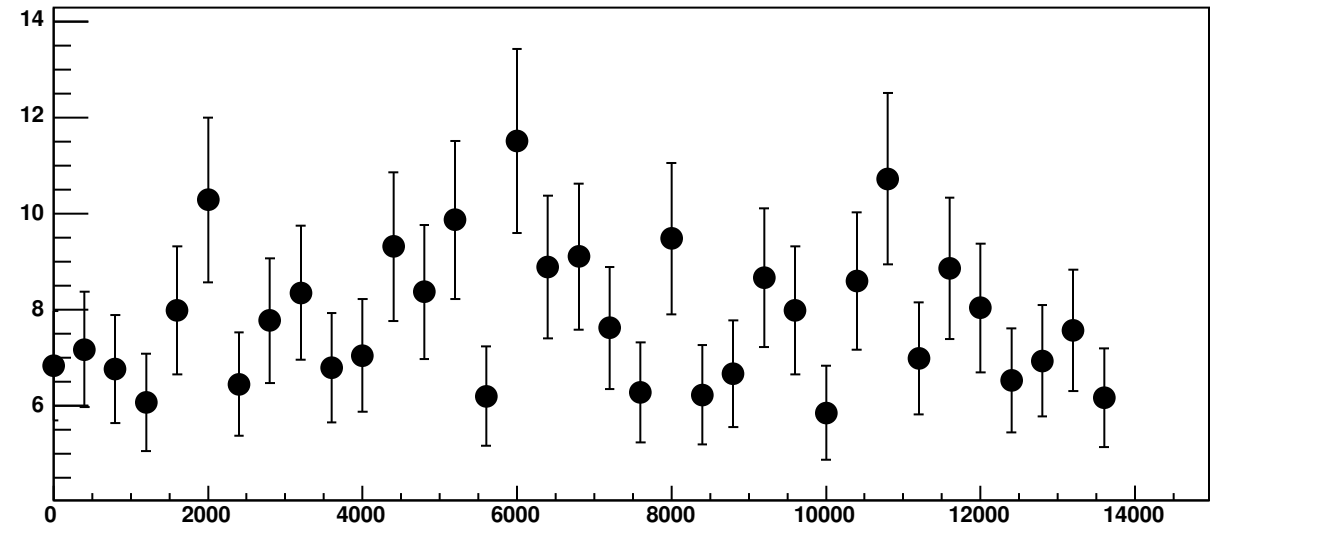

Chip 2, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

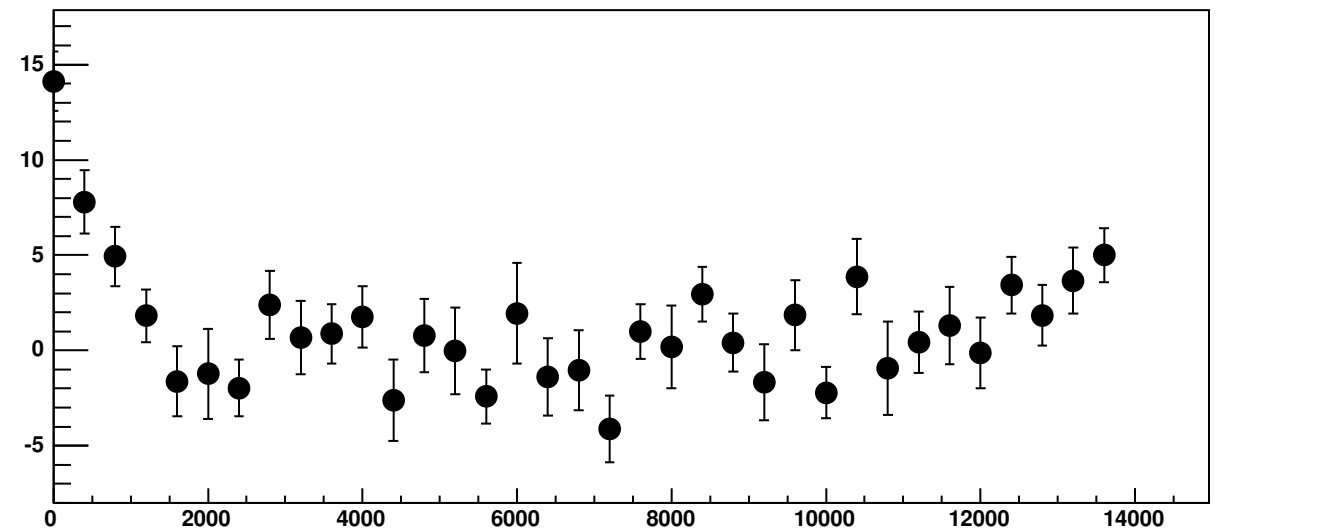
Chip 2, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

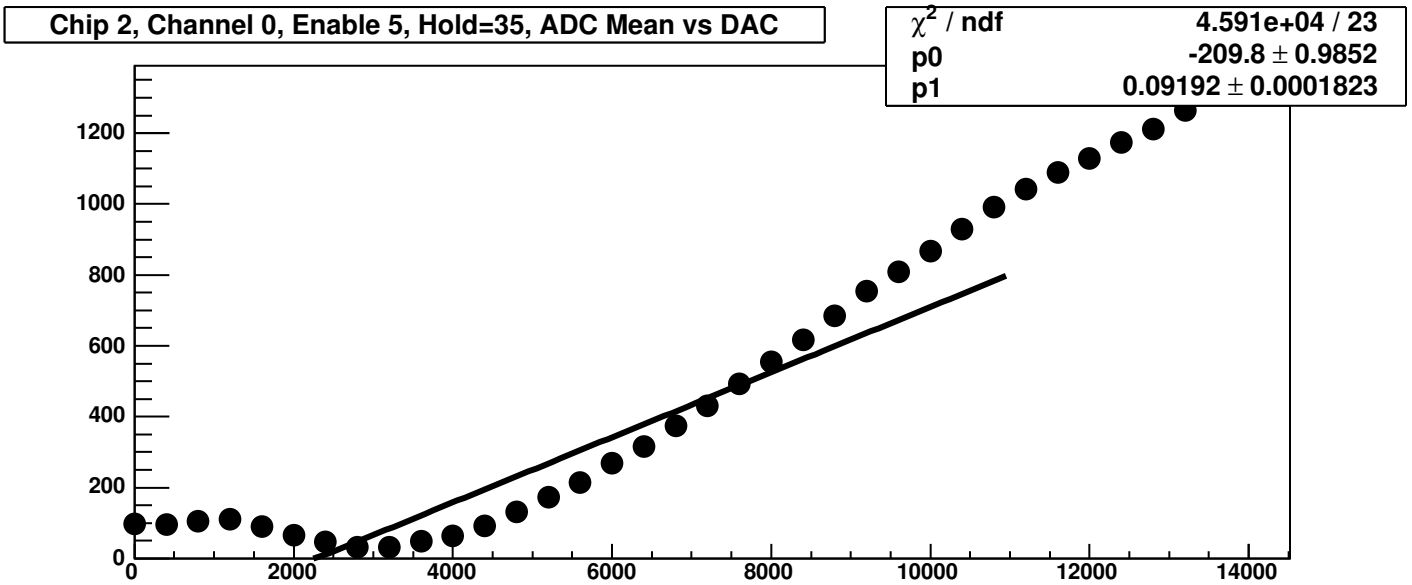


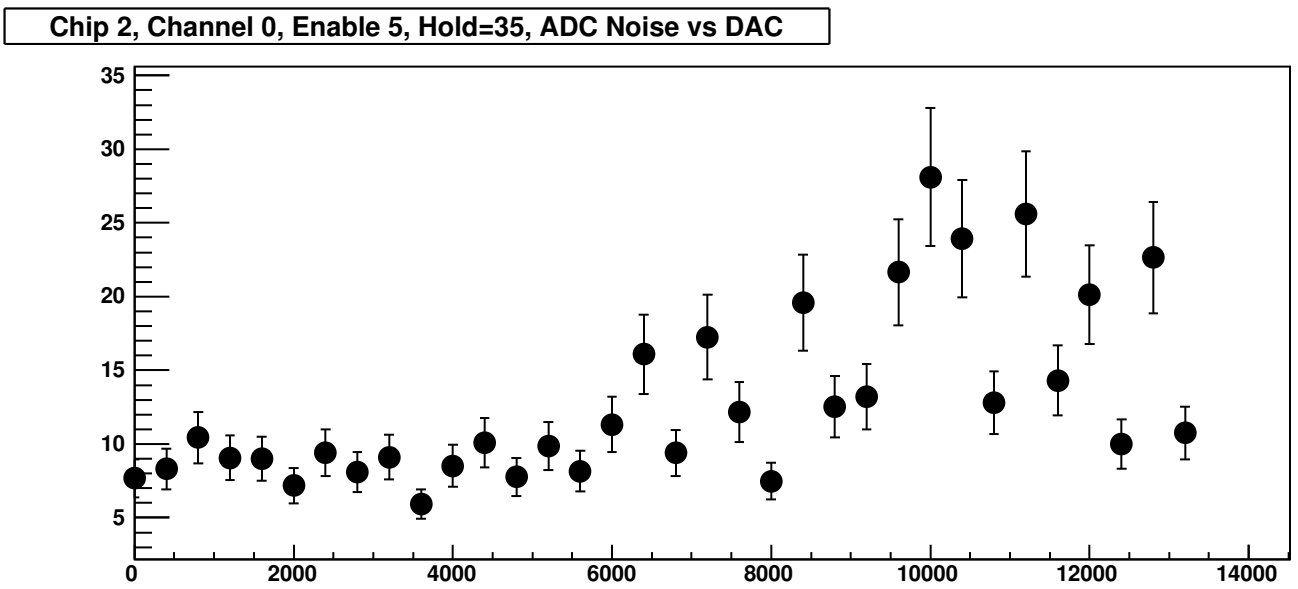
Chip 2, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

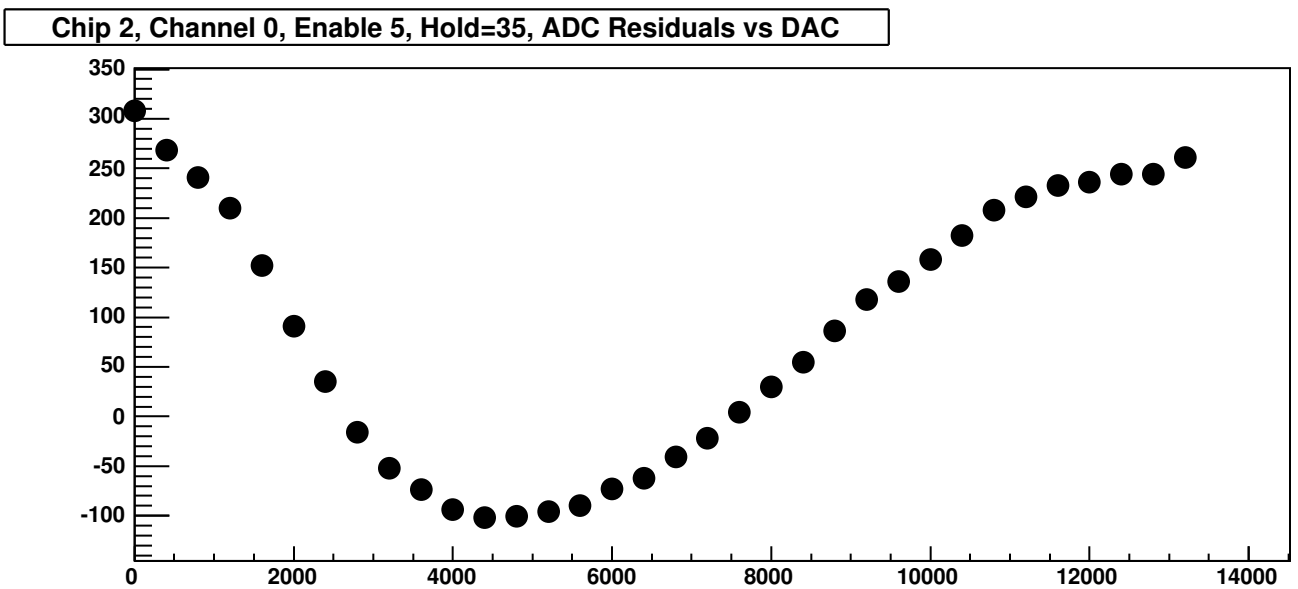


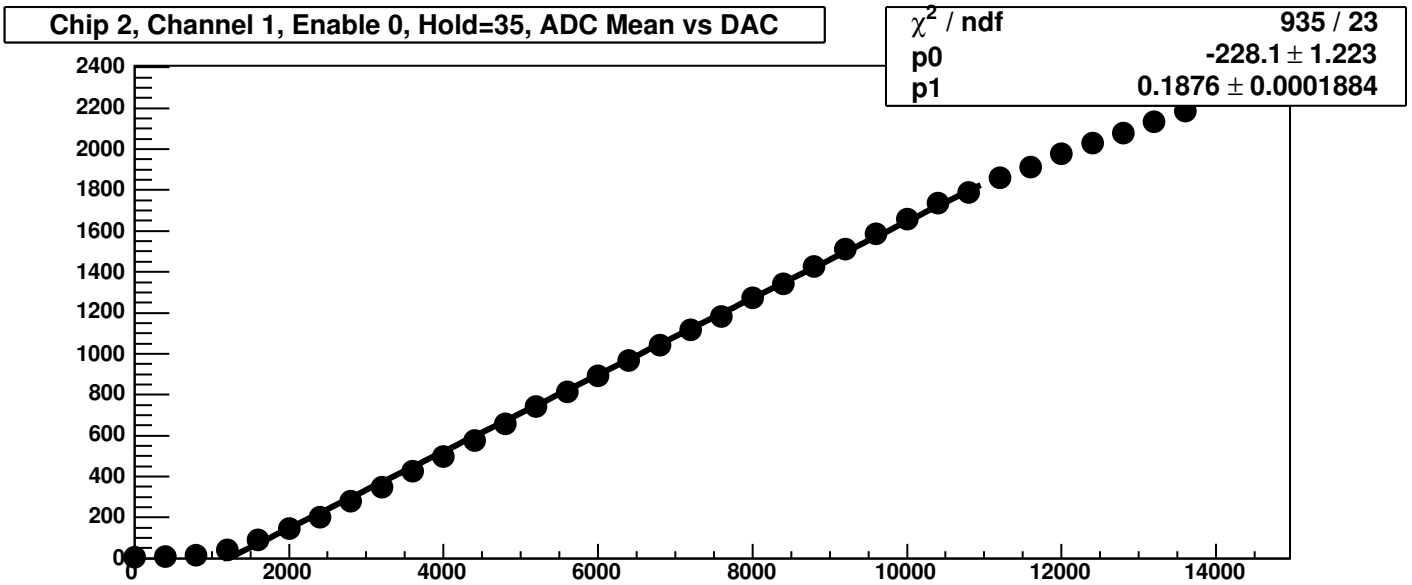
Chip 2, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

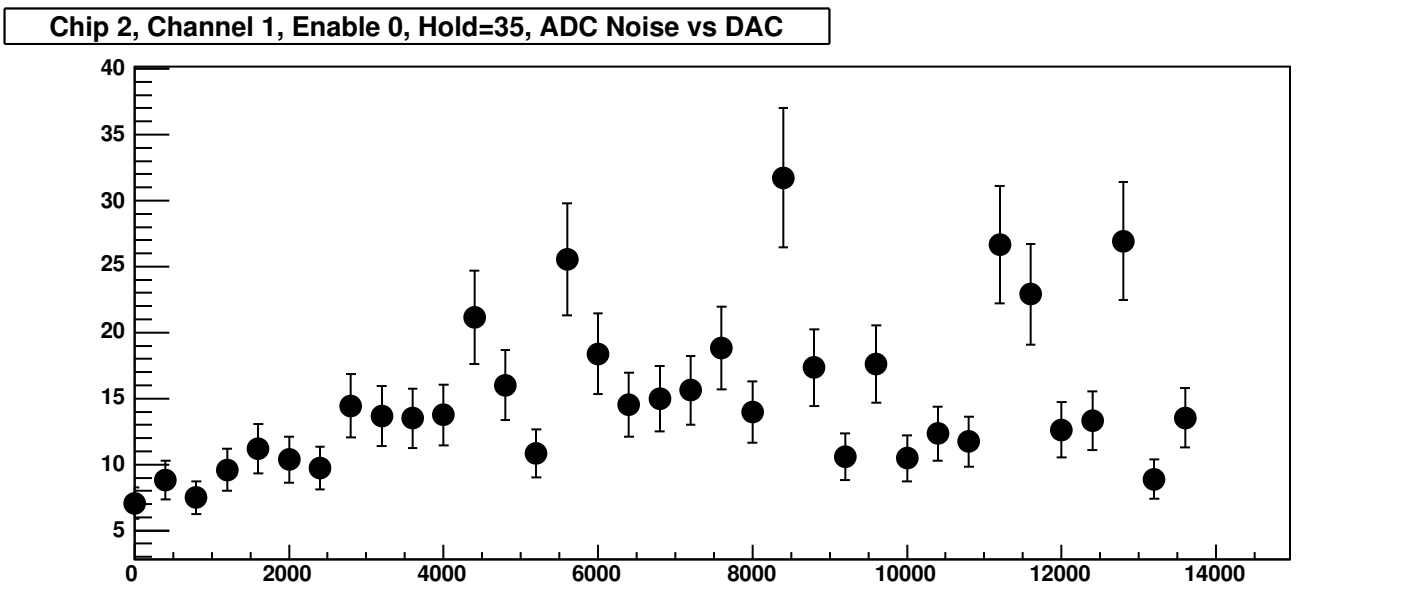


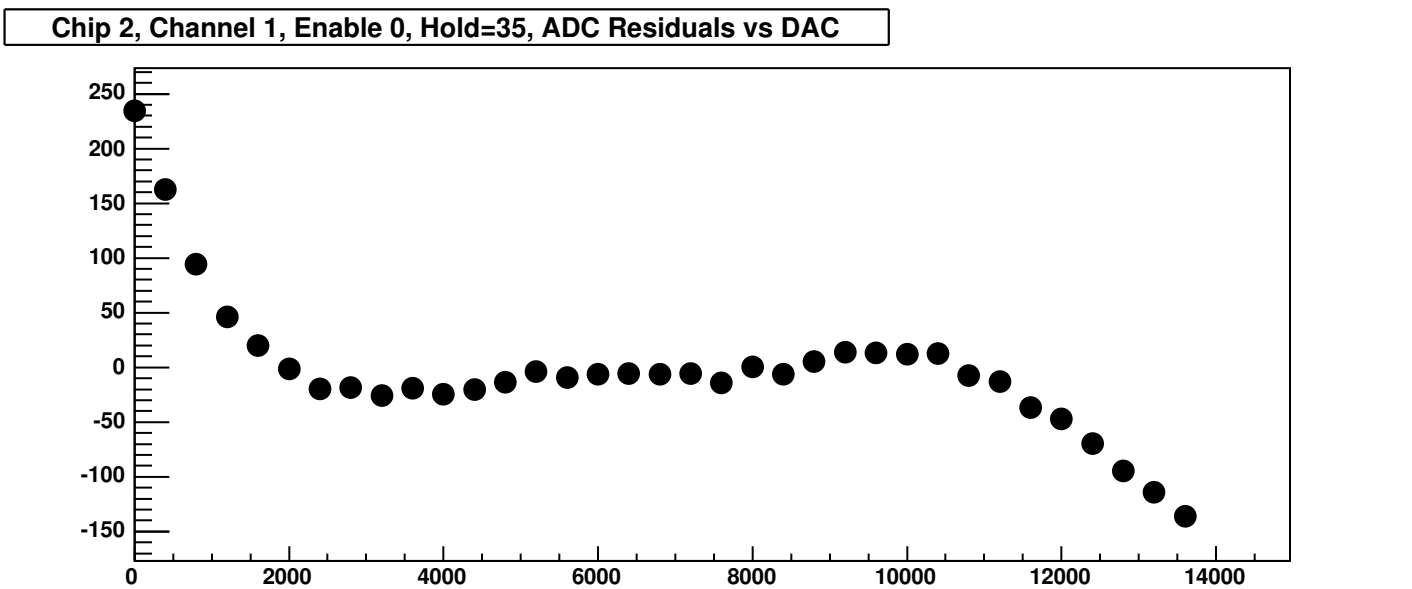

Chip 2, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4.591e+04 / 23
p0 -209.8 ± 0.9852
p1 0.09192 ± 0.0001823
0
200
400
600
800
1000
1200
0
2000
4000
6000
8000
10000
12000
14000

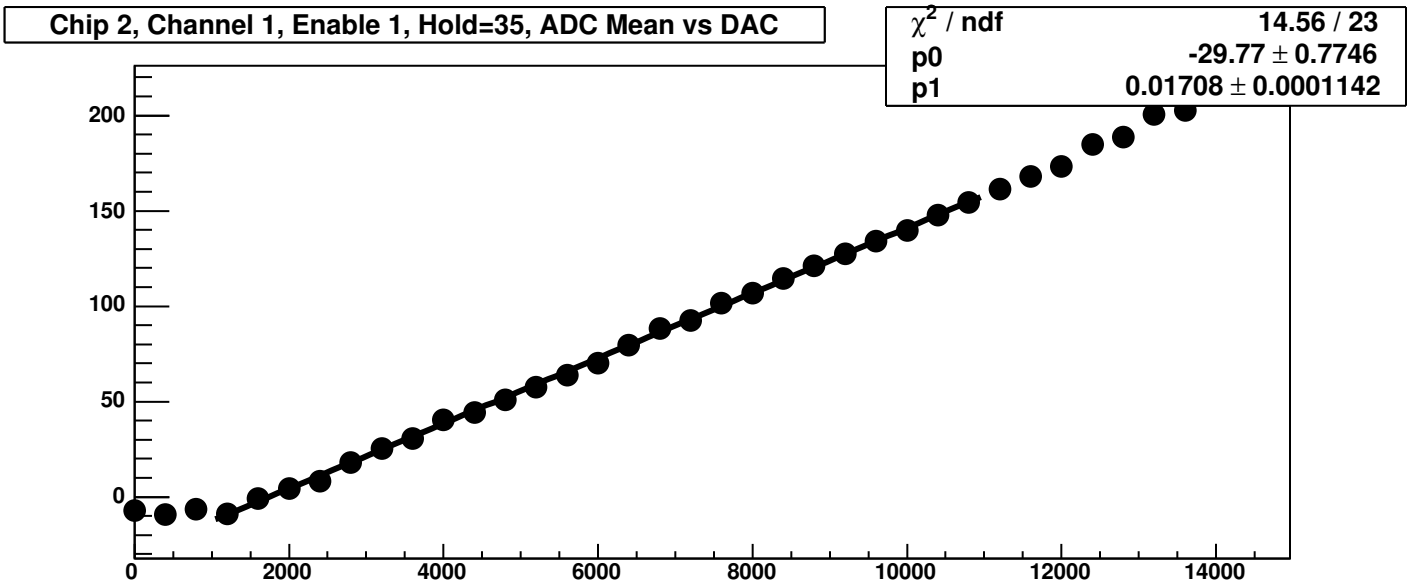

Chip 2, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
5
10
15
20
25
30
35
0
2000
4000
6000
8000
10000
12000
14000

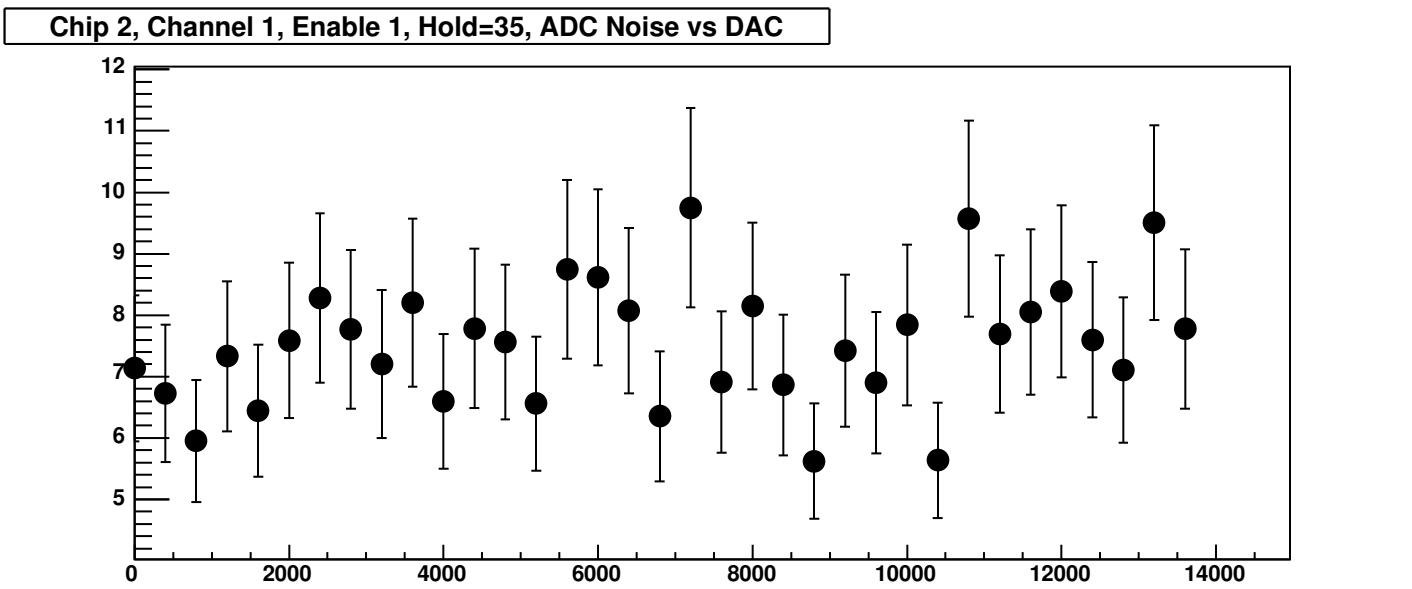

Chip 2, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
-100
-50
0
50
100
150
200
250
300
350
0
2000
4000
6000
8000
10000
12000
14000

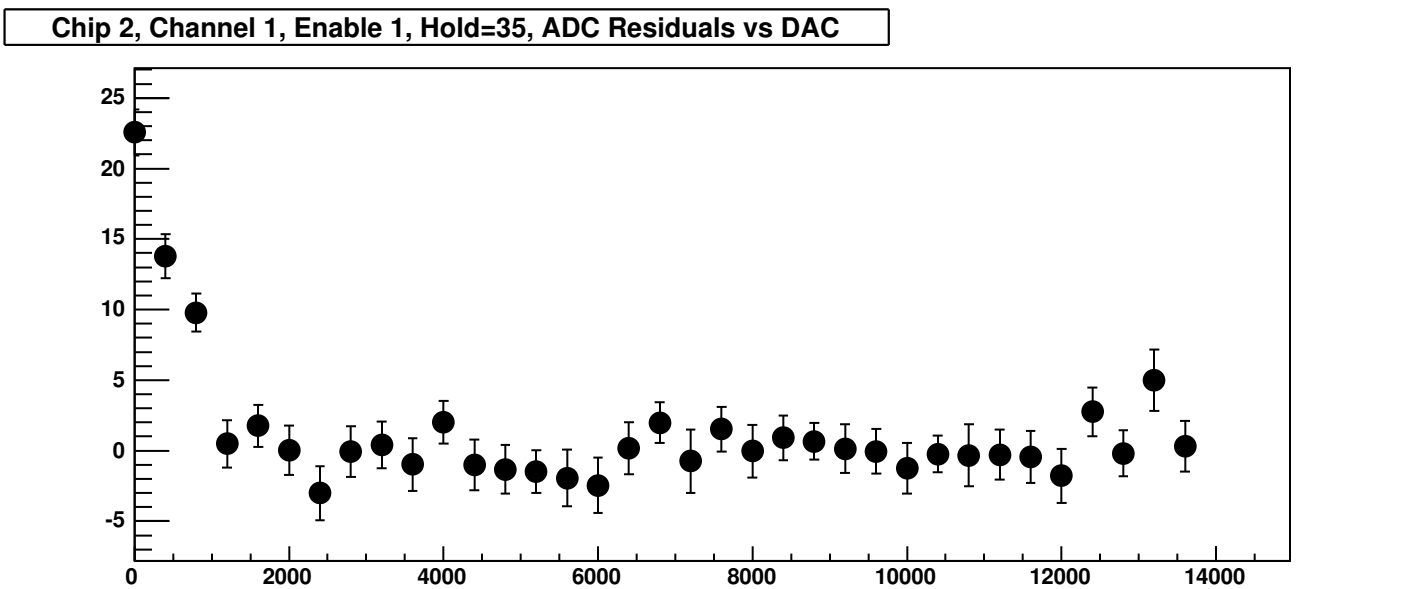

Chip 2, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 935 / 23
p0 -228.1 ± 1.223
p1 0.1876 ± 0.0001884
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

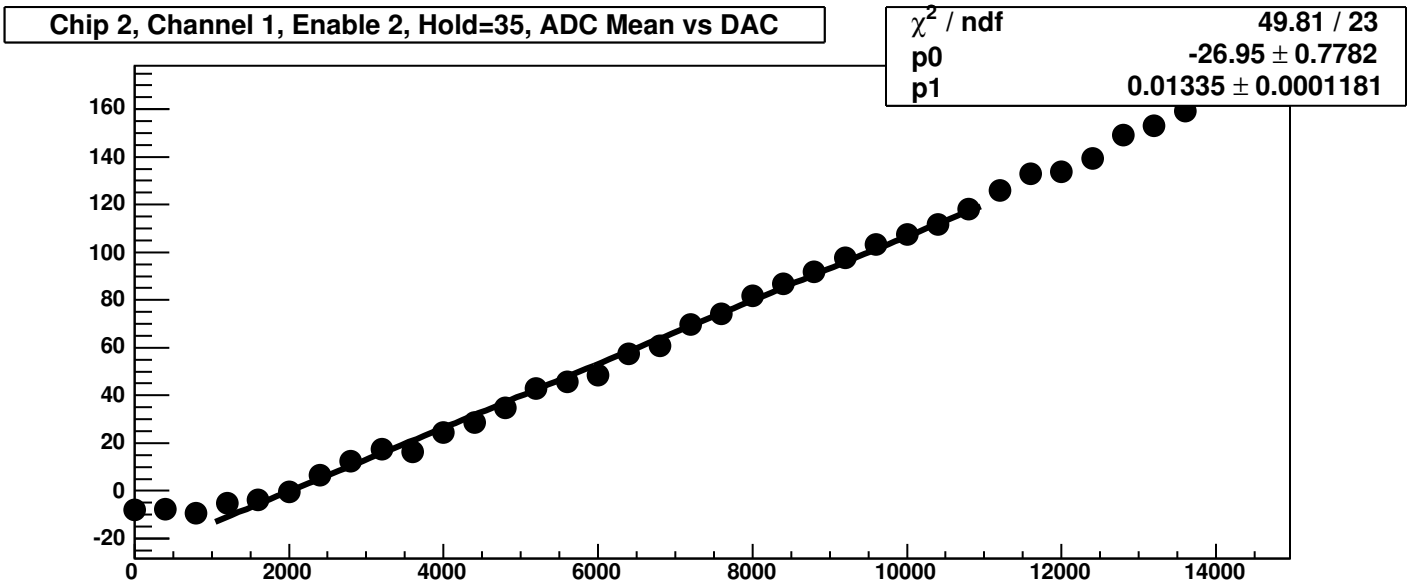

Chip 2, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

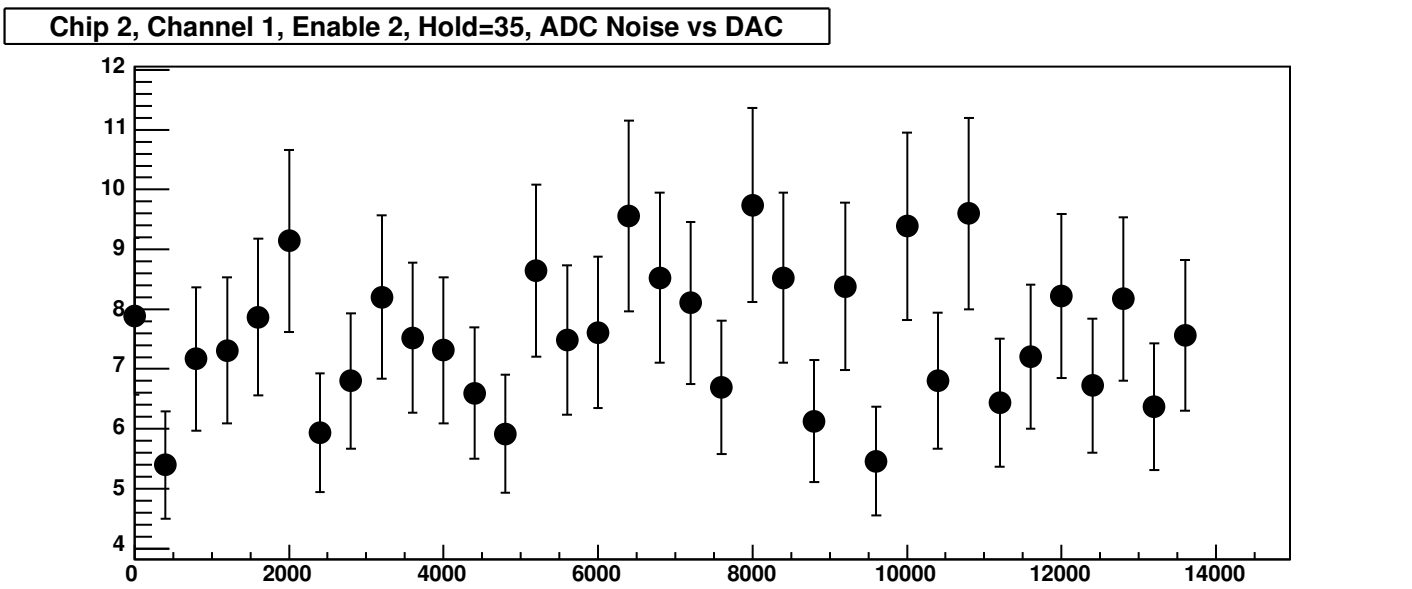

Chip 2, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

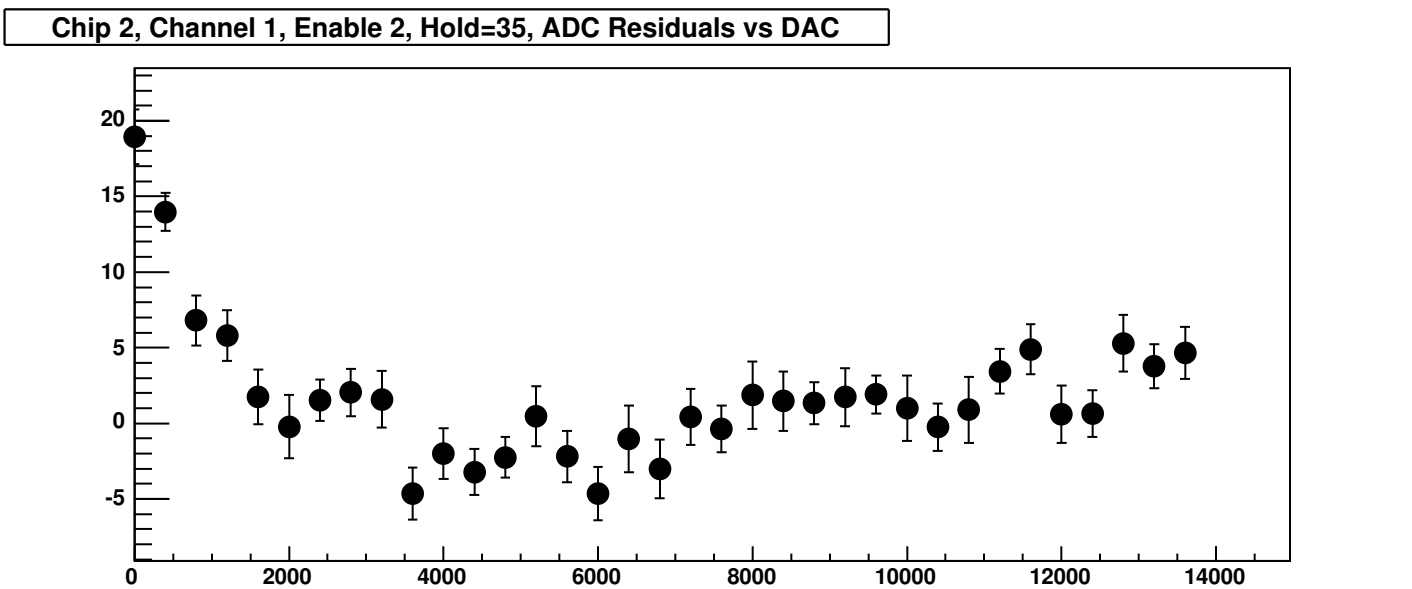

Chip 2, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 14.56 / 23
p0 -29.77 ± 0.7746
p1 0.01708 ± 0.0001142
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

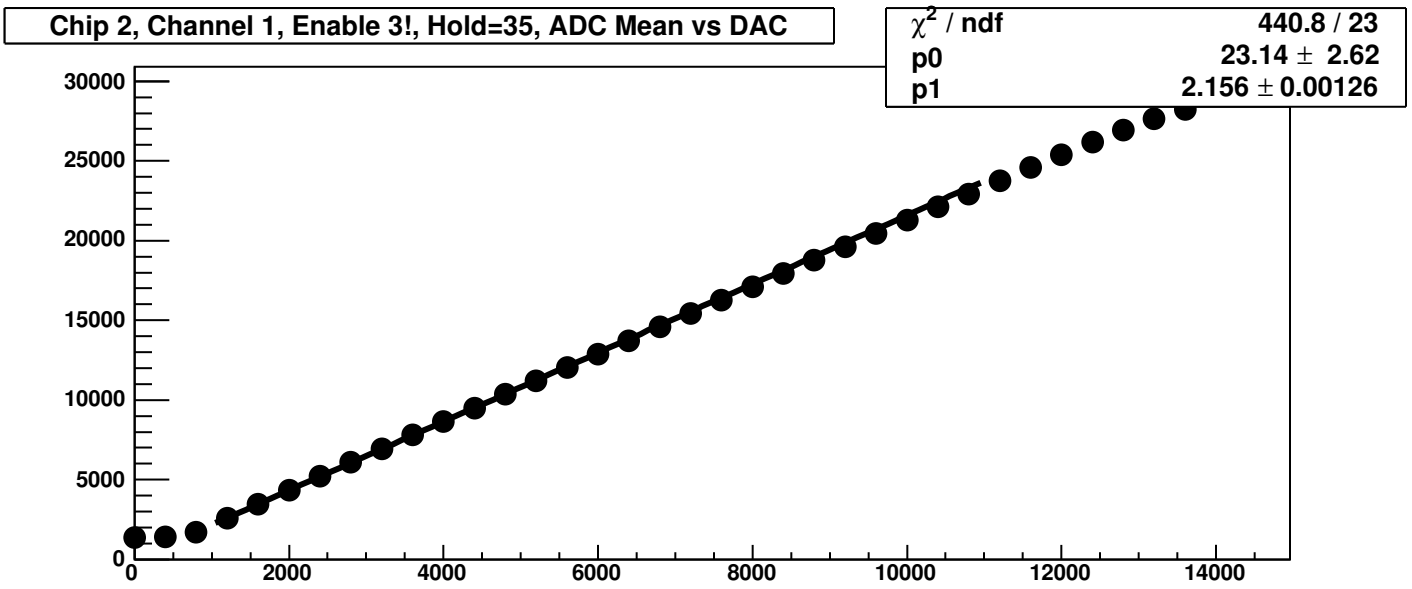

Chip 2, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

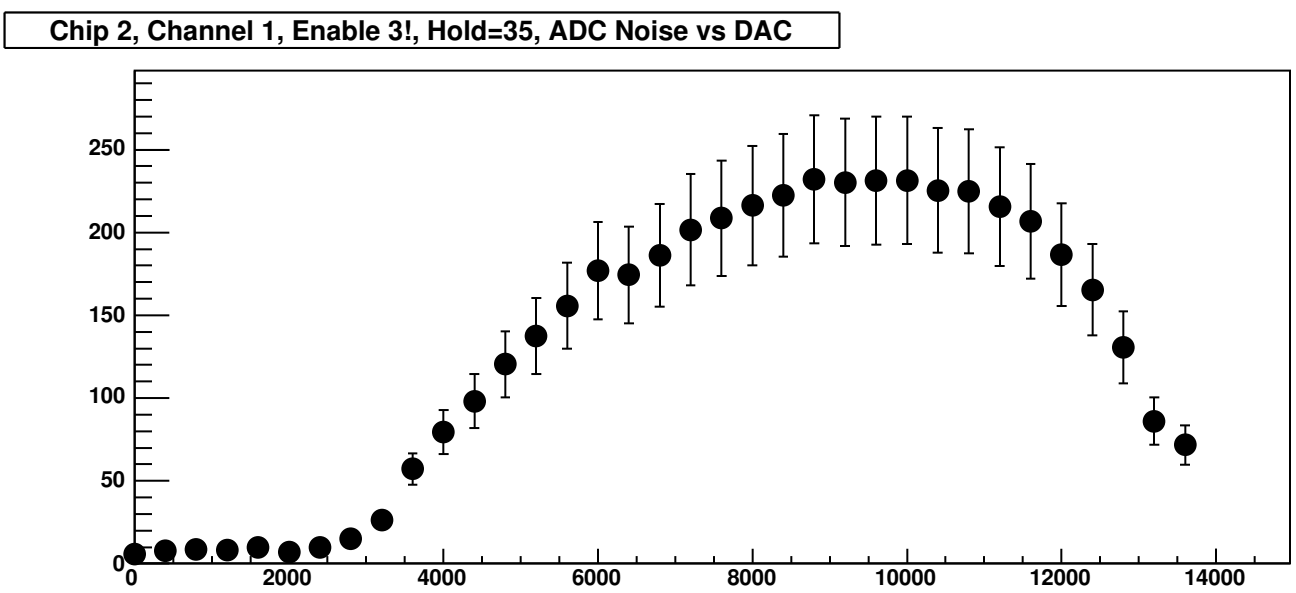

Chip 2, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

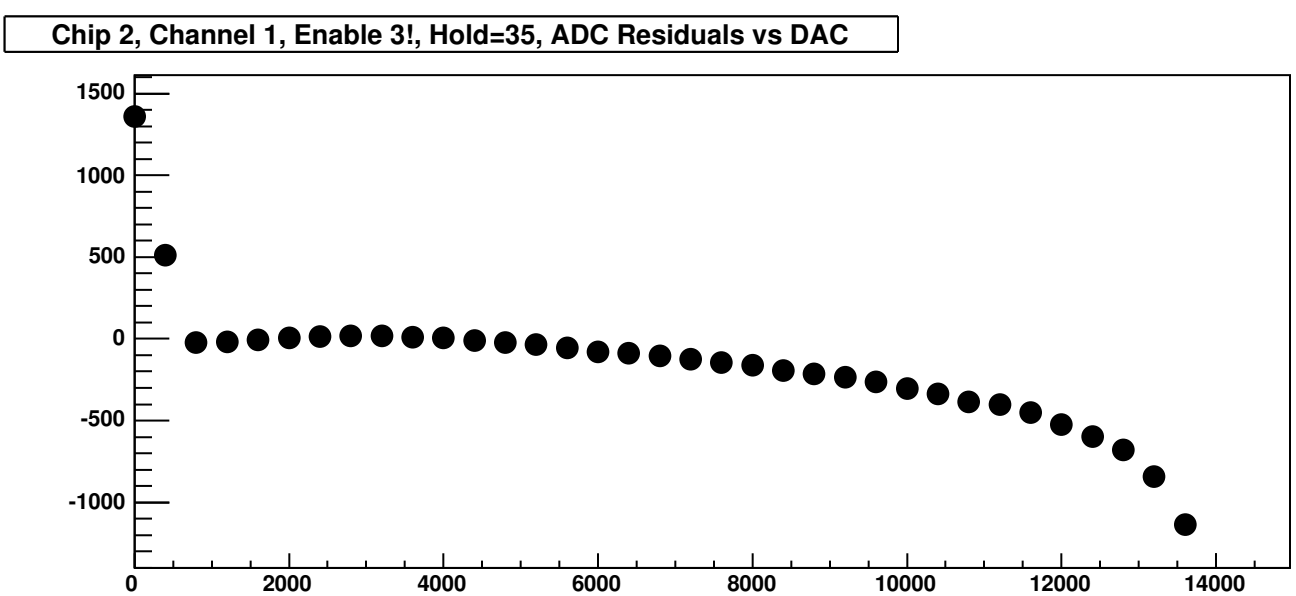

Chip 2, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 49.81 / 23
p0 -26.95 ± 0.7782
p1 0.01335 ± 0.0001181
160
140
120
100
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

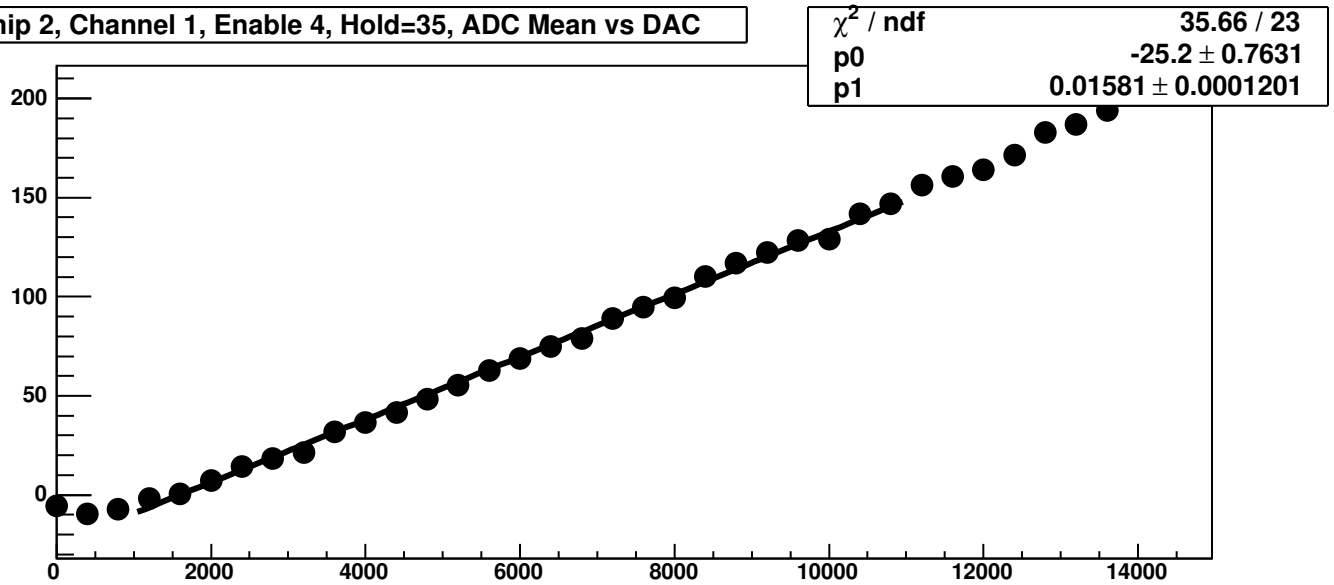

Chip 2, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 440.8 / 23
p0 23.14 ± 2.62
p1 2.156 ± 0.00126
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

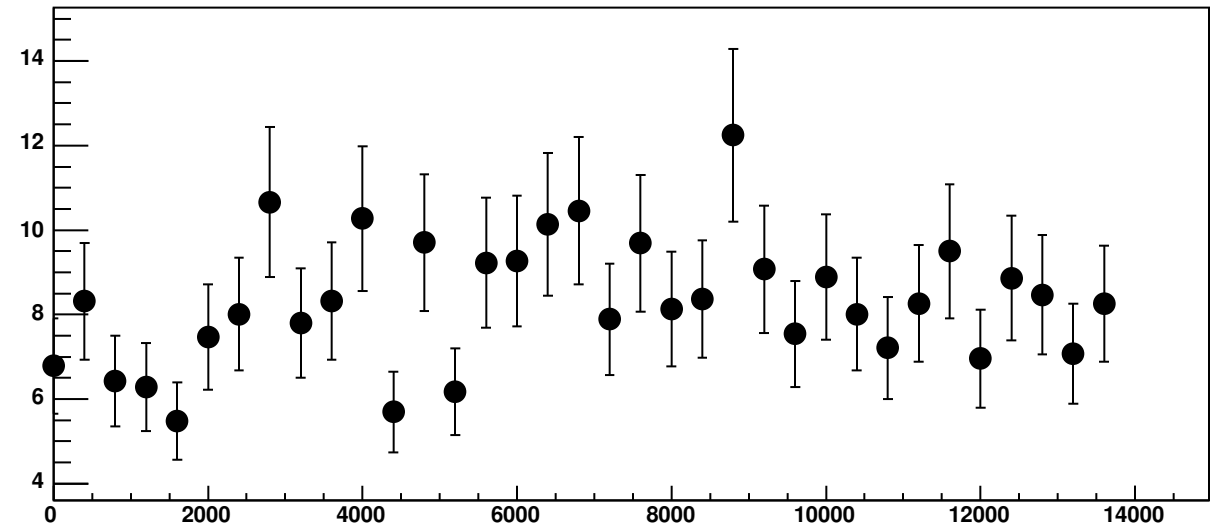

Chip 2, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

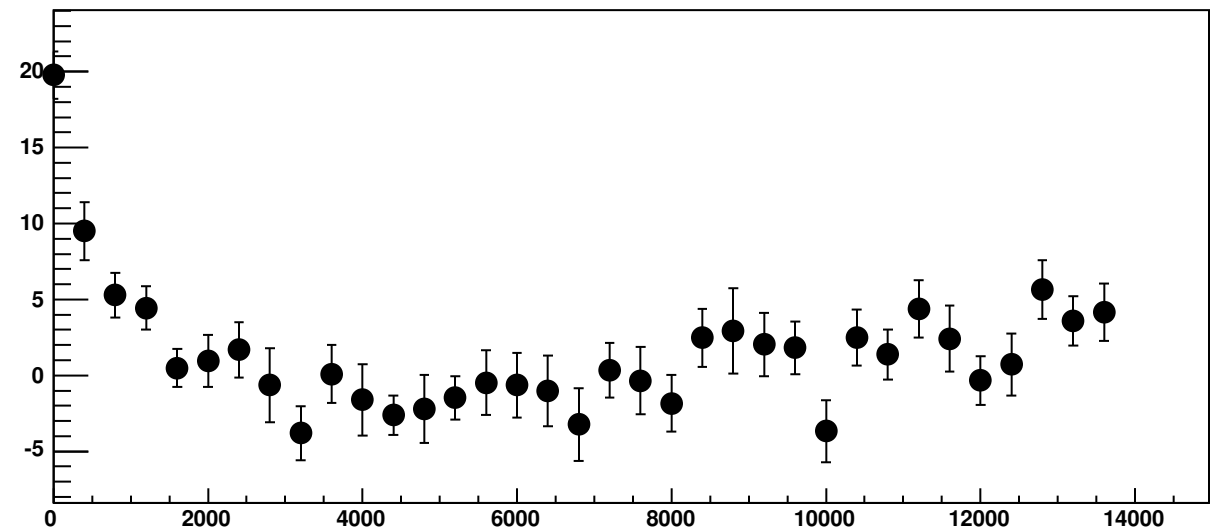
Chip 2, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



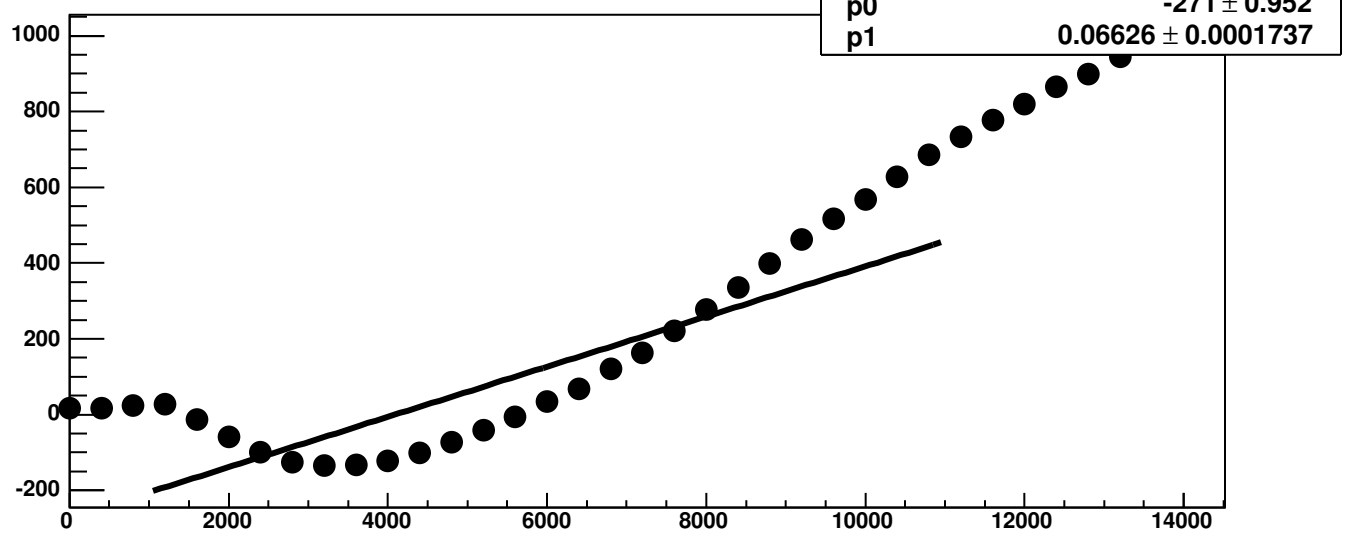
Chip 2, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



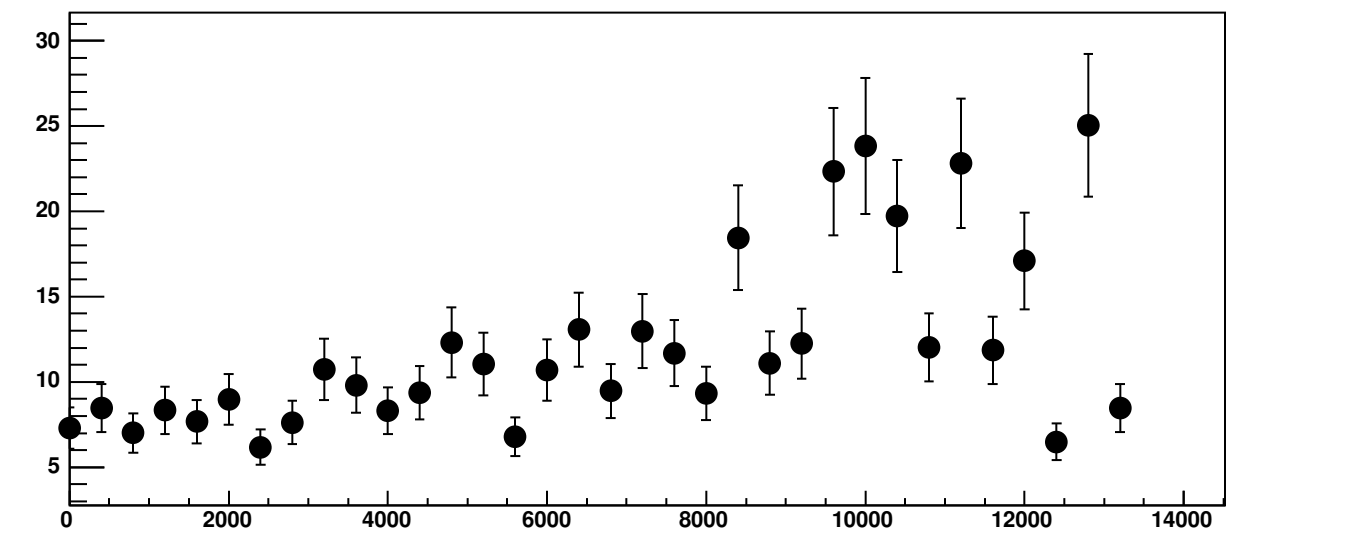
Chip 2, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



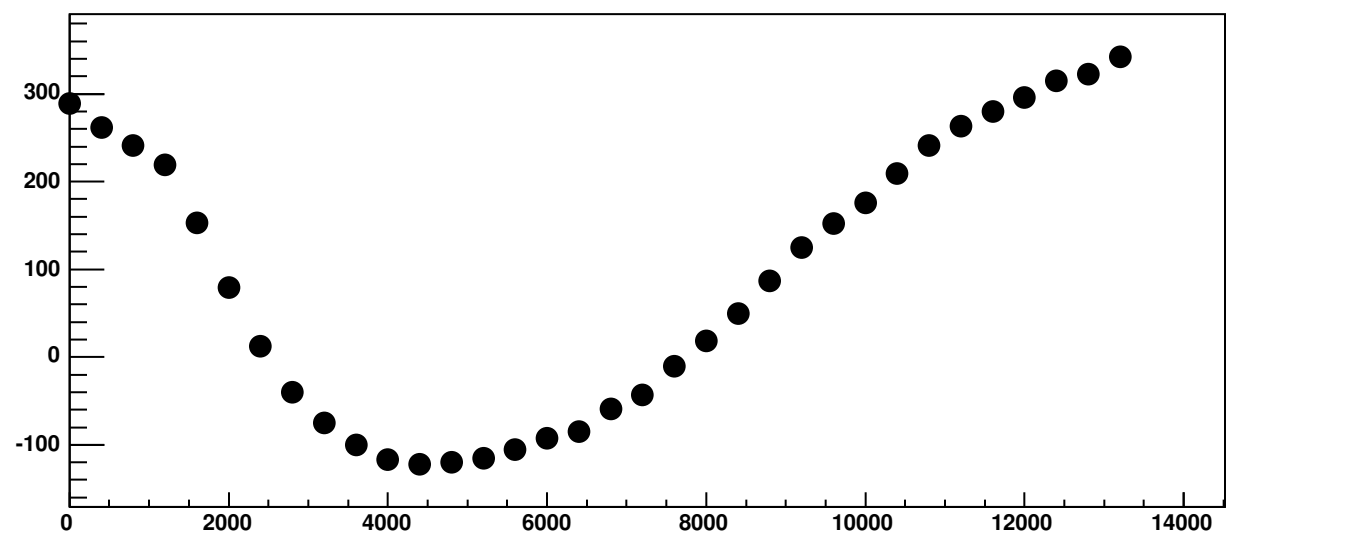
Chip 2, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

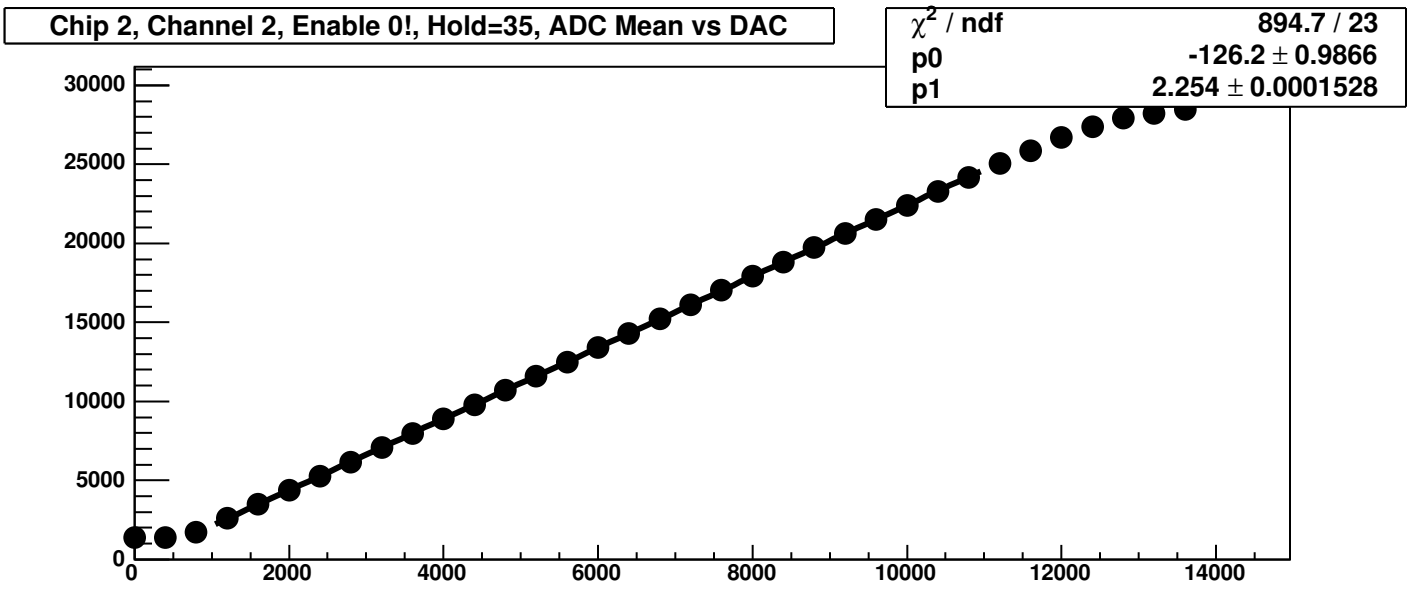


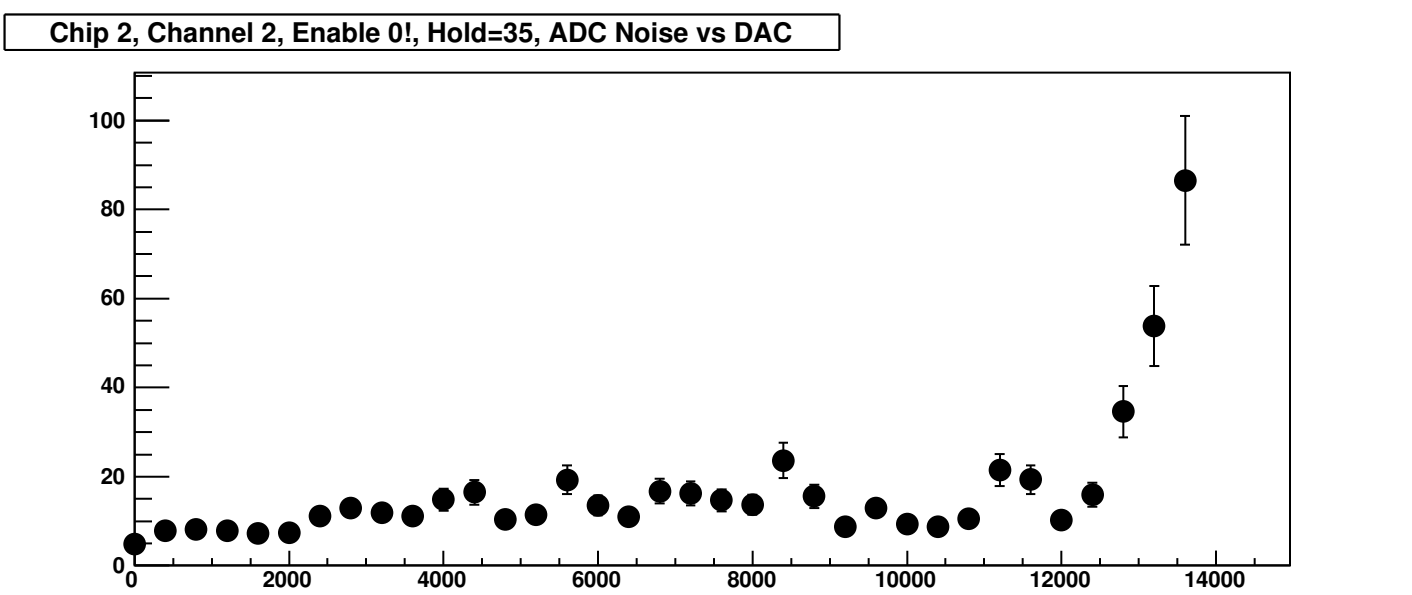
Chip 2, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC

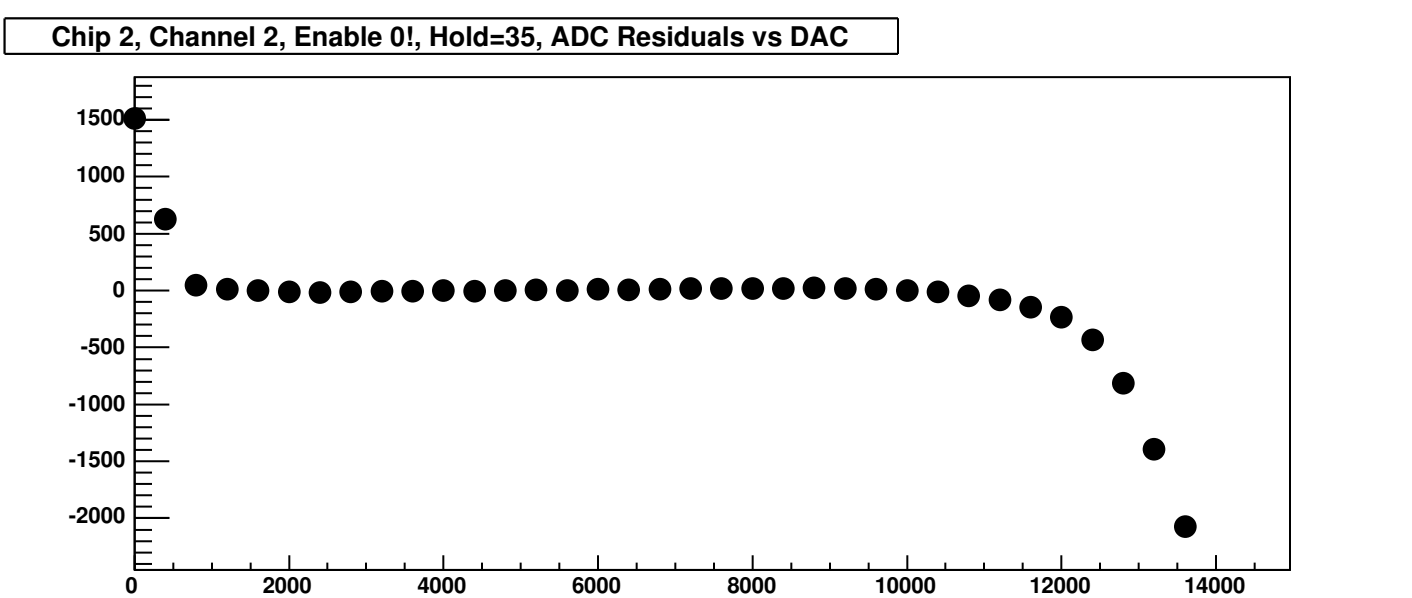


Chip 2, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

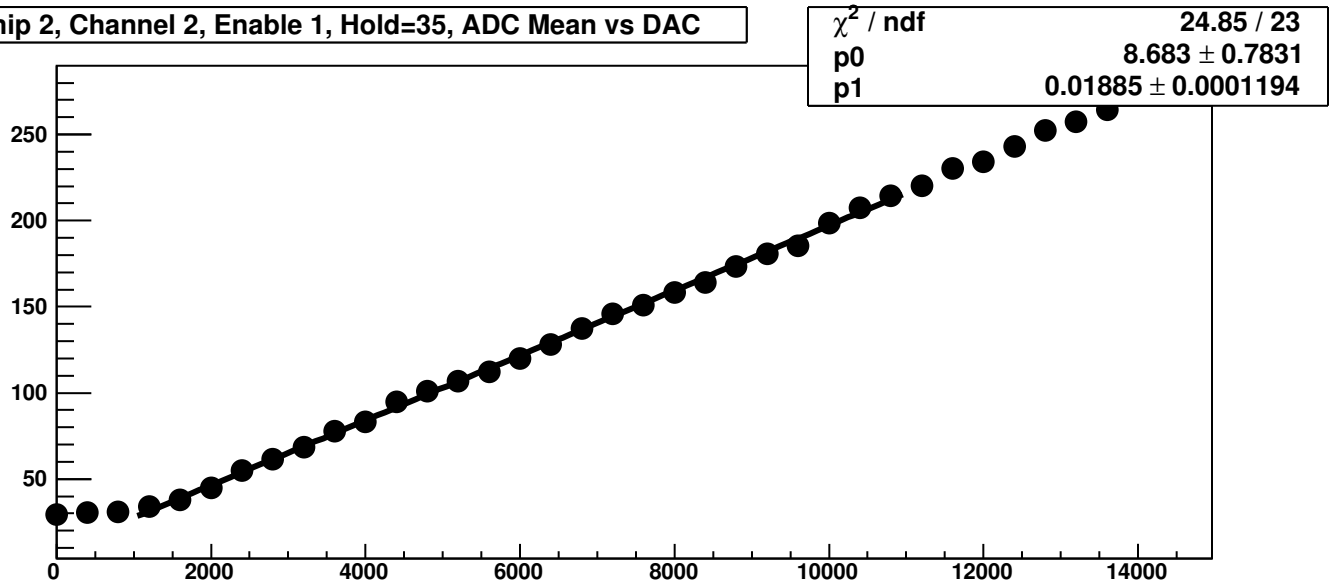



Chip 2, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 894.7 / 23
p0 -126.2 ± 0.9866
p1 2.254 ± 0.0001528
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

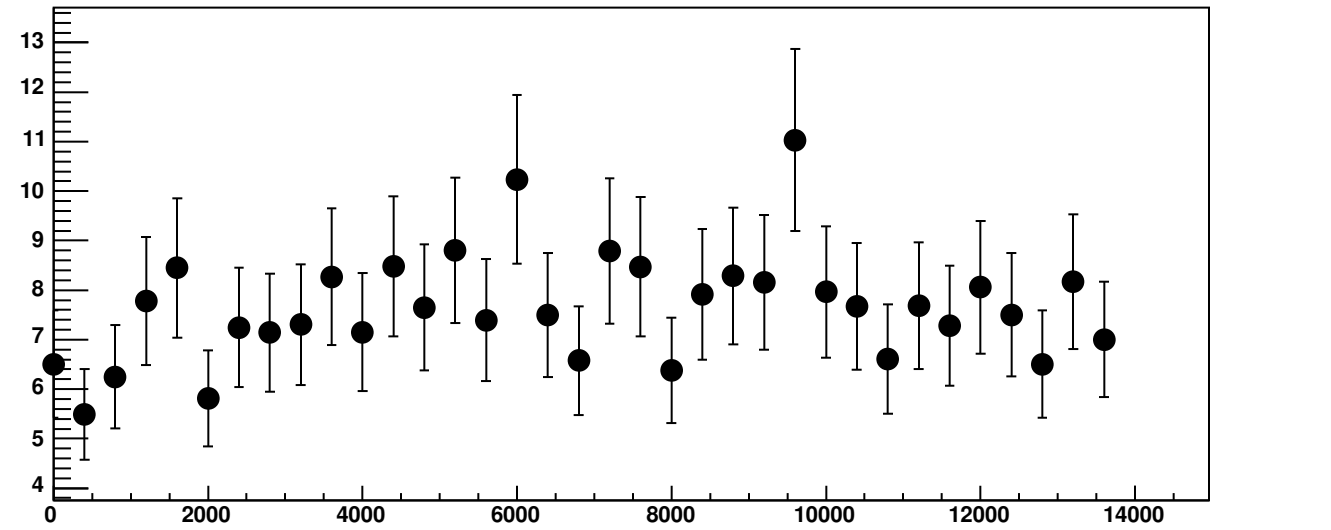

Chip 2, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

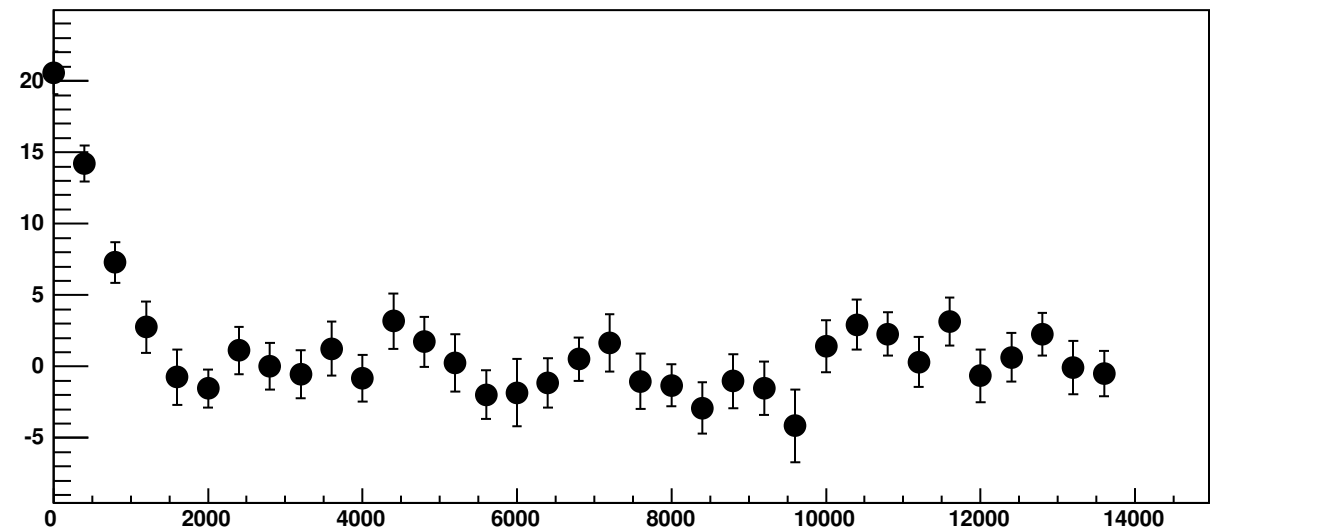
Chip 2, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



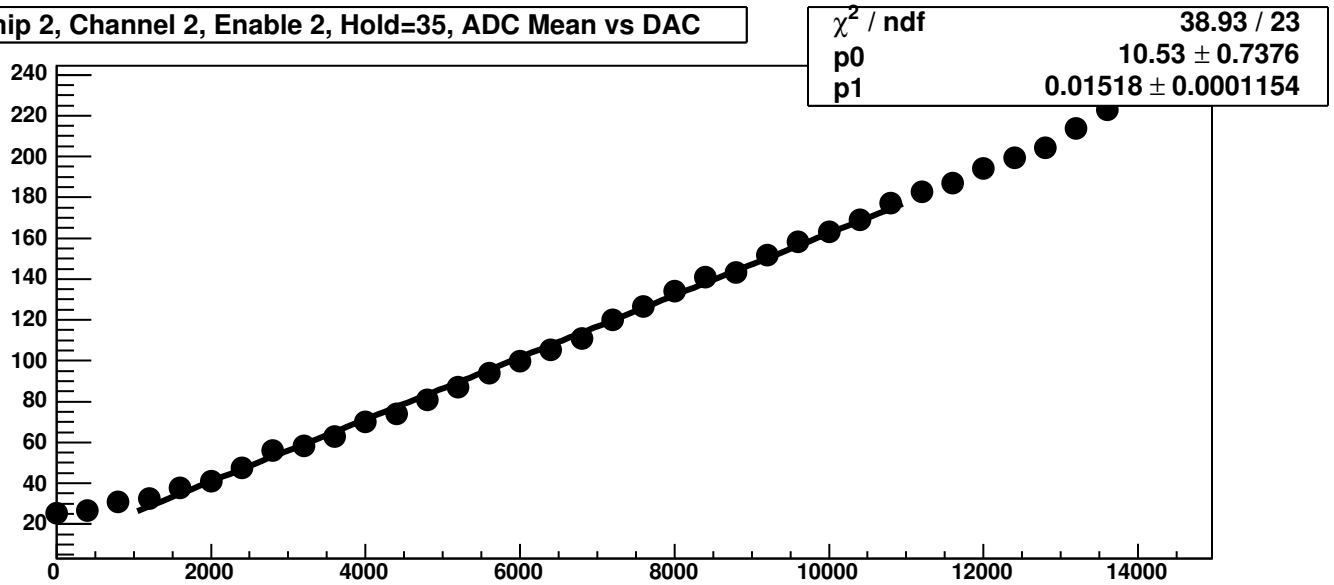
Chip 2, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



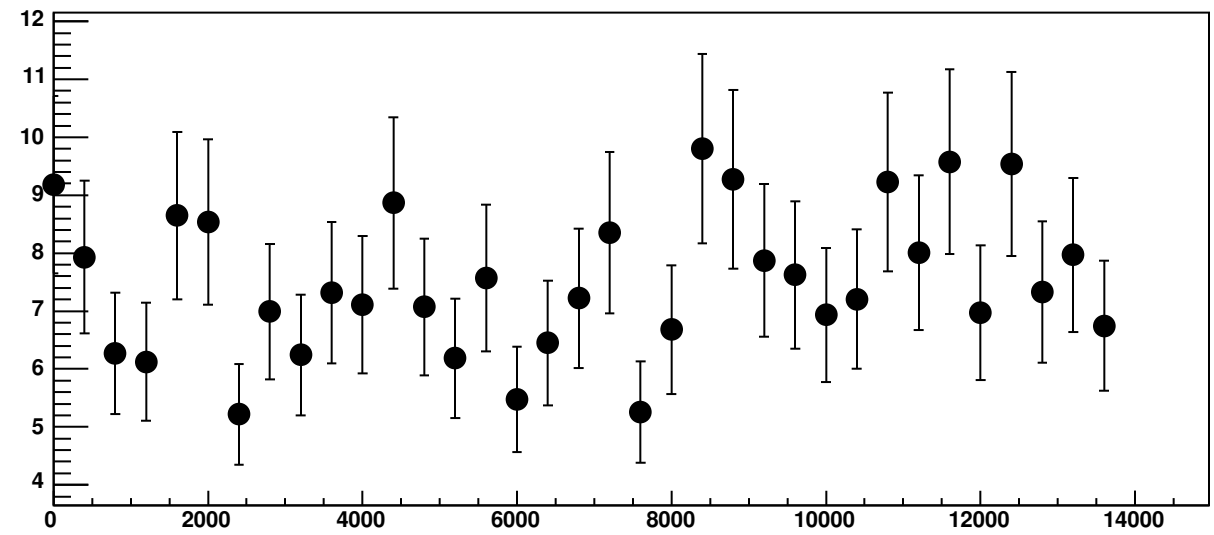
Chip 2, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



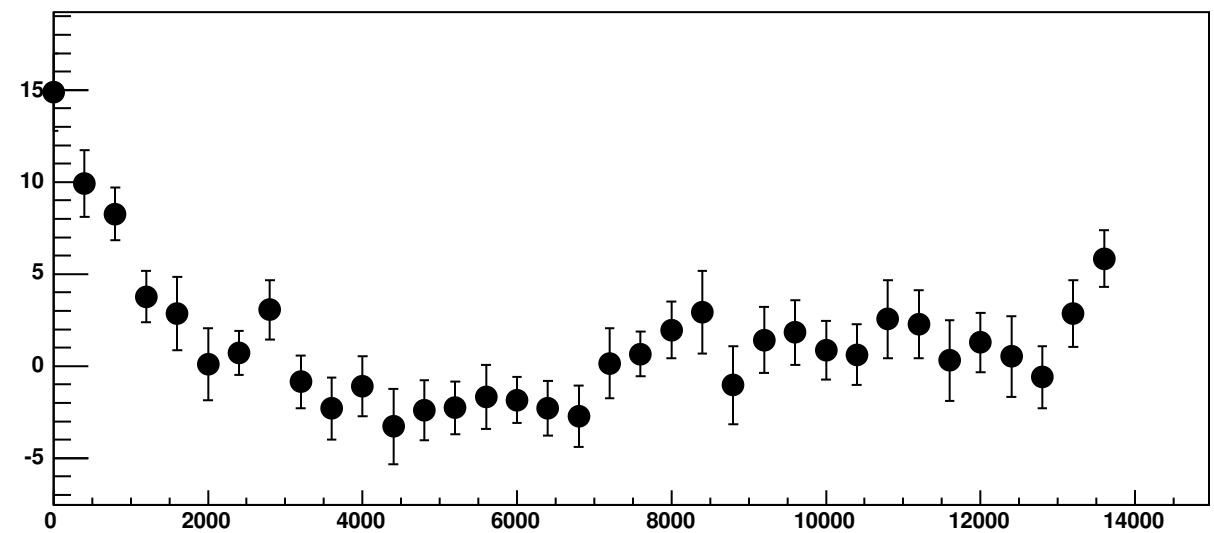
Chip 2, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

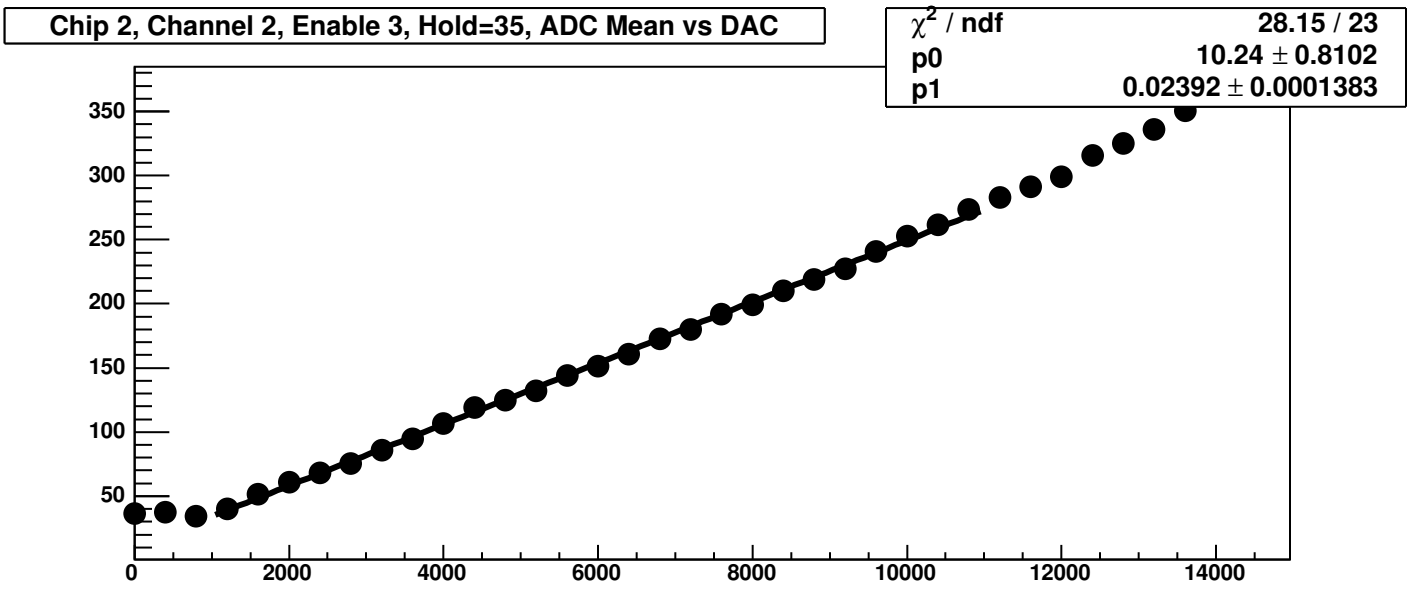


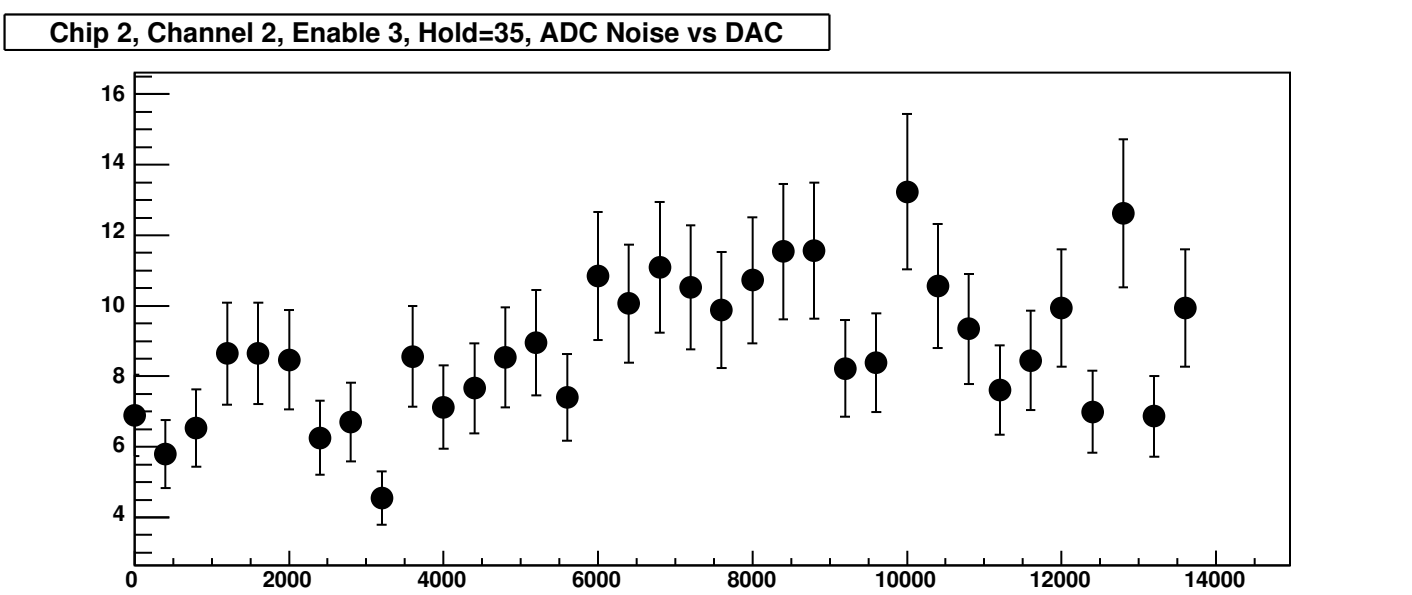
Chip 2, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

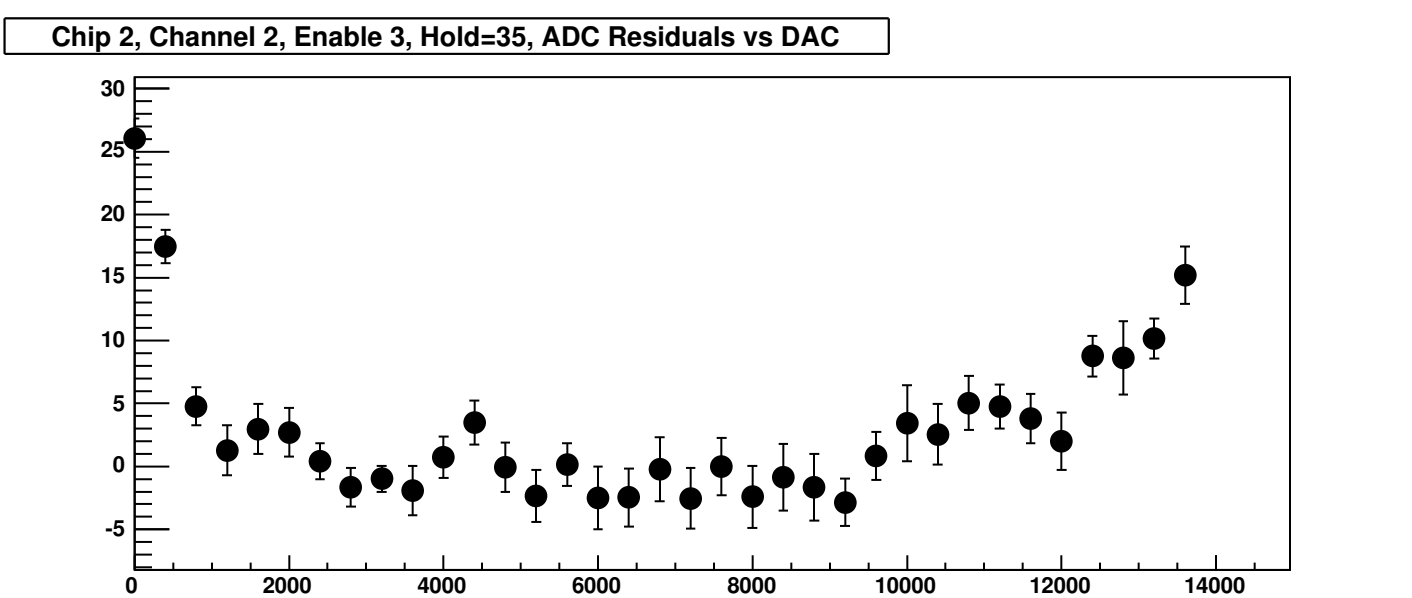


Chip 2, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

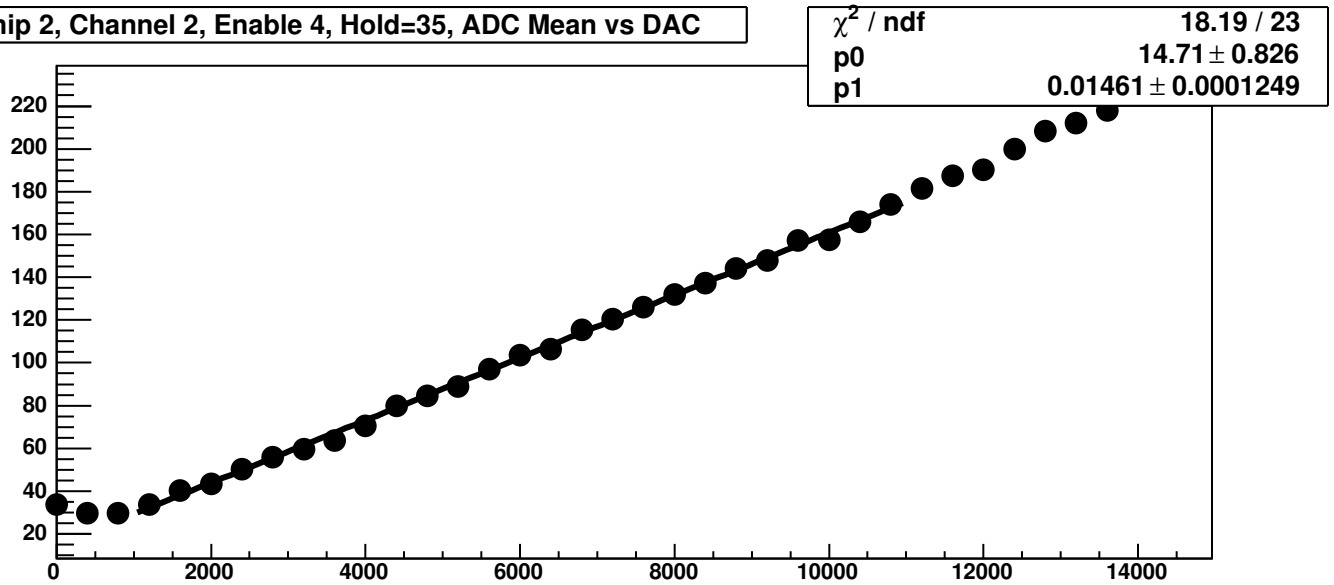



Chip 2, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.15 / 23
p0 10.24 ± 0.8102
p1 0.02392 ± 0.0001383
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

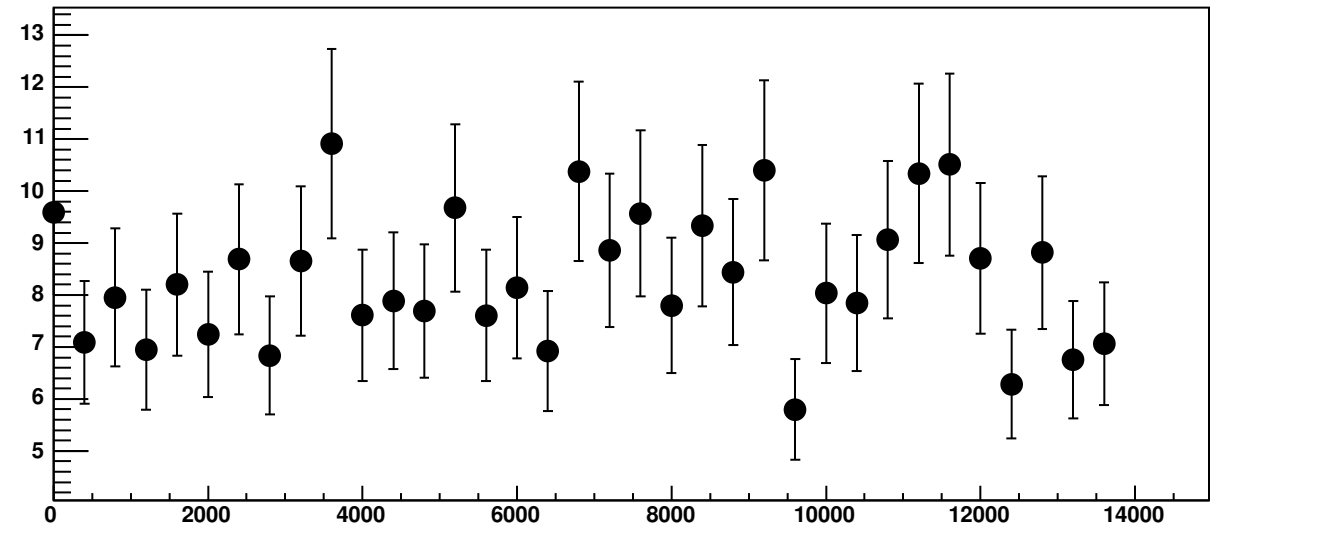

Chip 2, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

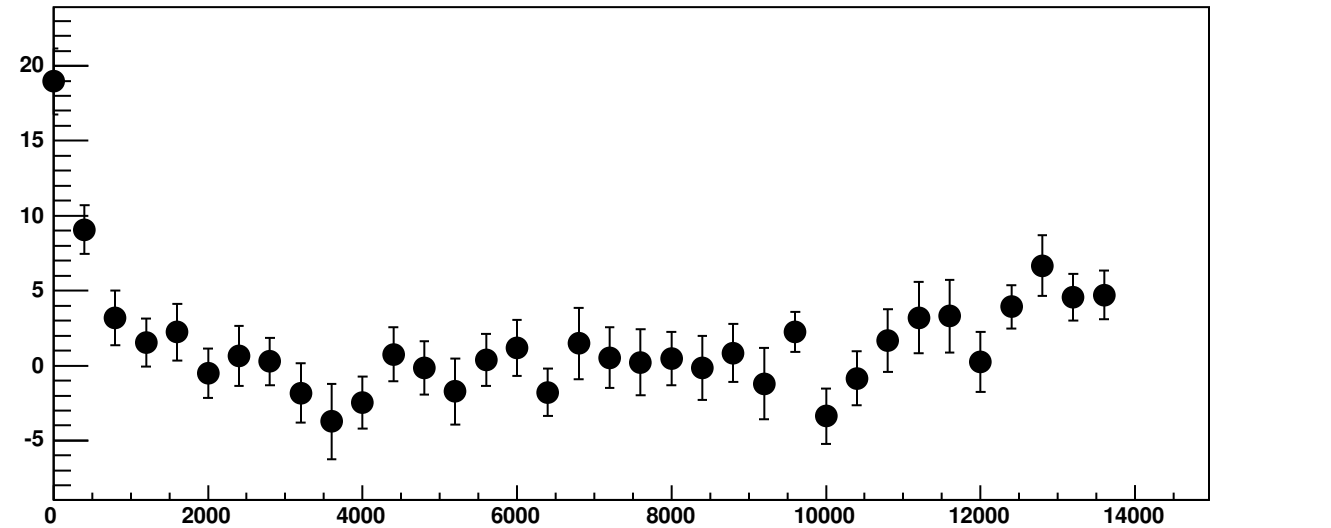
Chip 2, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC



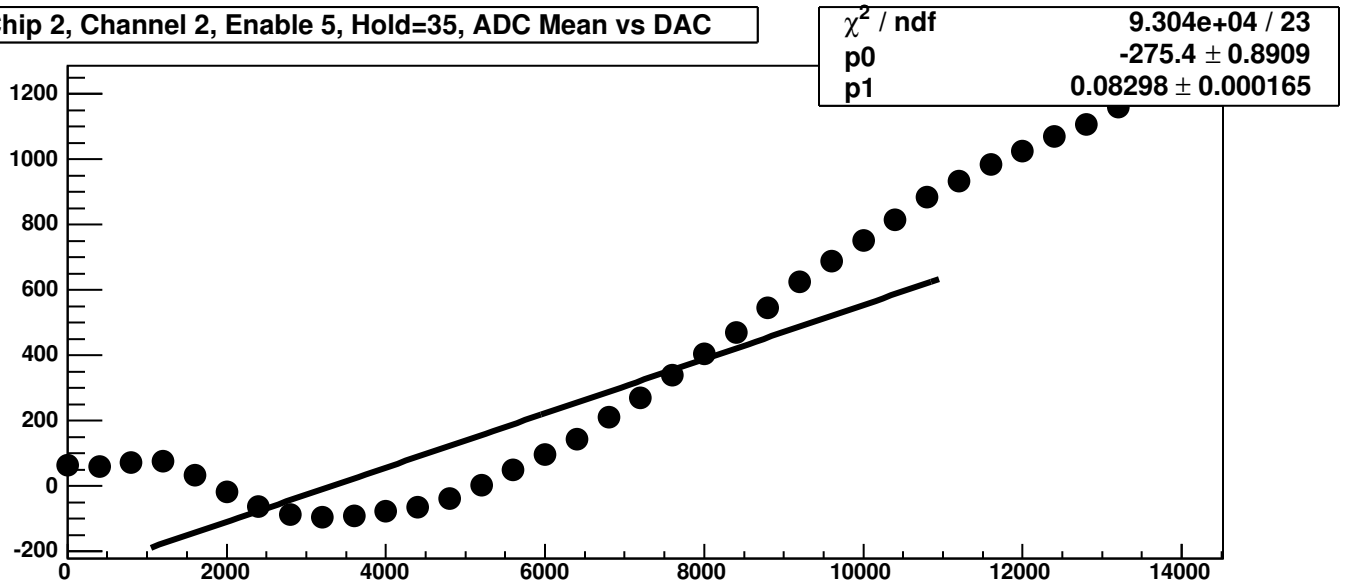
Chip 2, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



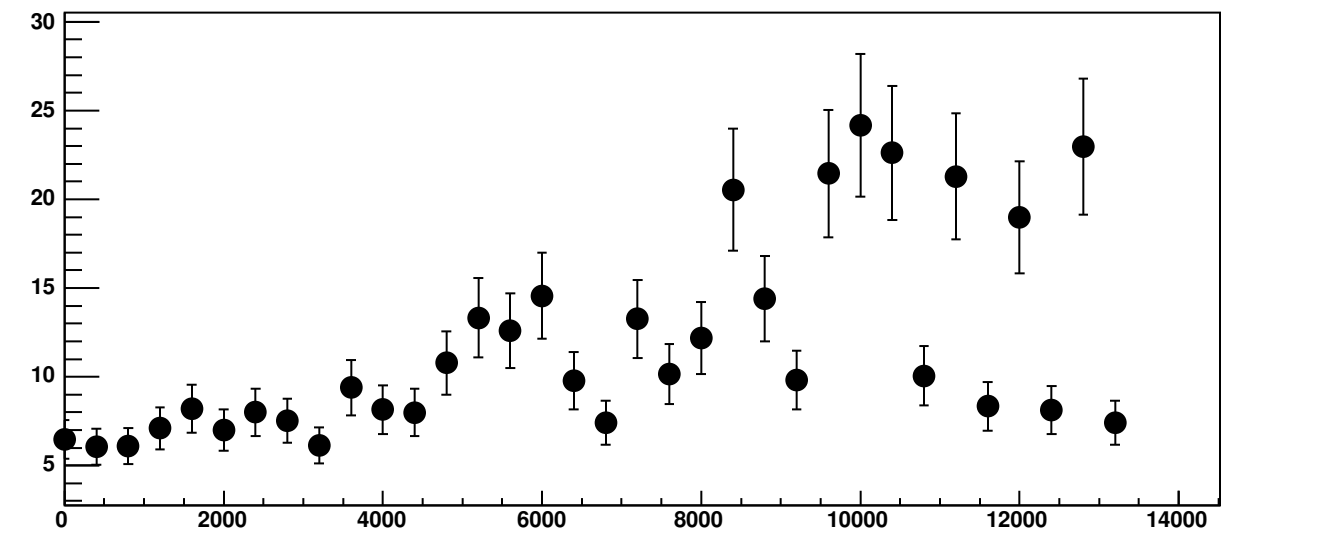
Chip 2, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



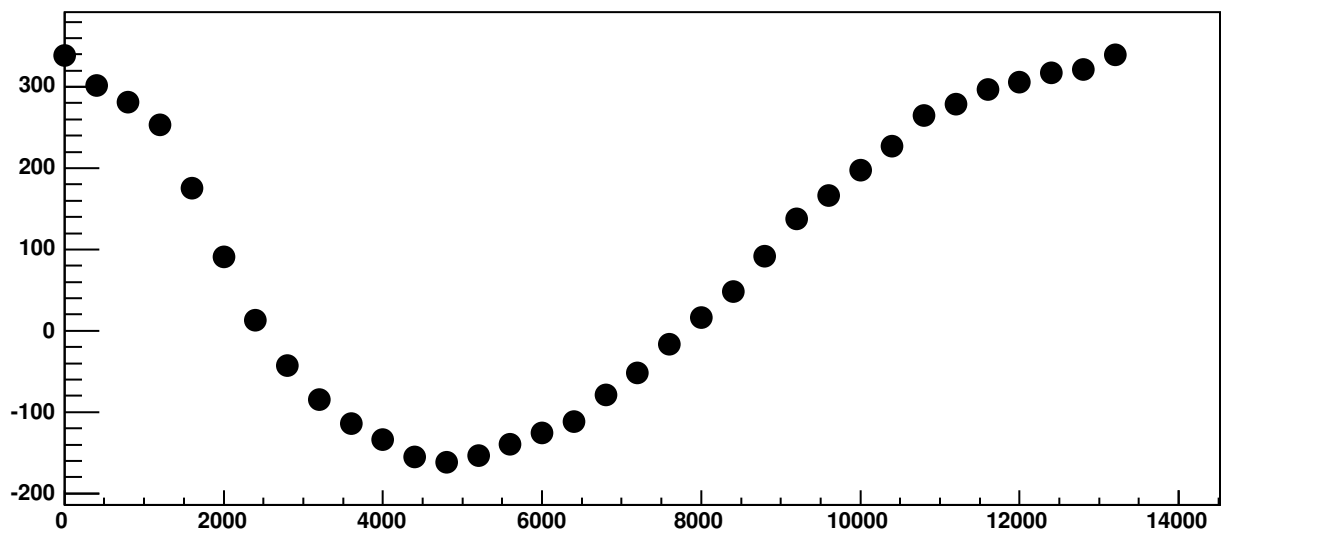
Chip 2, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

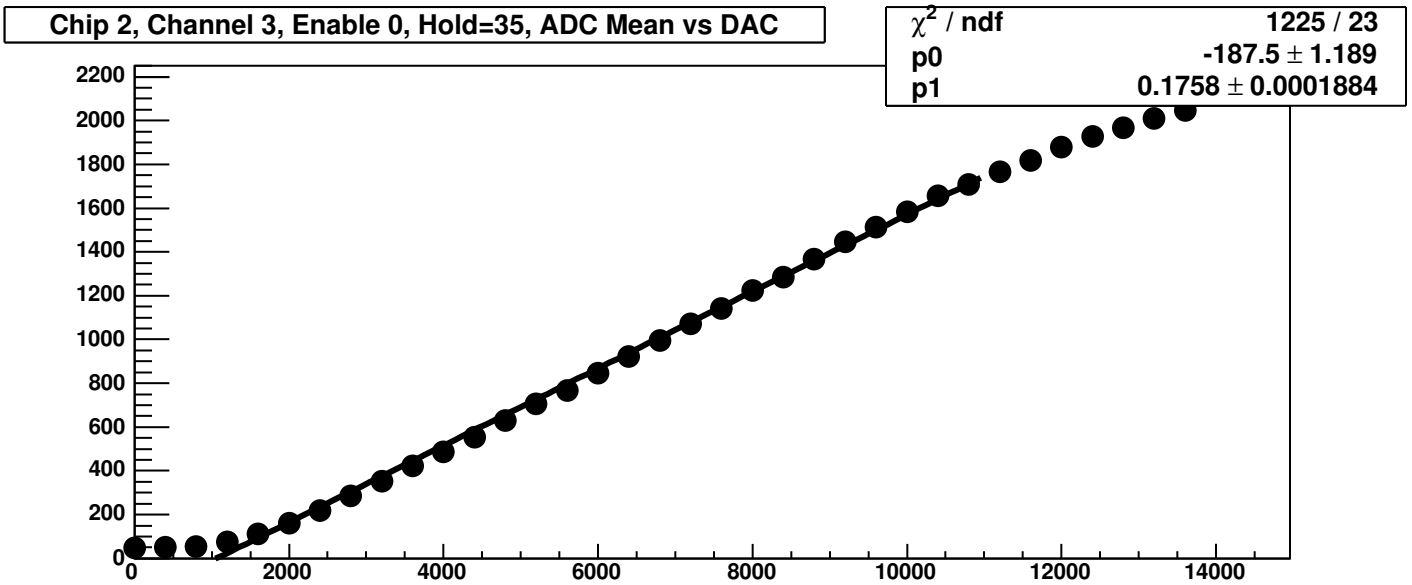


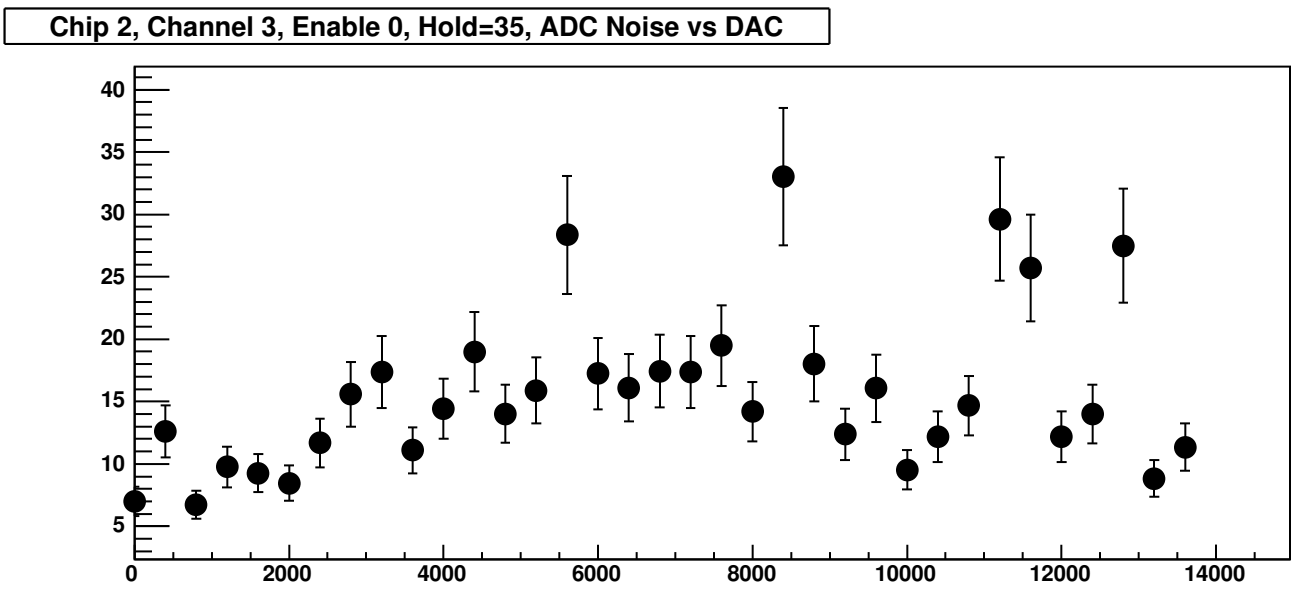
Chip 2, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC

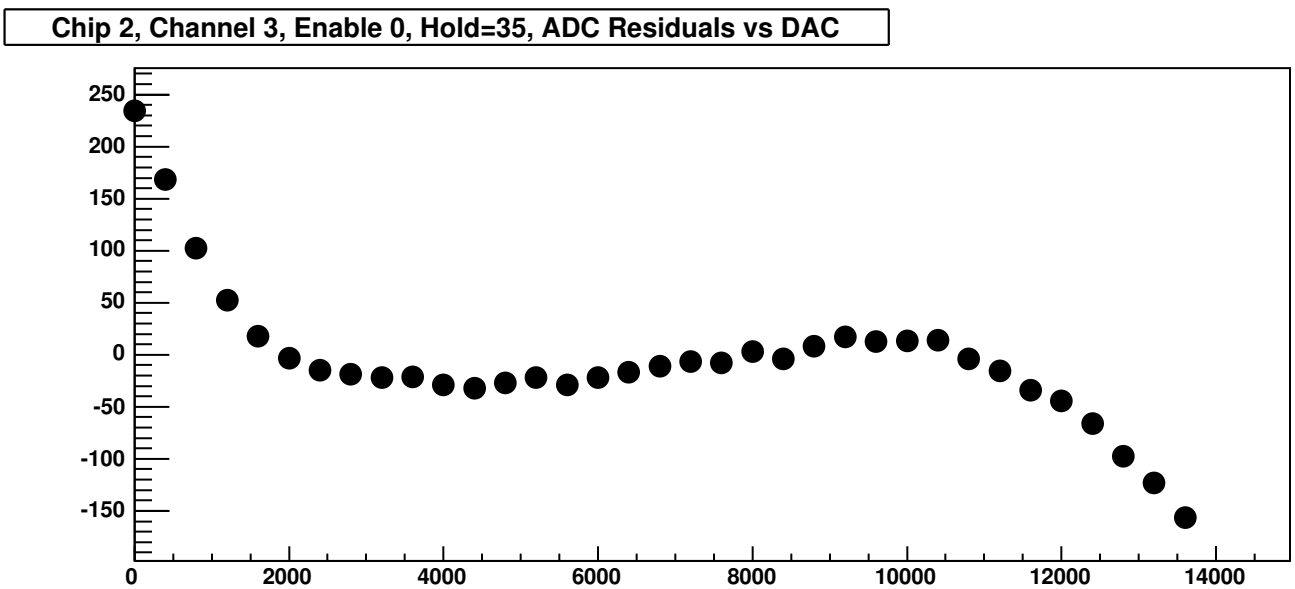


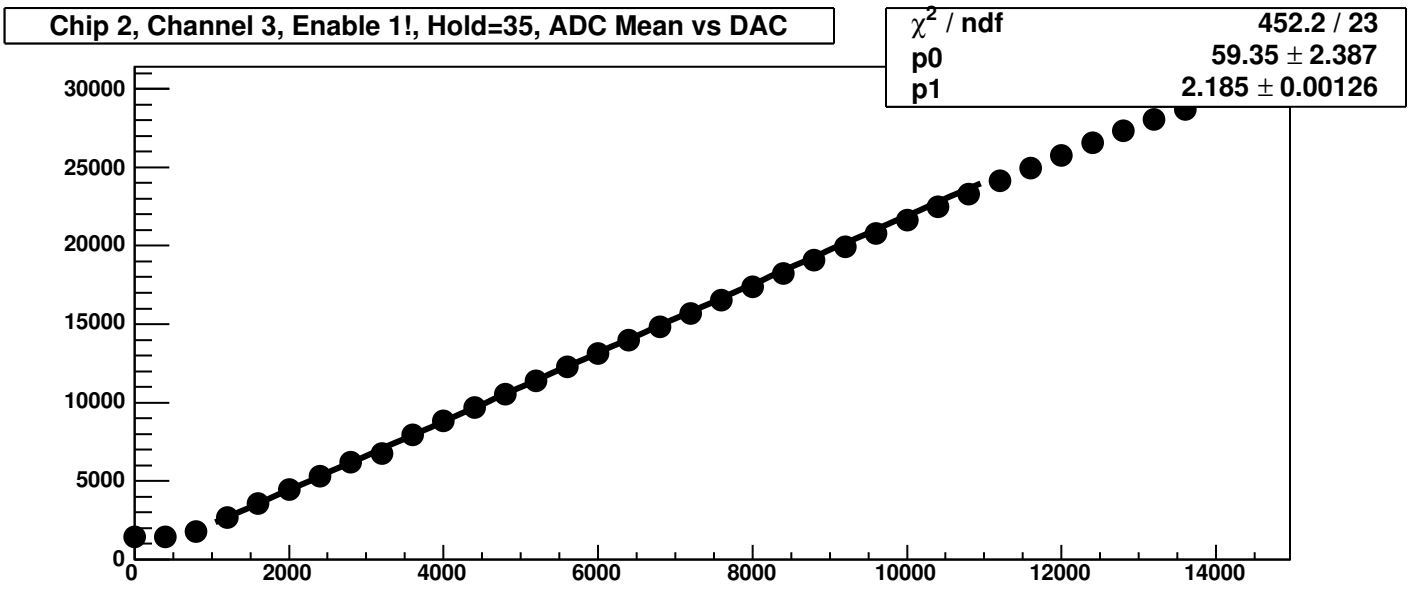
Chip 2, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

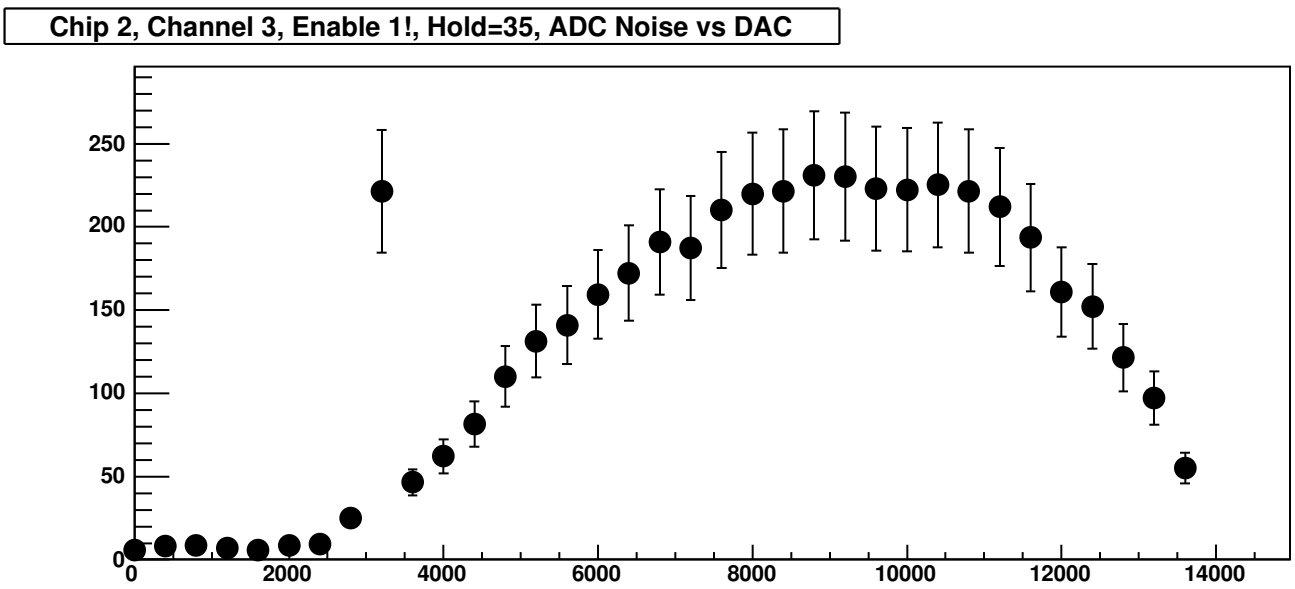


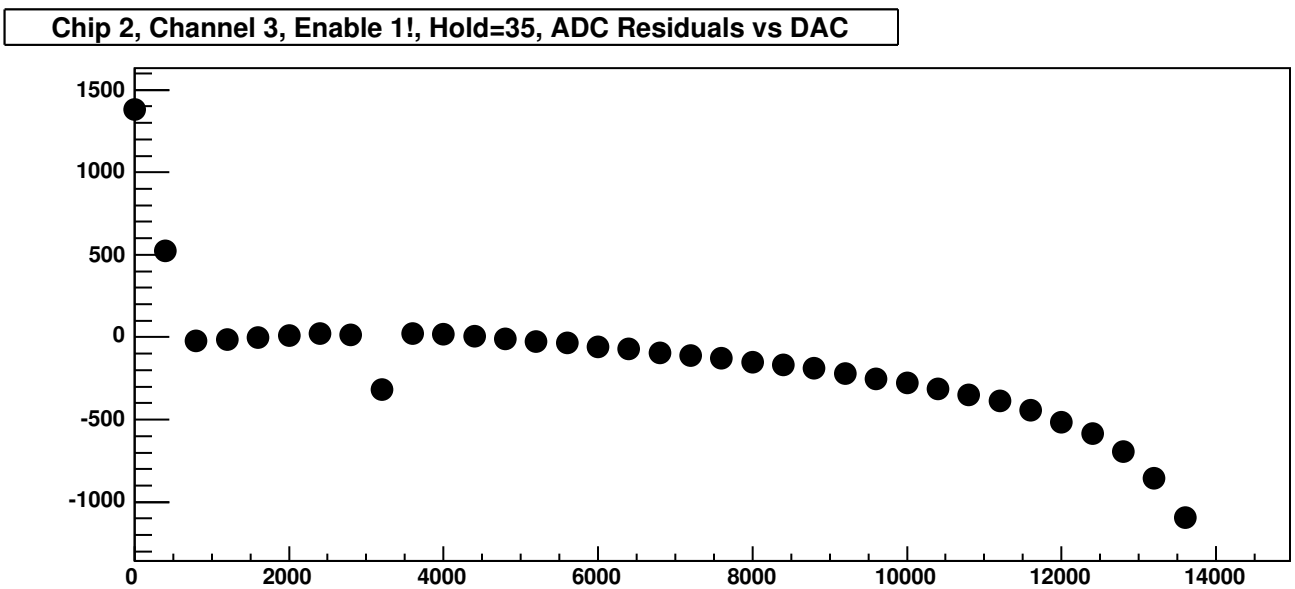

Chip 2, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1225 / 23
p0 -187.5 ± 1.189
p1 0.1758 ± 0.0001884
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

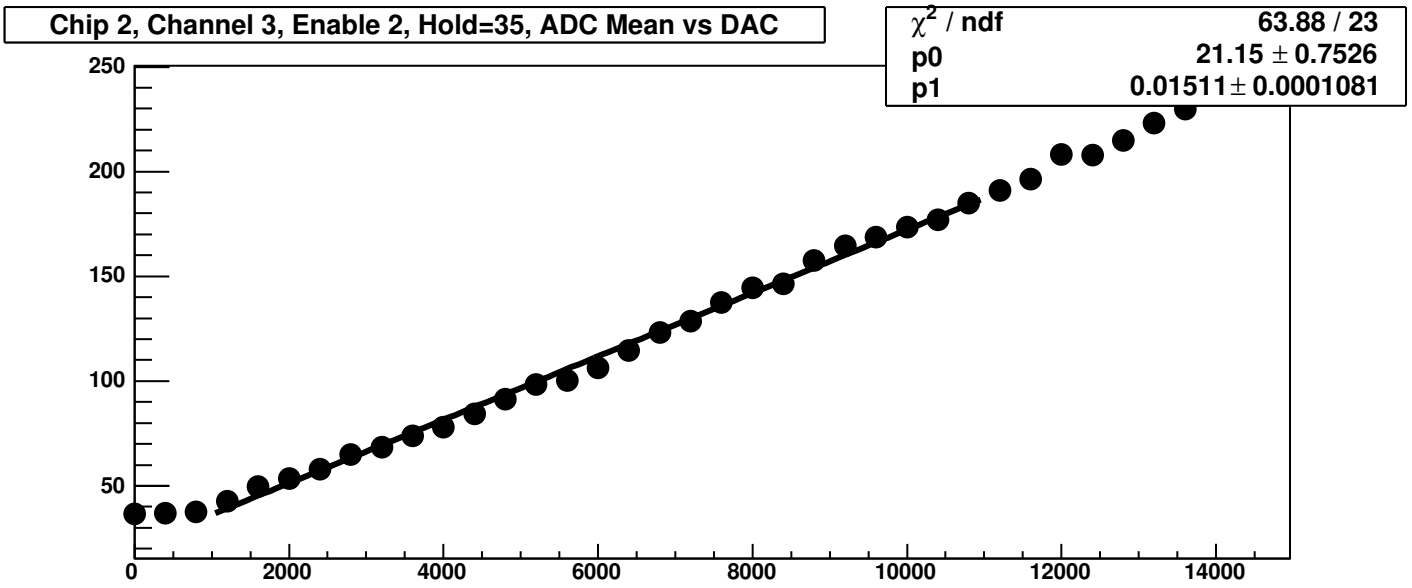

Chip 2, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

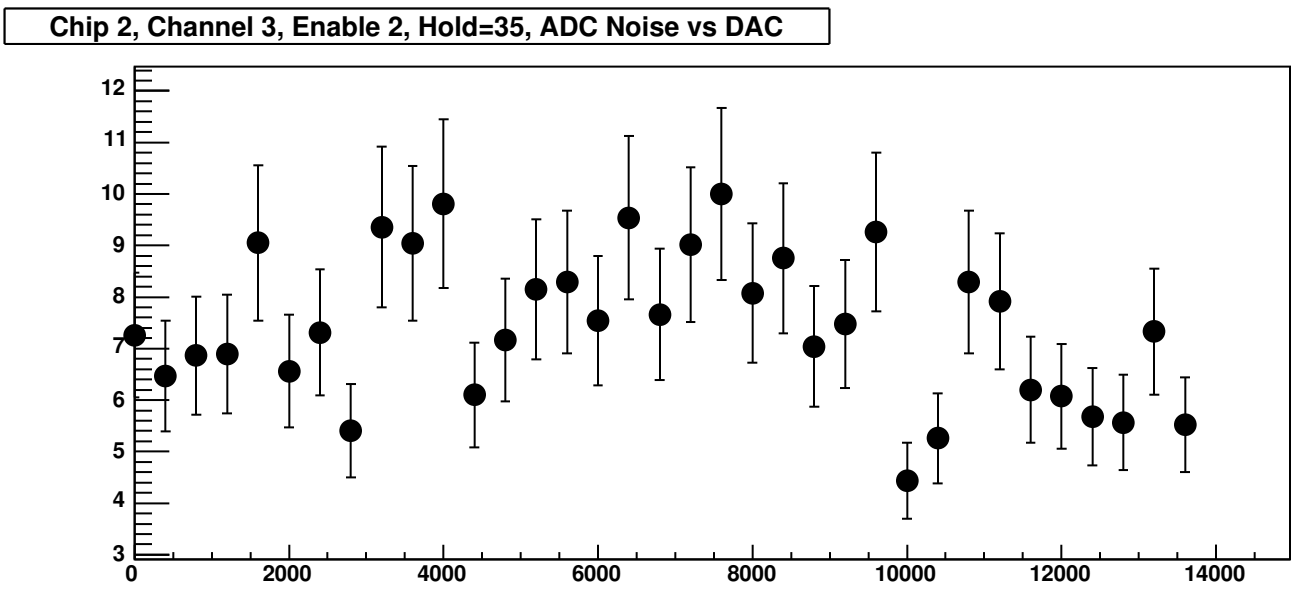

Chip 2, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

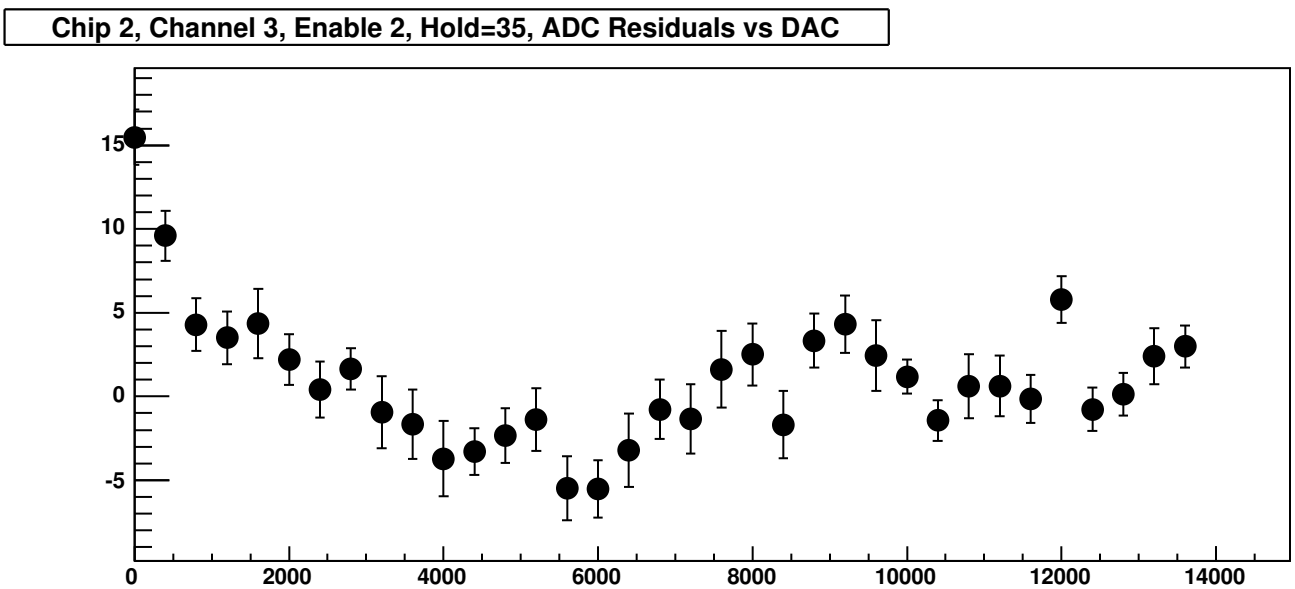

Chip 2, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 452.2 / 23
p0 59.35 ± 2.387
p1 2.185 ± 0.00126
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

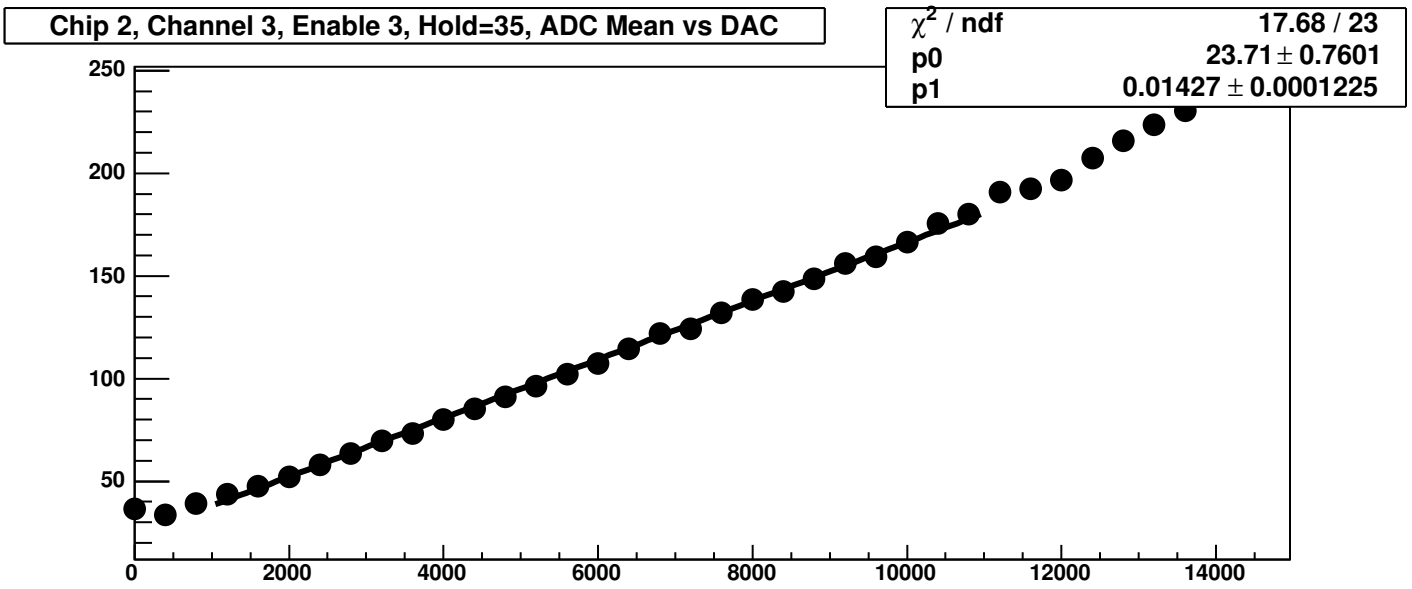

Chip 2, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

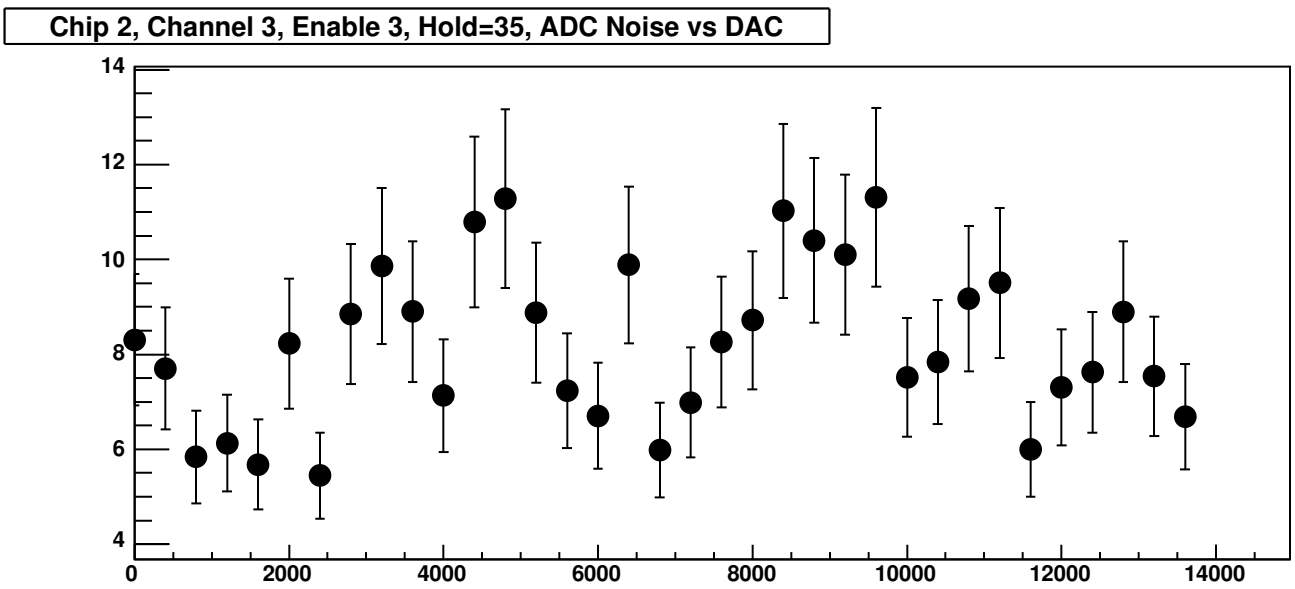

Chip 2, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

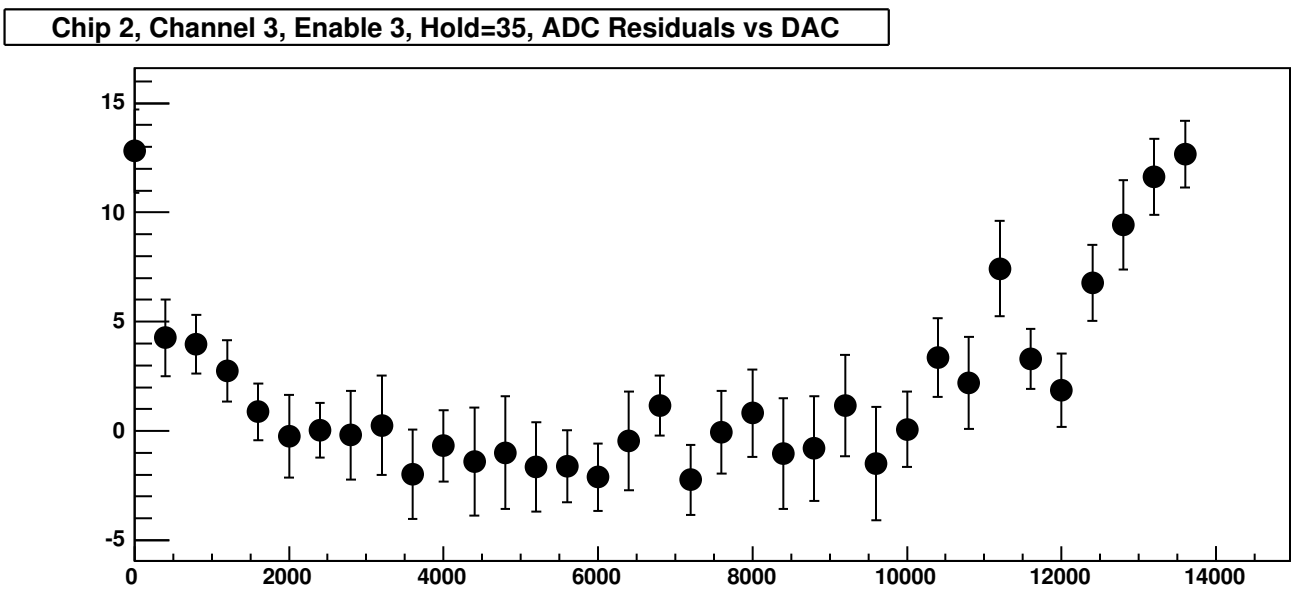

Chip 2, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 63.88 / 23
p0 21.15 ± 0.7526
p1 0.01511 ± 0.0001081
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

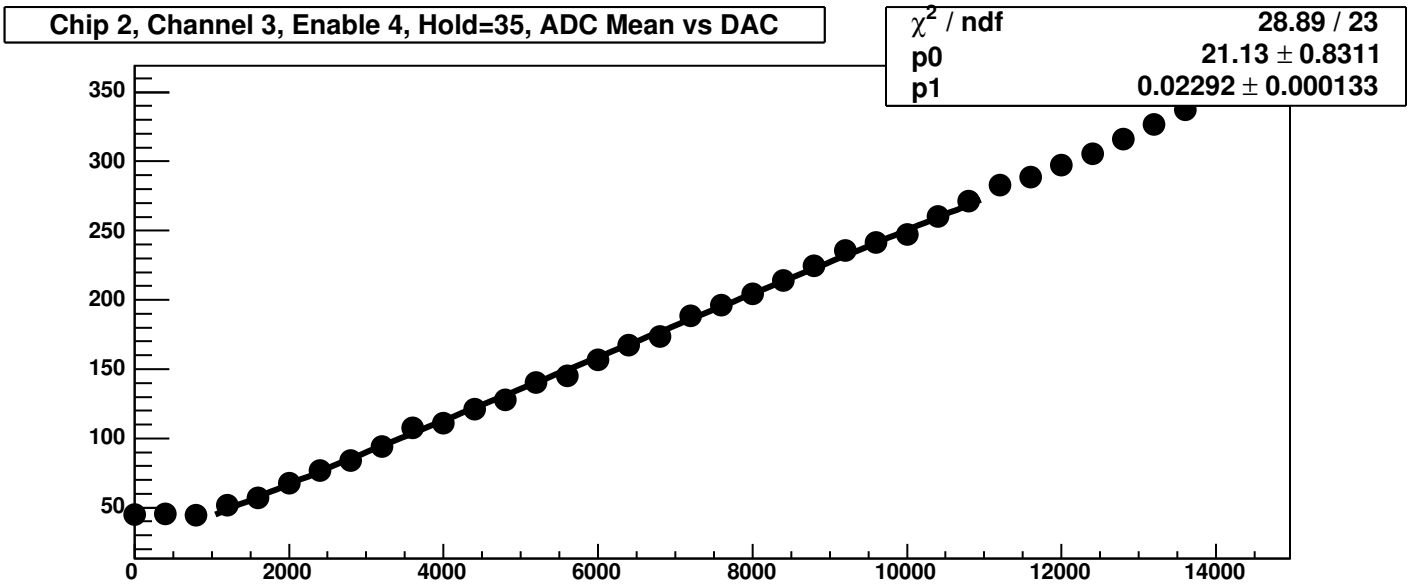

Chip 2, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

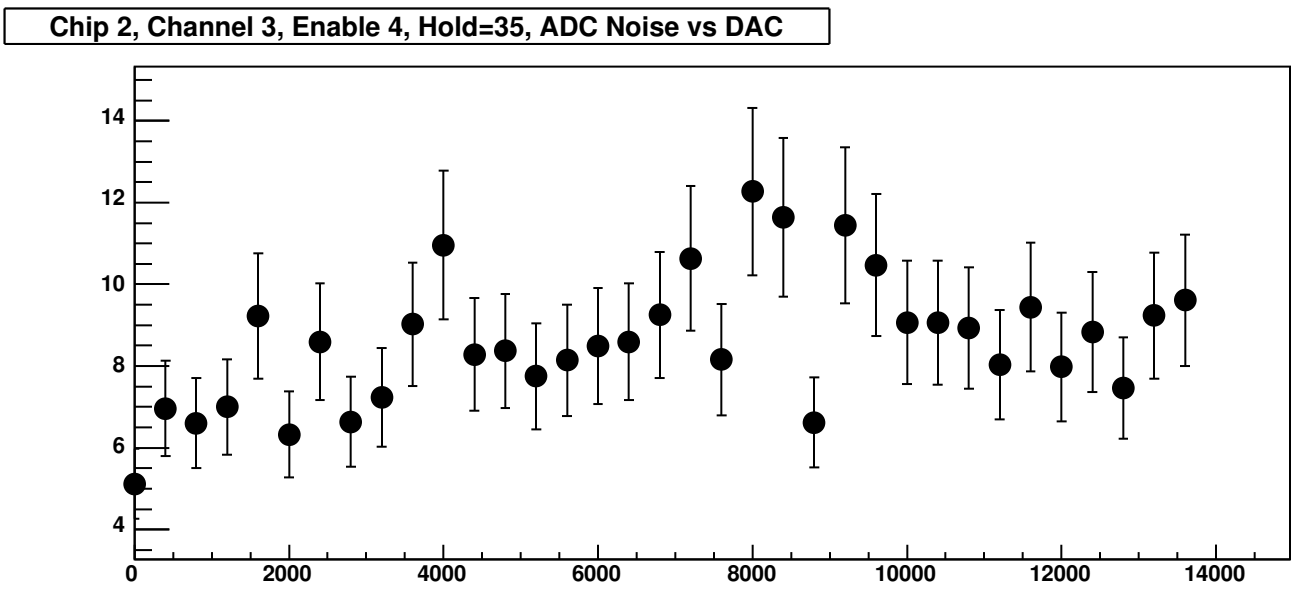

Chip 2, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

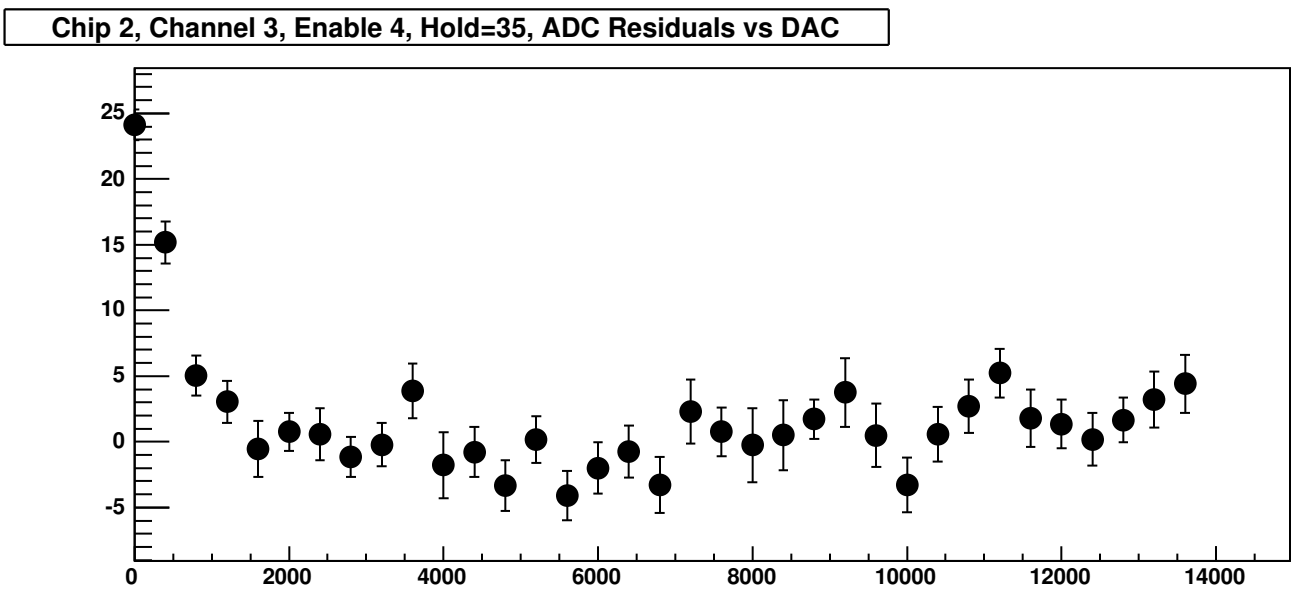

Chip 2, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 17.68 / 23
p0 23.71 ± 0.7601
p1 0.01427 ± 0.0001225
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

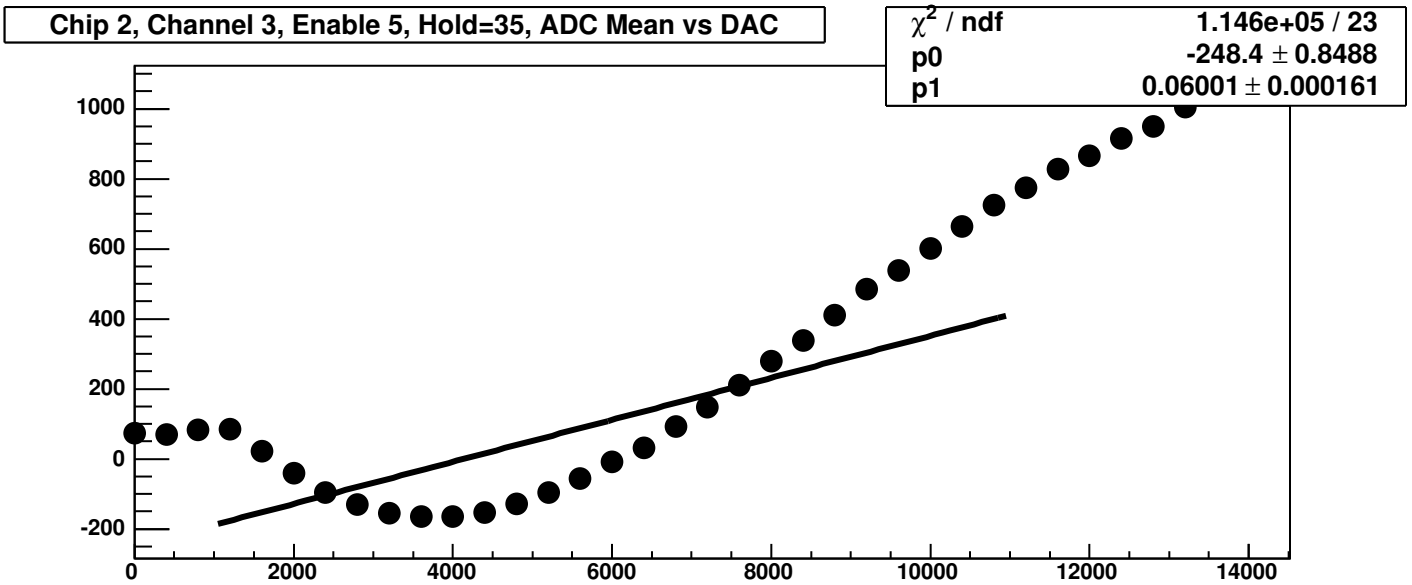

Chip 2, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

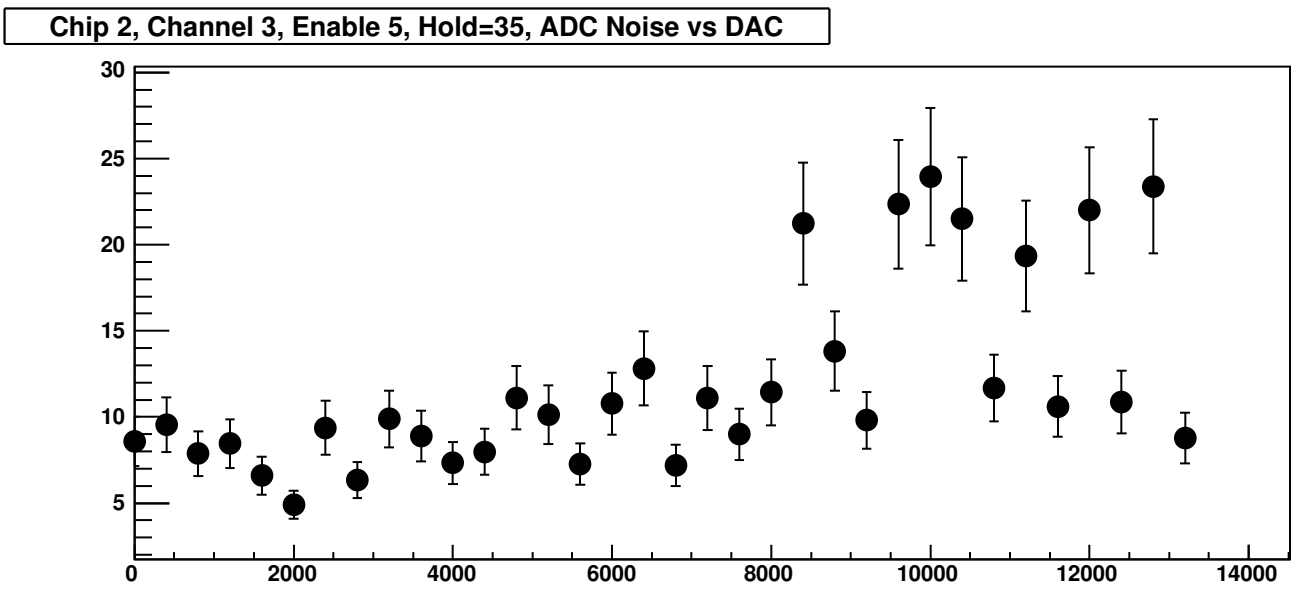

Chip 2, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

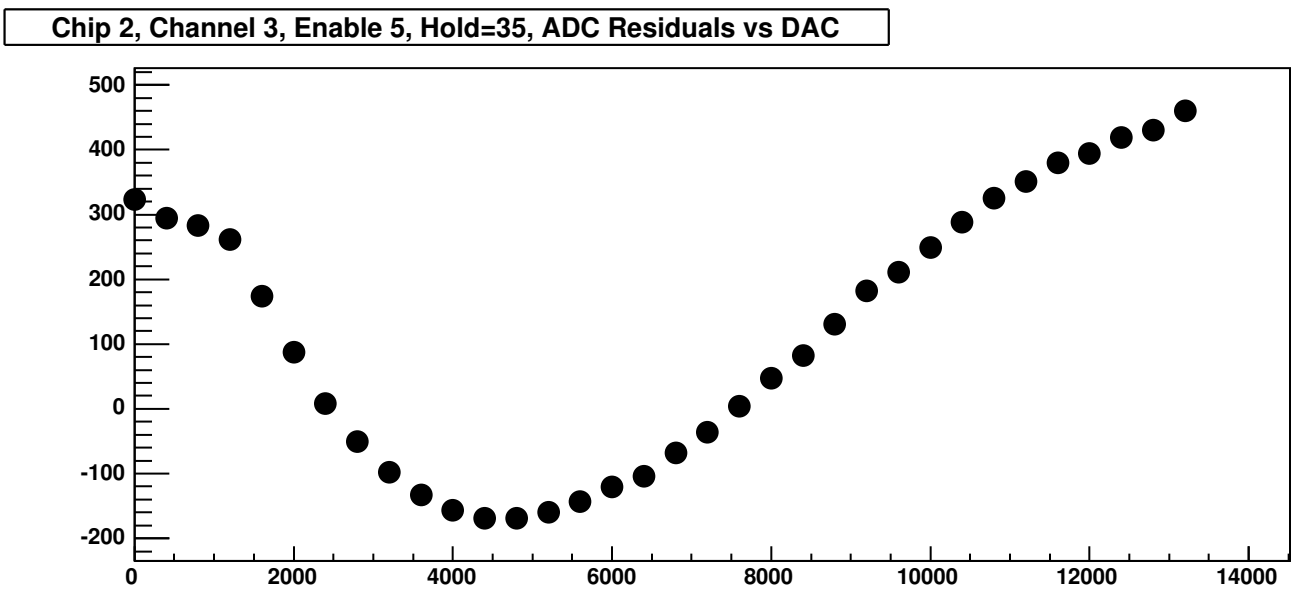

Chip 2, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 28.89 / 23
p0 21.13 ± 0.8311
p1 0.02292 ± 0.000133
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

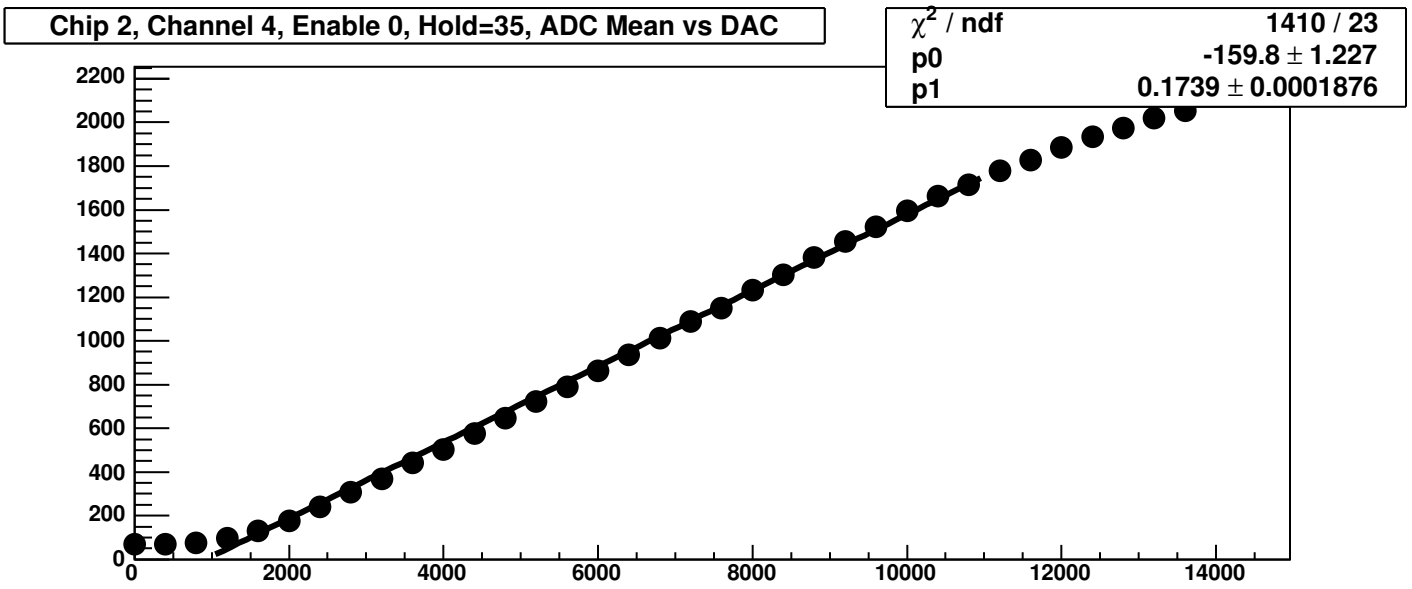

Chip 2, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

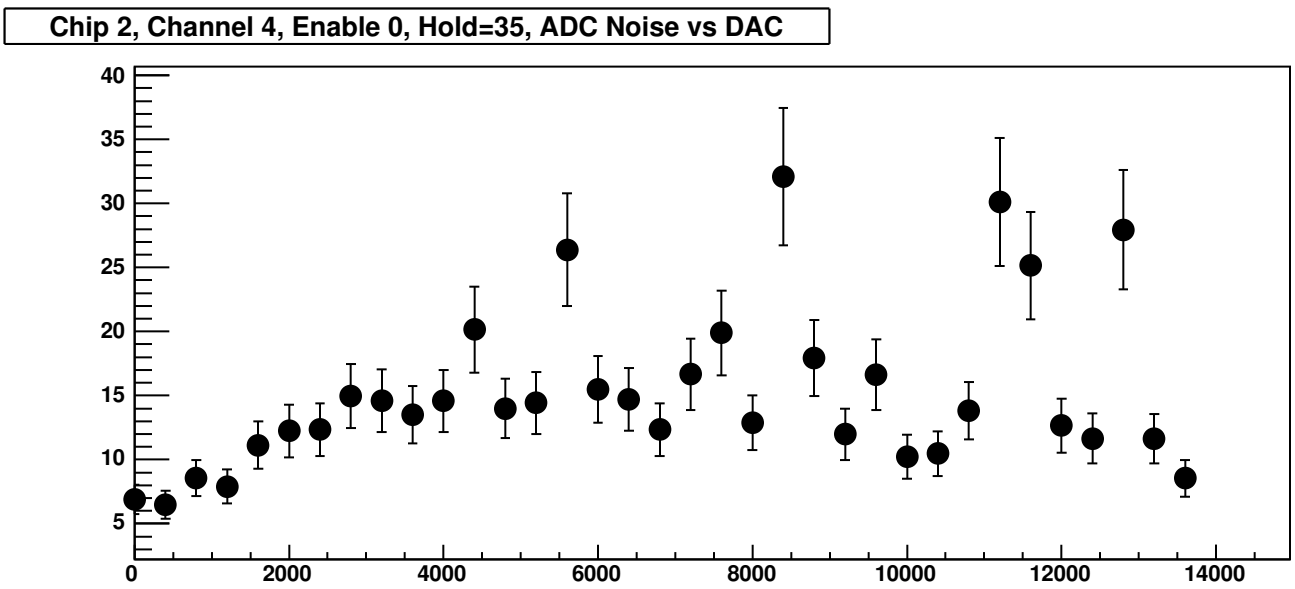

Chip 2, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

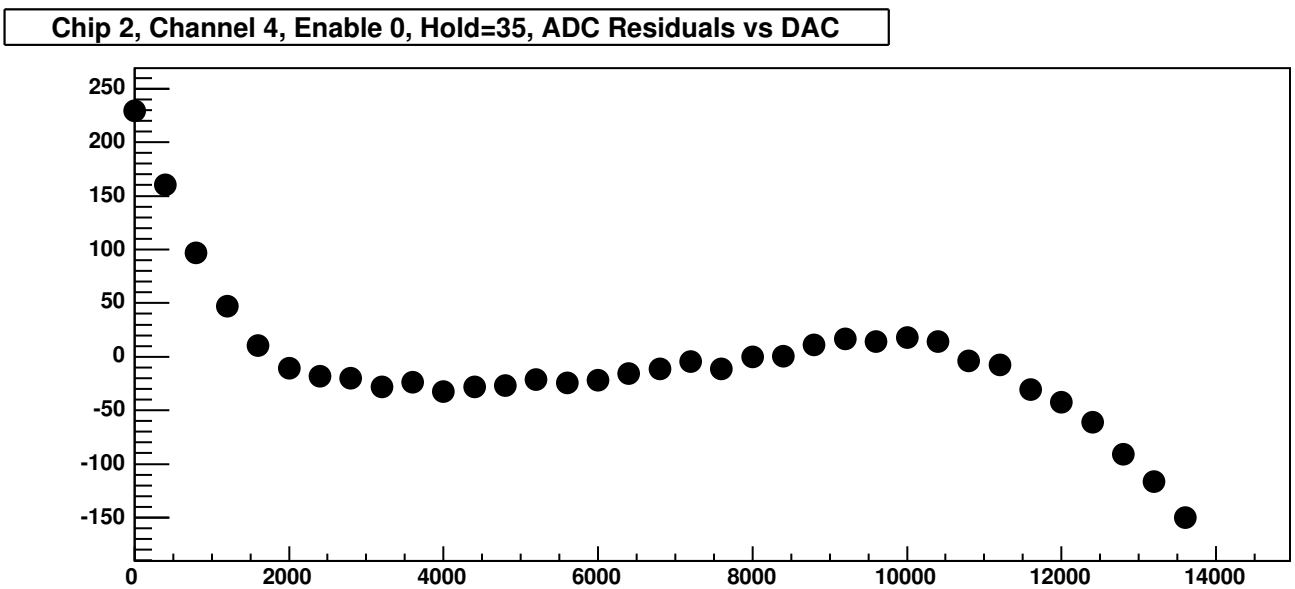

Chip 2, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.146e+05 / 23
p0 -248.4 ± 0.8488
p1 0.06001 ± 0.000161
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

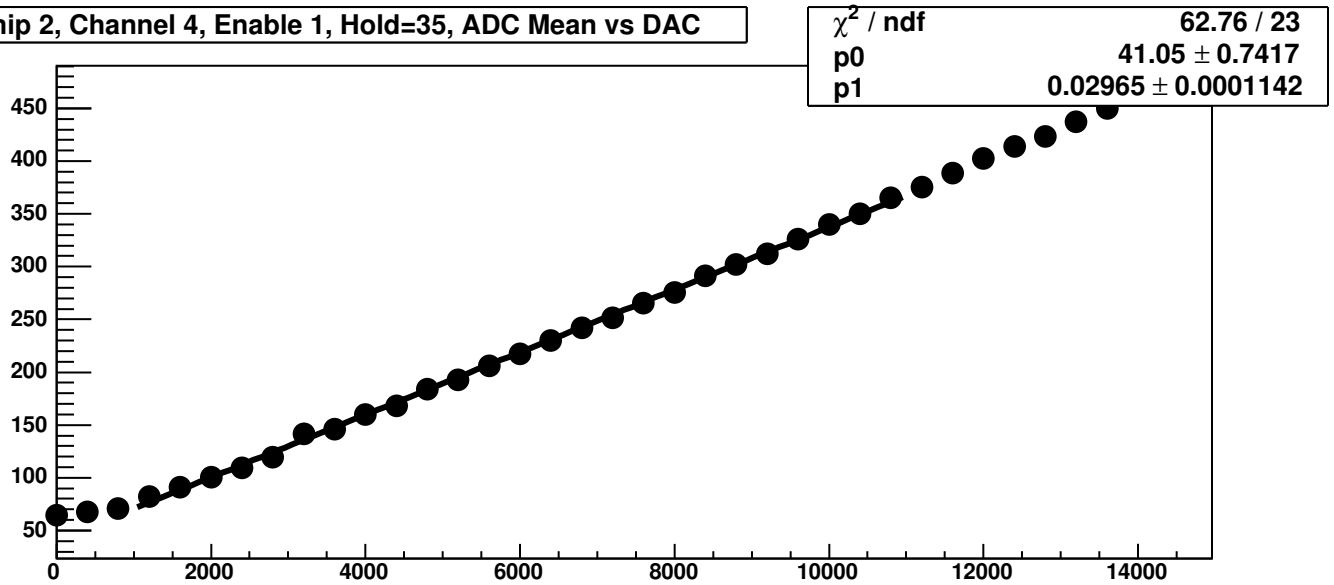

Chip 2, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1410 / 23
p0 -159.8 ± 1.227
p1 0.1739 ± 0.0001876
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

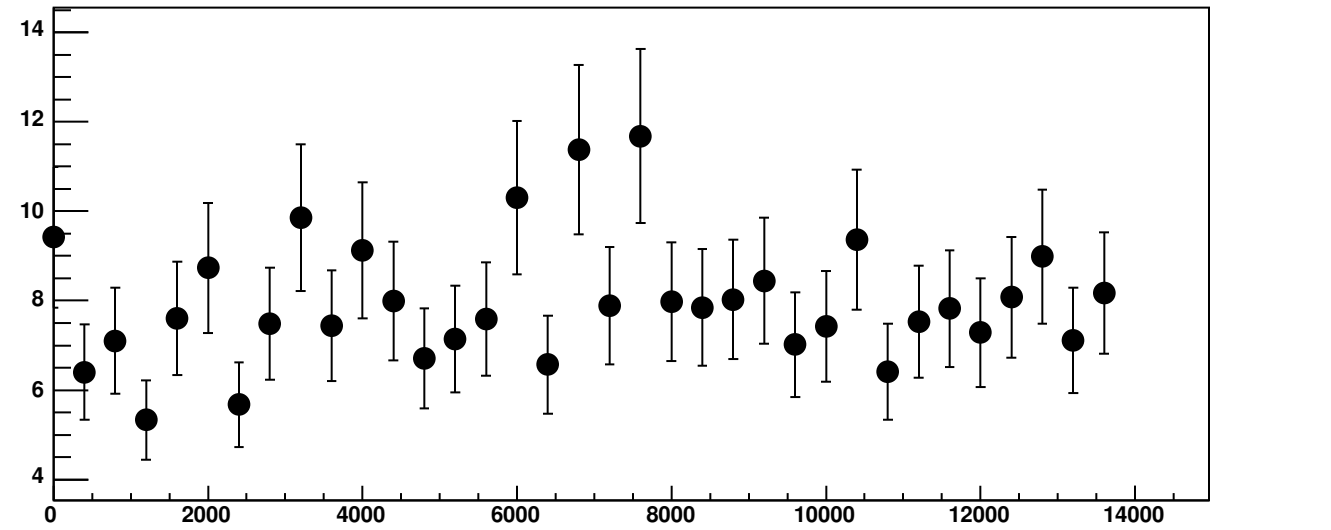

Chip 2, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

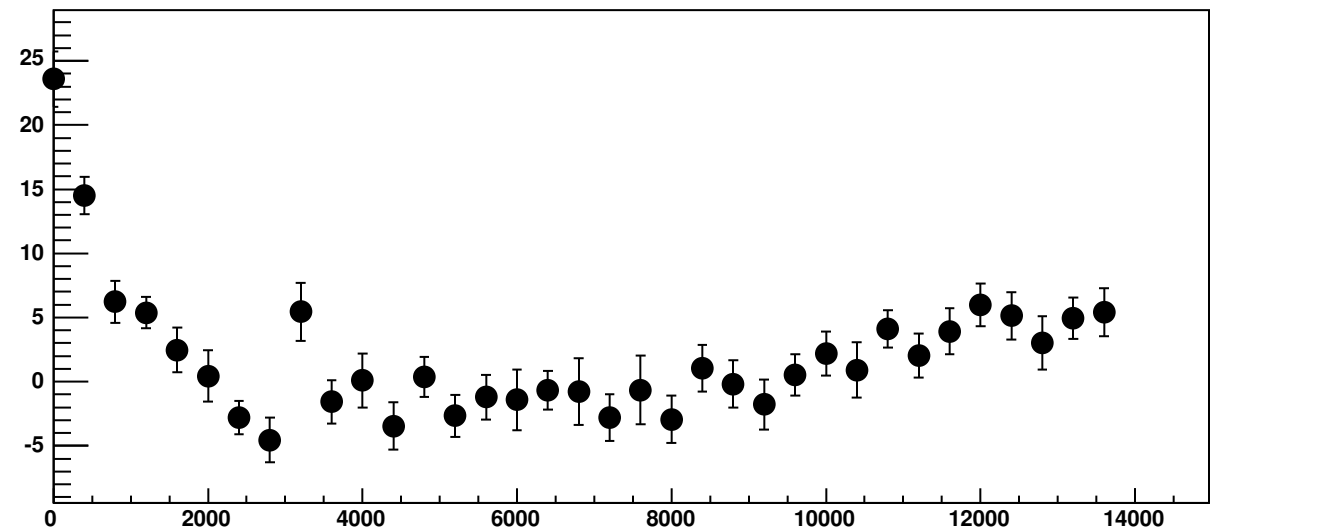
Chip 2, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC



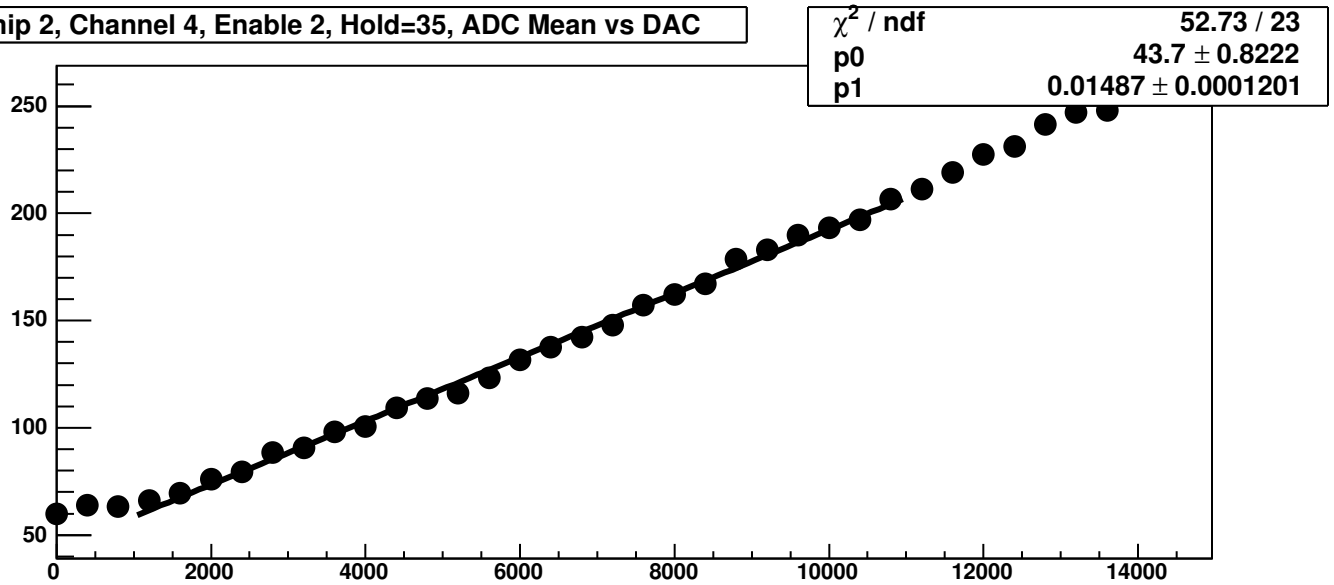
Chip 2, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC



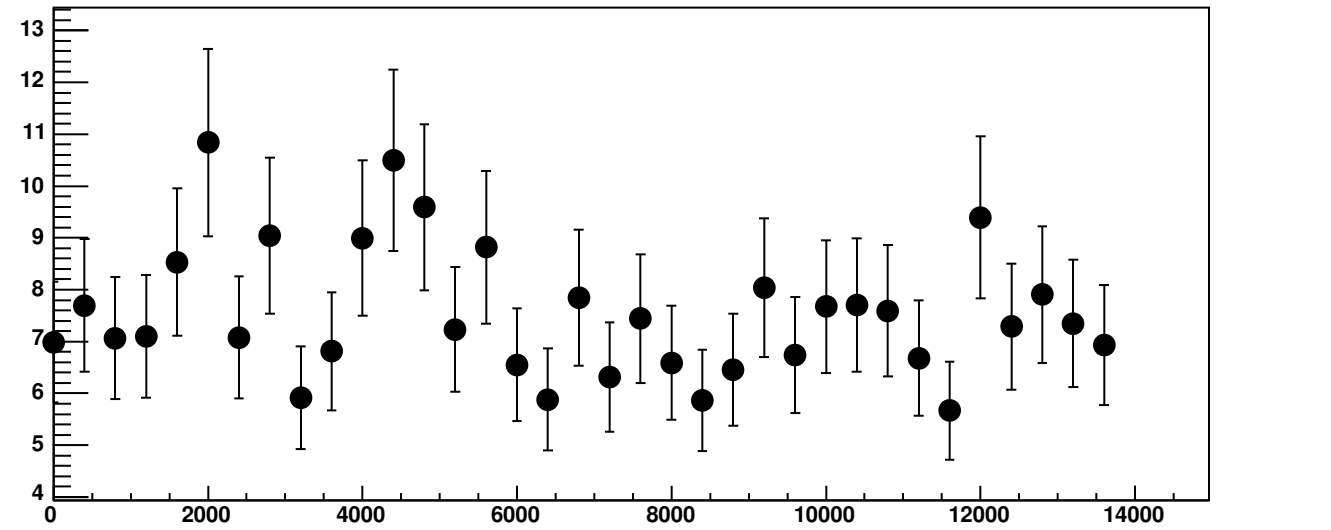
Chip 2, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC



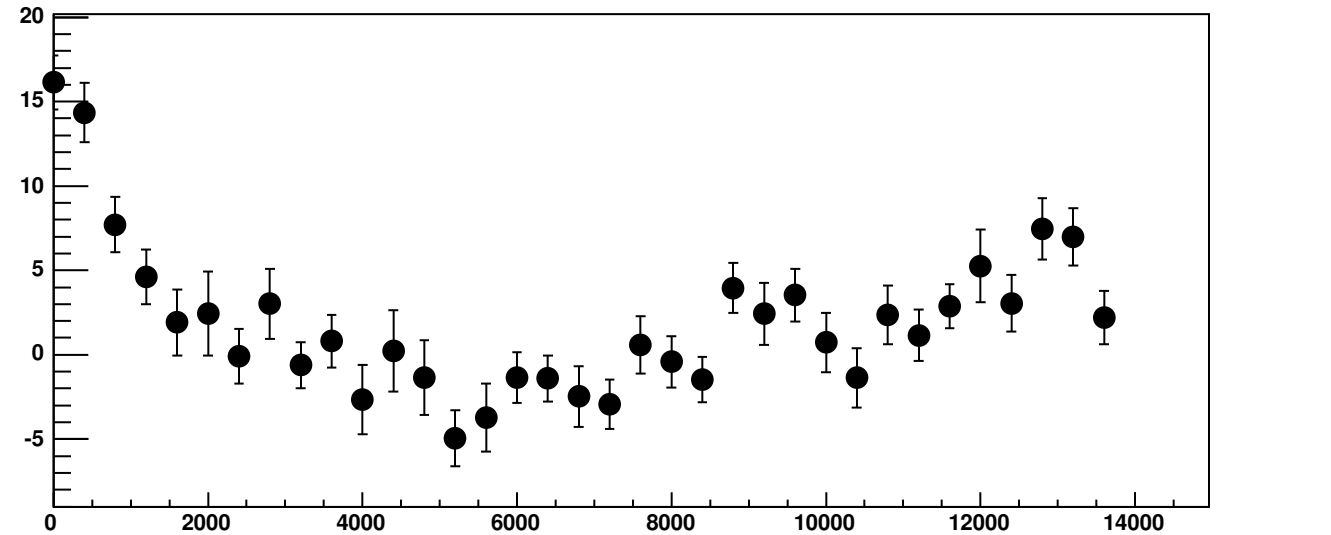
Chip 2, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



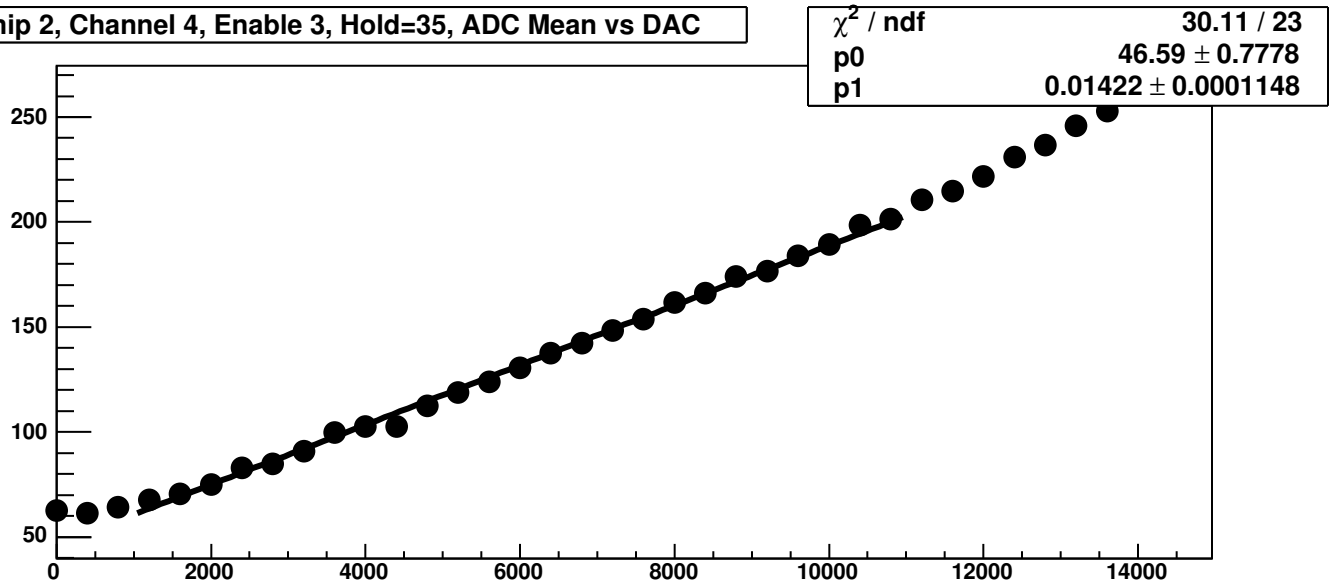
Chip 2, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



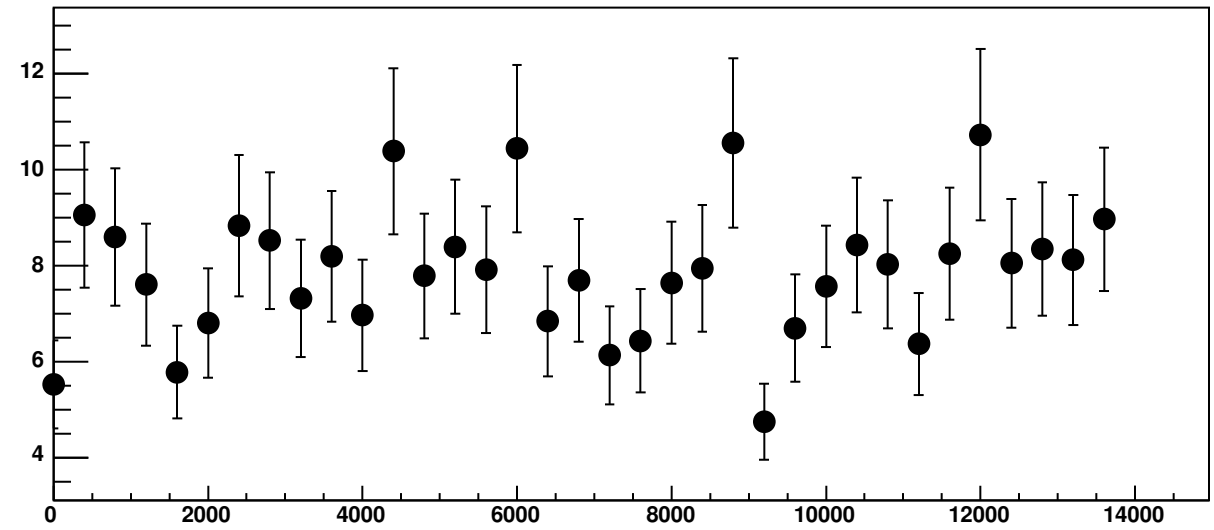
Chip 2, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



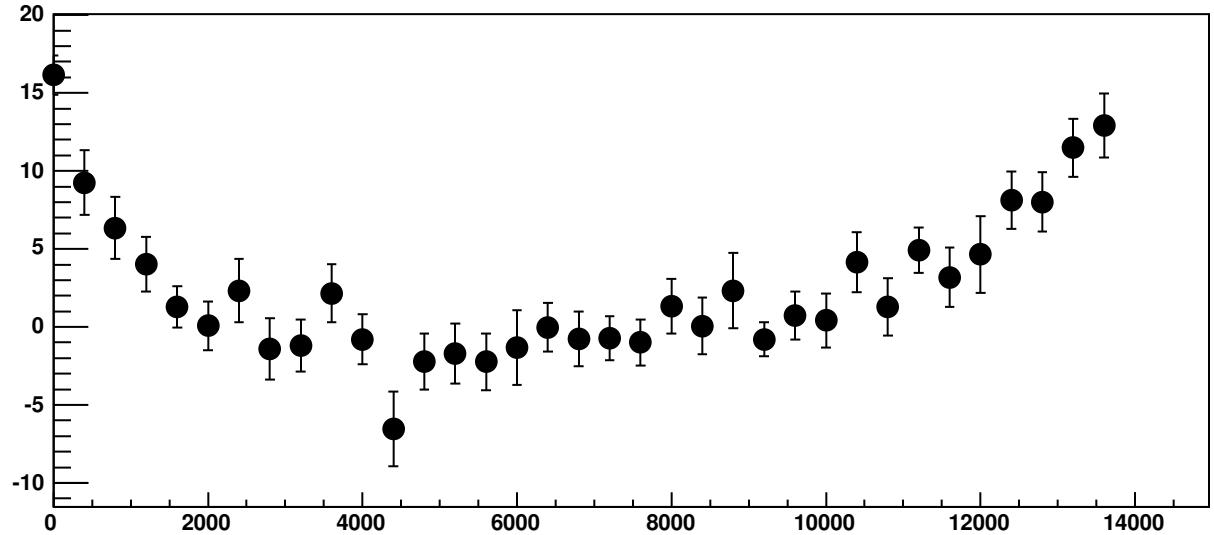
Chip 2, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

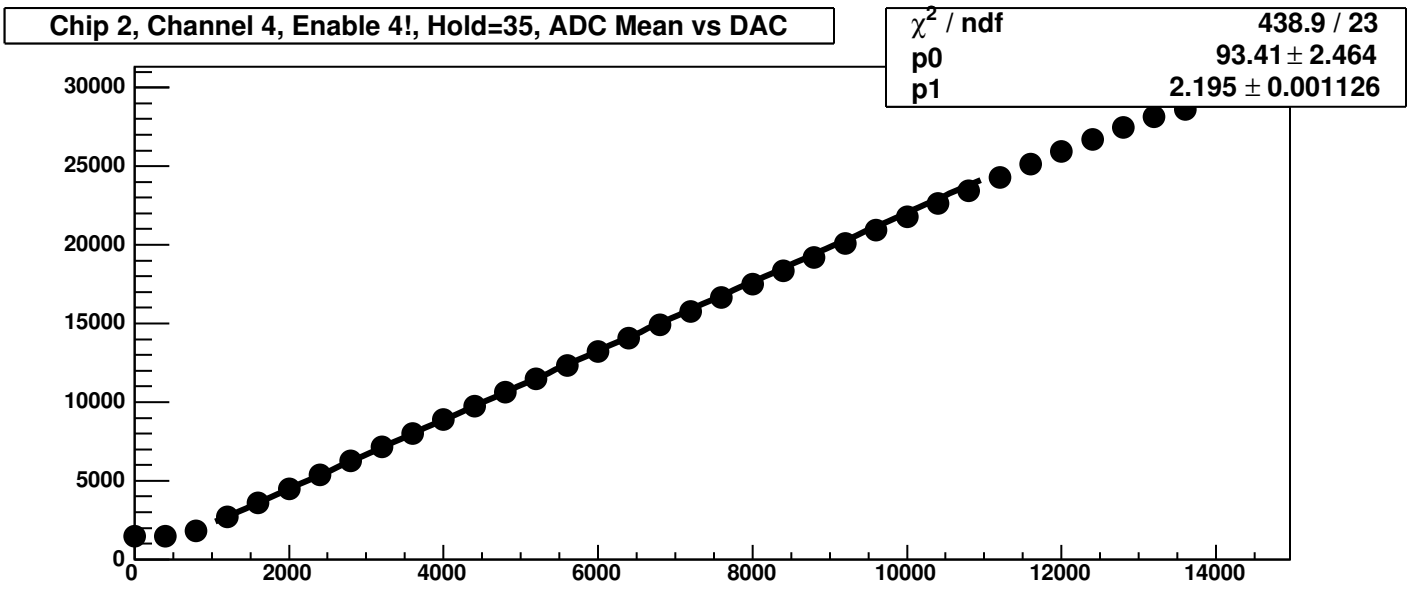


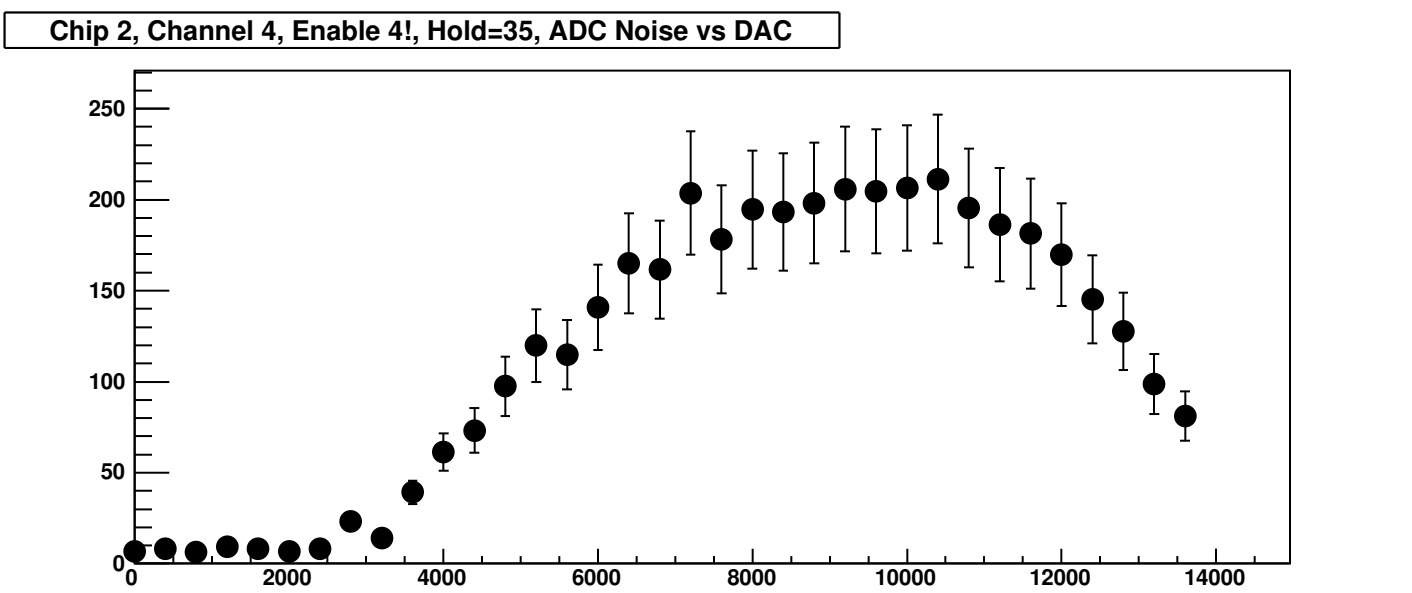
Chip 2, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC

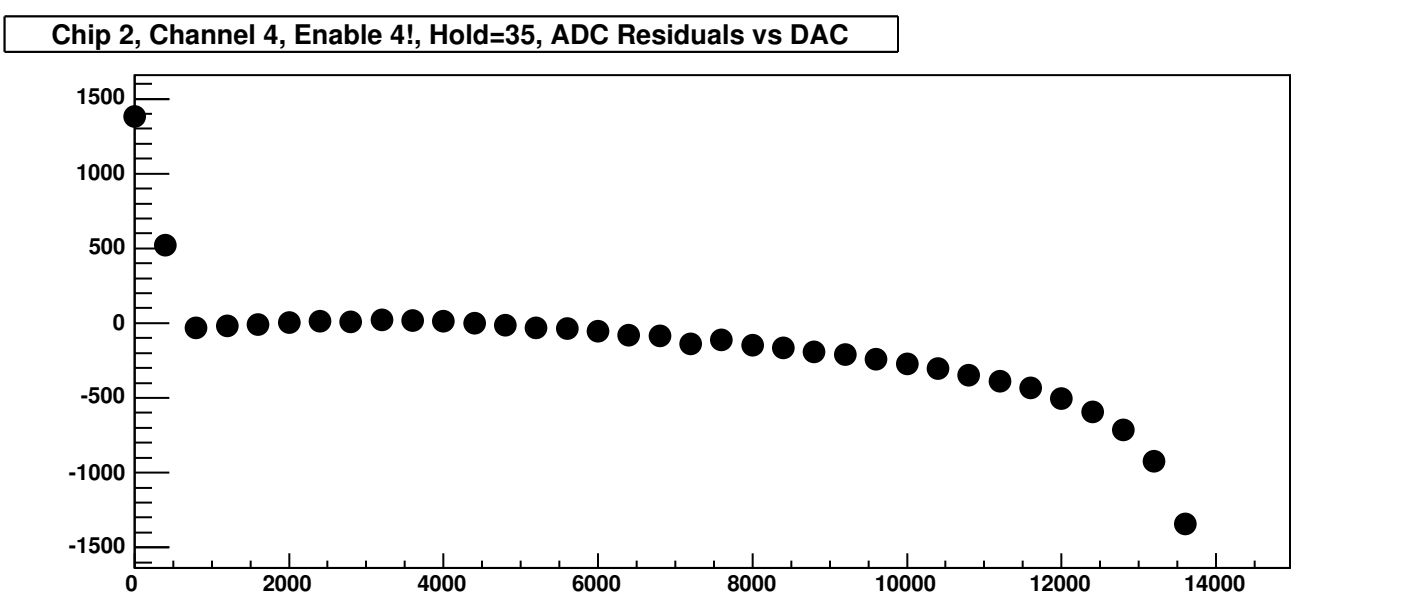


Chip 2, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

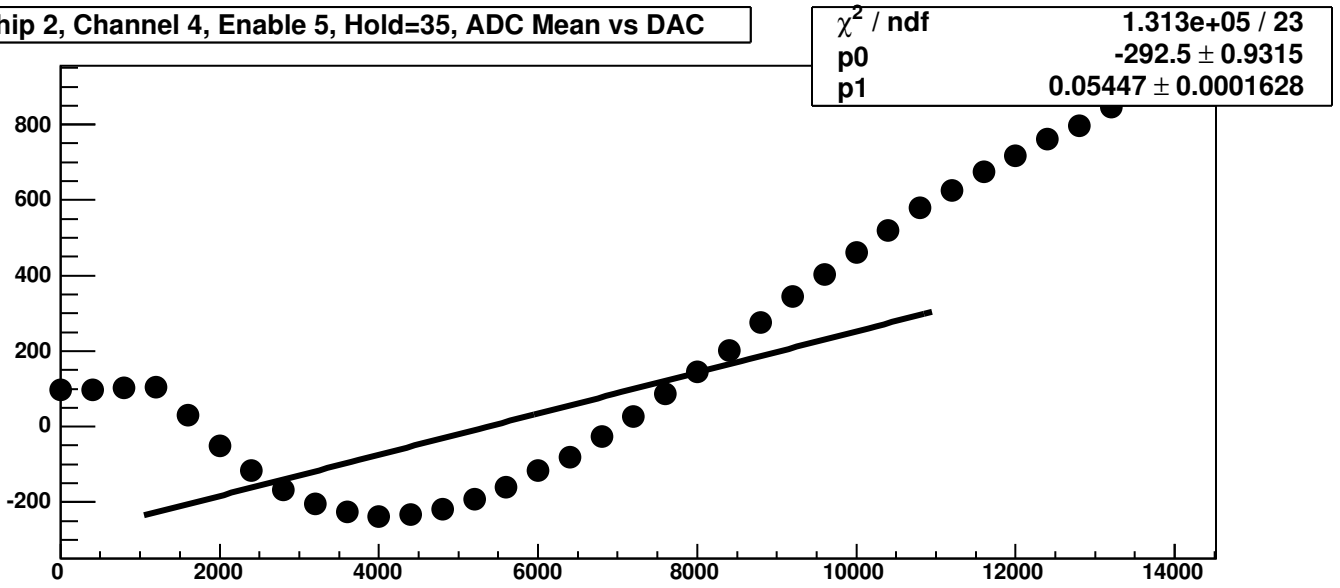



Chip 2, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 438.9 / 23
p0 93.41 ± 2.464
p1 2.195 ± 0.001126
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

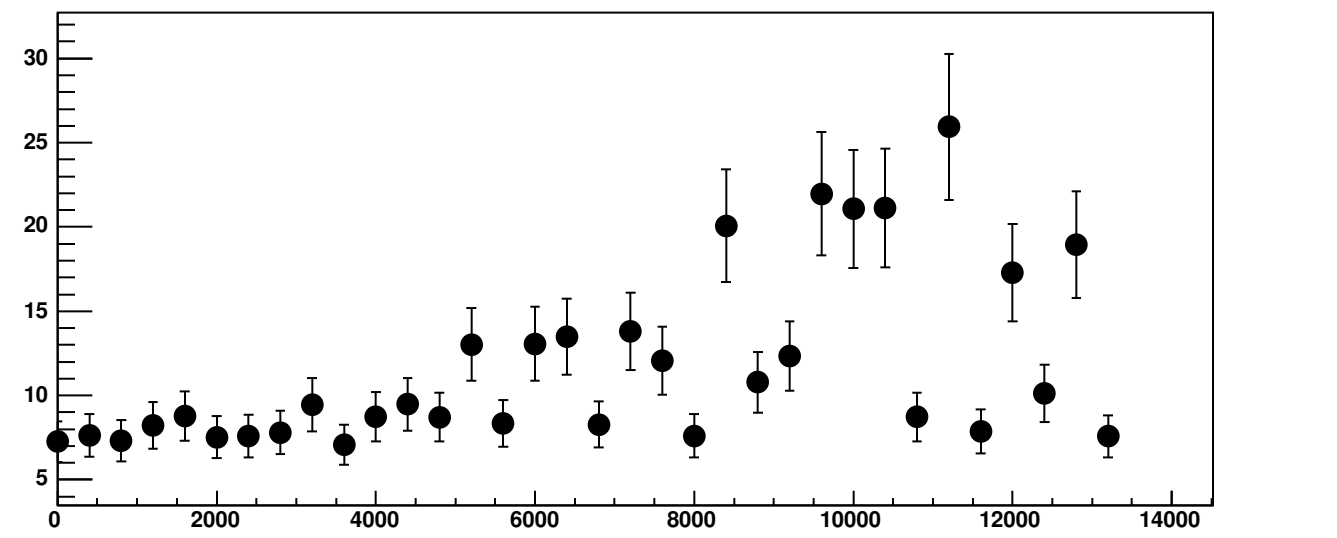

Chip 2, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

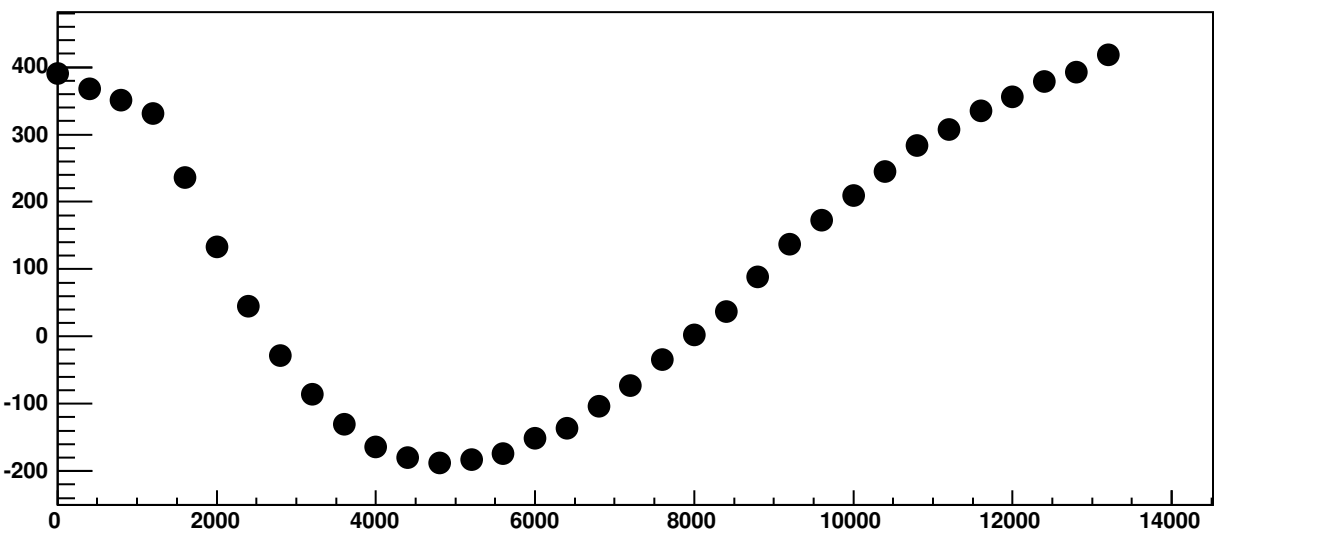
Chip 2, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



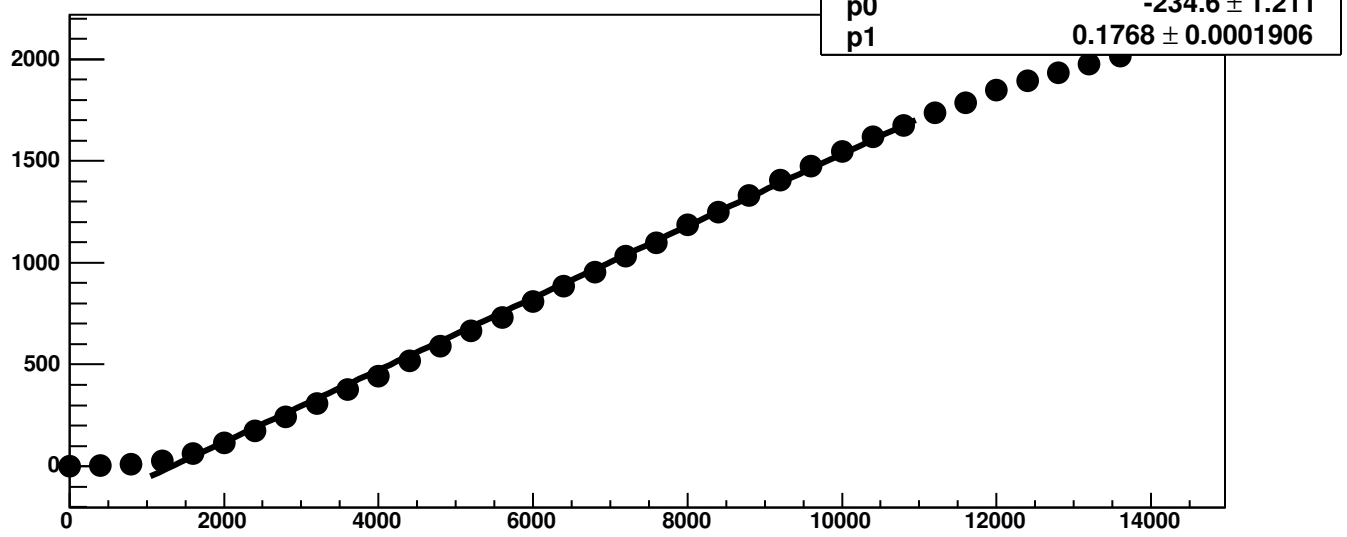
Chip 2, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



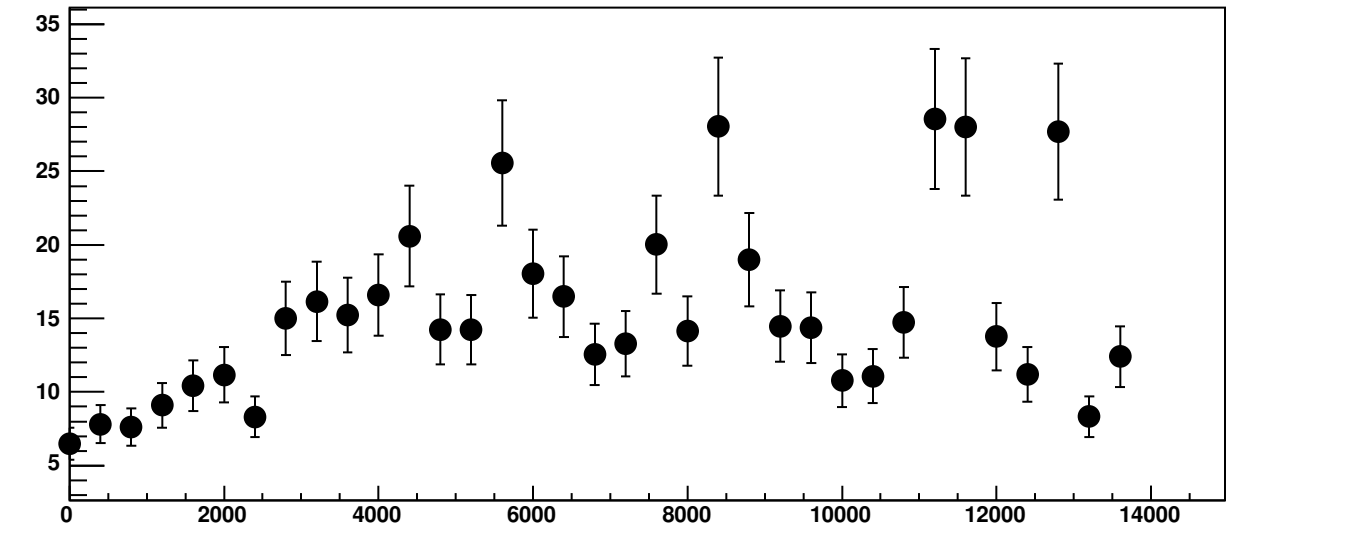
Chip 2, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



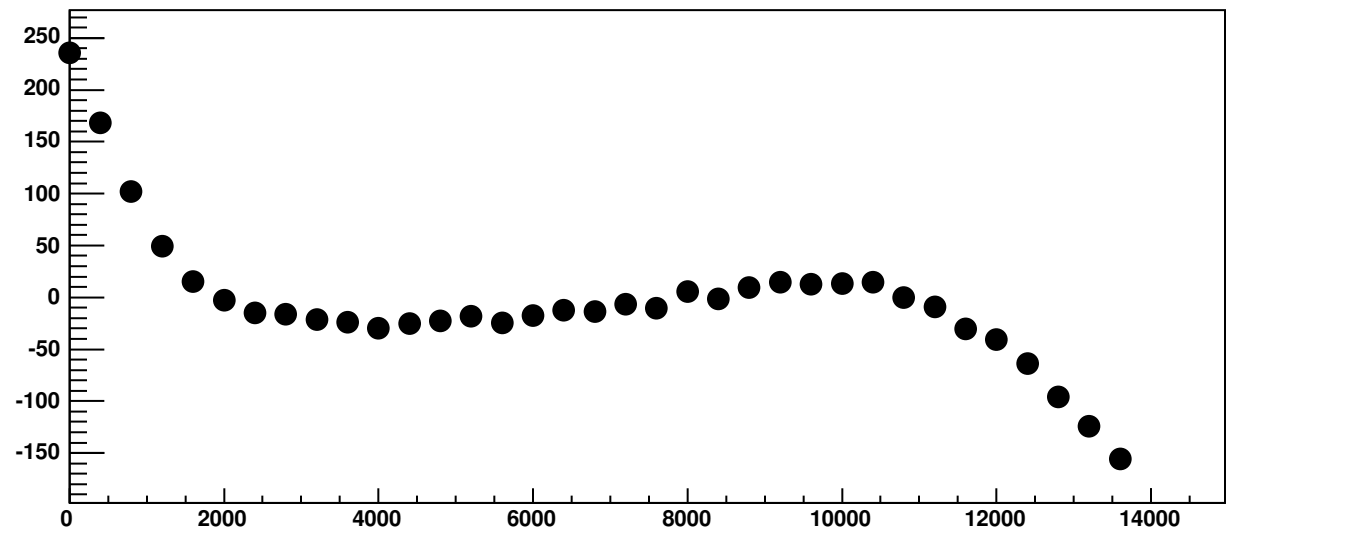
Chip 2, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

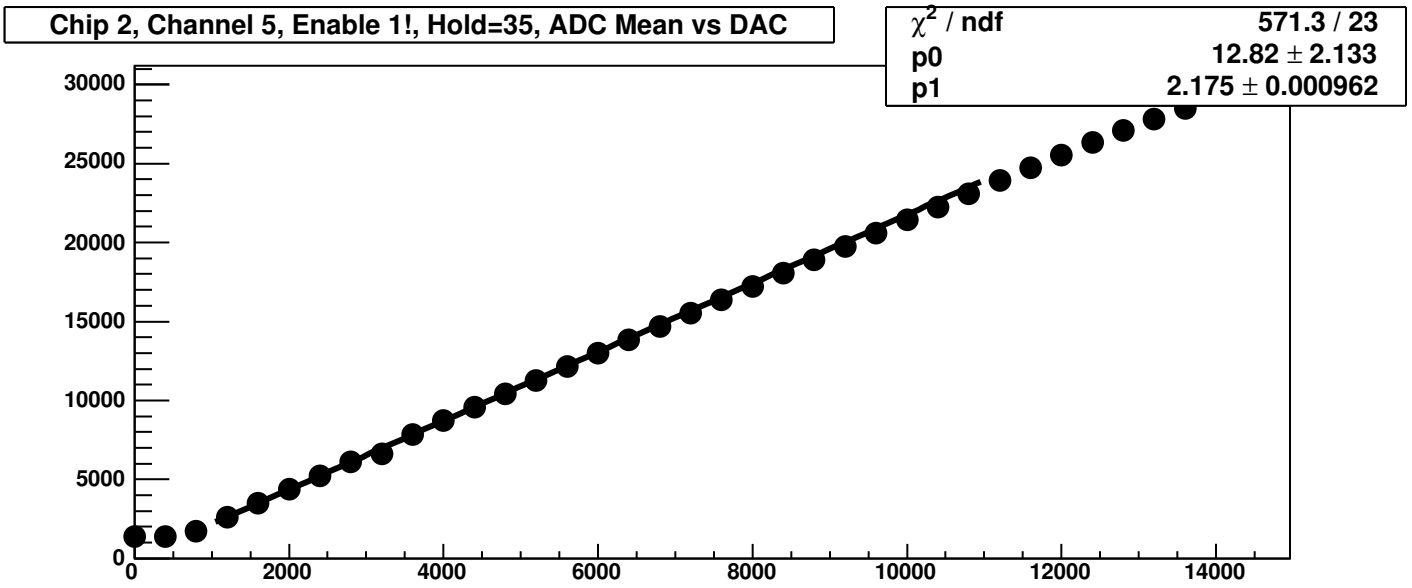


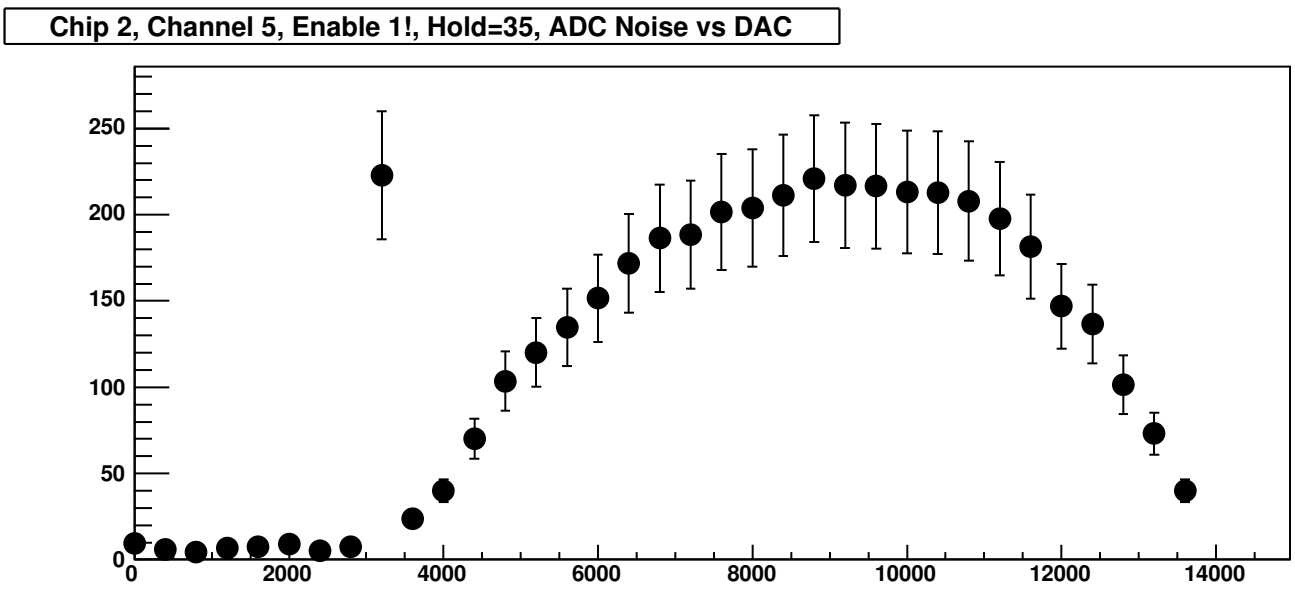
Chip 2, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC

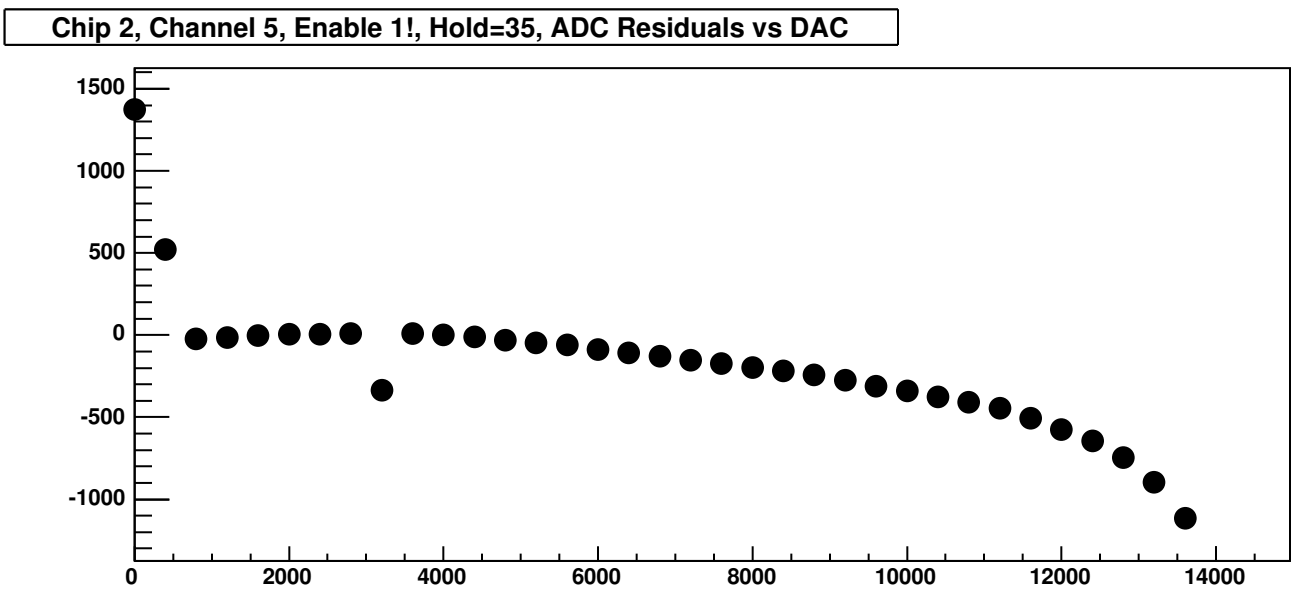


Chip 2, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

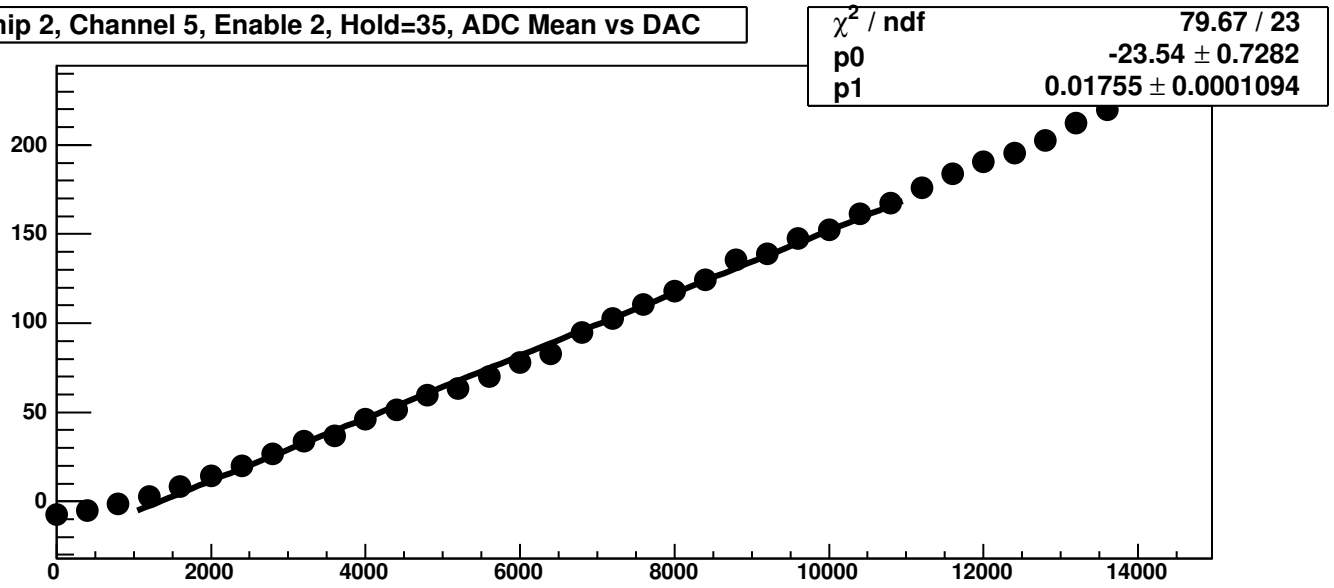



Chip 2, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 571.3 / 23
p0 12.82 ± 2.133
p1 2.175 ± 0.000962
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

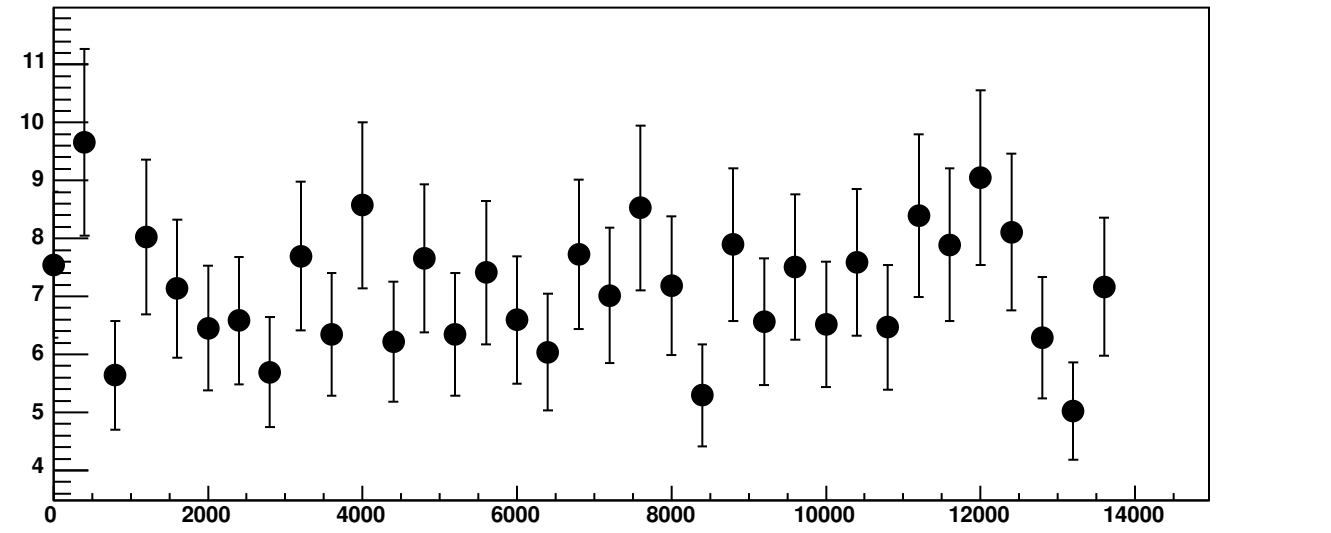

Chip 2, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

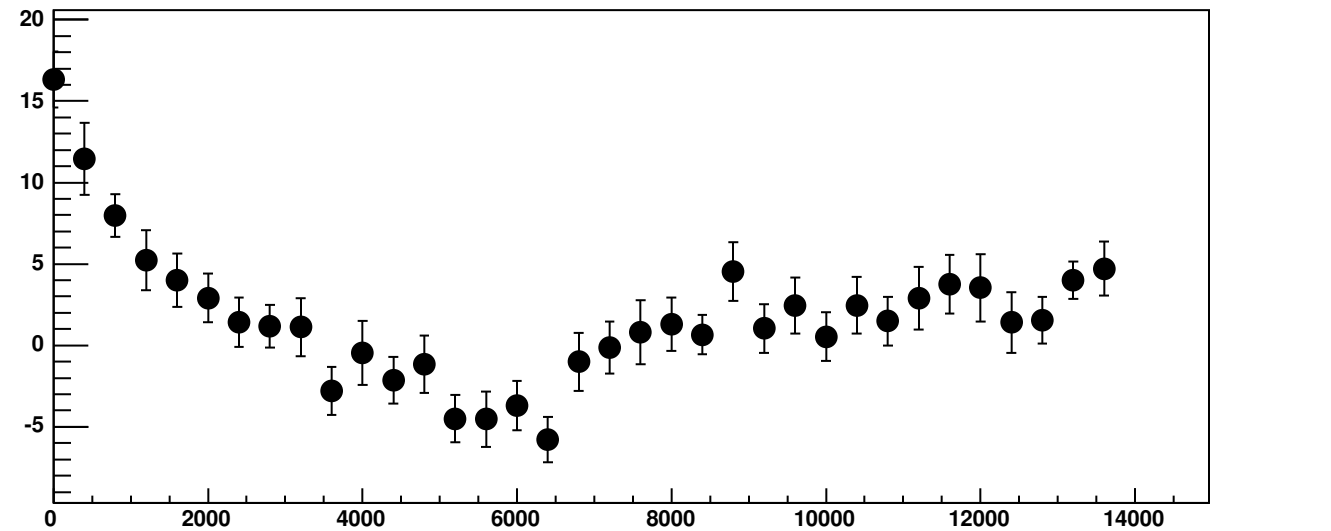
Chip 2, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

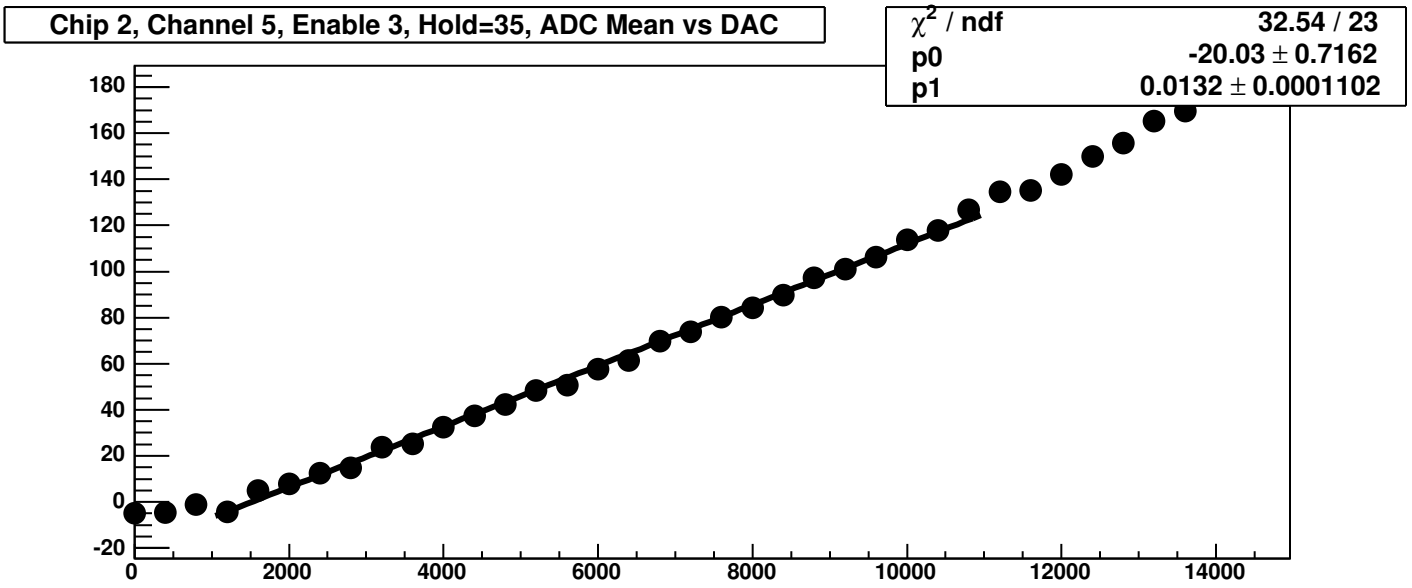


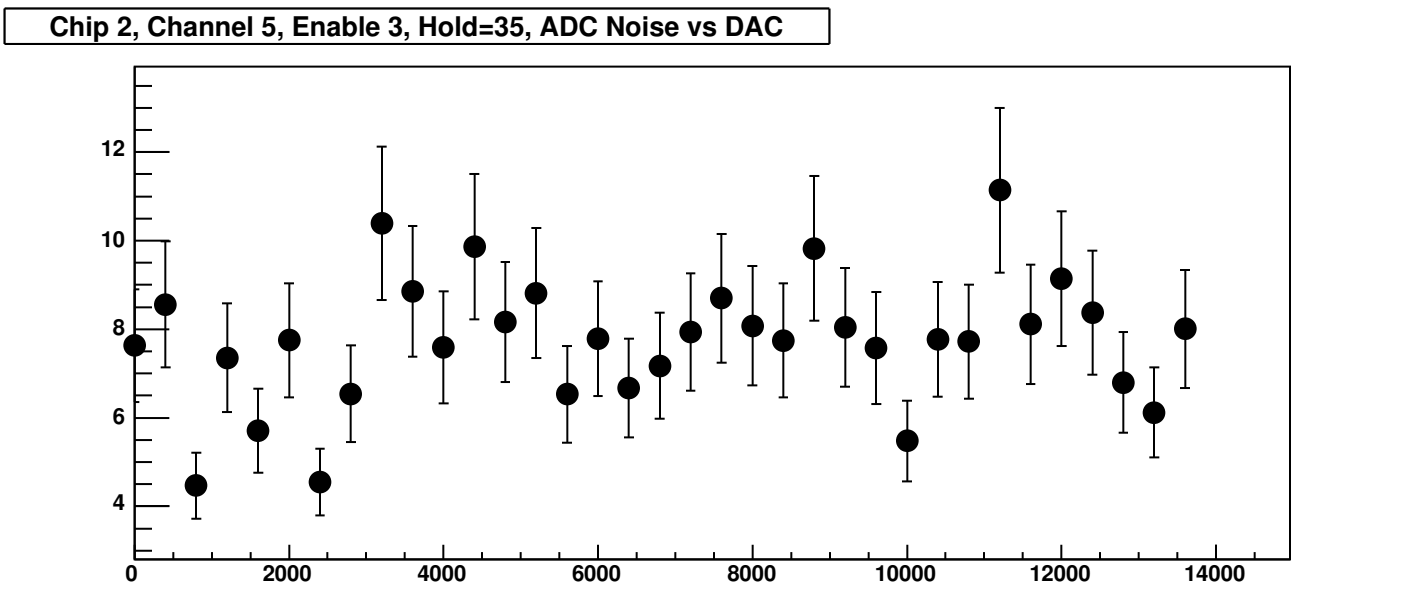
Chip 2, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC

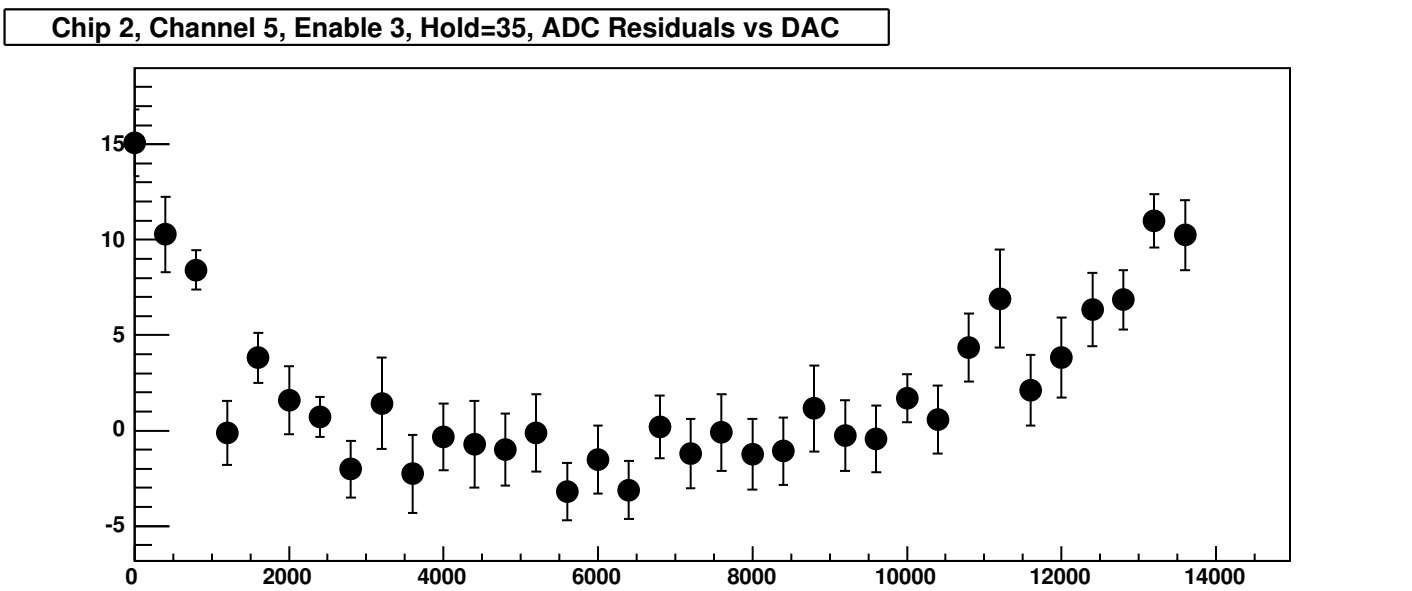


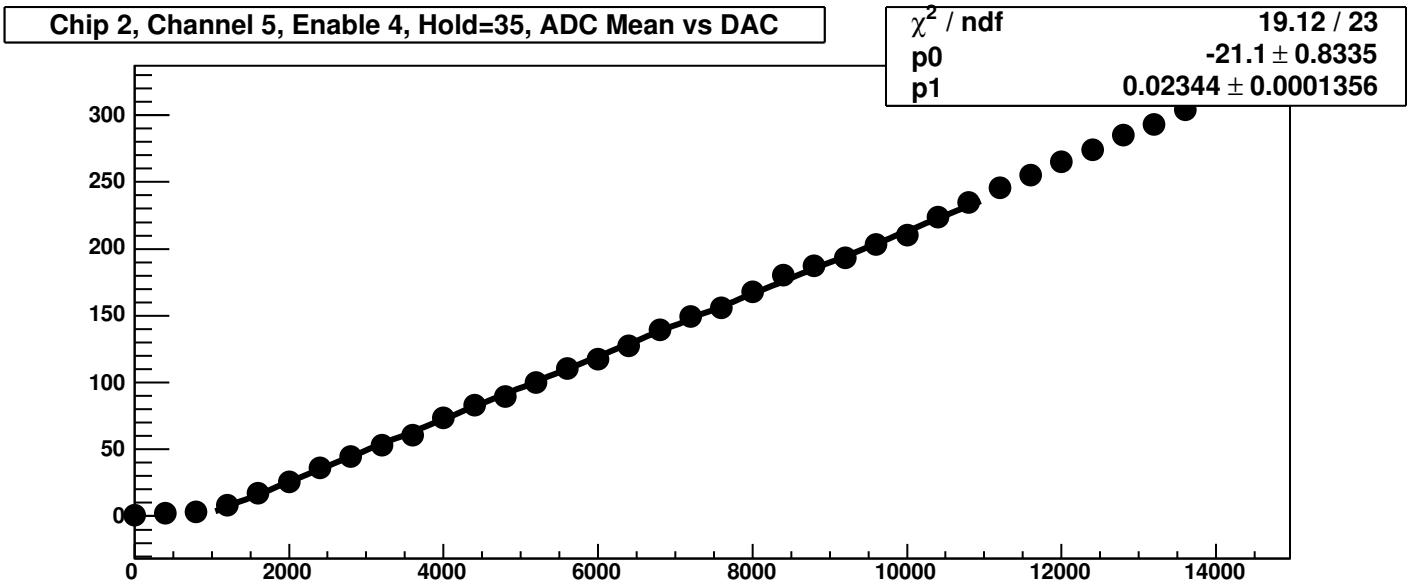
Chip 2, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

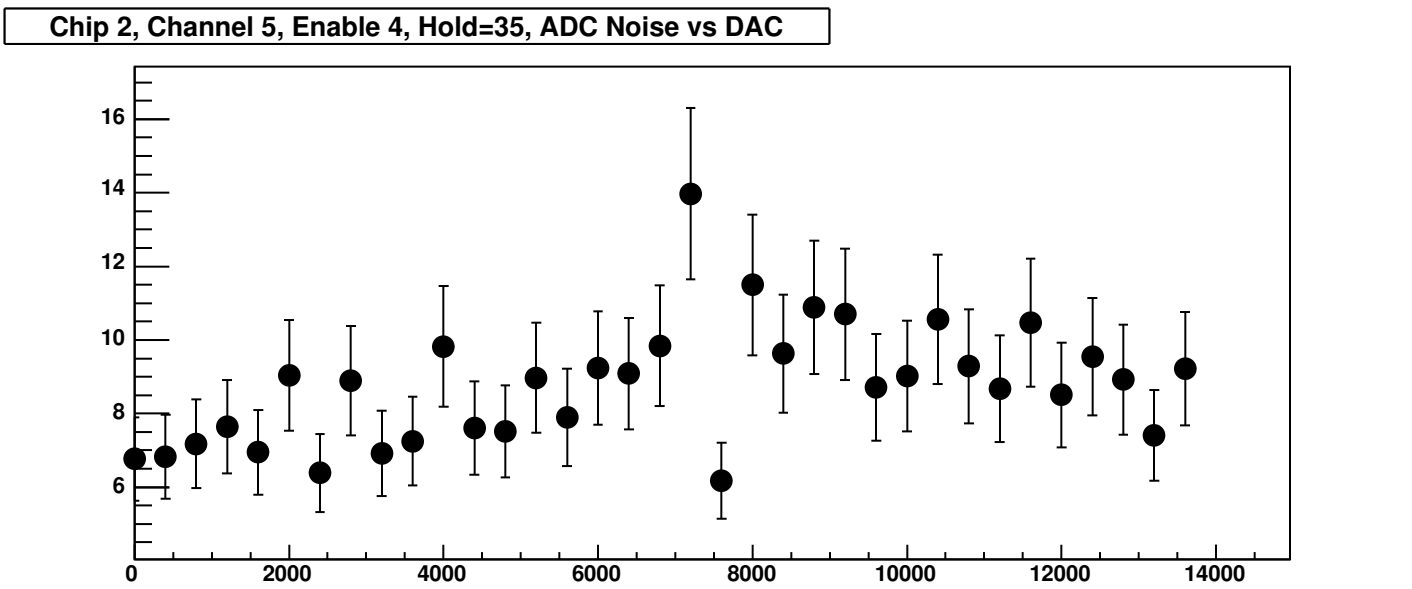


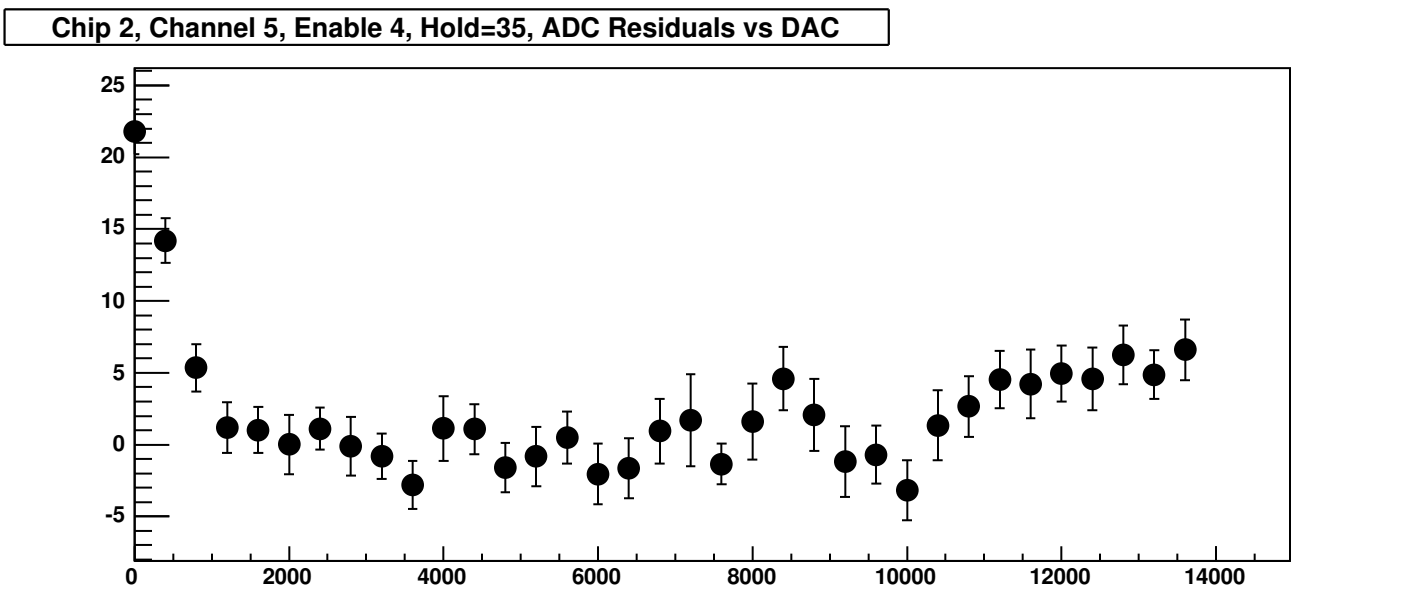

Chip 2, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 32.54 / 23
p0 -20.03 ± 0.7162
p1 0.0132 ± 0.0001102
180
160
140
120
100
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000

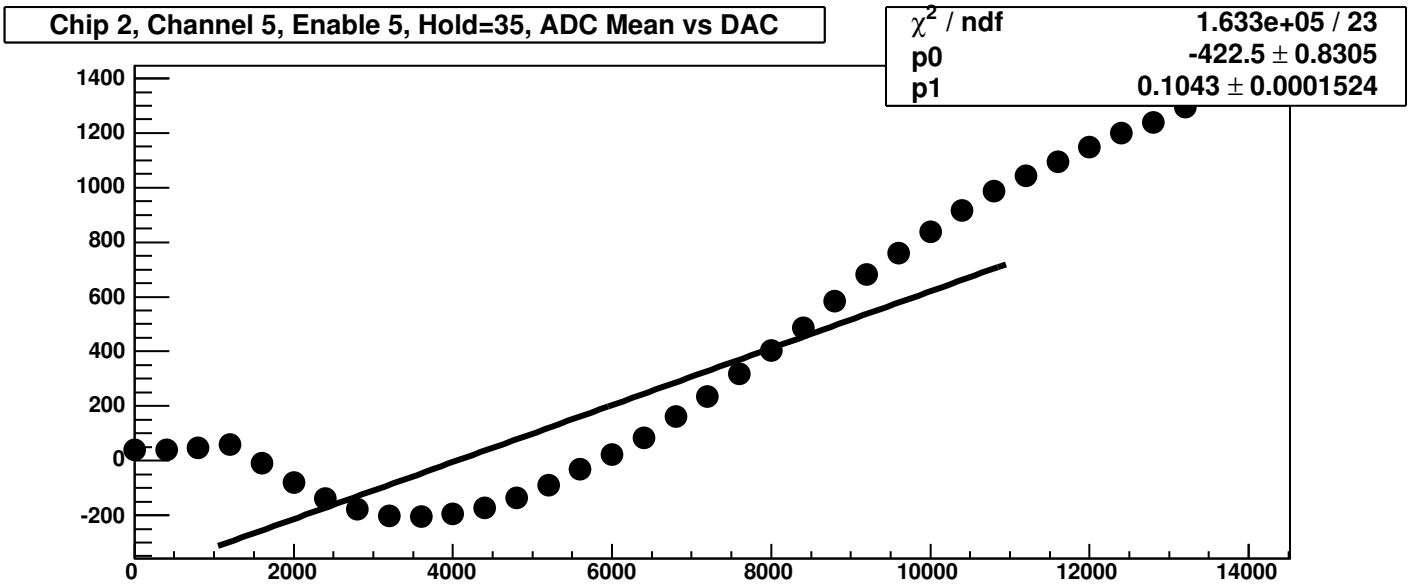

Chip 2, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

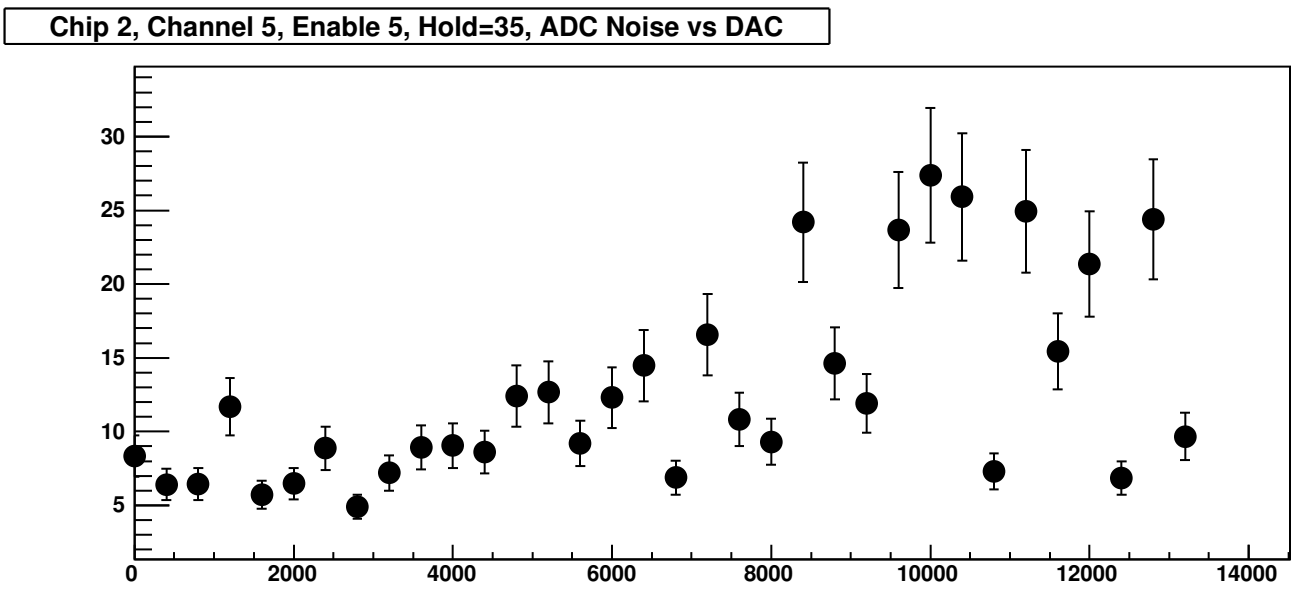

Chip 2, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

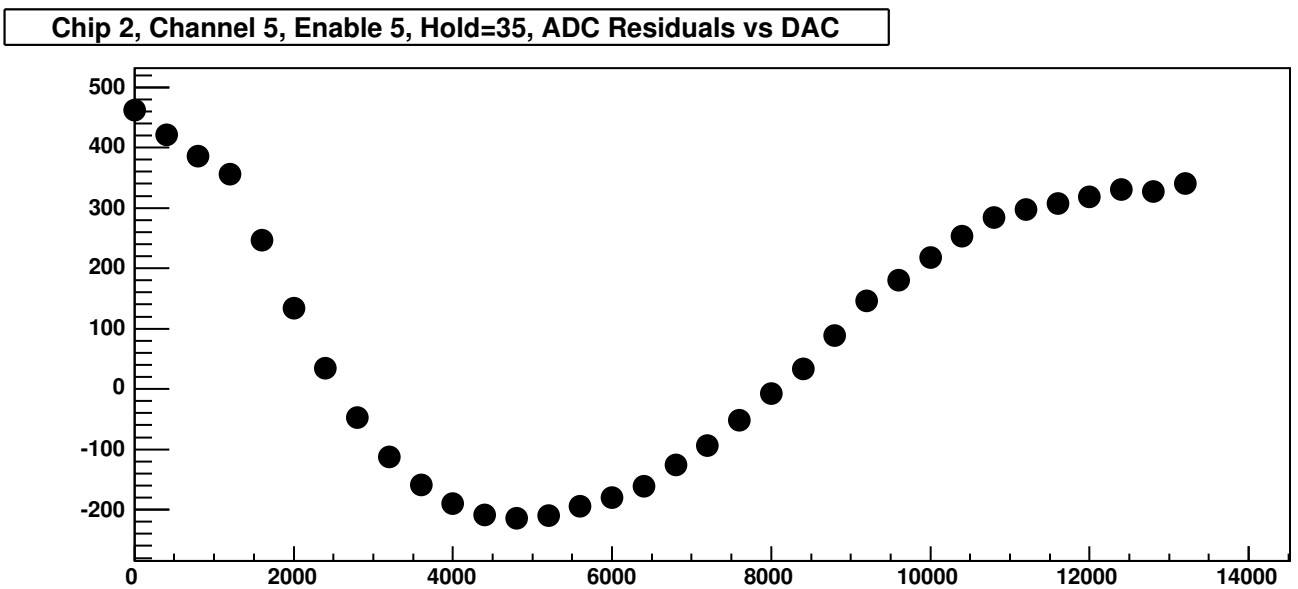

Chip 2, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 19.12 / 23
p0 -21.1 ± 0.8335
p1 0.02344 ± 0.0001356
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

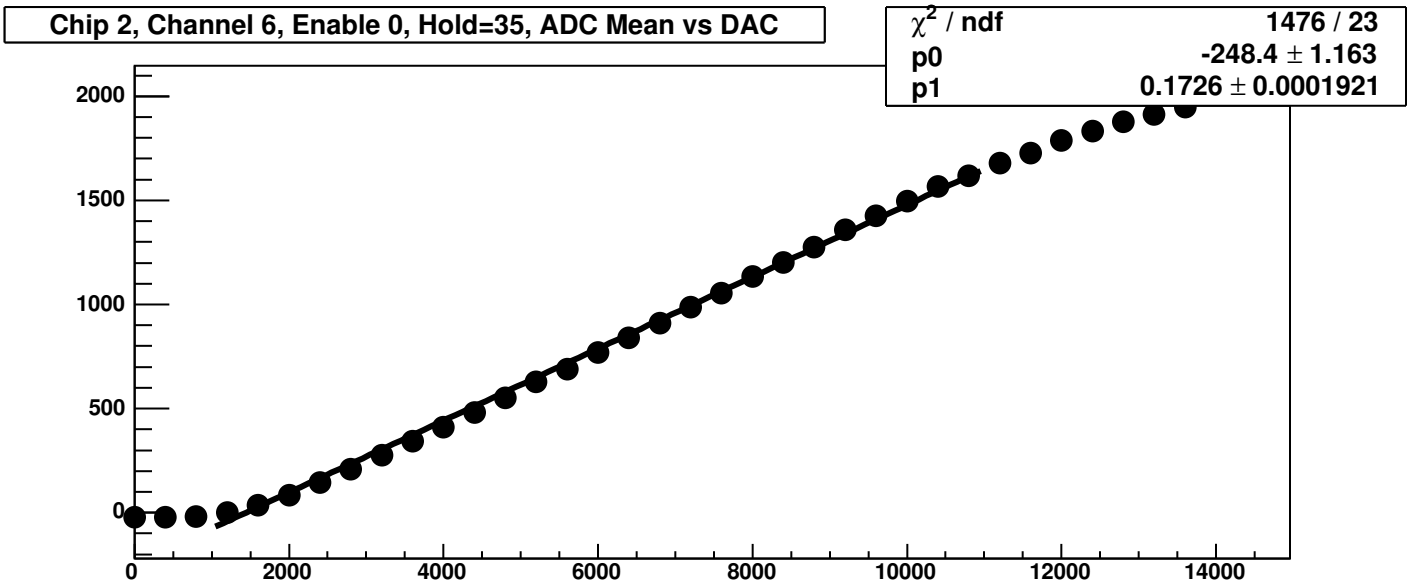

Chip 2, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

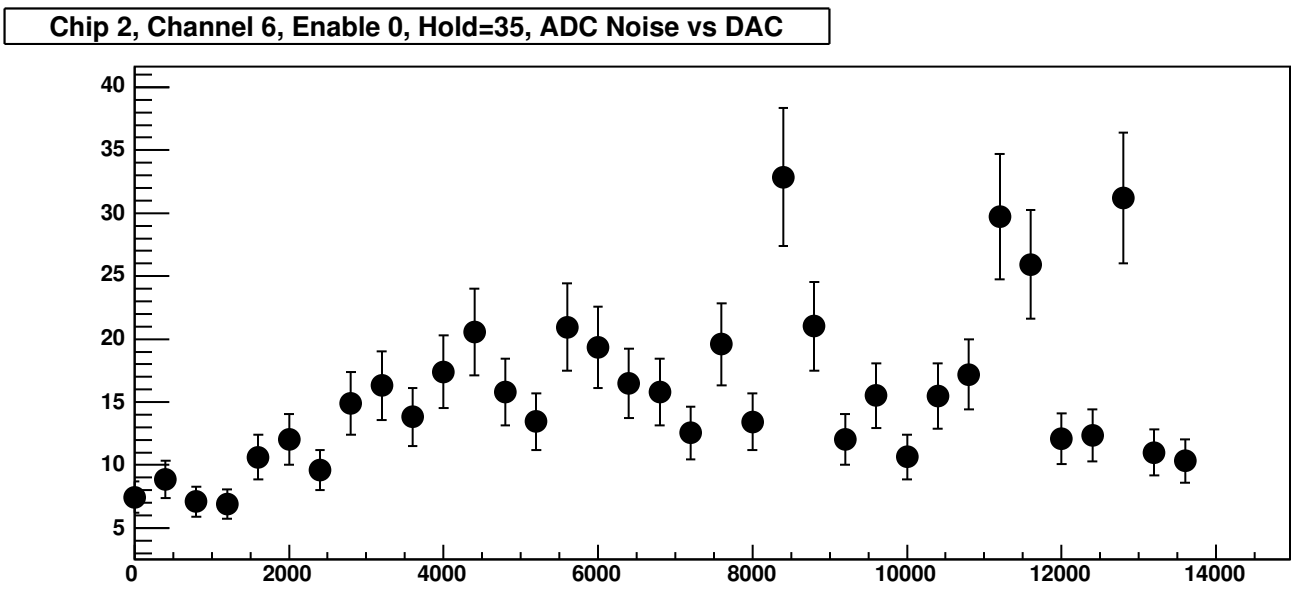

Chip 2, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

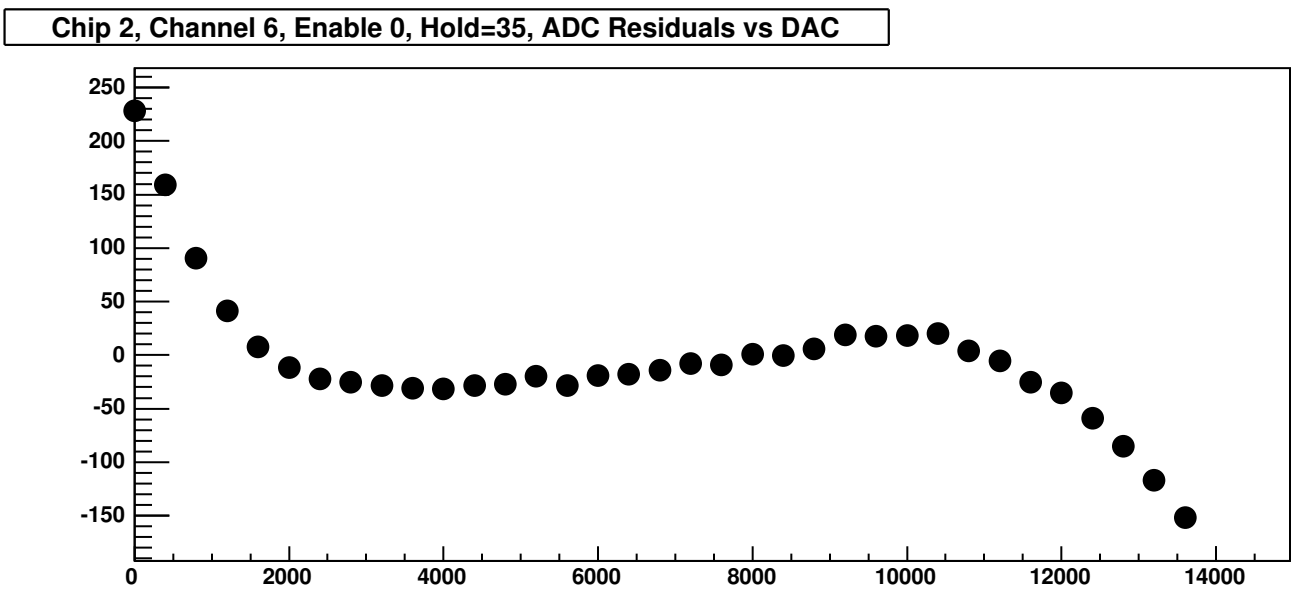

Chip 2, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.633e+05 / 23
p0 -422.5 ± 0.8305
p1 0.1043 ± 0.0001524
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

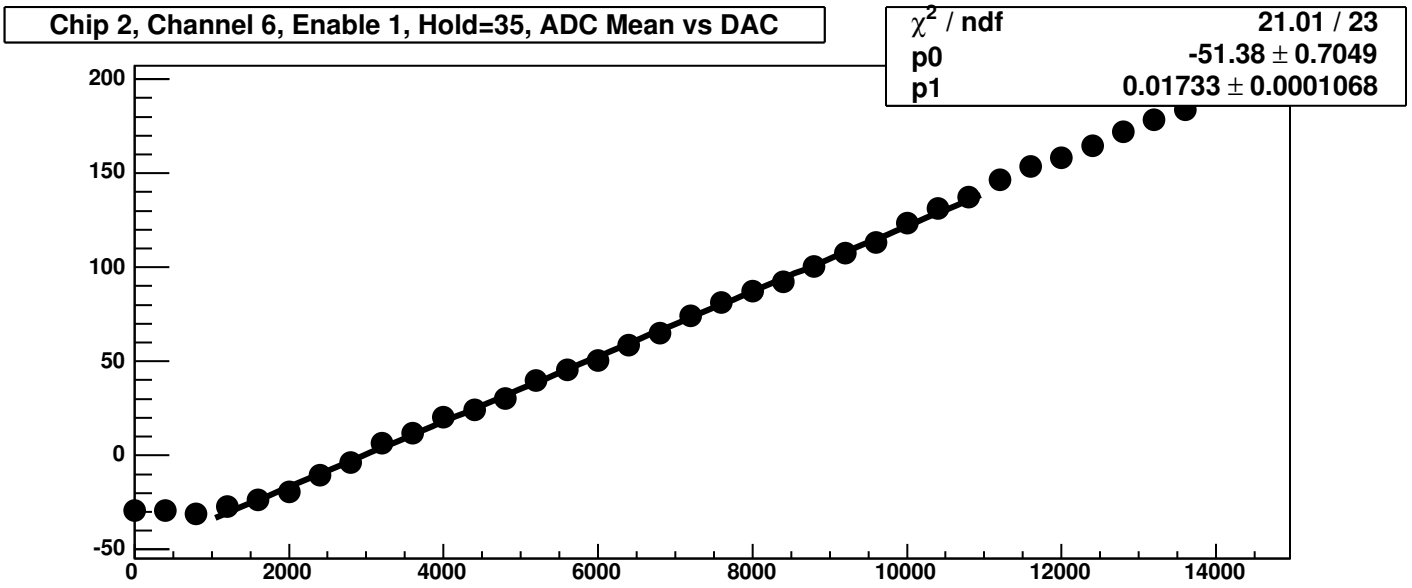

Chip 2, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

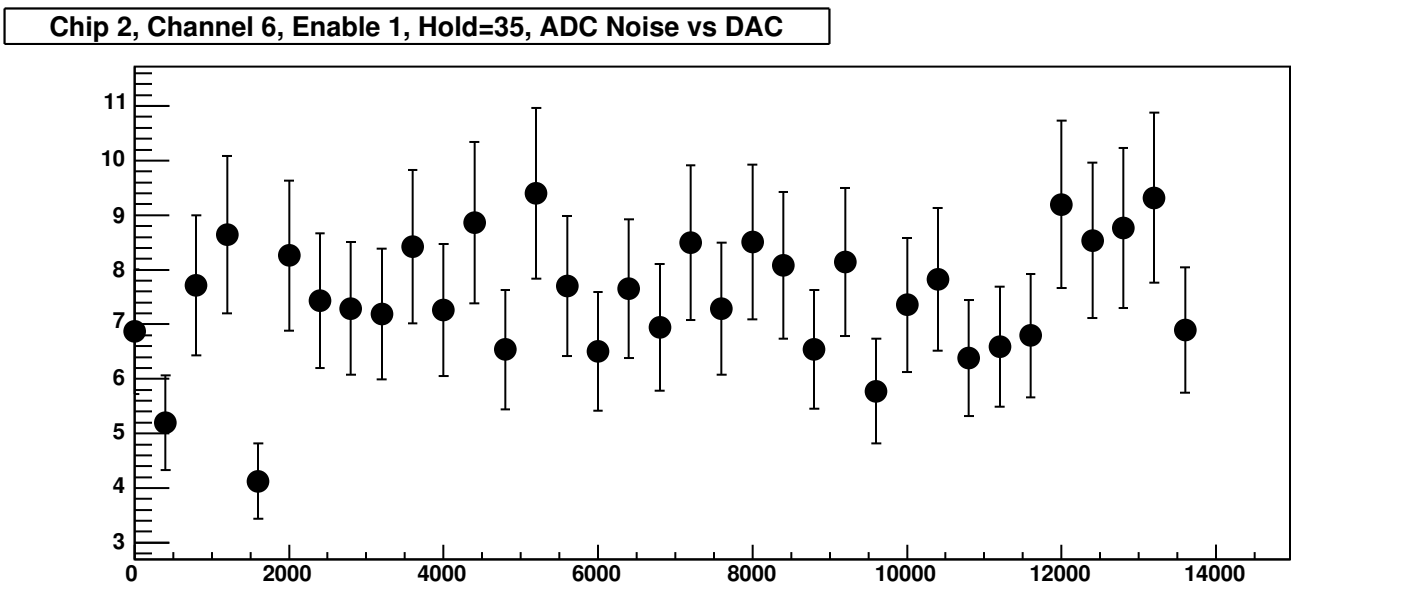

Chip 2, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

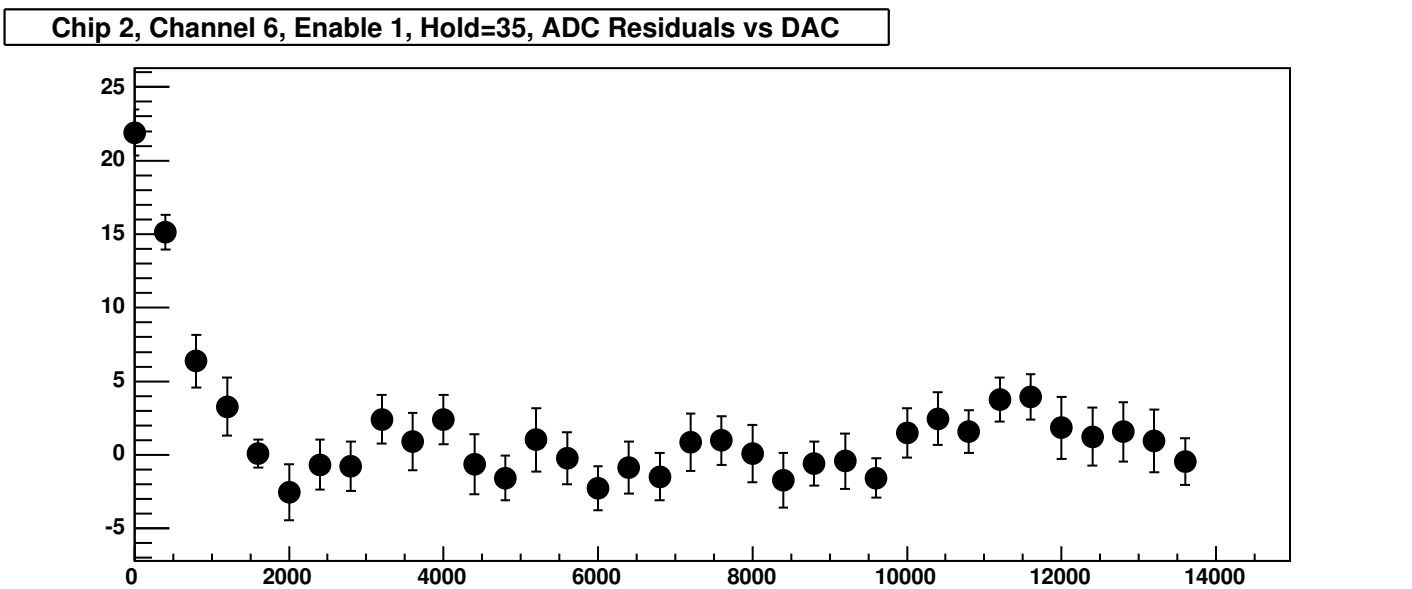

Chip 2, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1476 / 23
p0 -248.4 ± 1.163
p1 0.1726 ± 0.0001921
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

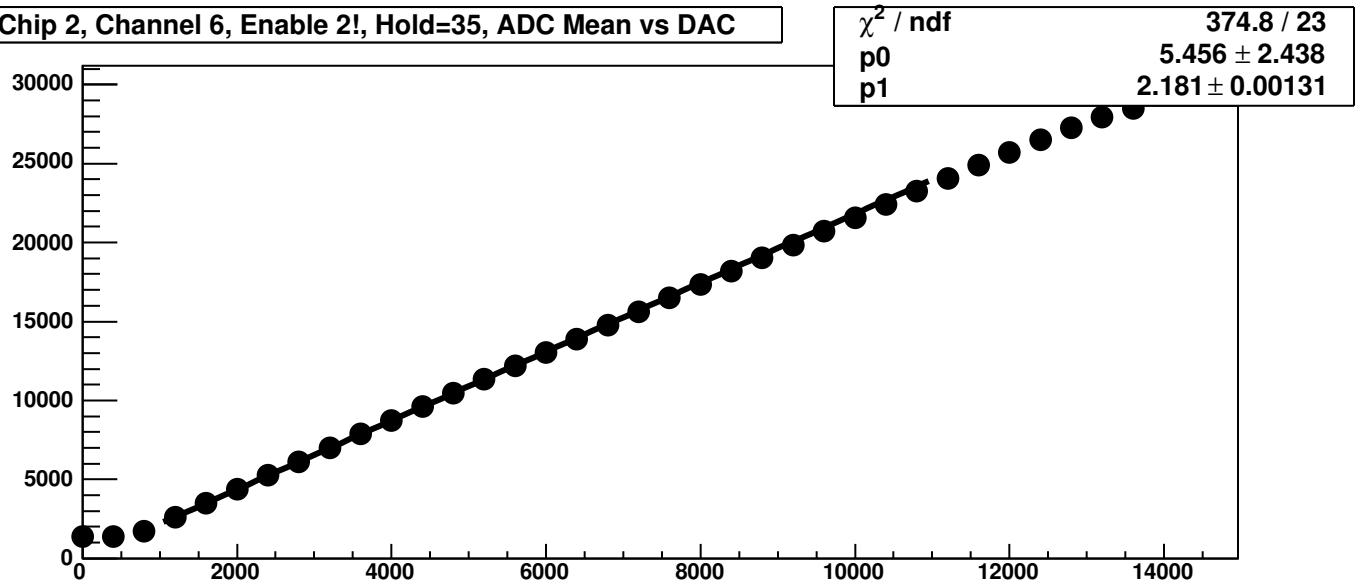

Chip 2, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 21.01 / 23
p0 -51.38 ± 0.7049
p1 0.01733 ± 0.0001068
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

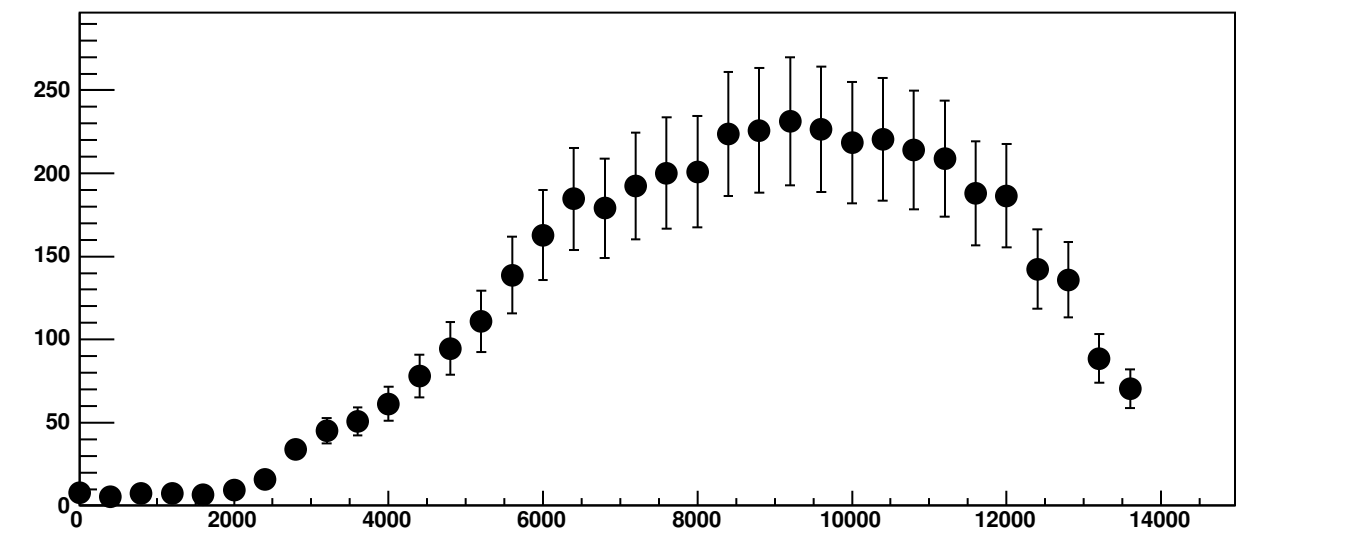

Chip 2, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

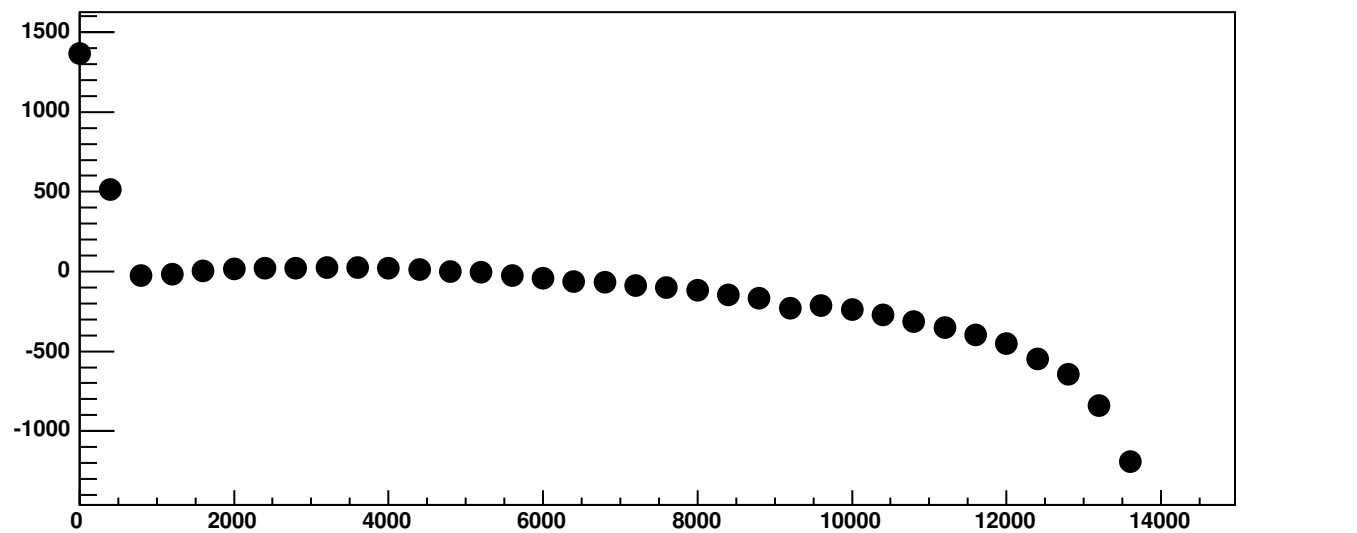
Chip 2, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



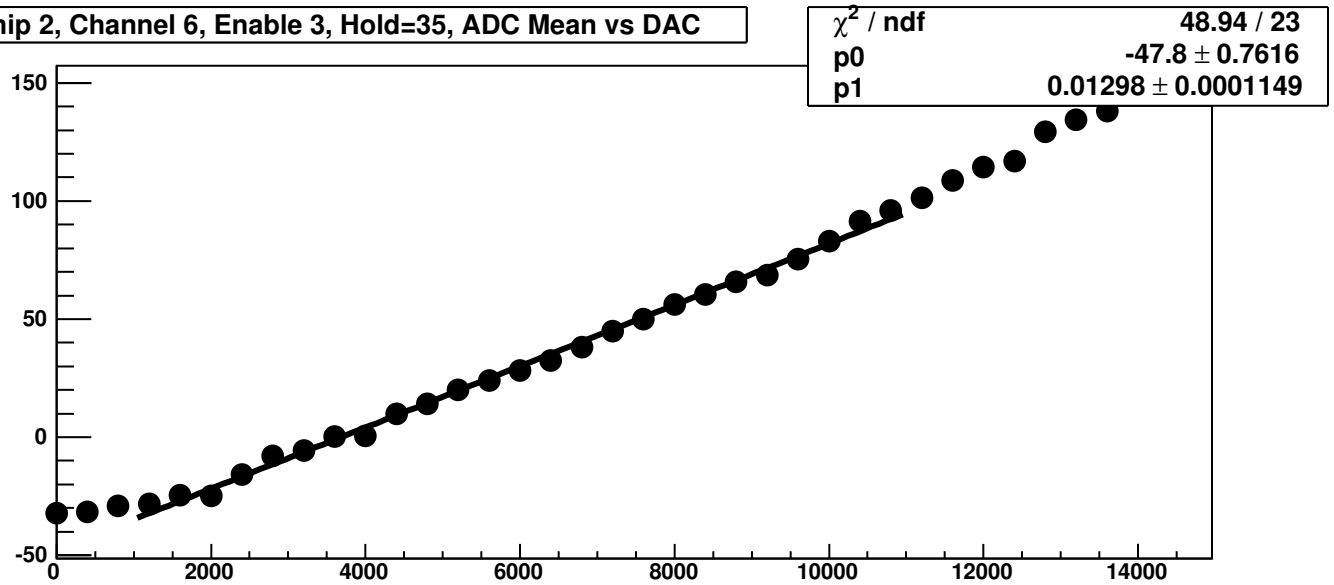
Chip 2, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



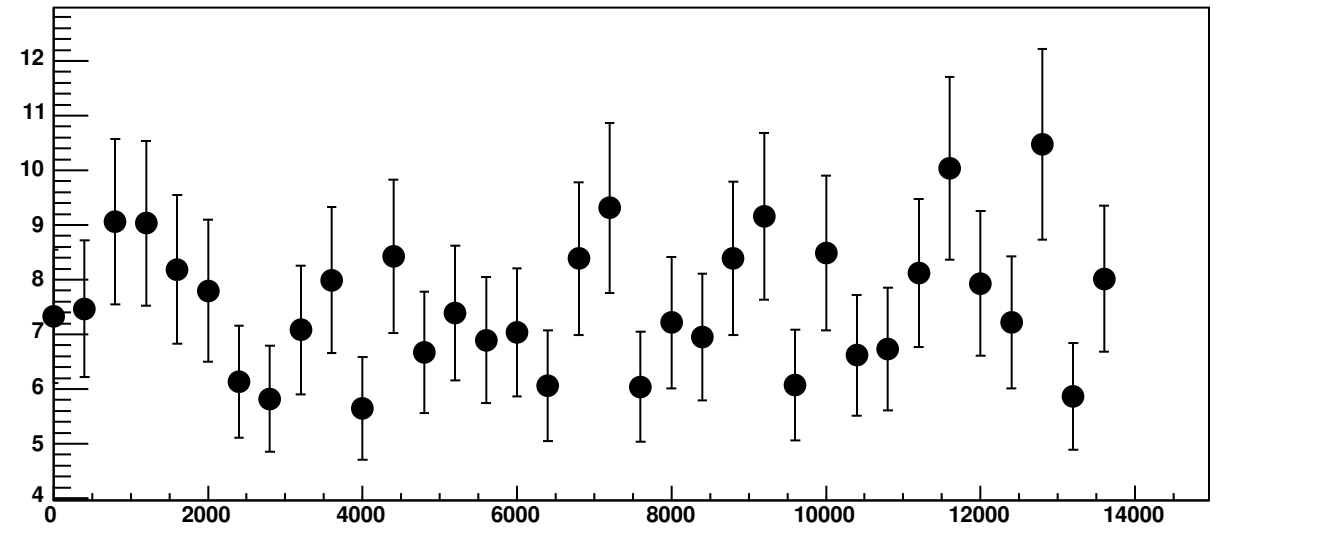
Chip 2, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC



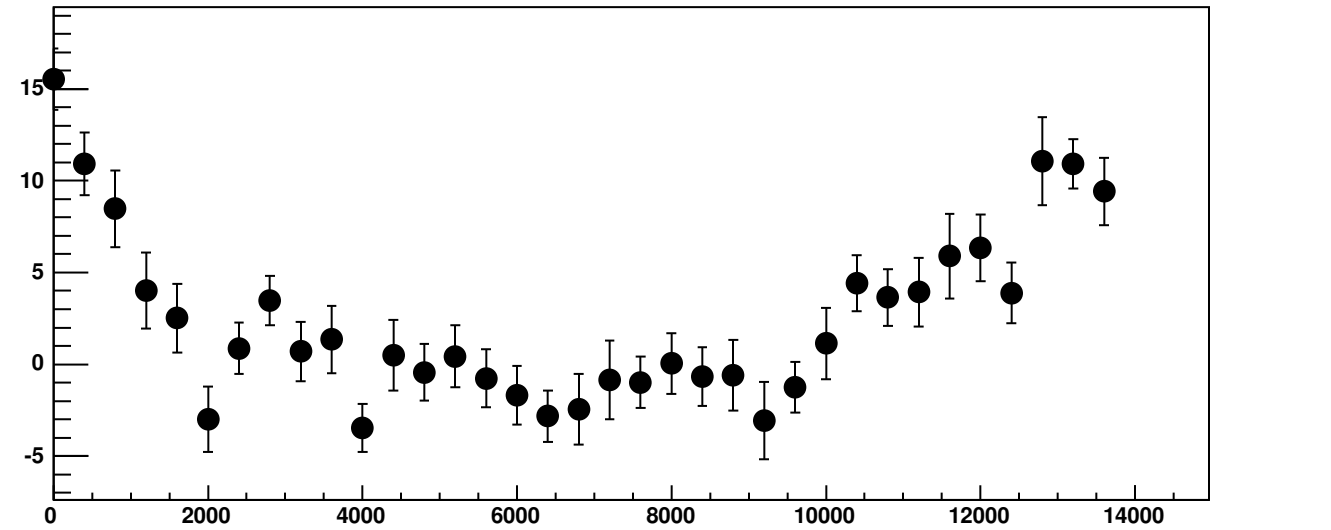
Chip 2, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



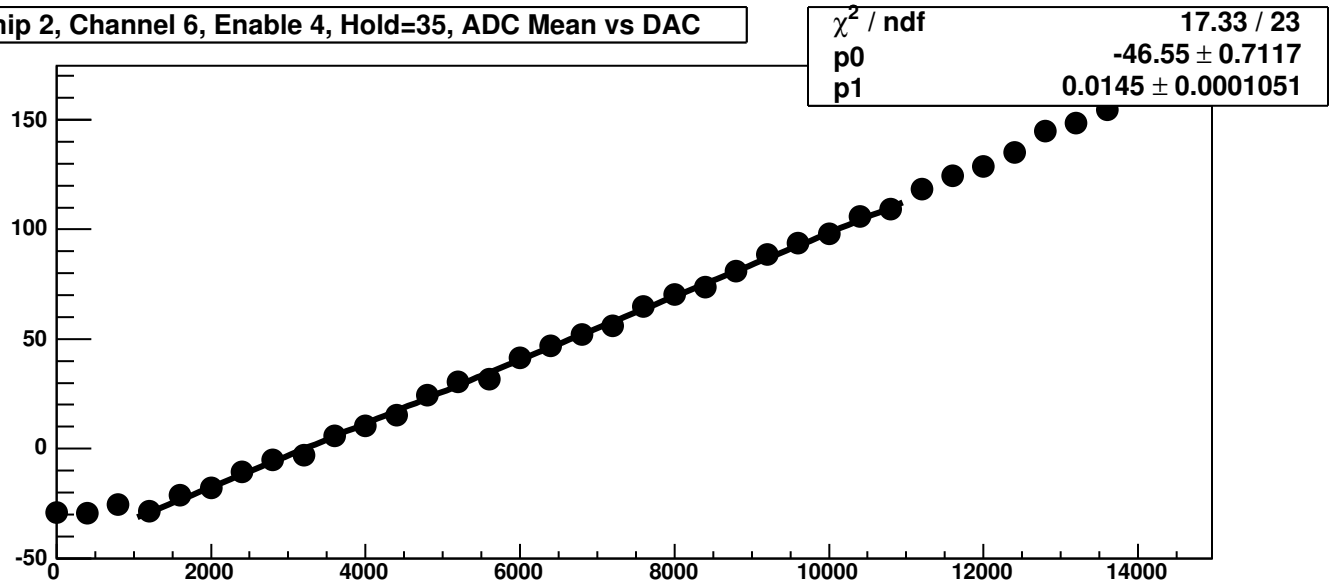
Chip 2, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



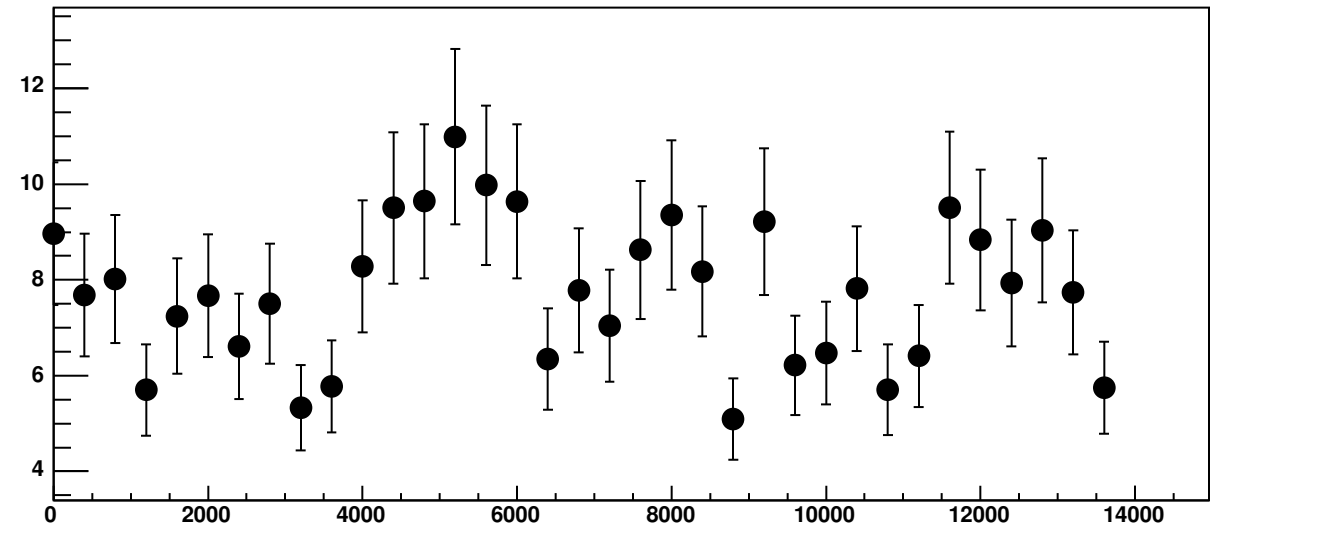
Chip 2, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



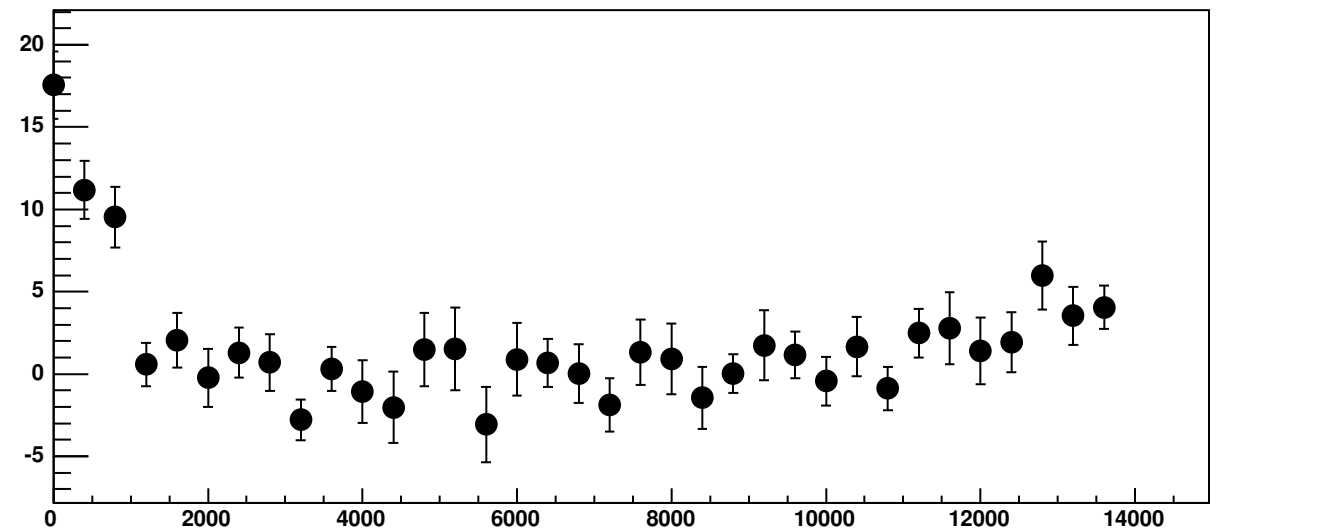
Chip 2, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

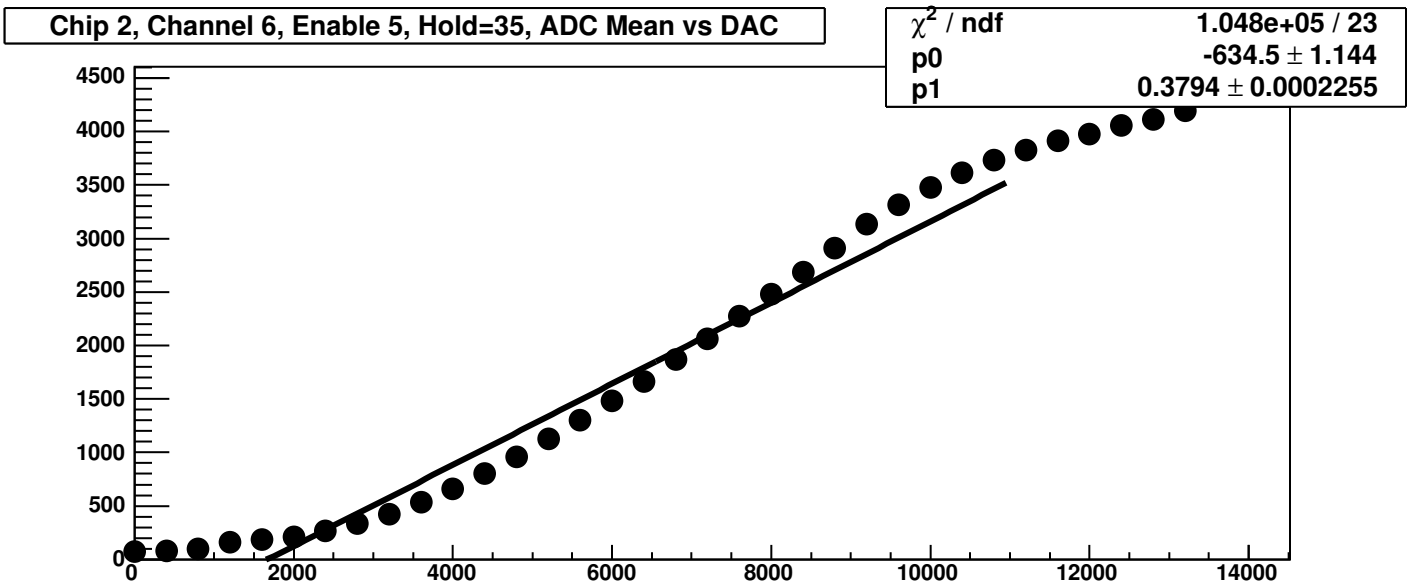


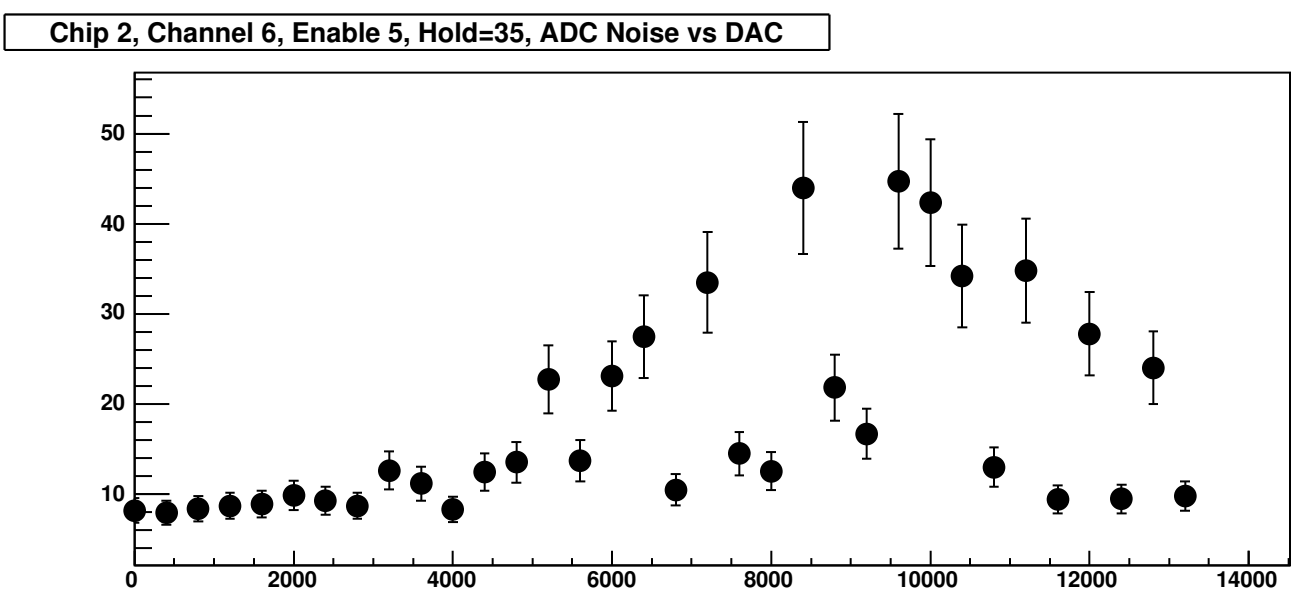
Chip 2, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

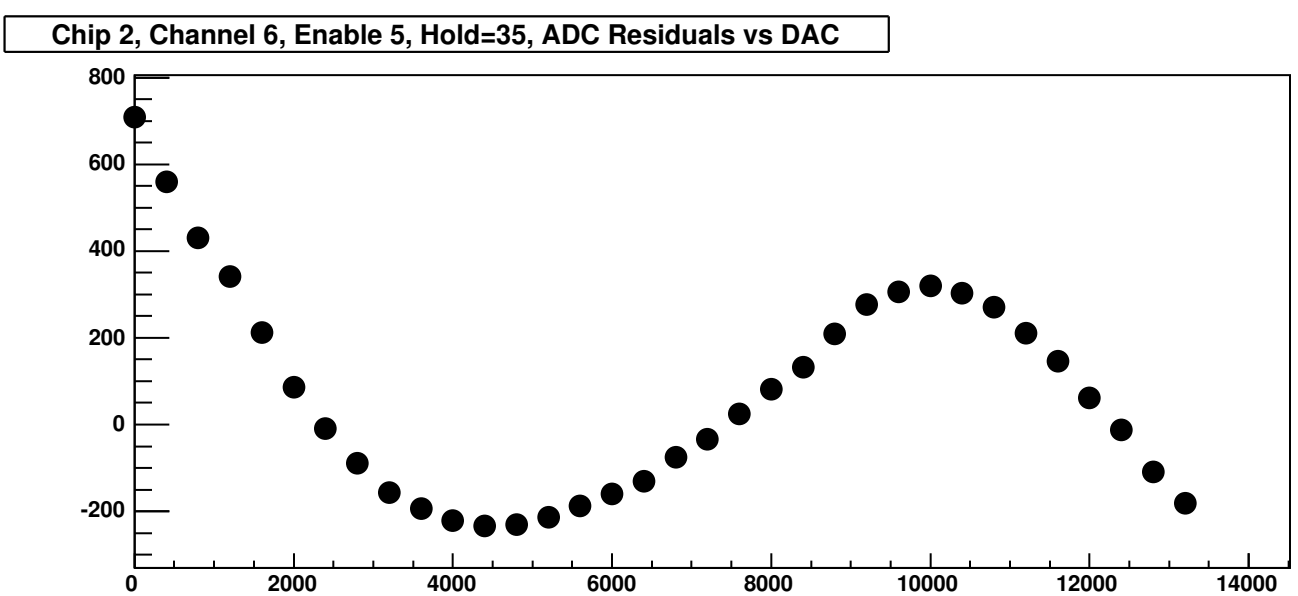


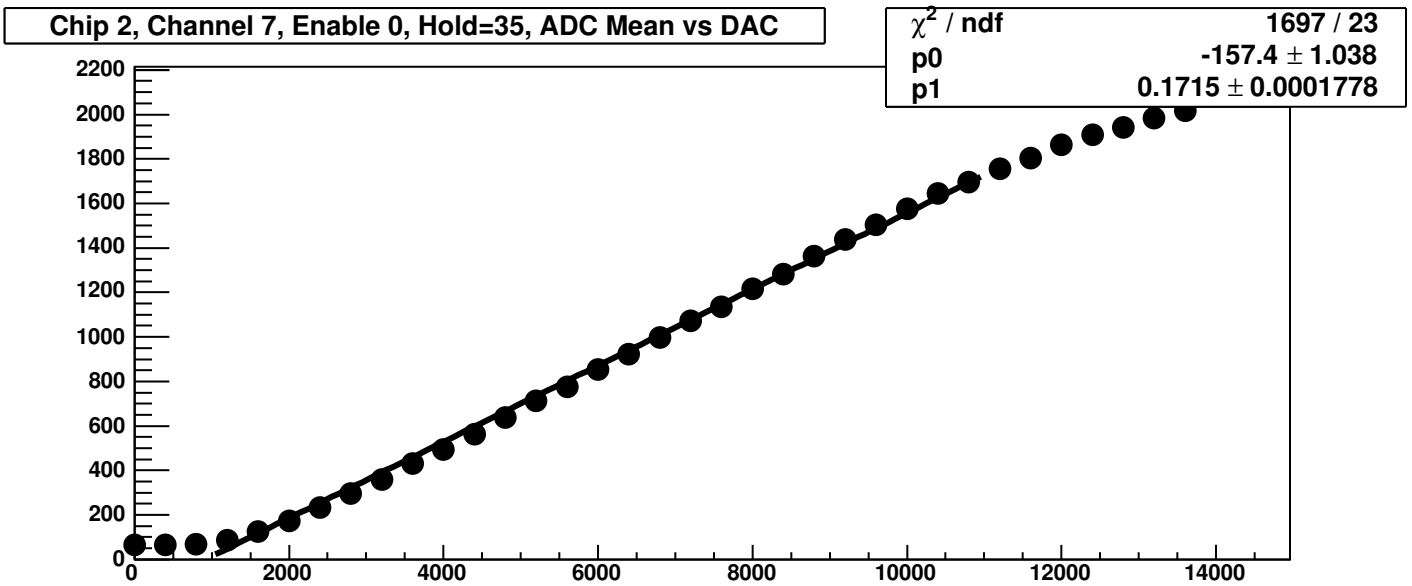
Chip 2, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

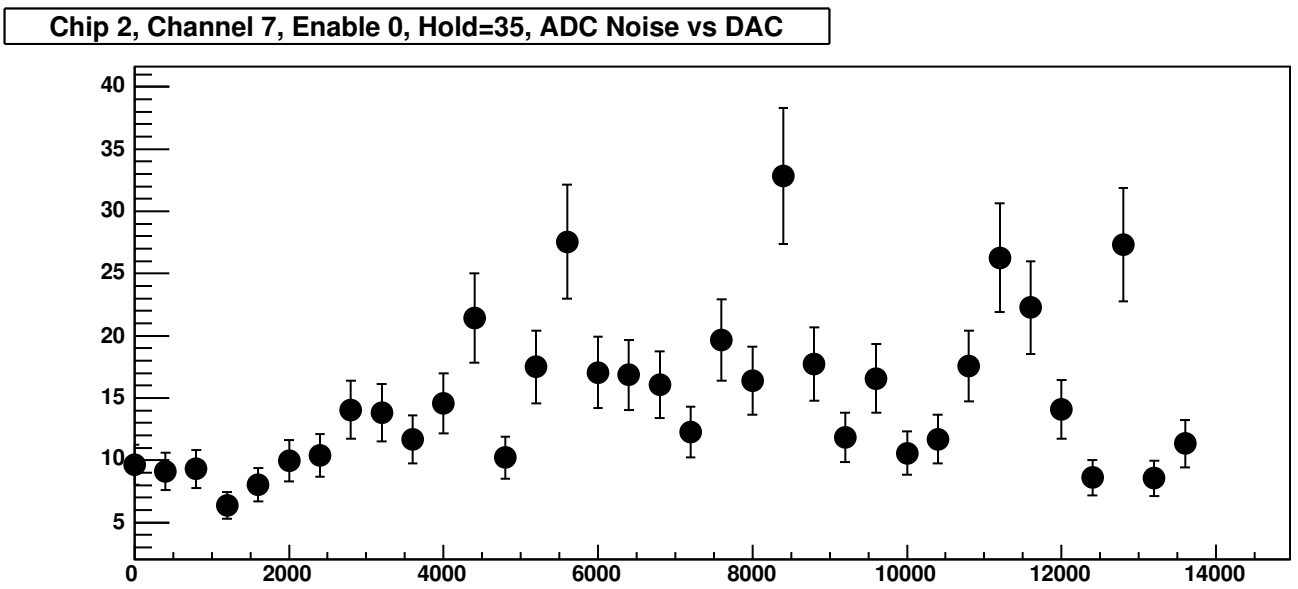


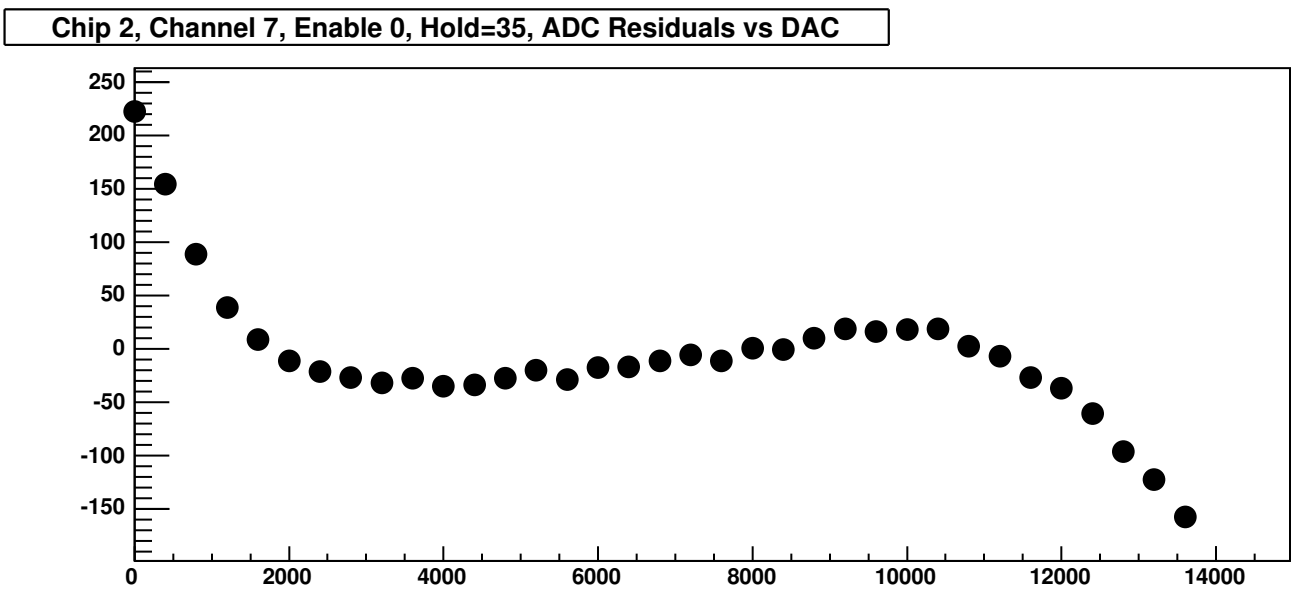

Chip 2, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.048e+05 / 23
p0 -634.5 ± 1.144
p1 0.3794 ± 0.0002255
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

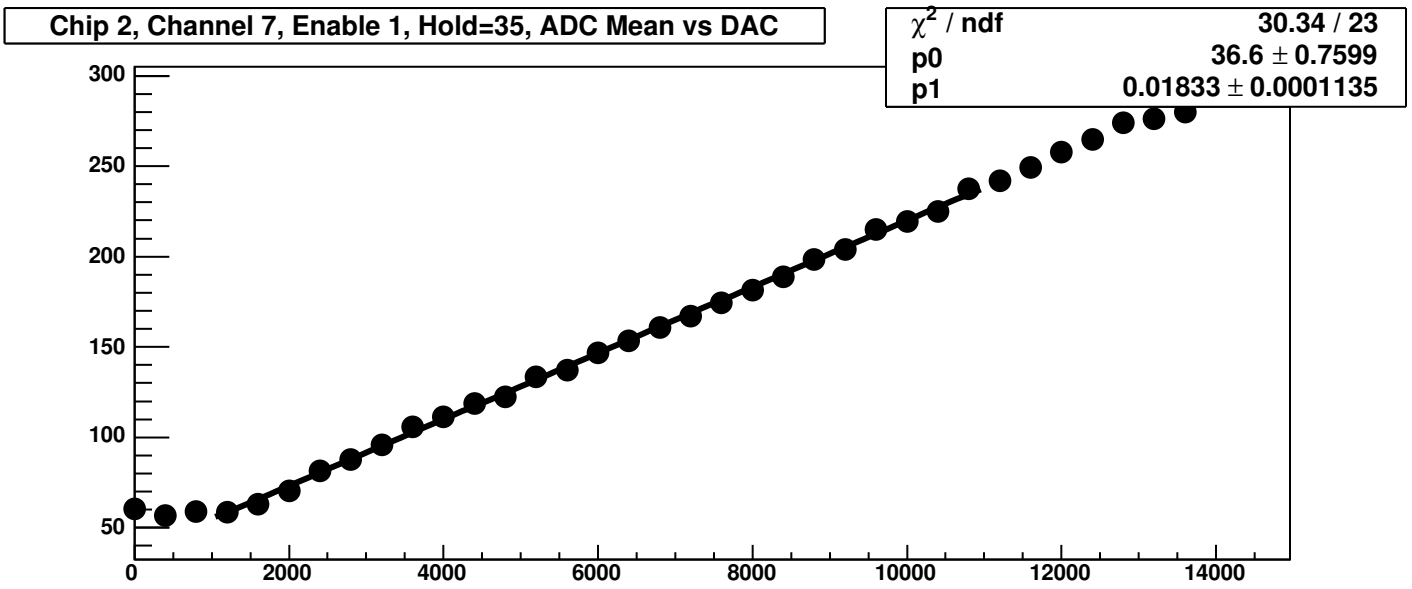

Chip 2, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

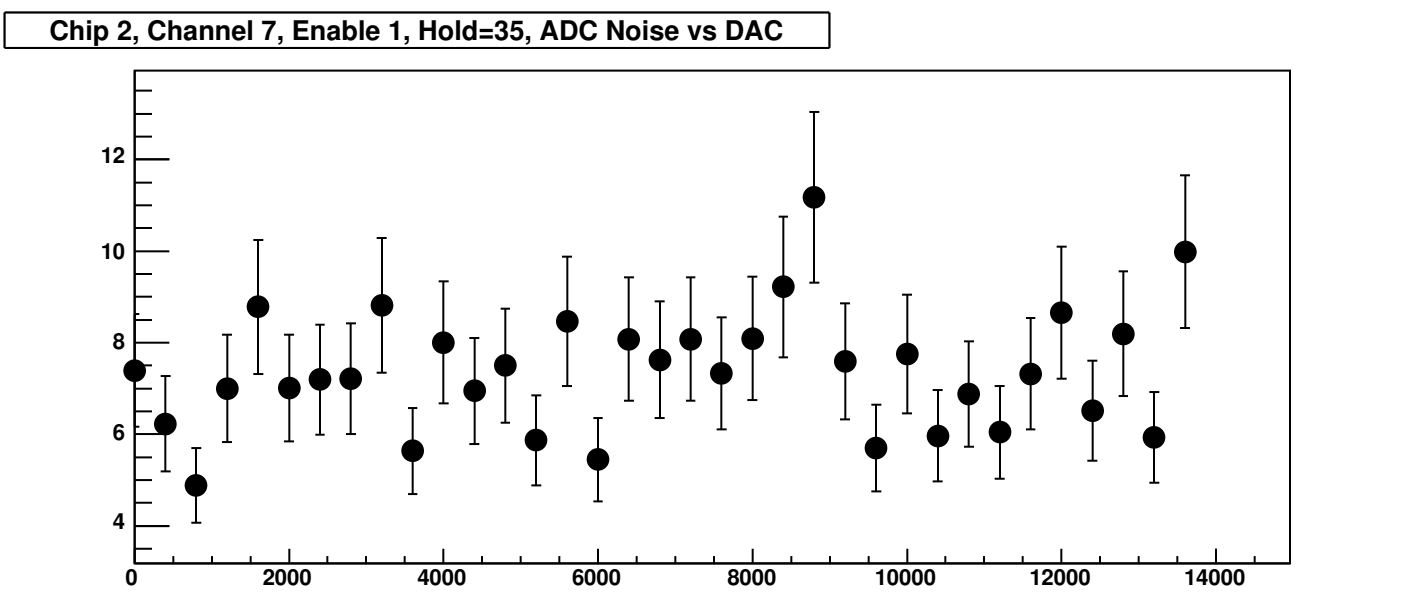

Chip 2, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

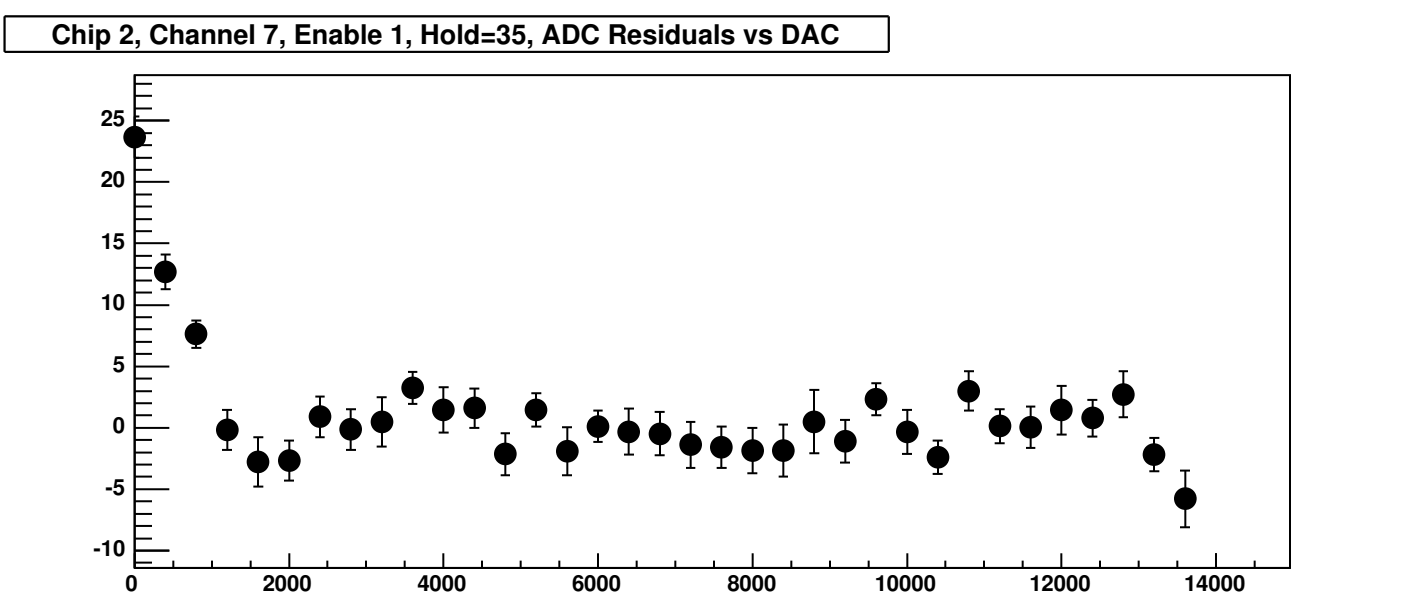

Chip 2, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1697 / 23
p0 -157.4 ± 1.038
p1 0.1715 ± 0.0001778
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

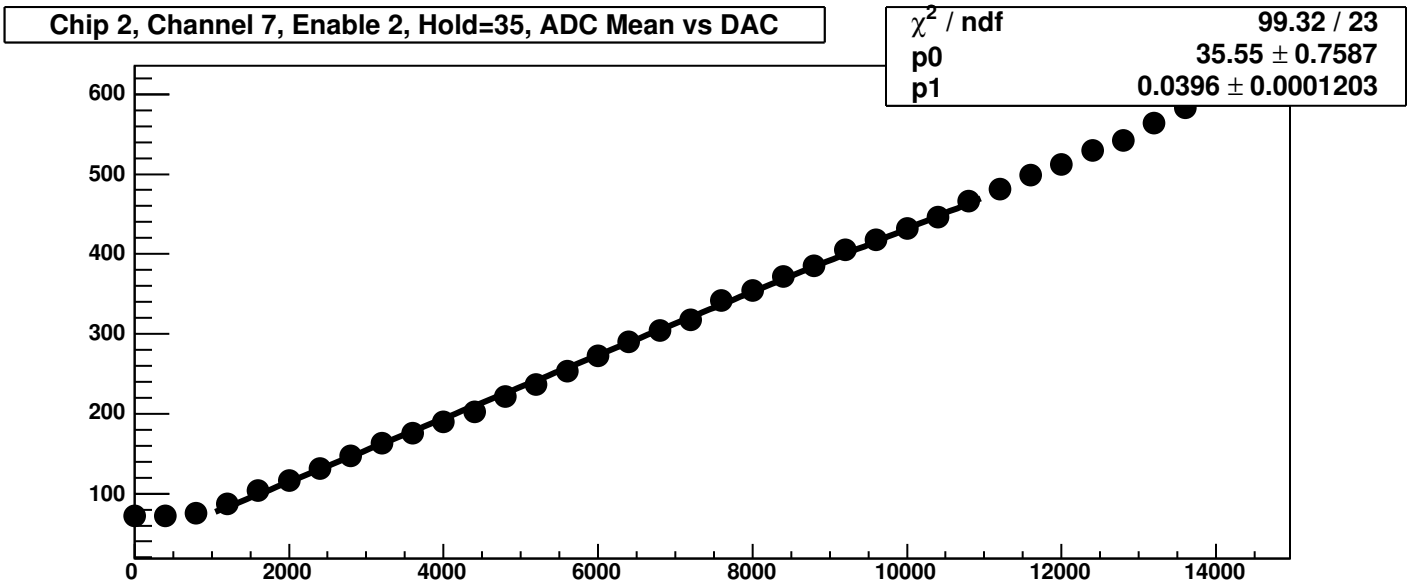

Chip 2, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

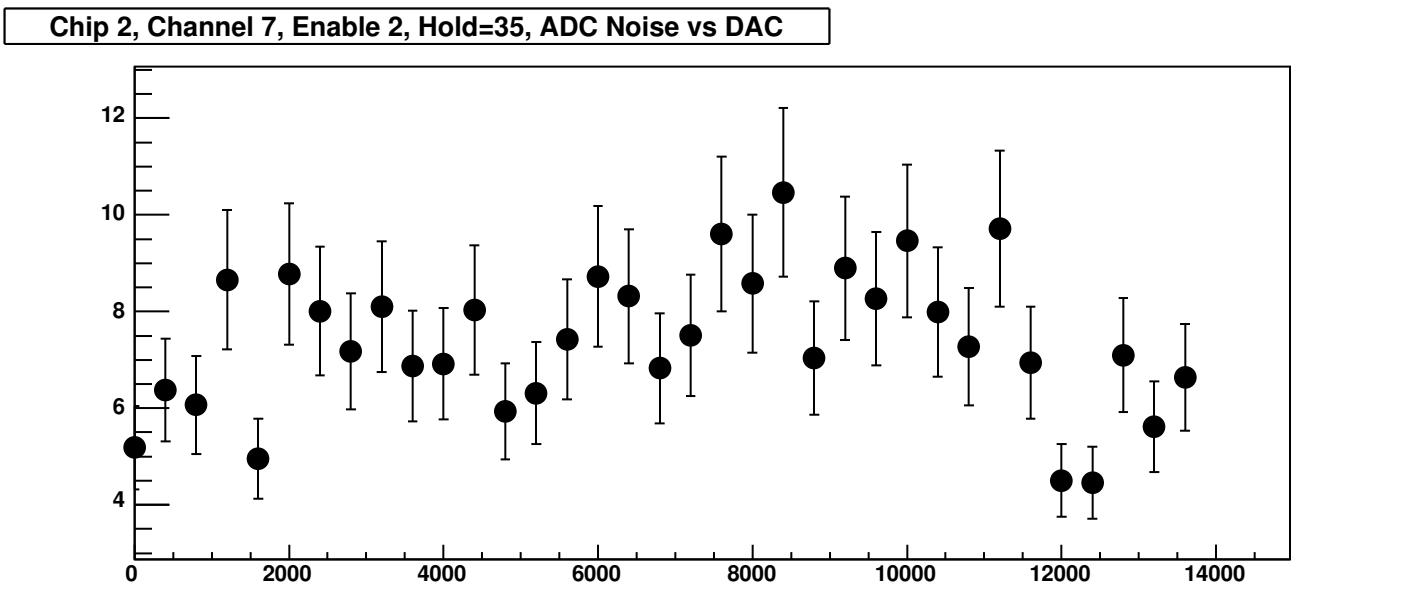

Chip 2, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

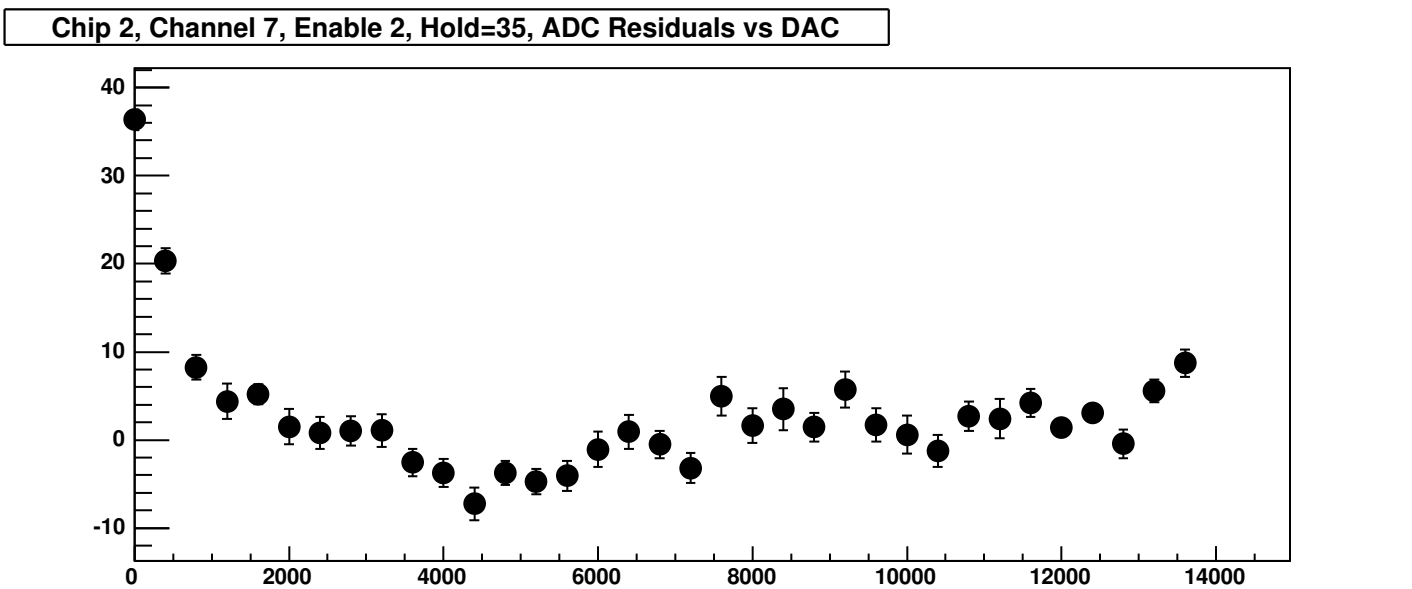

Chip 2, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 30.34 / 23
p0 36.6 ± 0.7599
p1 0.01833 ± 0.0001135
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

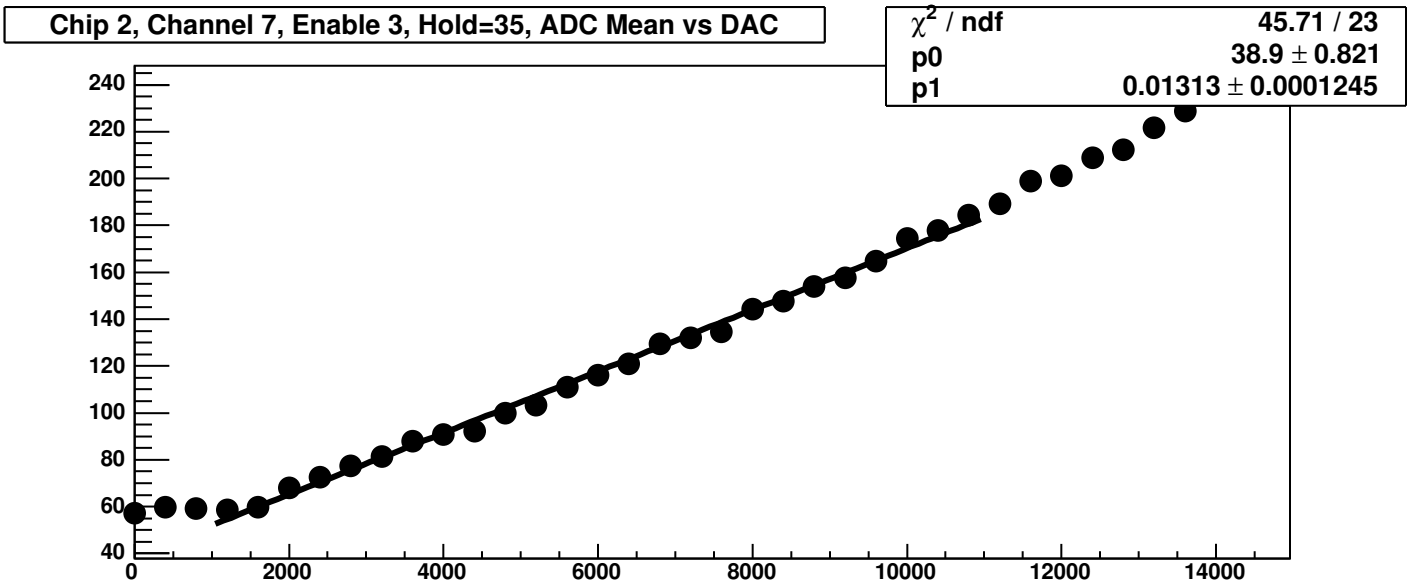

Chip 2, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

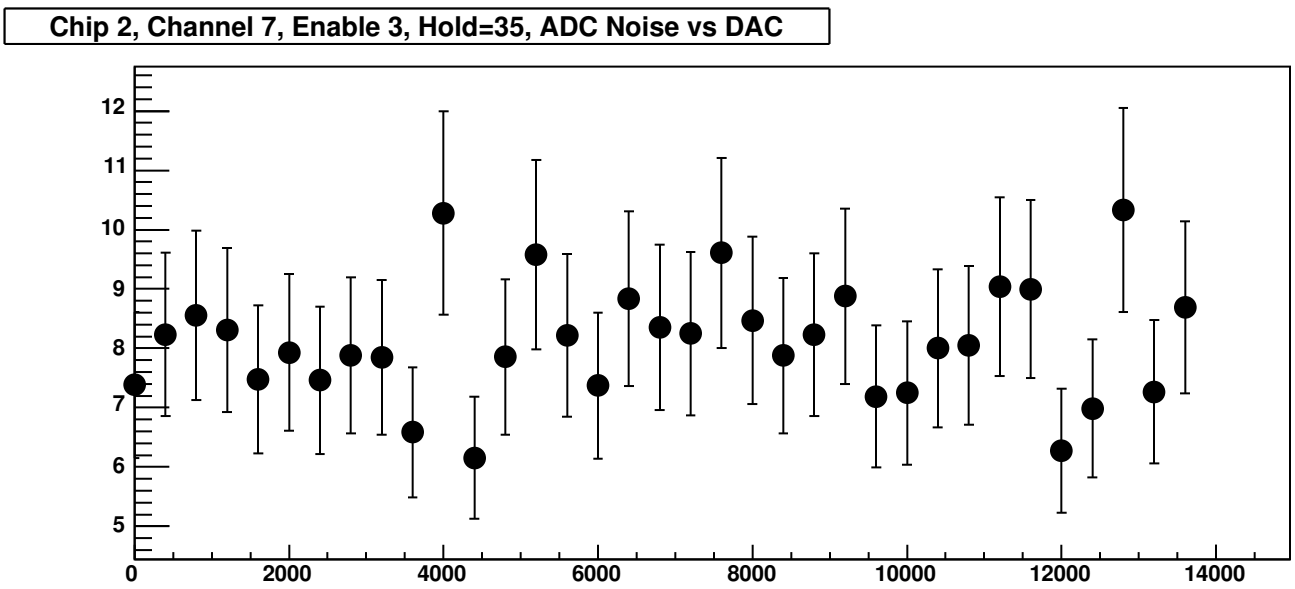

Chip 2, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

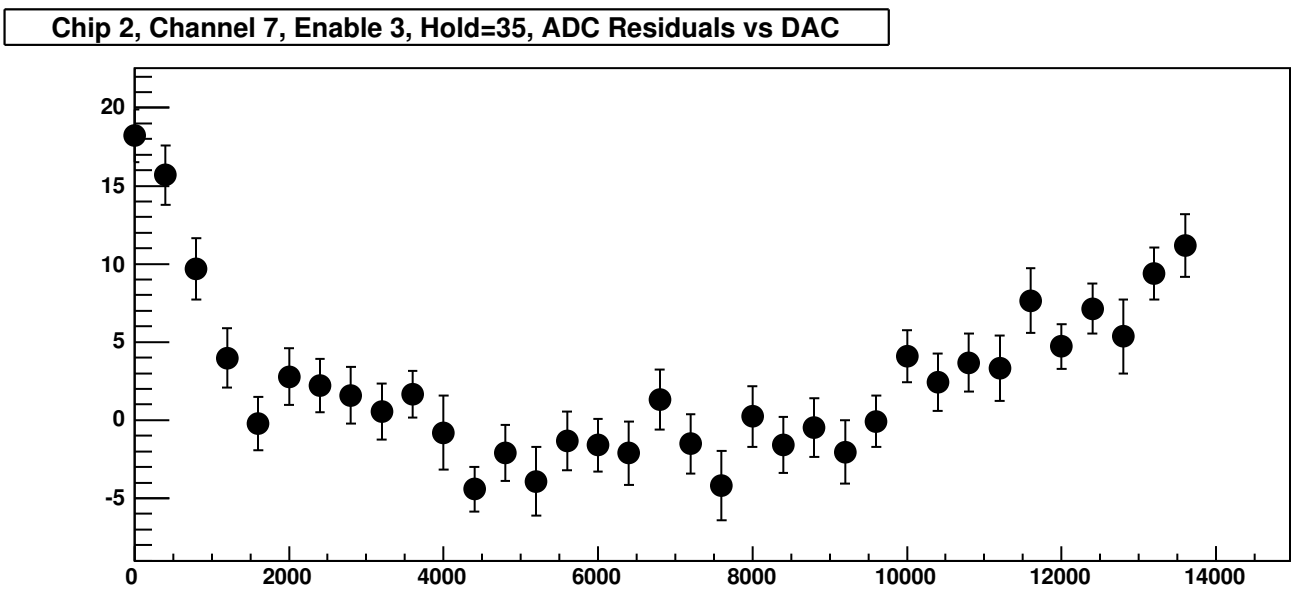

Chip 2, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 99.32 / 23
p0 35.55 ± 0.7587
p1 0.0396 ± 0.0001203
600
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

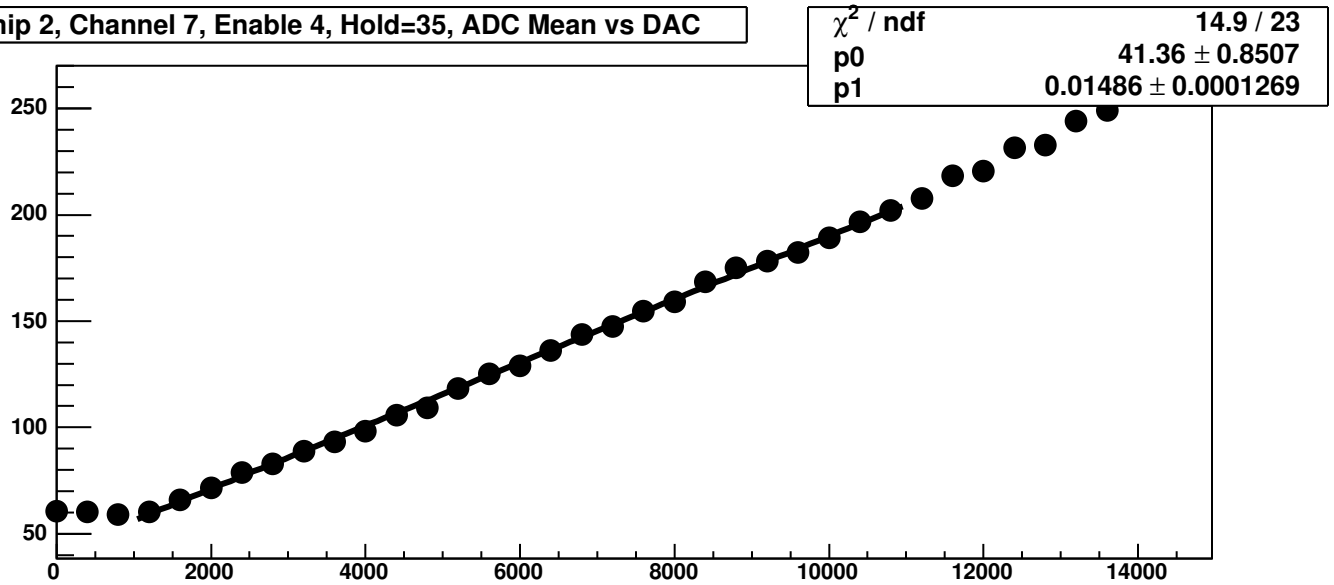

Chip 2, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 45.71 / 23
p0 38.9 ± 0.821
p1 0.01313 ± 0.0001245
240
220
200
180
160
140
120
100
80
60
40
0
2000
4000
6000
8000
10000
12000
14000

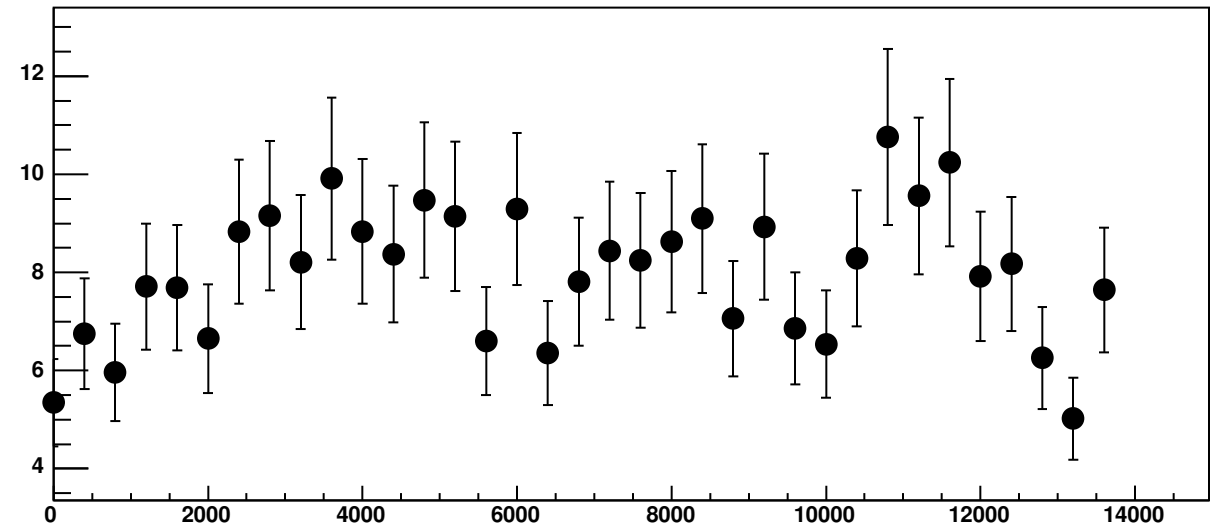

Chip 2, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

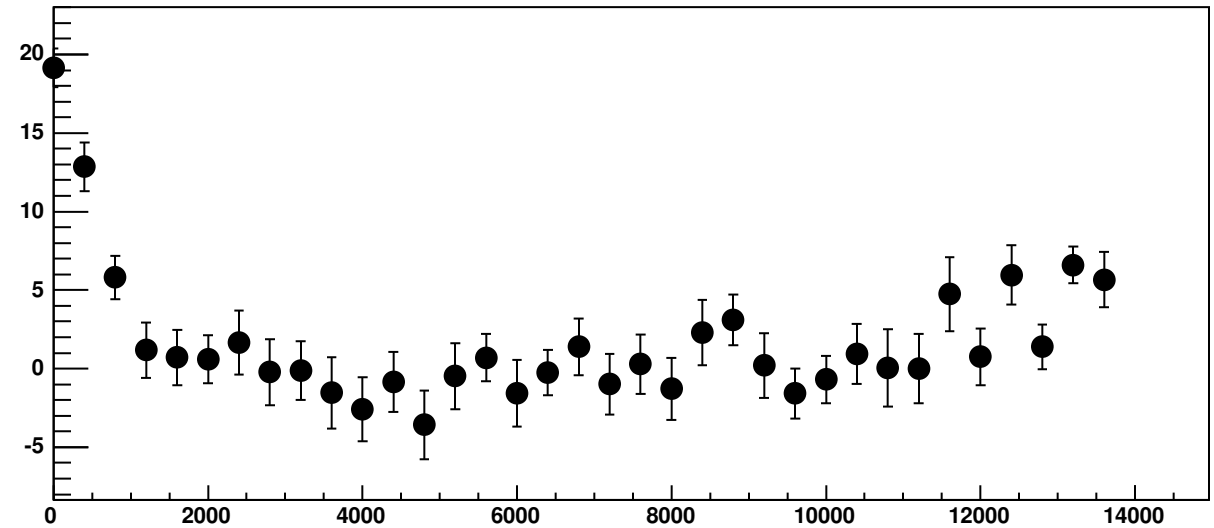
Chip 2, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC



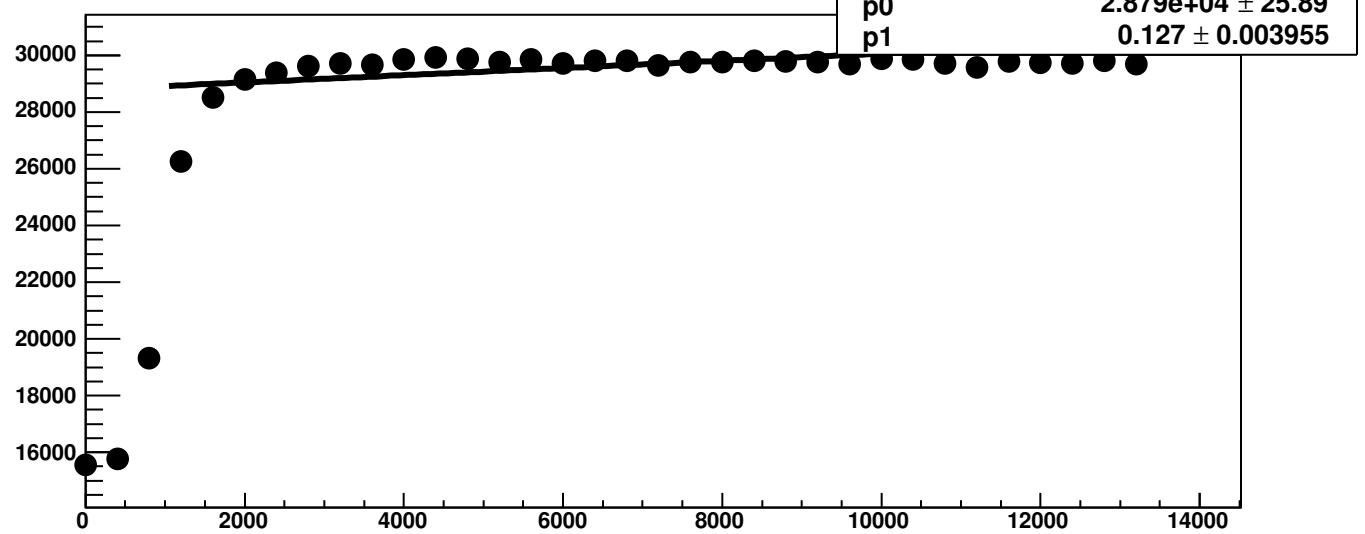
Chip 2, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC



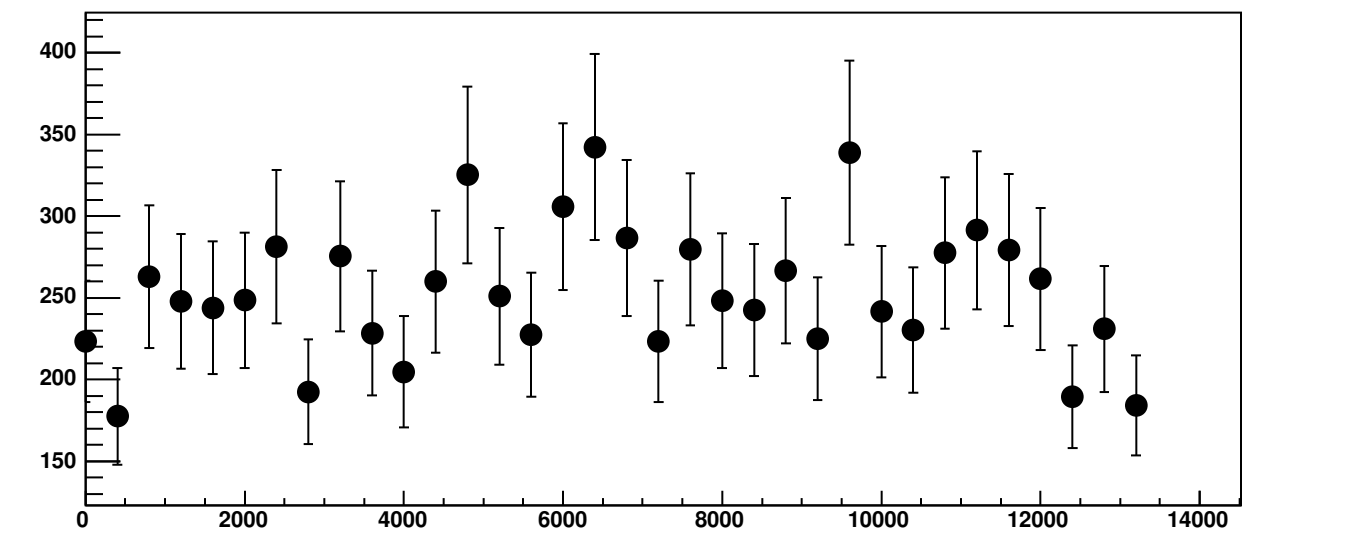
Chip 2, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC



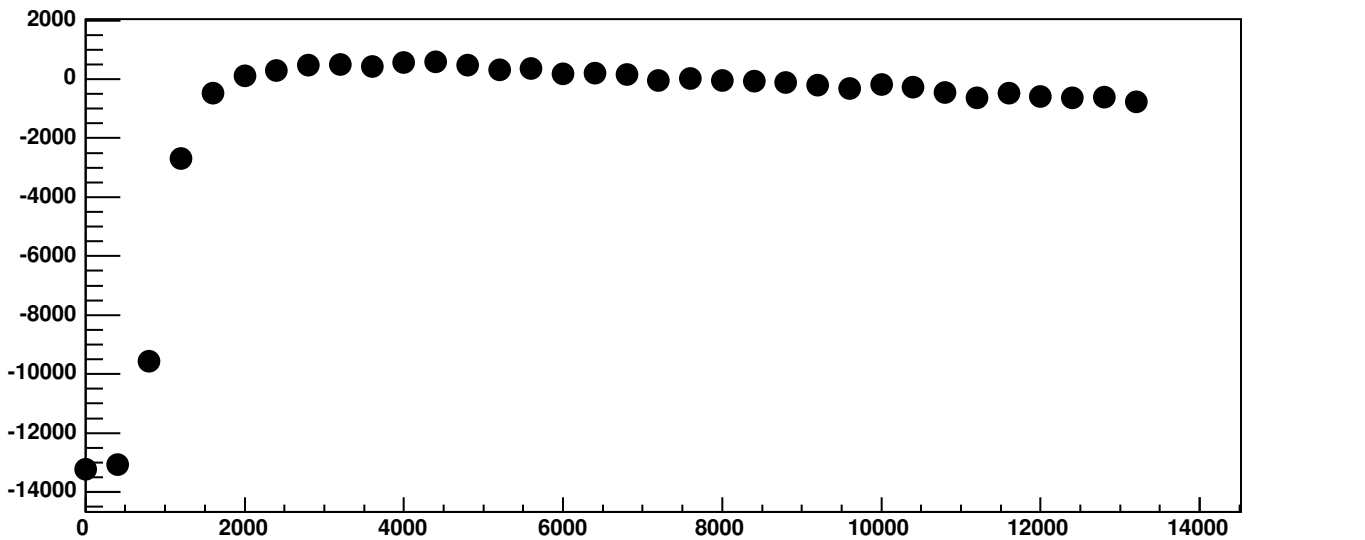
Chip 2, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC

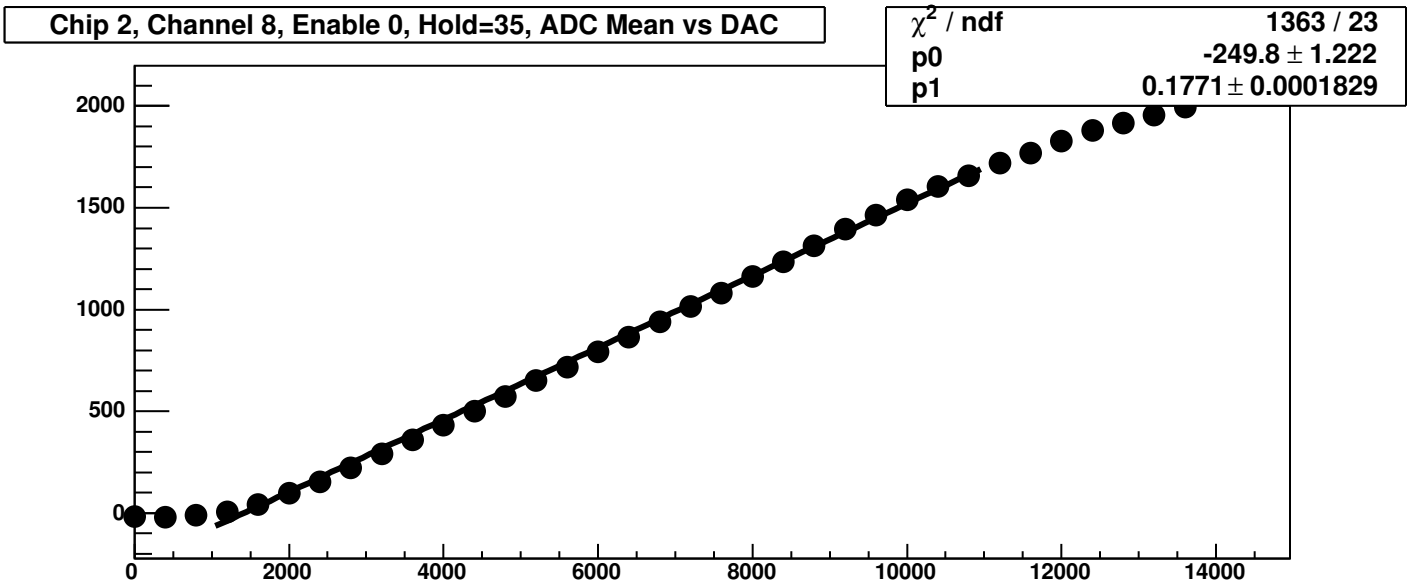


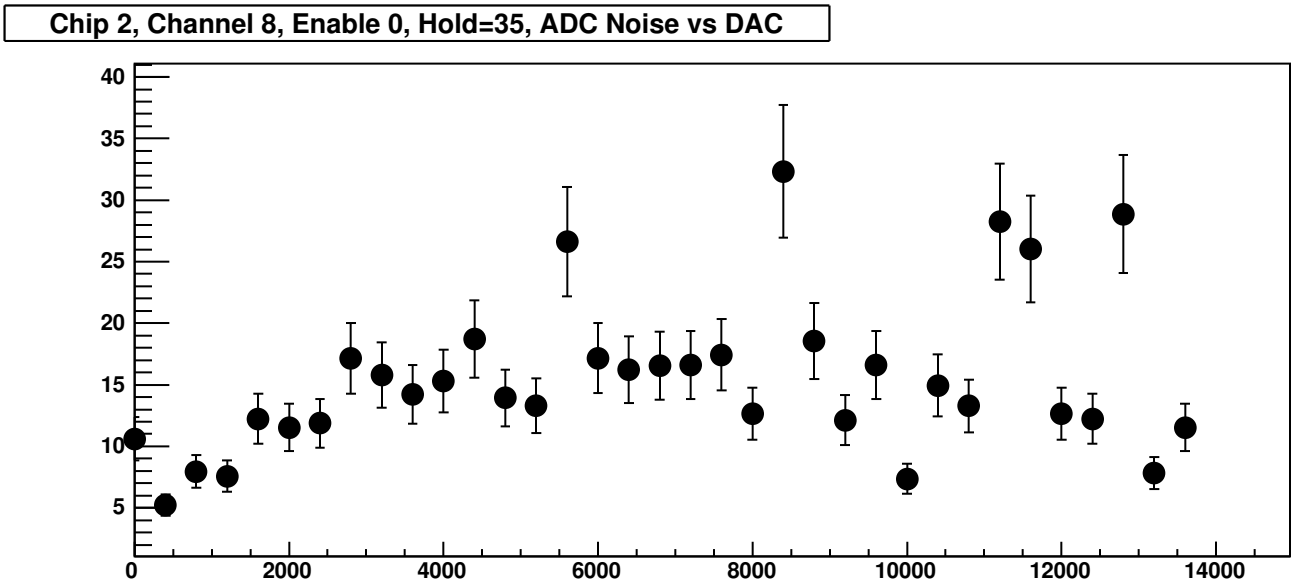
Chip 2, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC

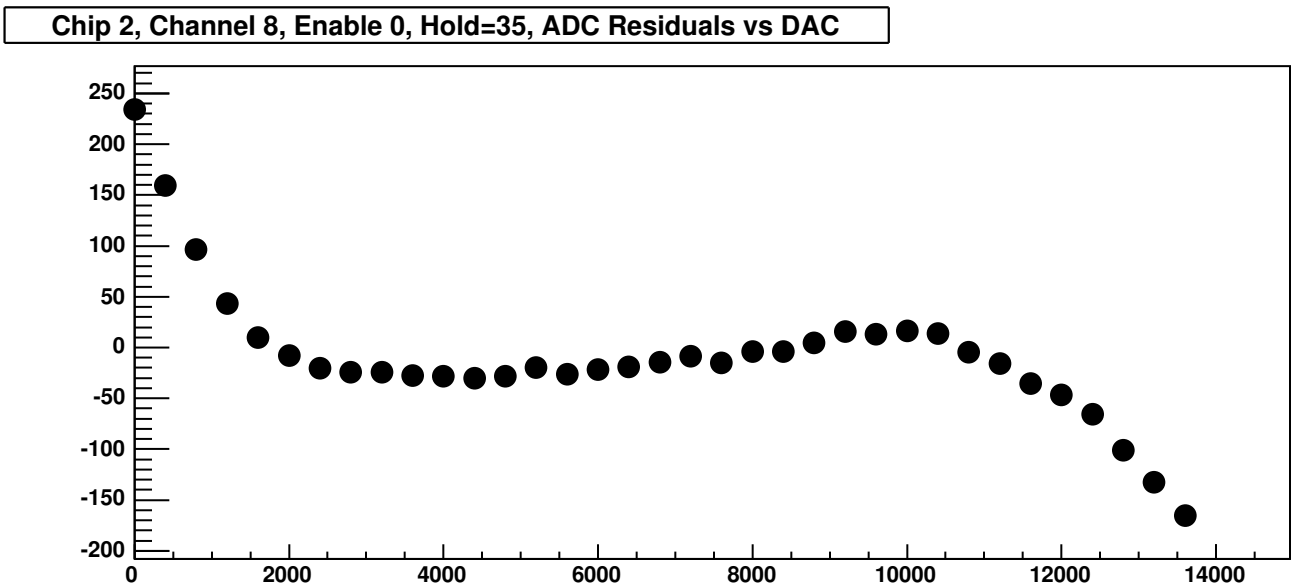


Chip 2, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC

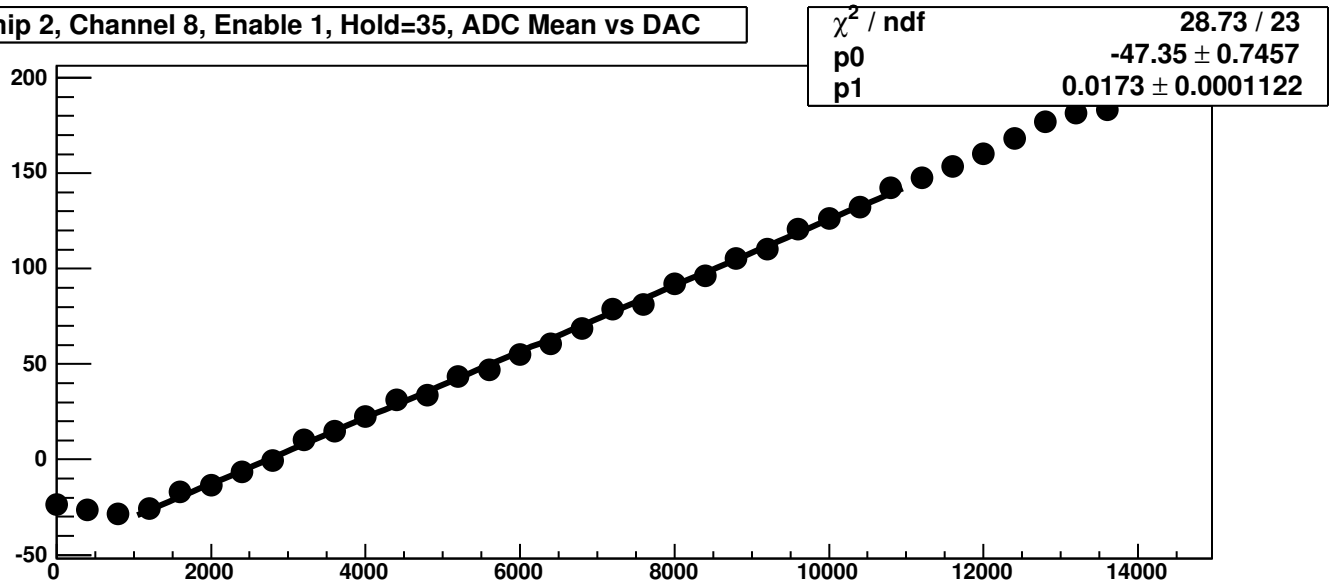



Chip 2, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1363 / 23
p0 -249.8 ± 1.222
p1 0.1771 ± 0.0001829
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

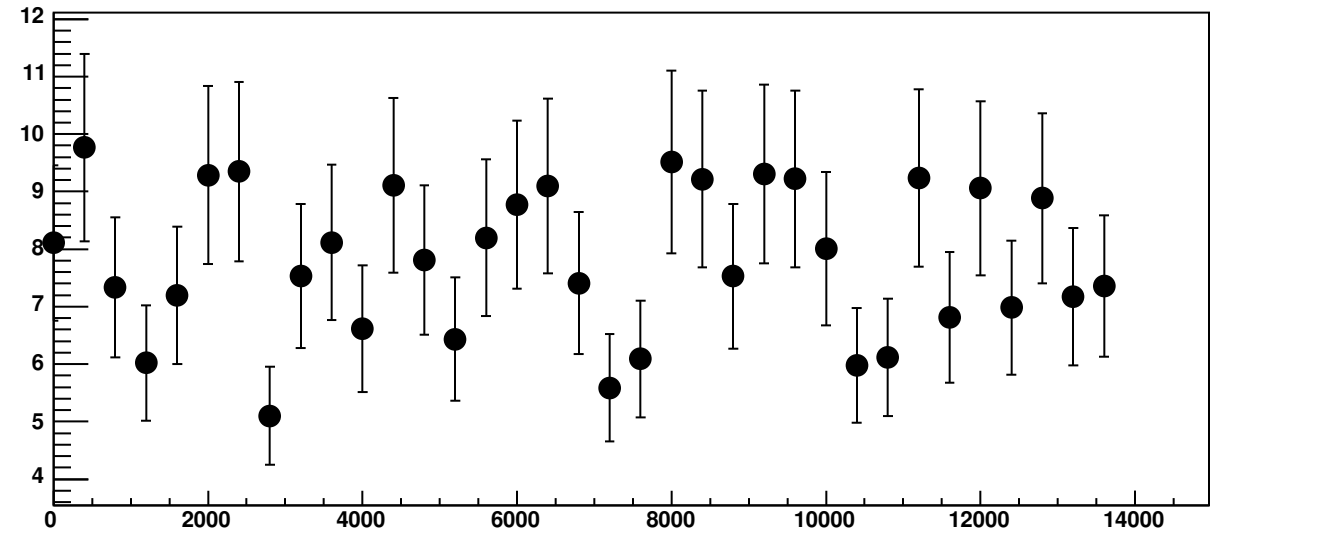

Chip 2, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

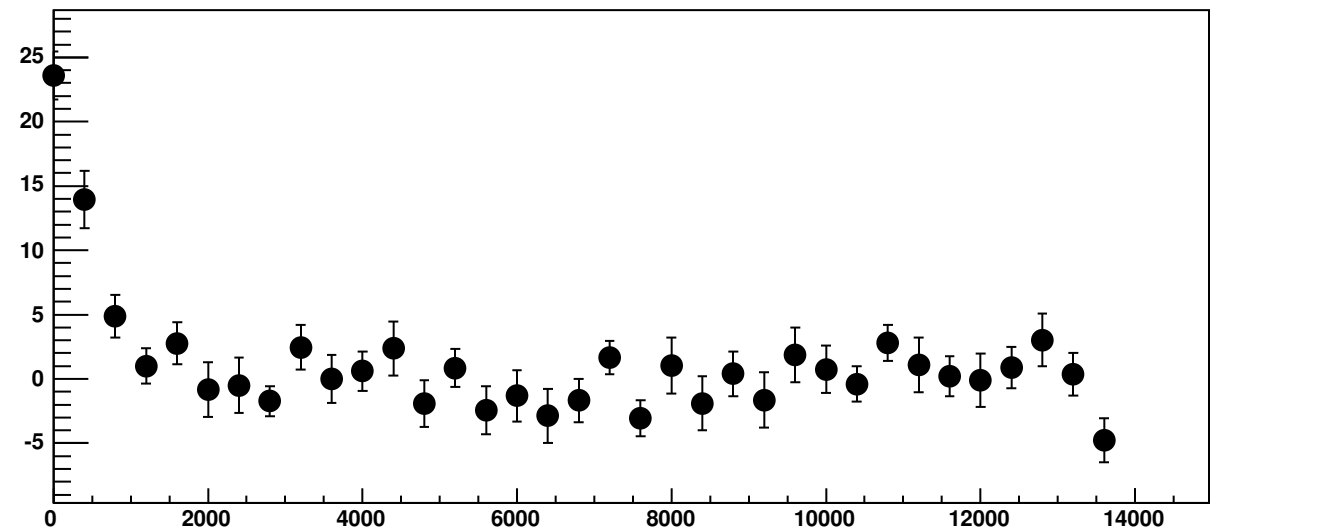
Chip 2, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

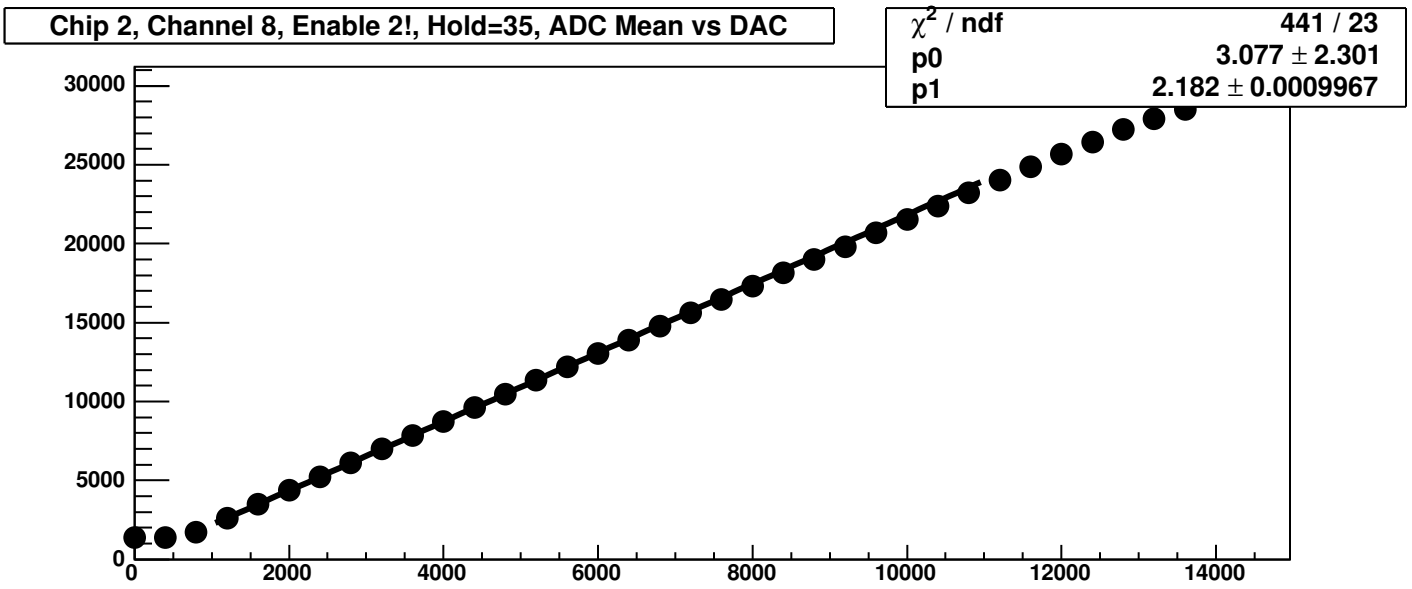


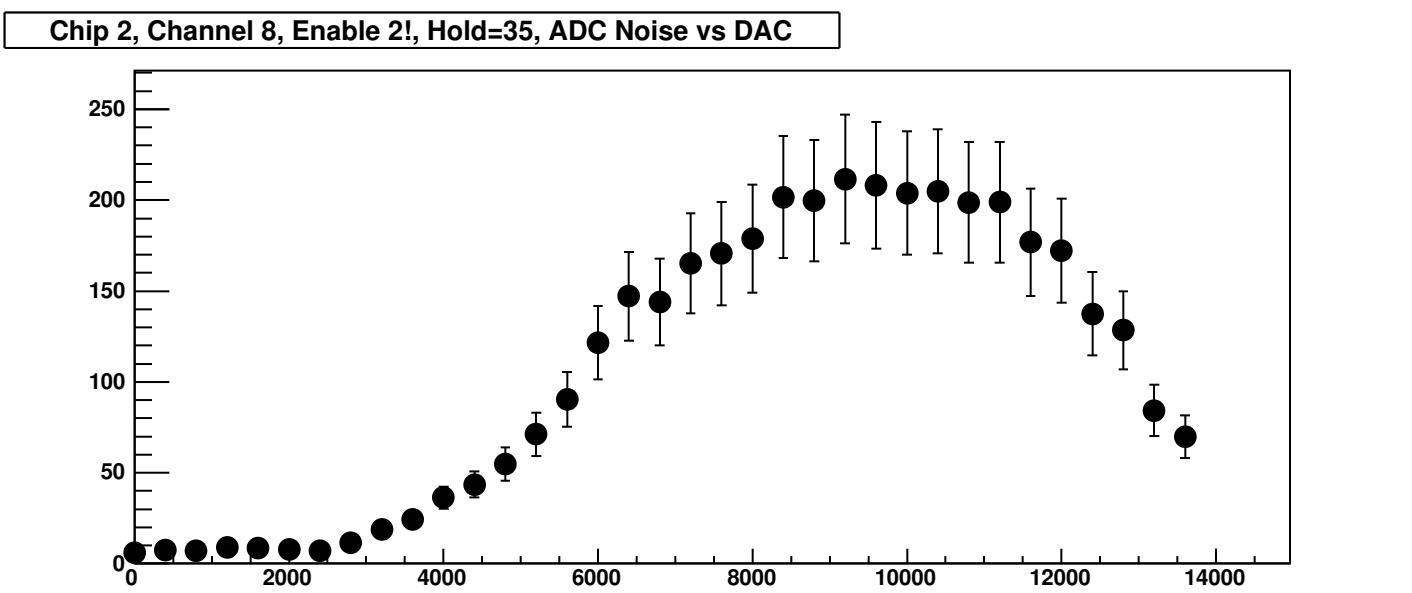
Chip 2, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC

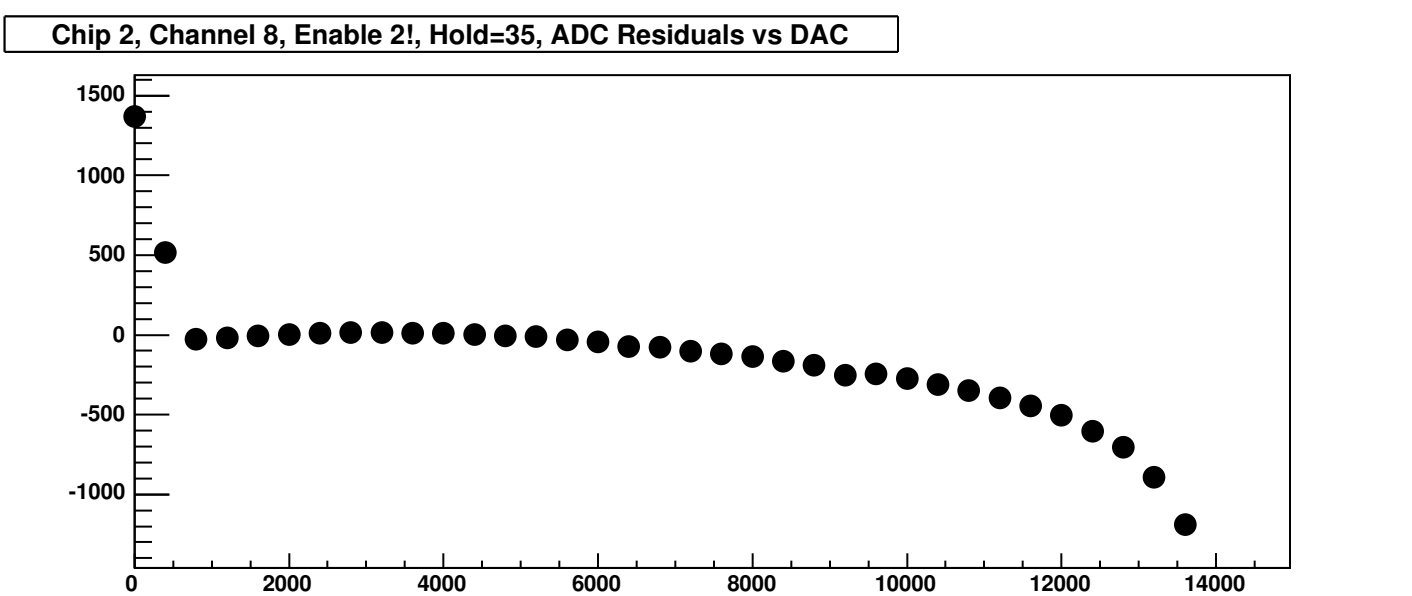


Chip 2, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

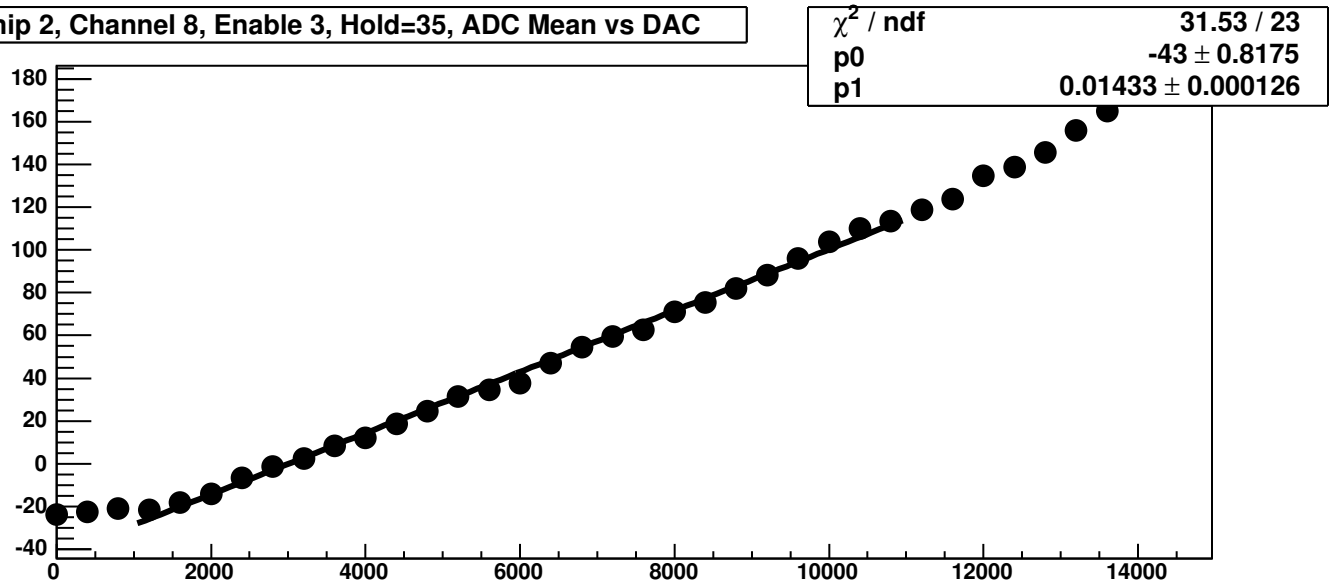



Chip 2, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 441 / 23
p0 3.077 ± 2.301
p1 2.182 ± 0.0009967
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

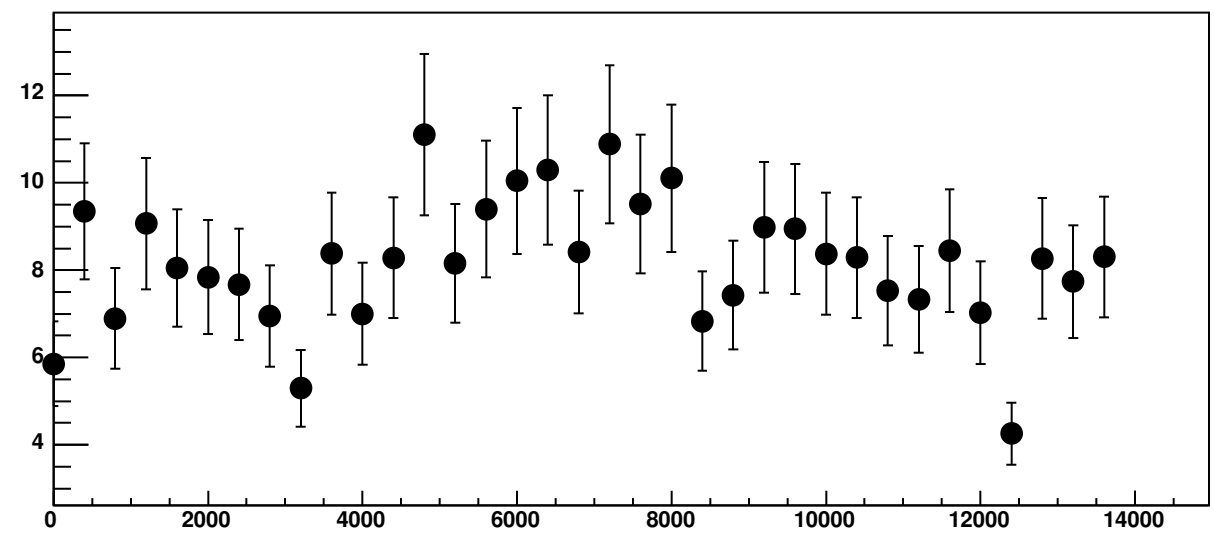

Chip 2, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

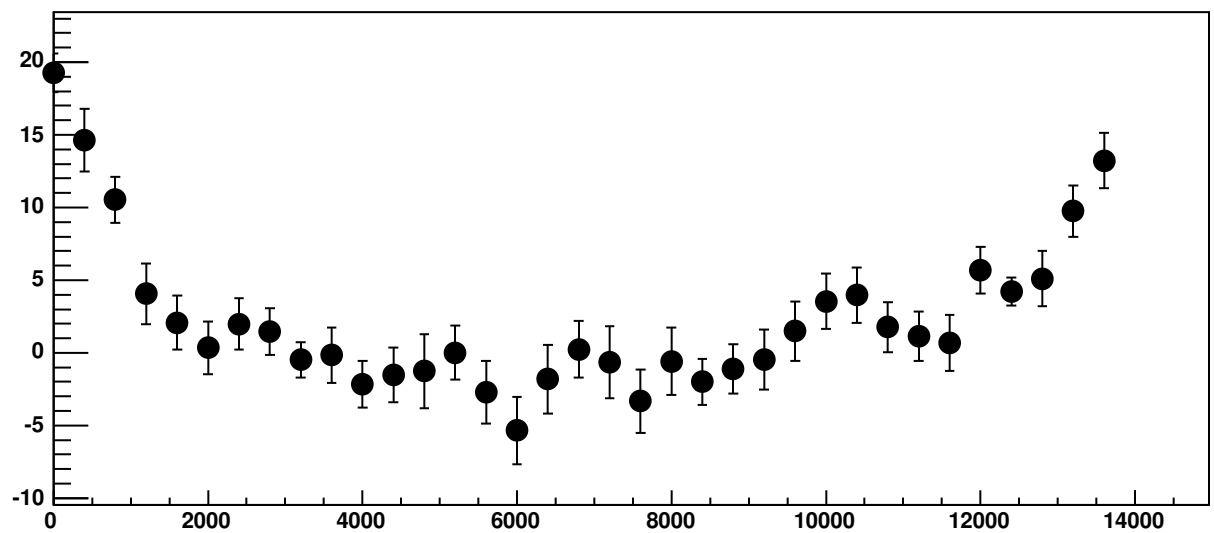
Chip 2, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

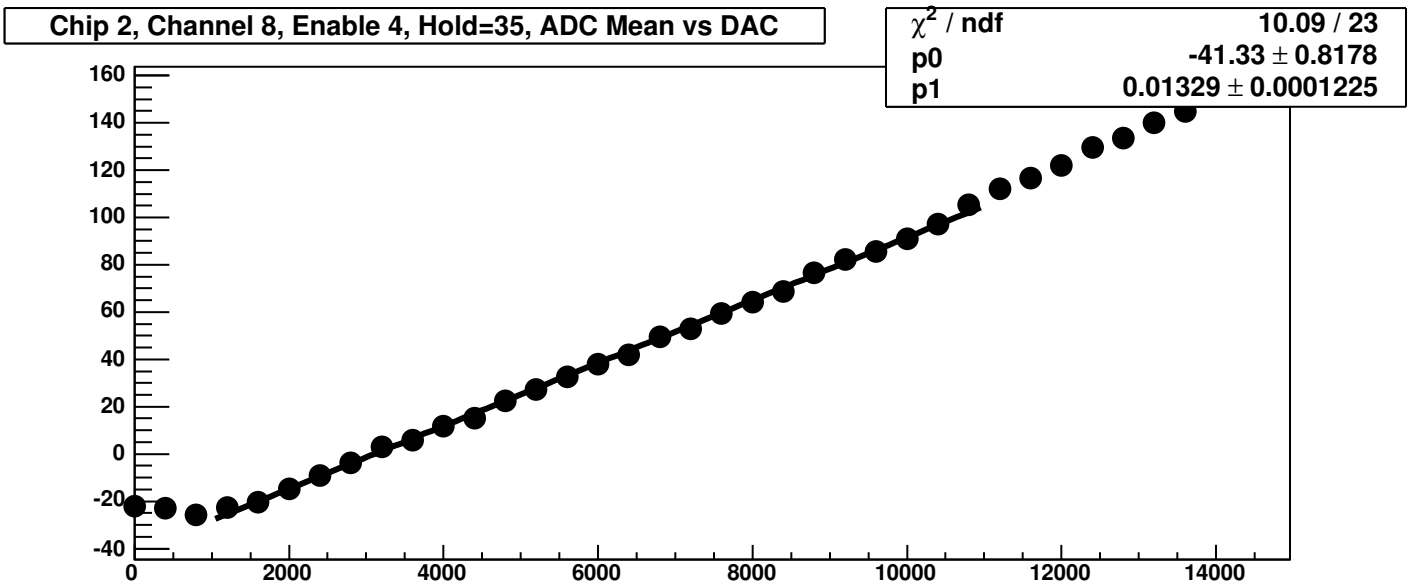


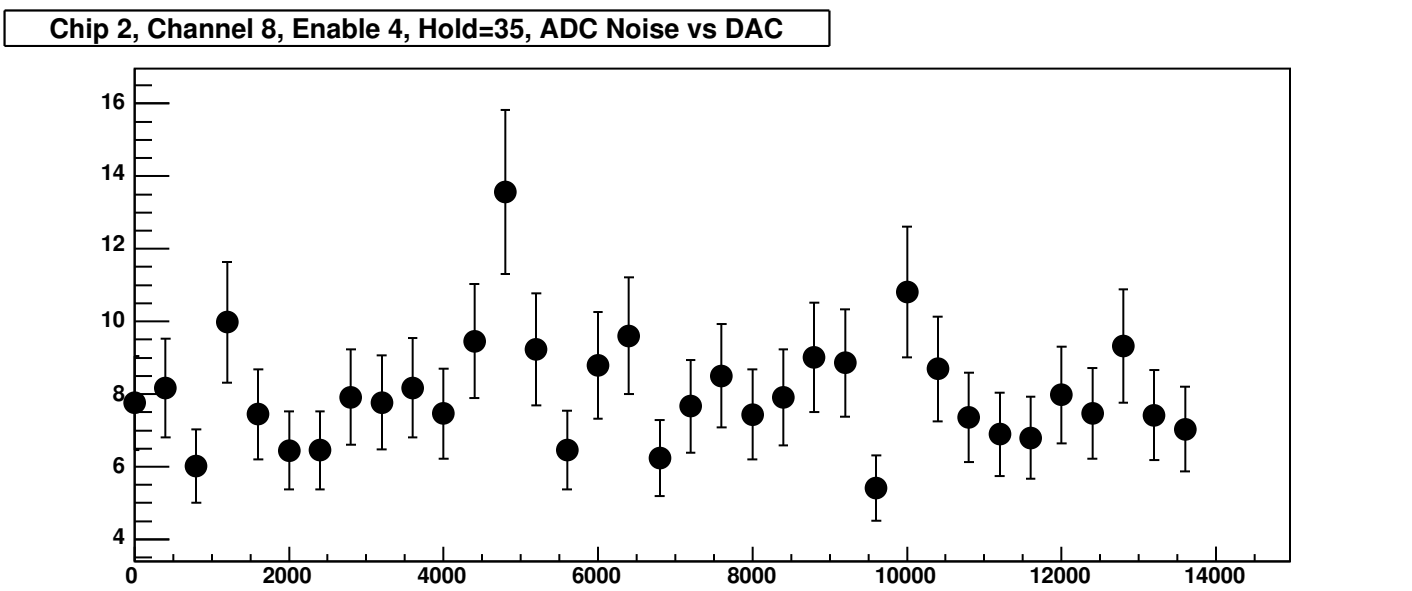
Chip 2, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC

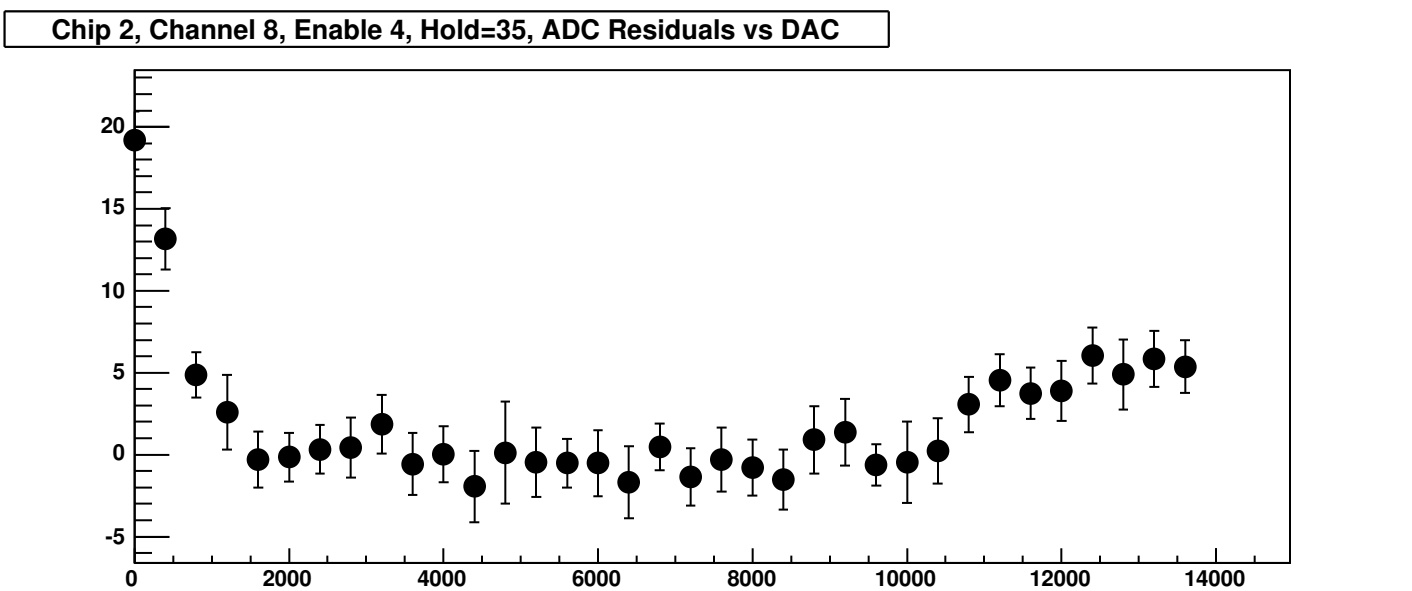


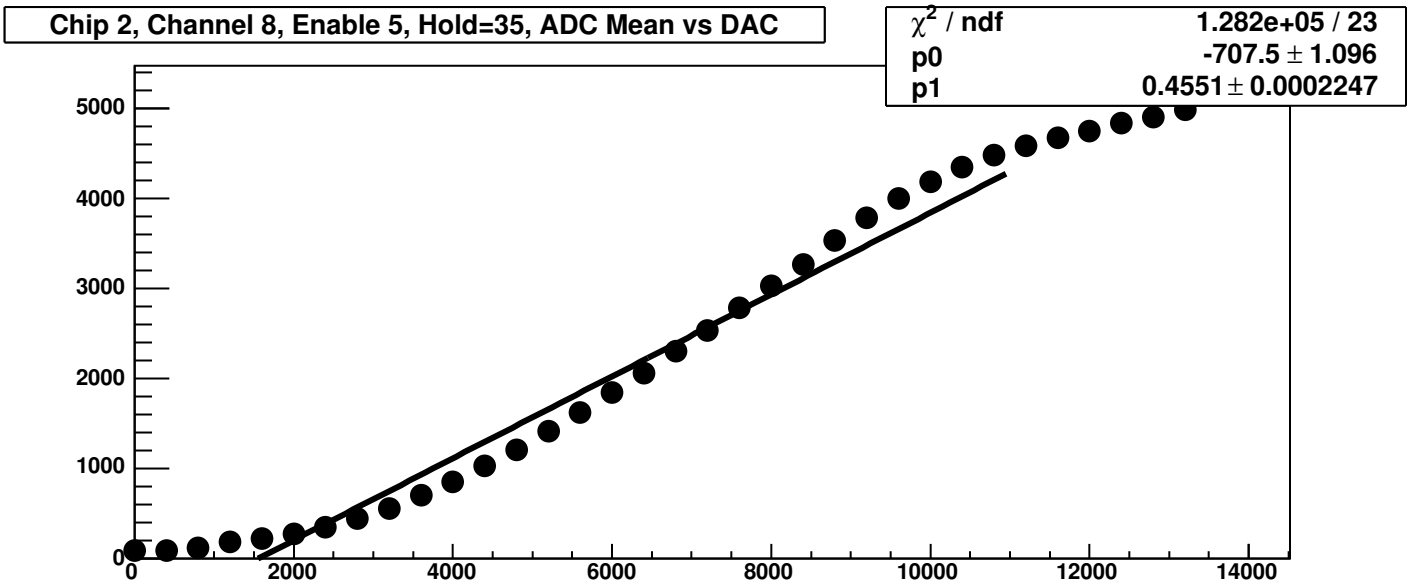
Chip 2, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

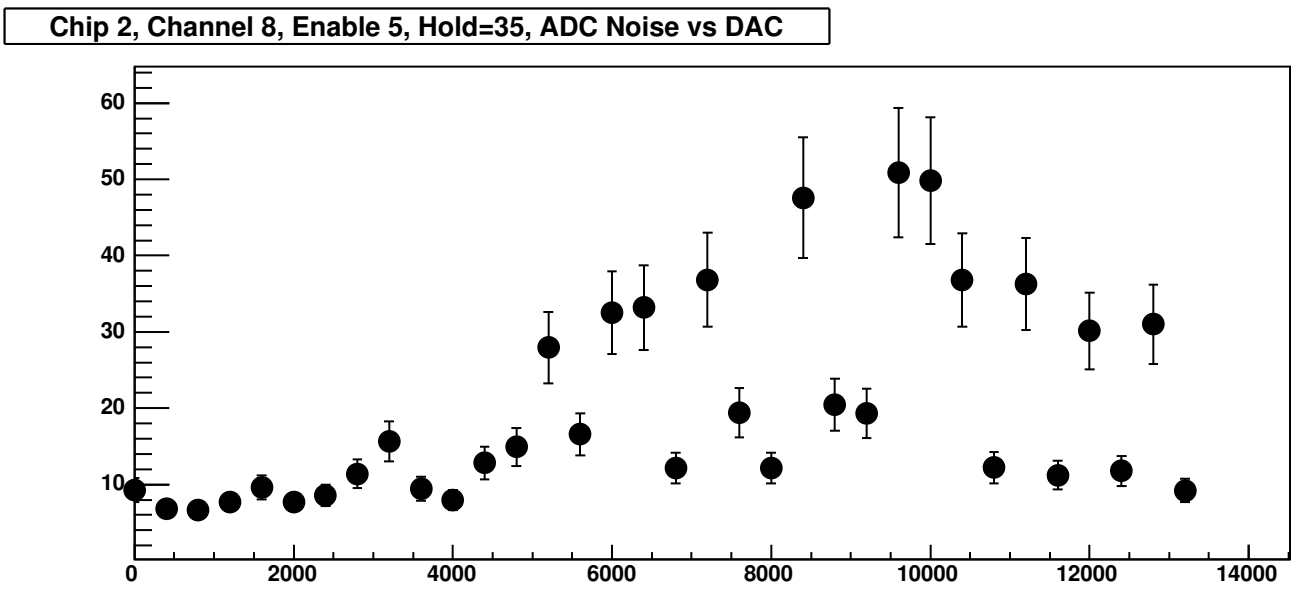


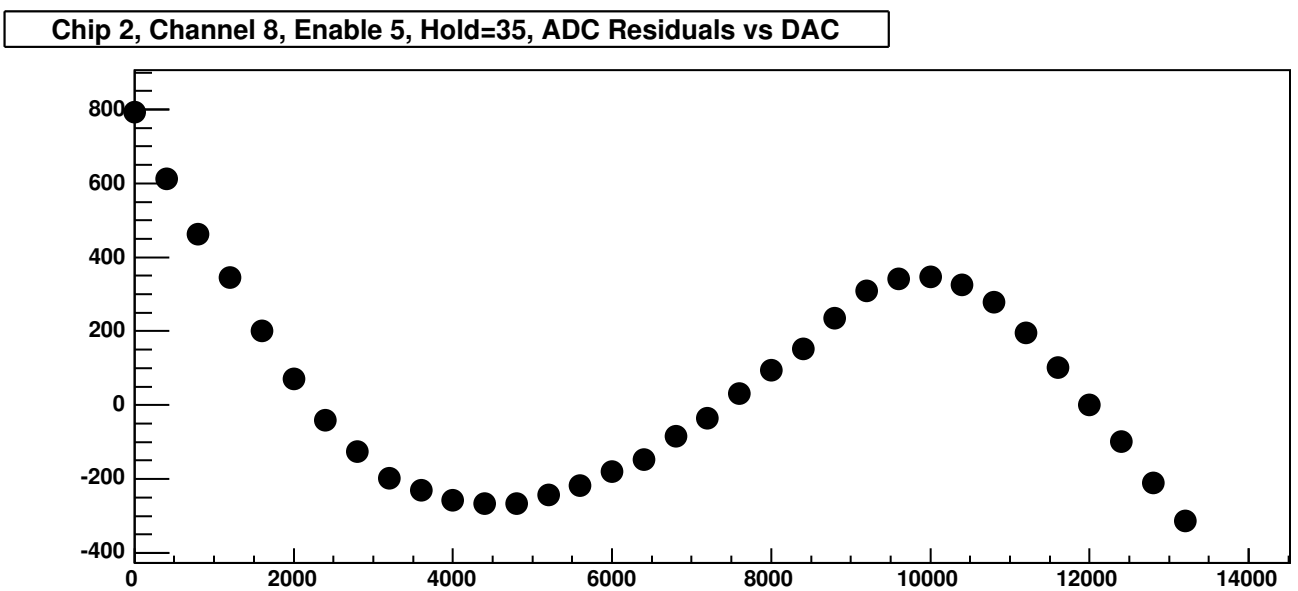

Chip 2, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 10.09 / 23
p0 -41.33 ± 0.8178
p1 0.01329 ± 0.0001225
160
140
120
100
80
60
40
20
0
-20
-40
0
2000
4000
6000
8000
10000
12000
14000

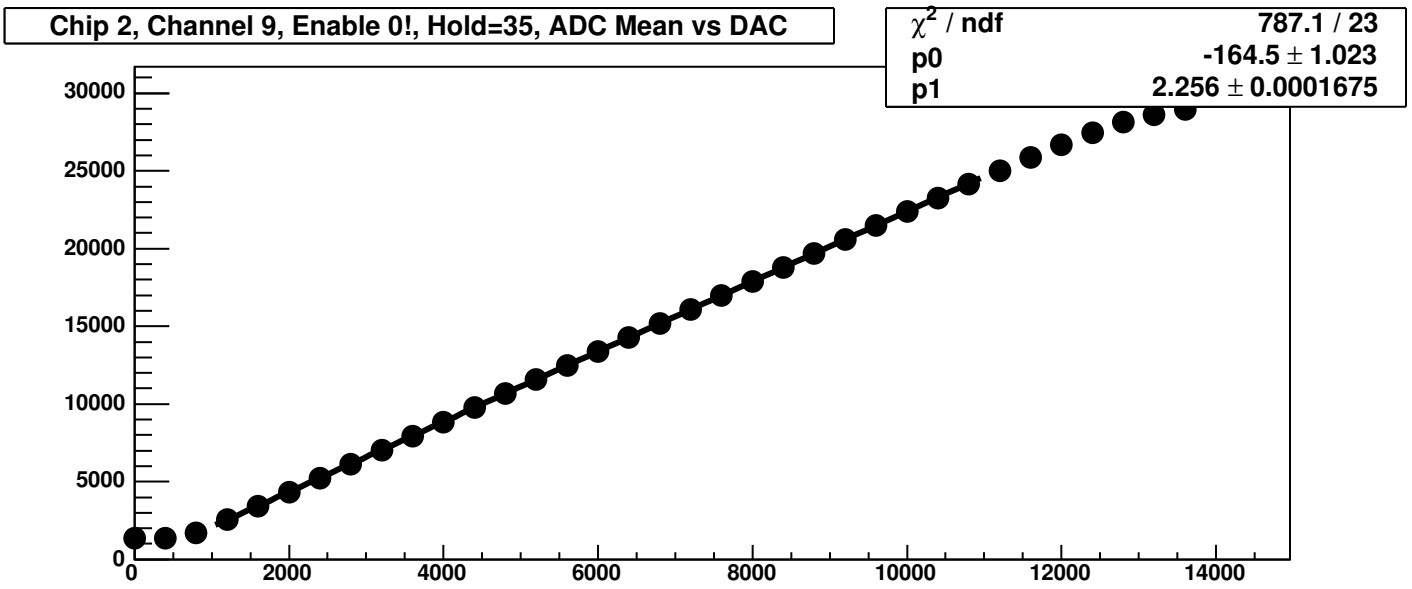

Chip 2, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

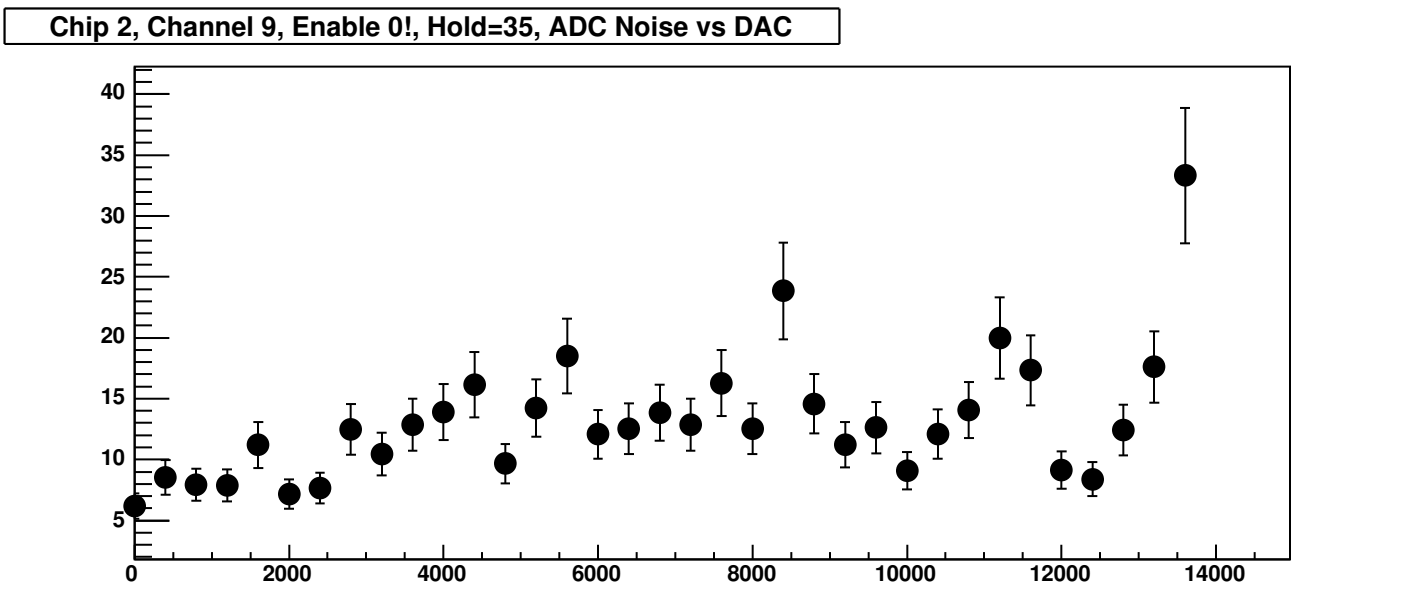

Chip 2, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

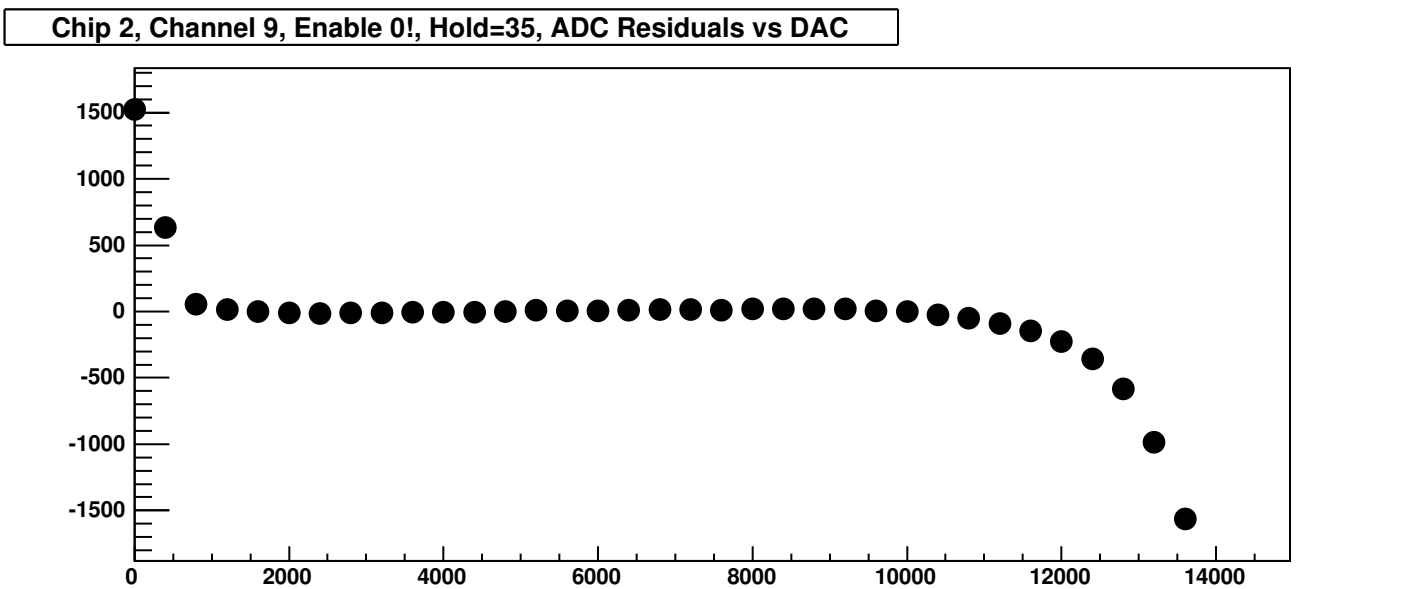

Chip 2, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.282e+05 / 23
p0 -707.5 ± 1.096
p1 0.4551 ± 0.0002247
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

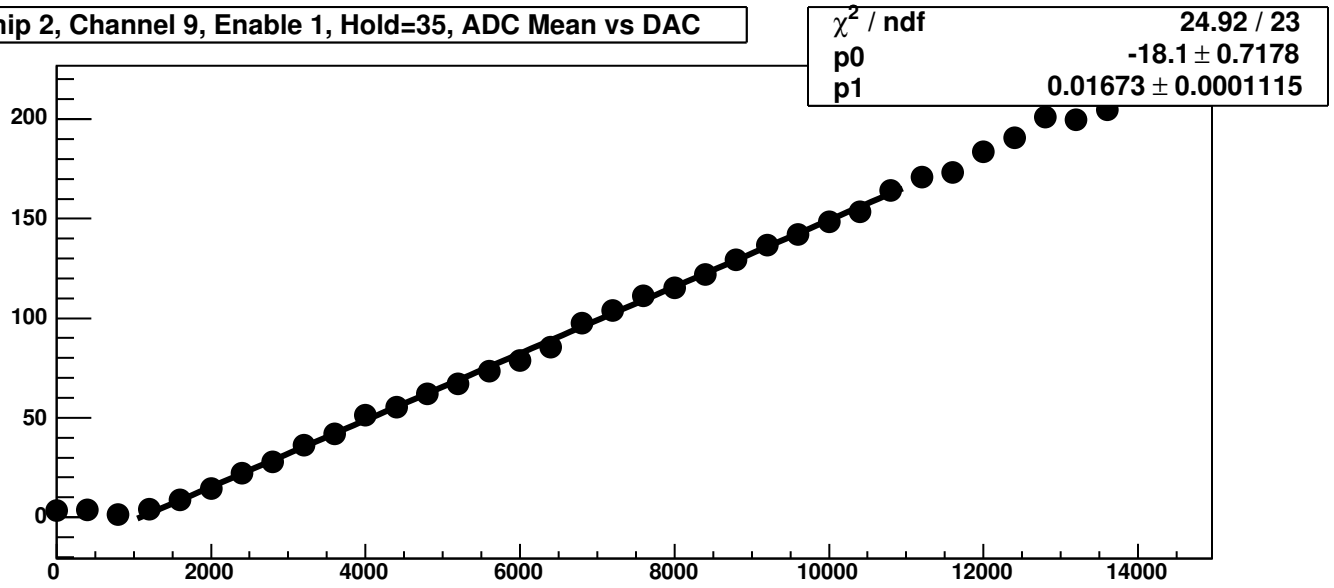

Chip 2, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 787.1 / 23
p0 -164.5 ± 1.023
p1 2.256 ± 0.0001675
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

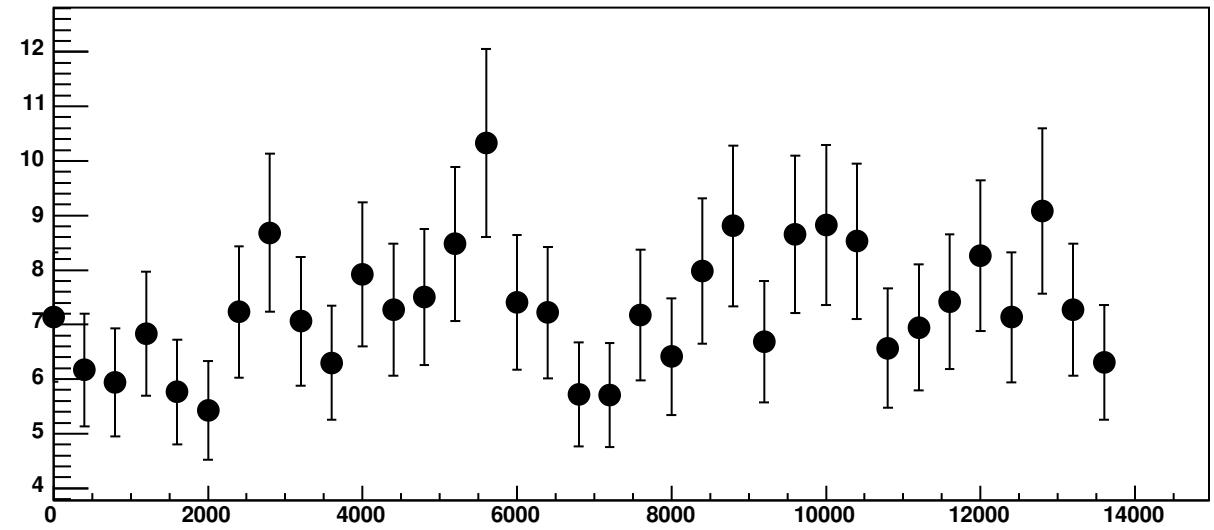

Chip 2, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

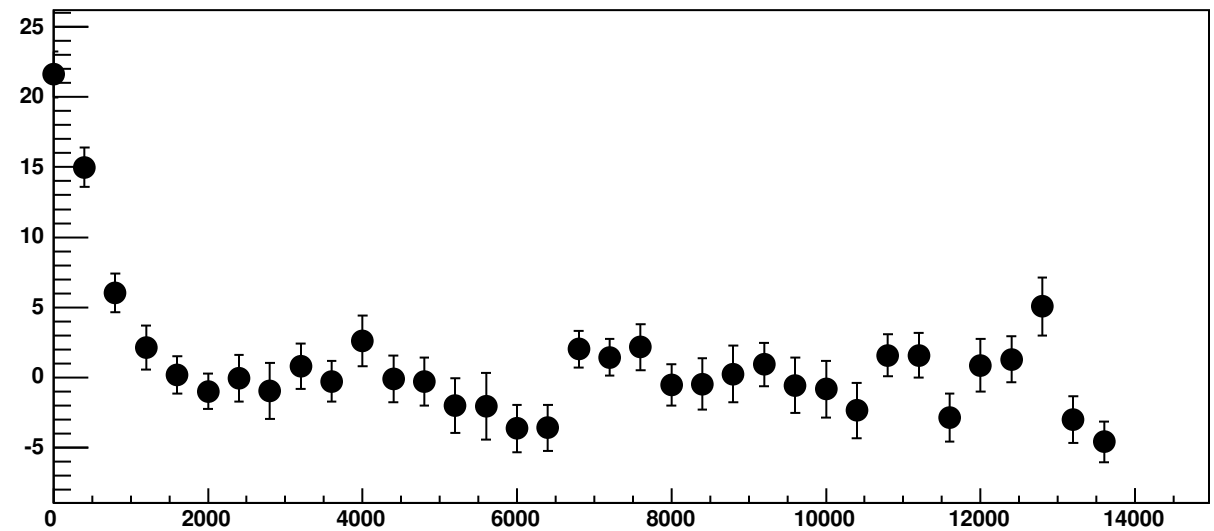
Chip 2, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



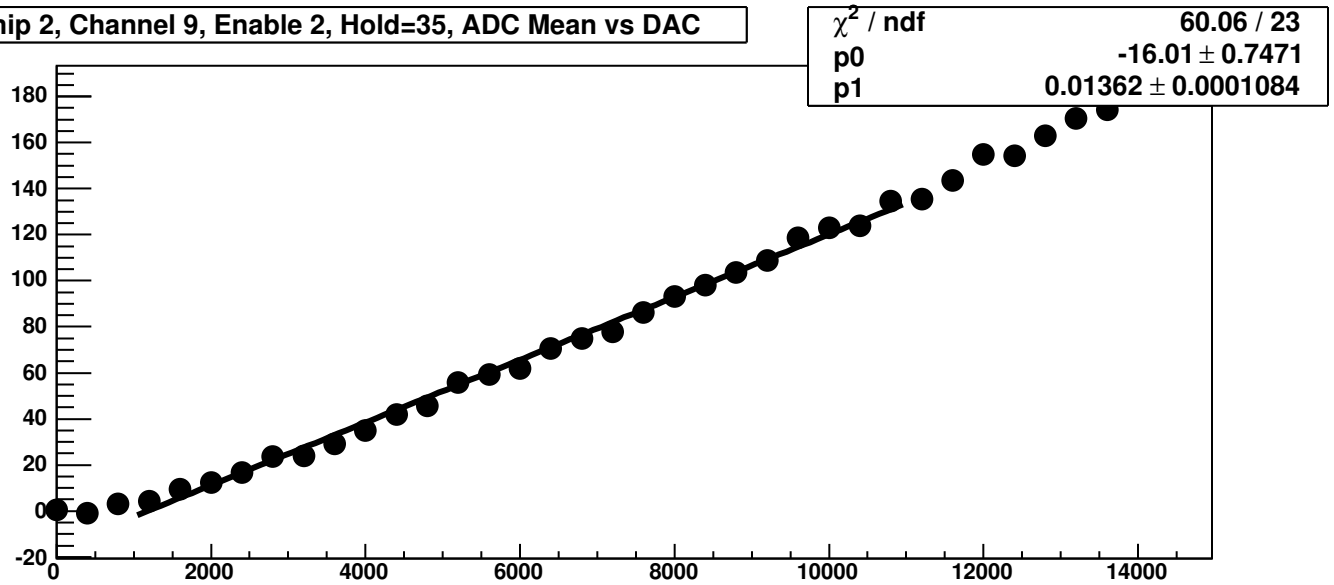
Chip 2, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



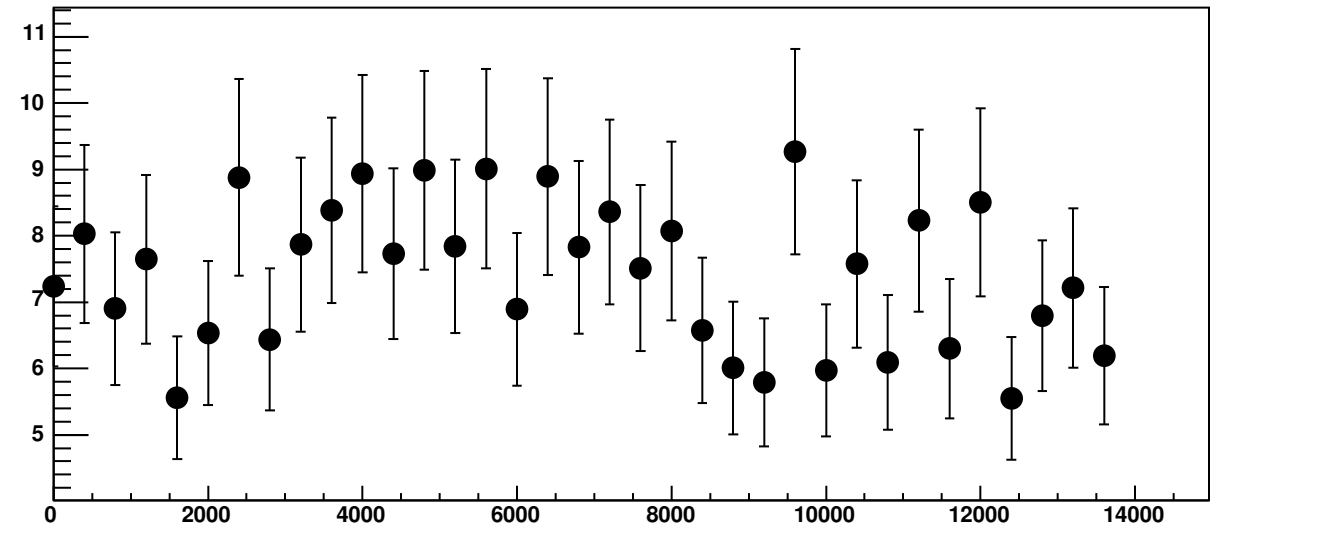
Chip 2, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



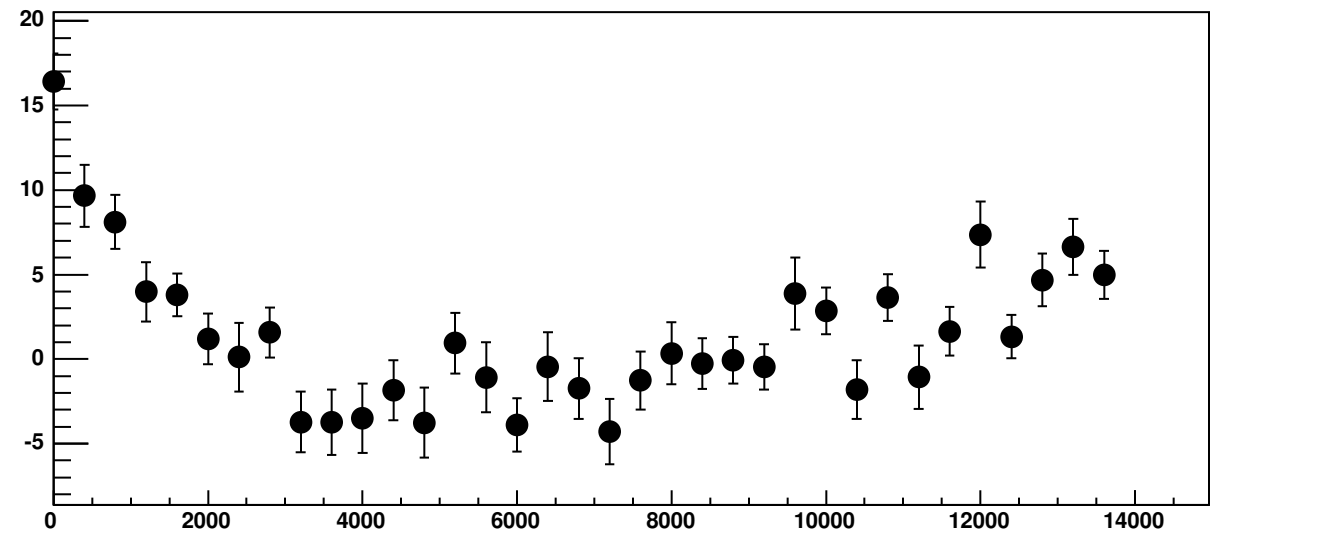
Chip 2, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

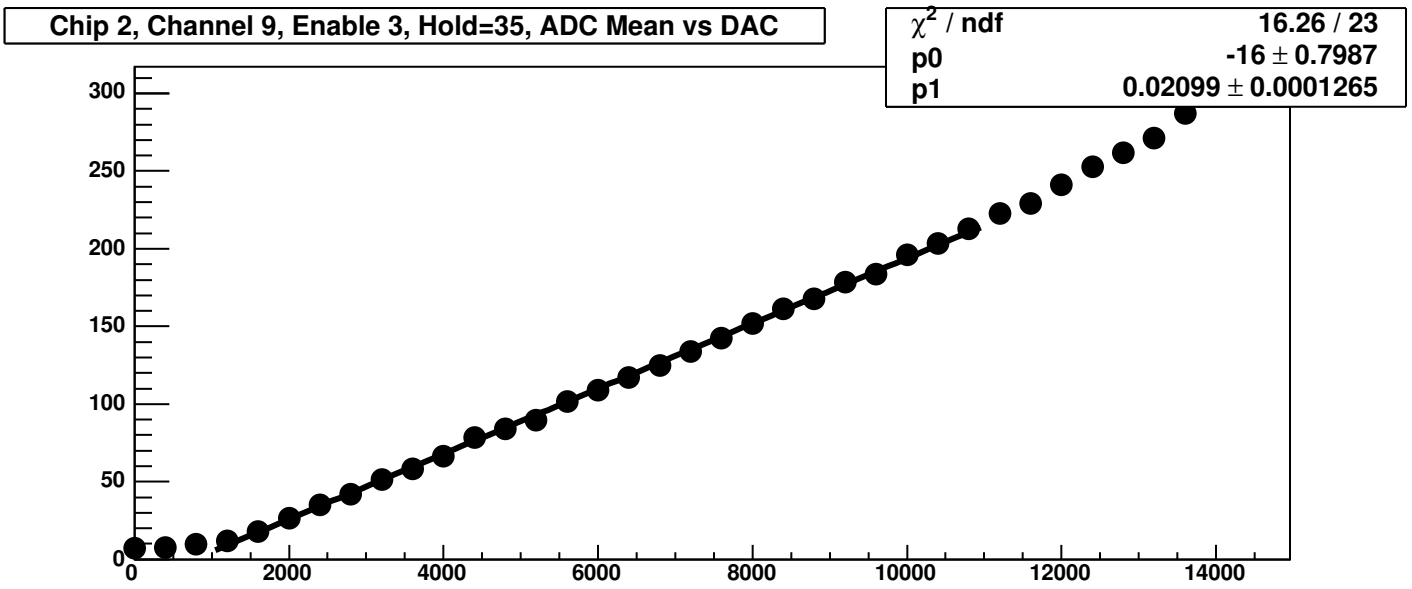


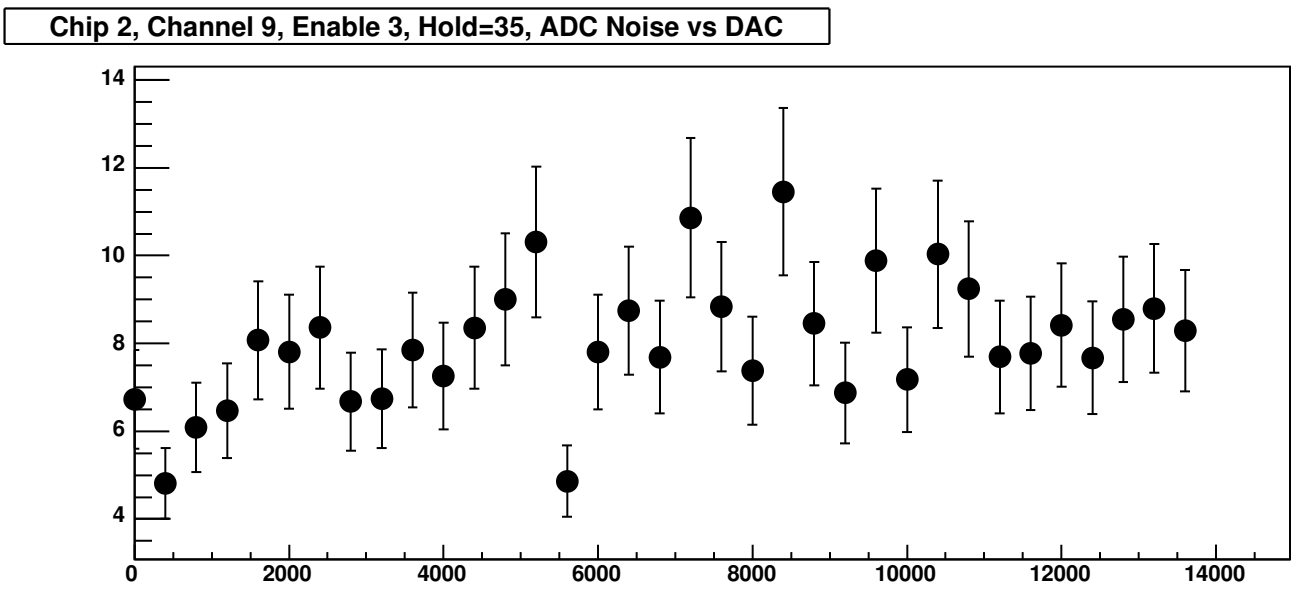
Chip 2, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC

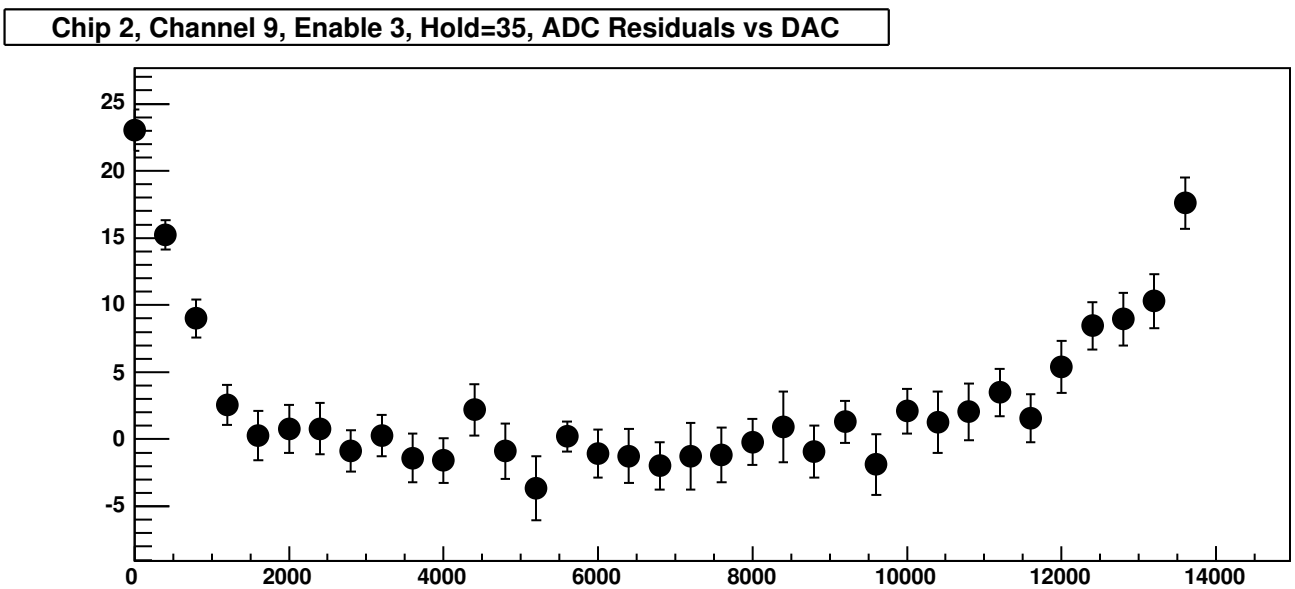


Chip 2, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

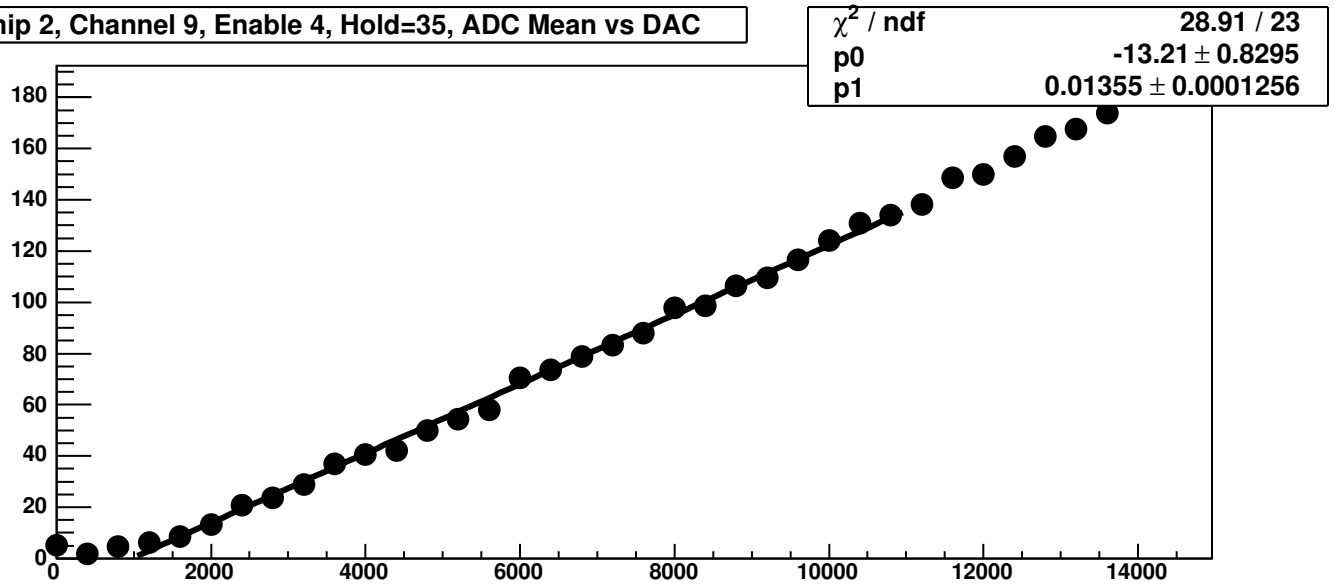



Chip 2, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 16.26 / 23
p0 -16 ± 0.7987
p1 0.02099 ± 0.0001265
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

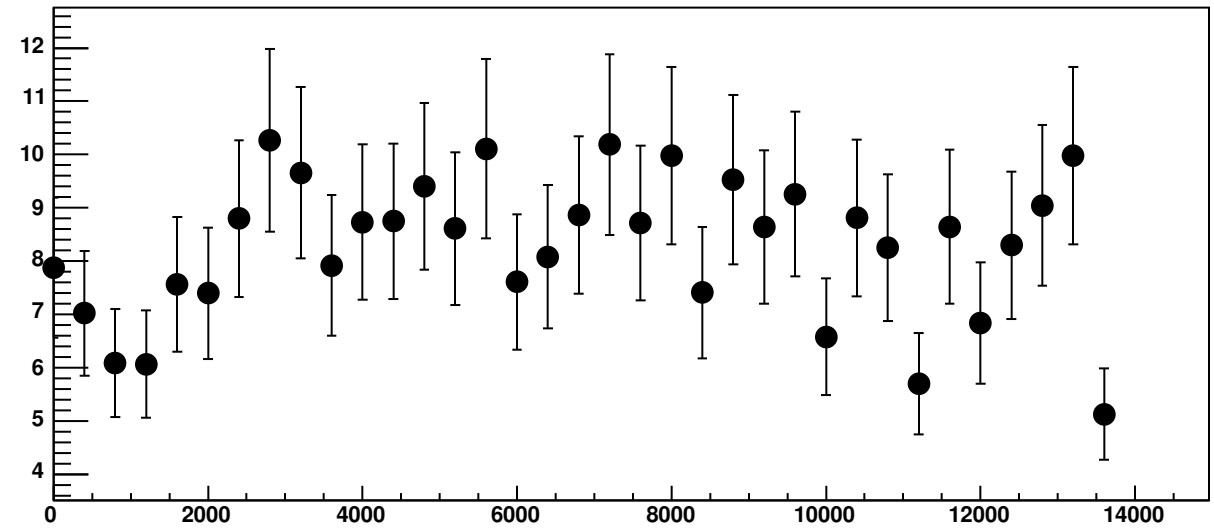

Chip 2, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

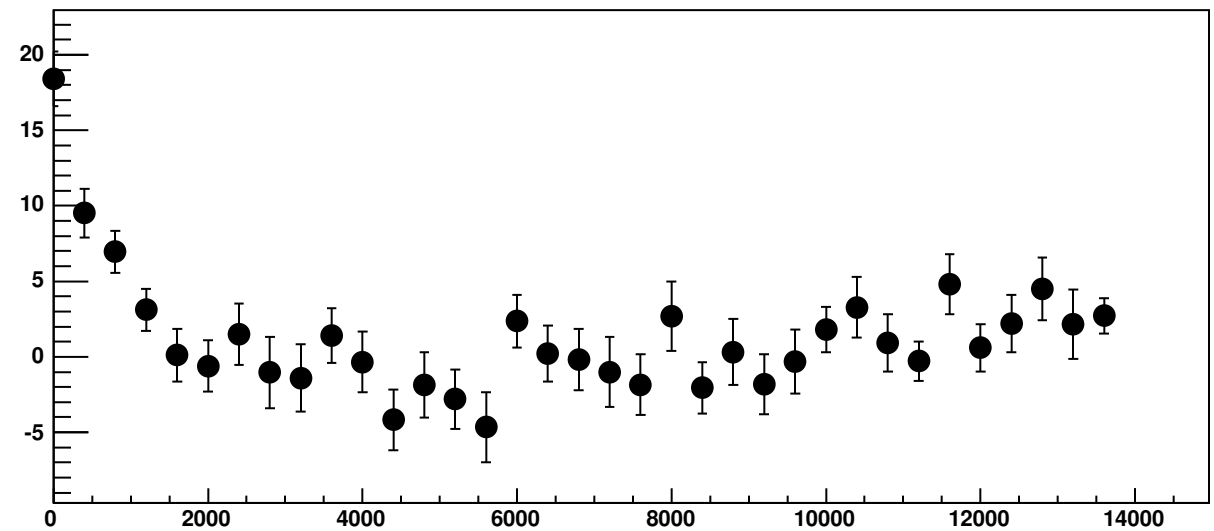
Chip 2, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



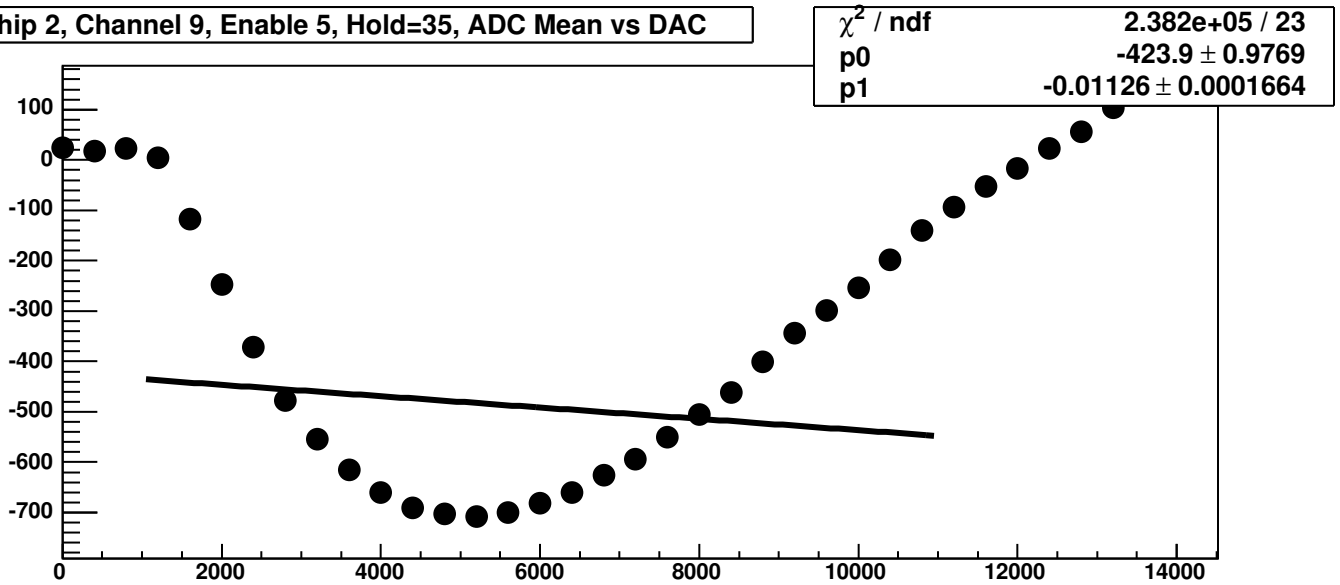
Chip 2, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



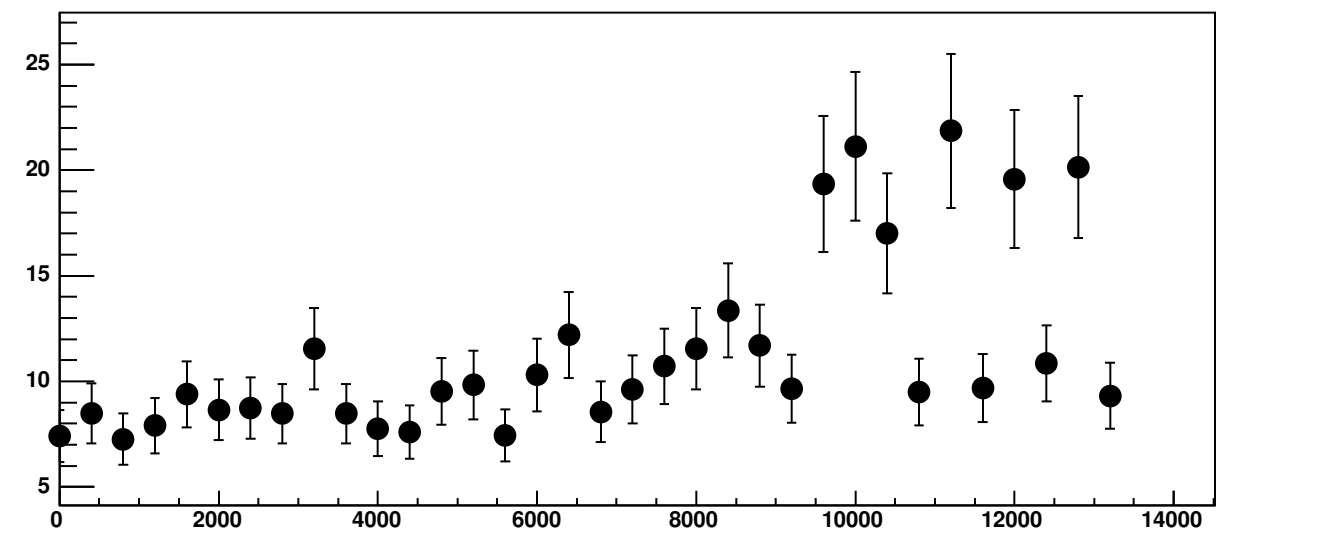
Chip 2, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



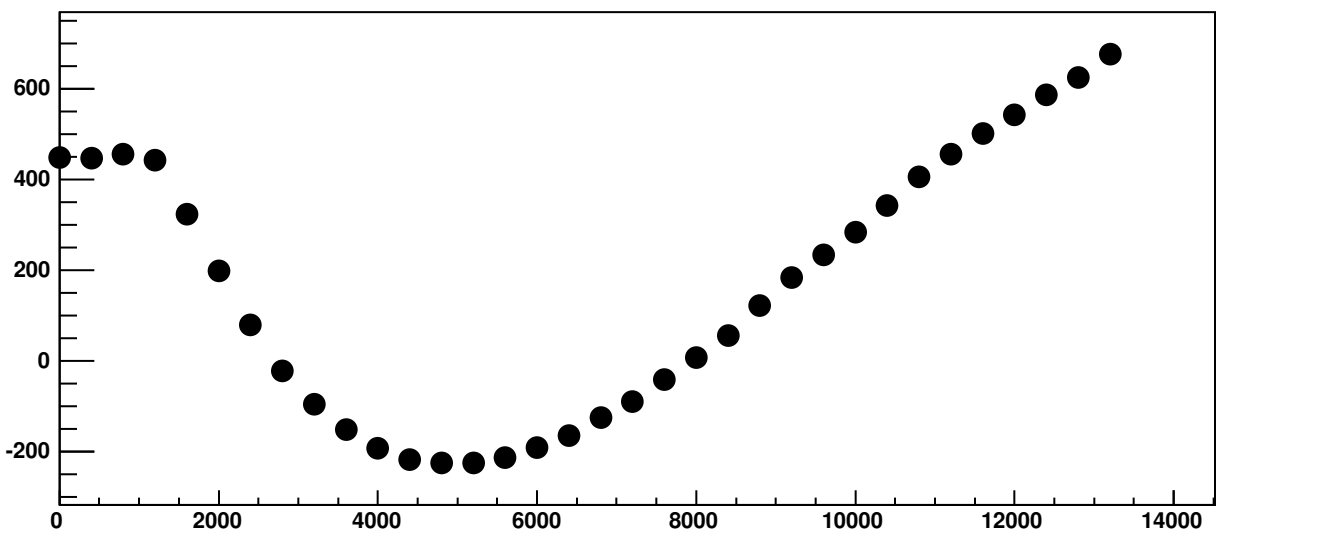
Chip 2, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

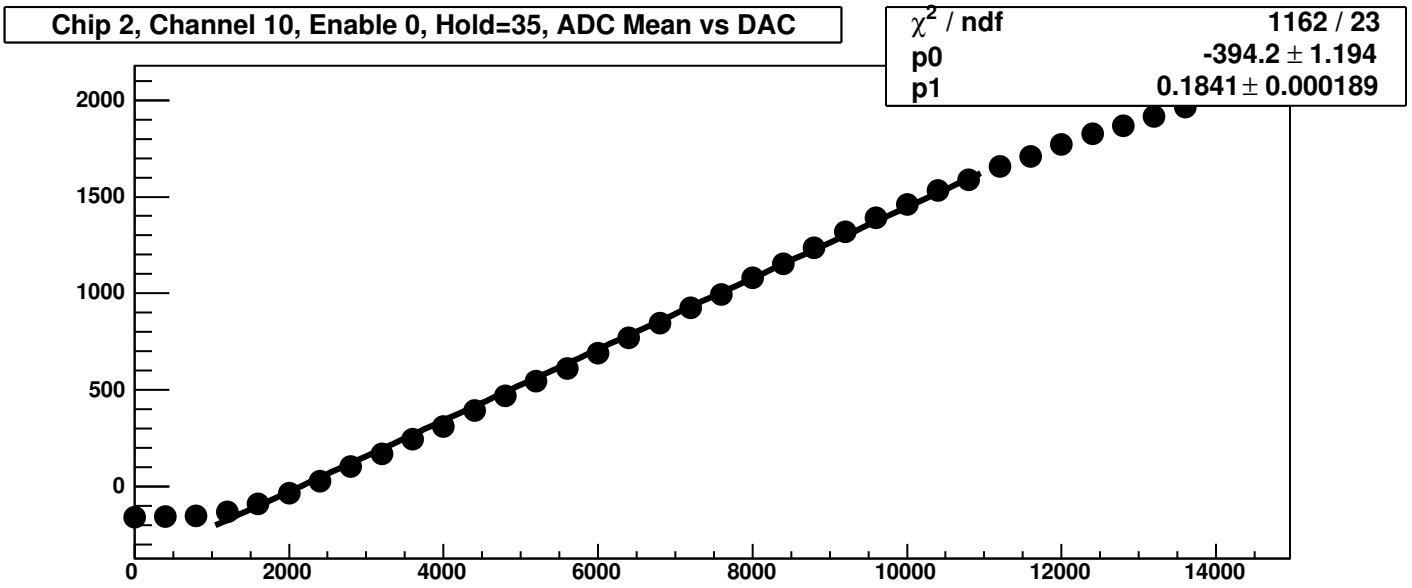


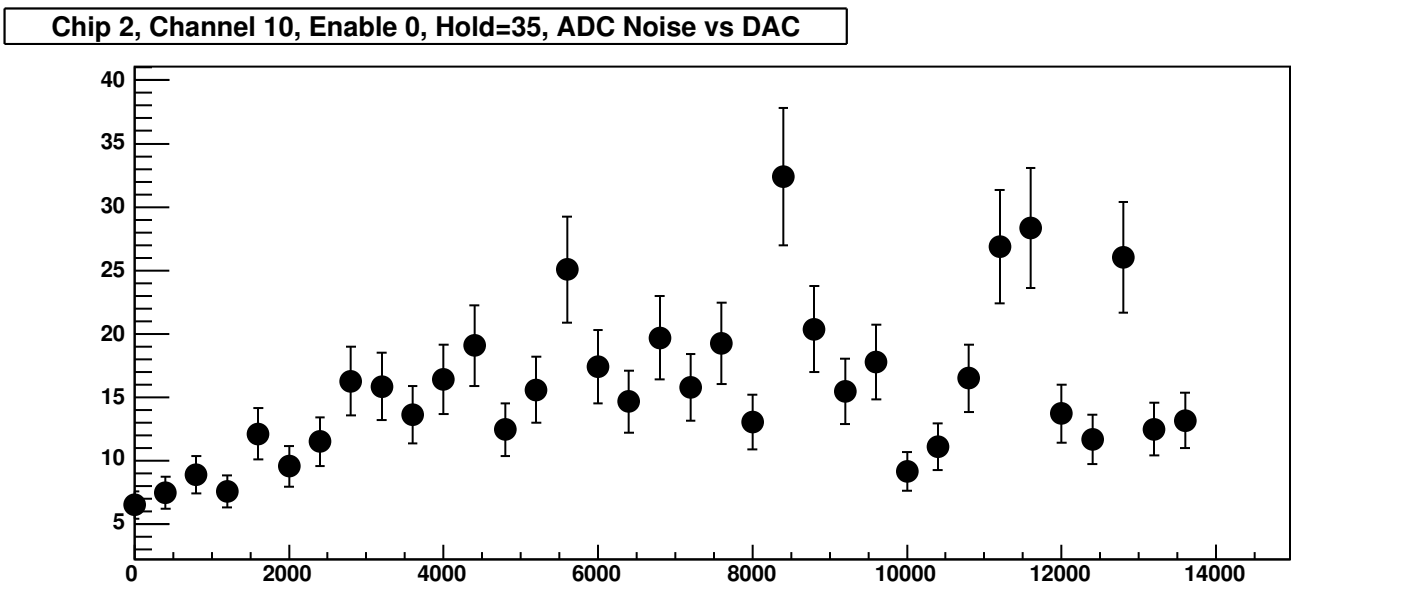
Chip 2, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

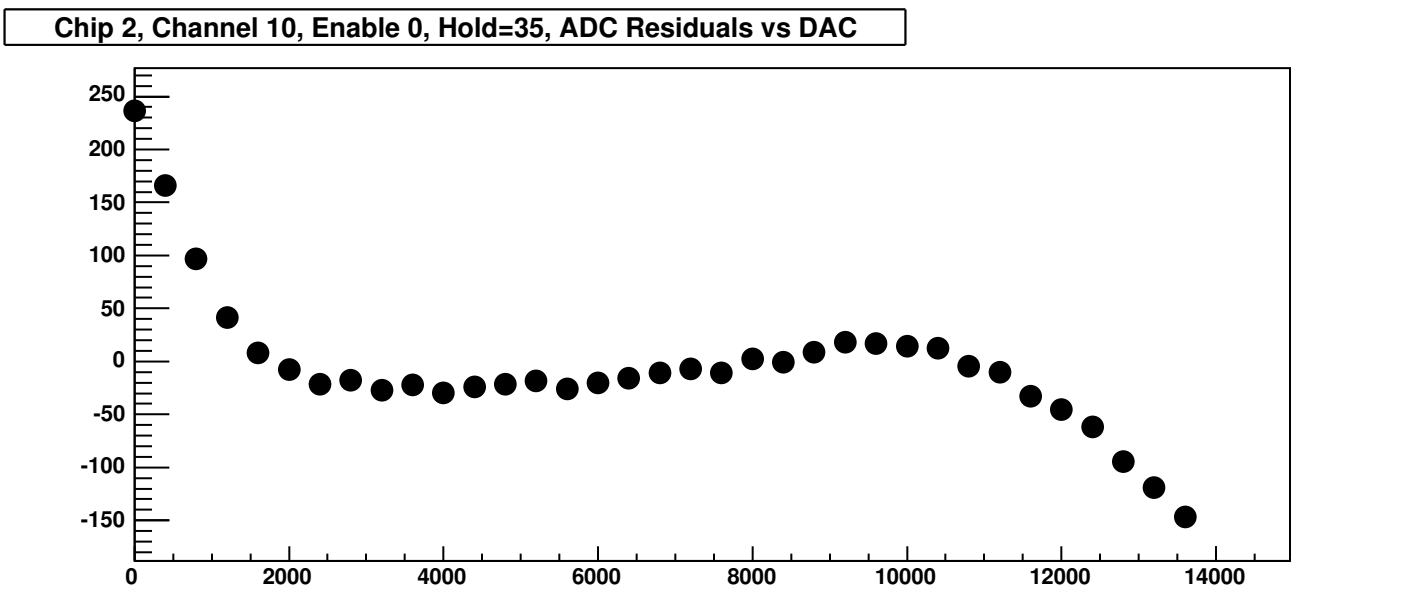


Chip 2, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

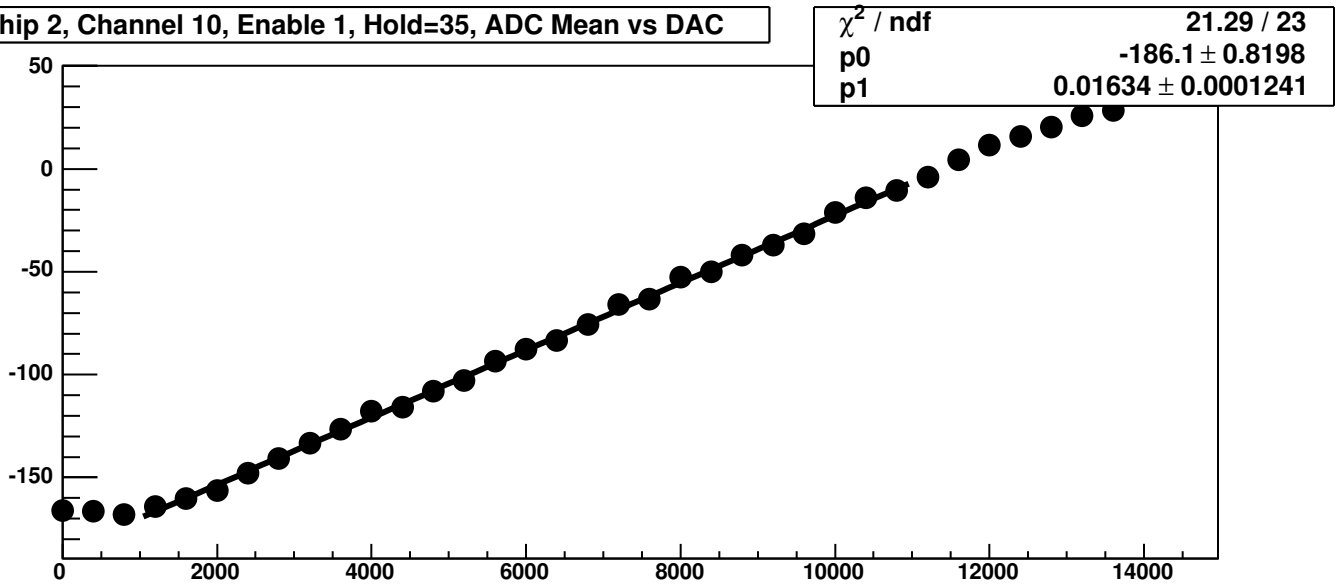



Chip 2, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1162 / 23
p0 -394.2 ± 1.194
p1 0.1841 ± 0.000189
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

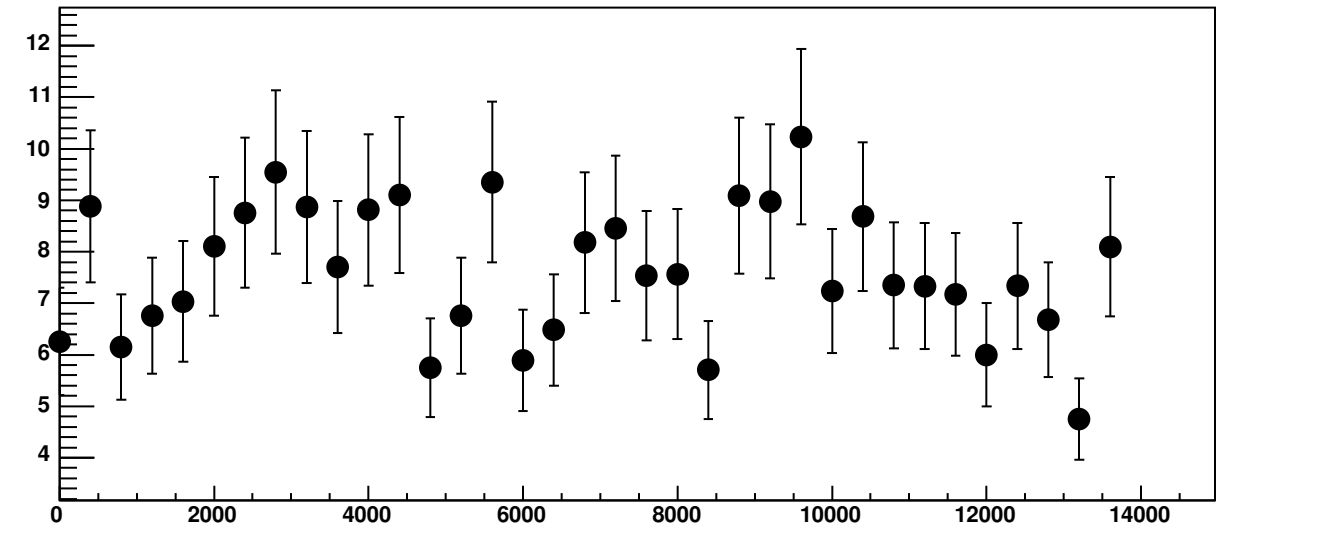

Chip 2, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

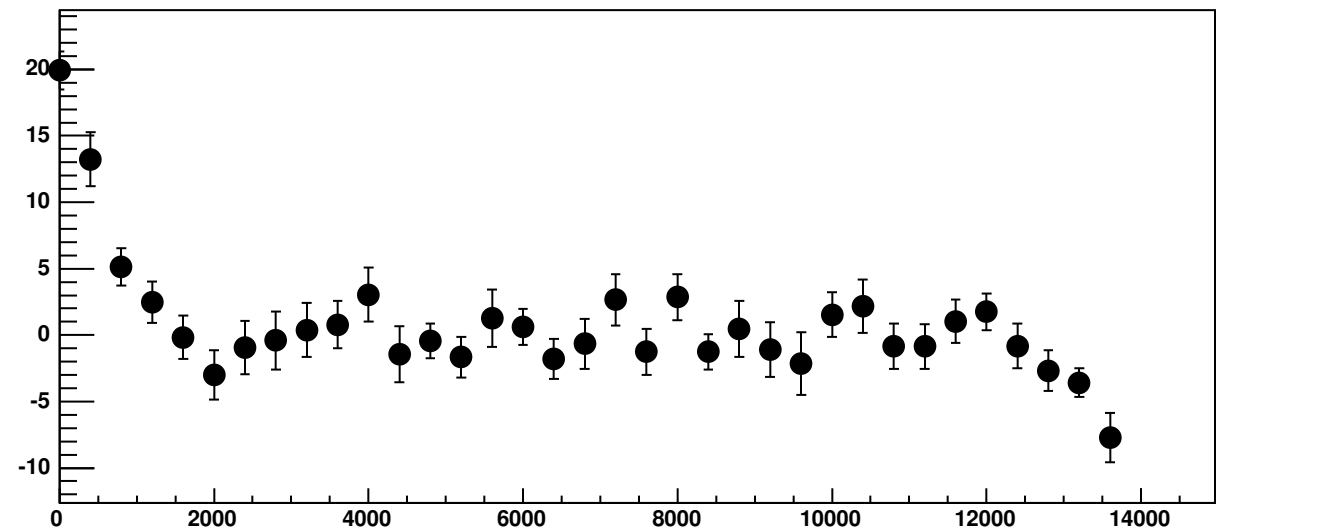
Chip 2, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

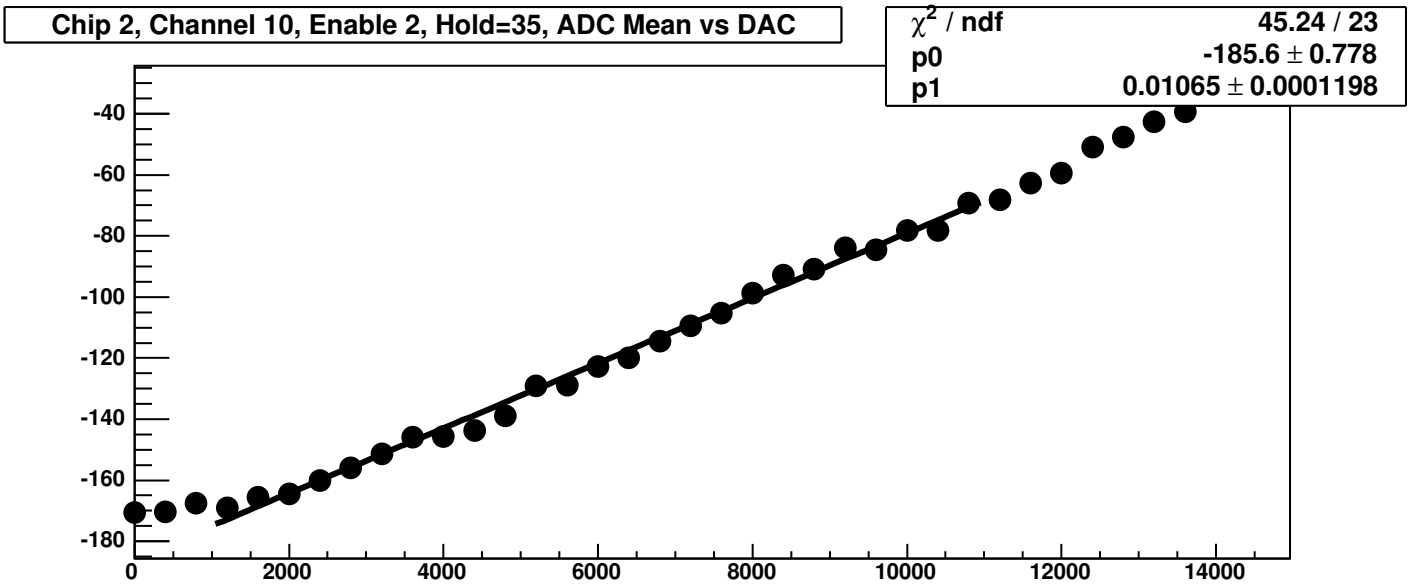


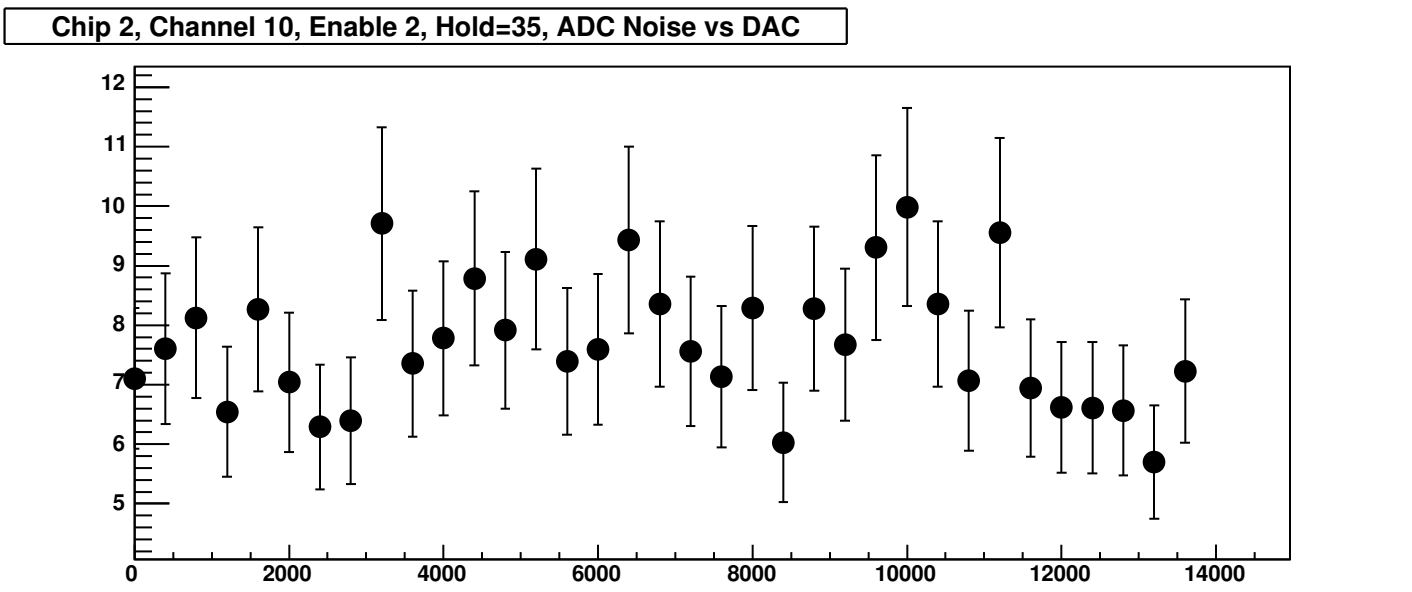
Chip 2, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC

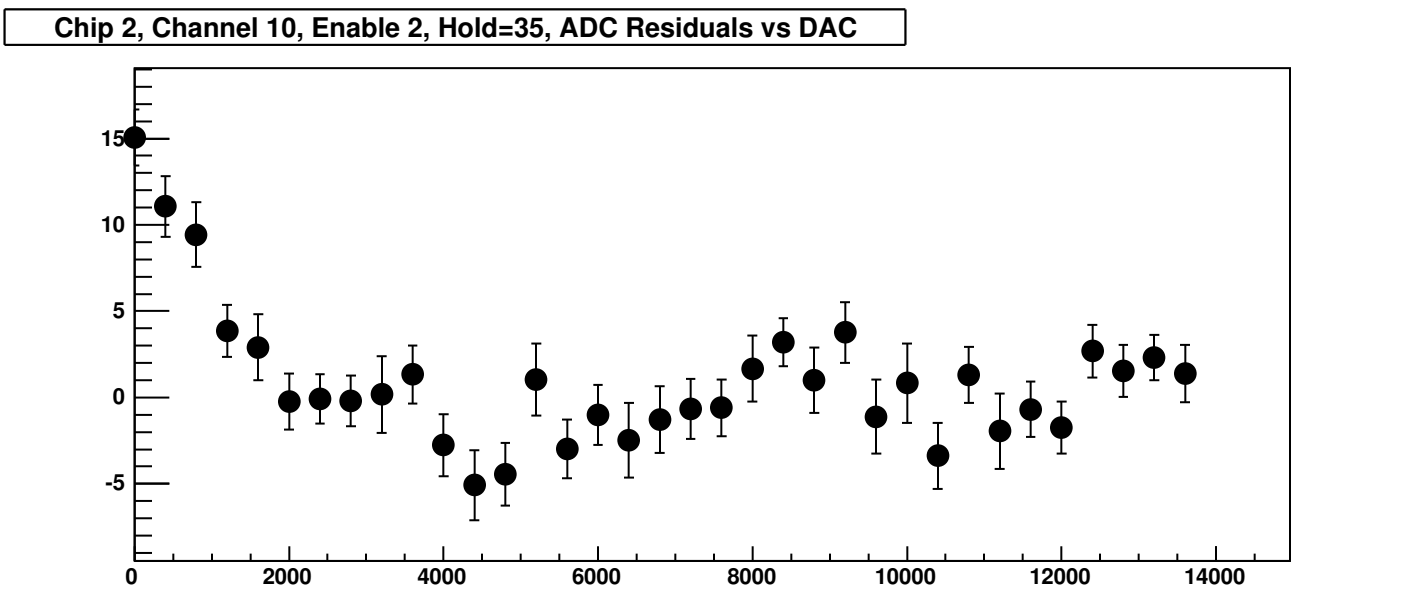


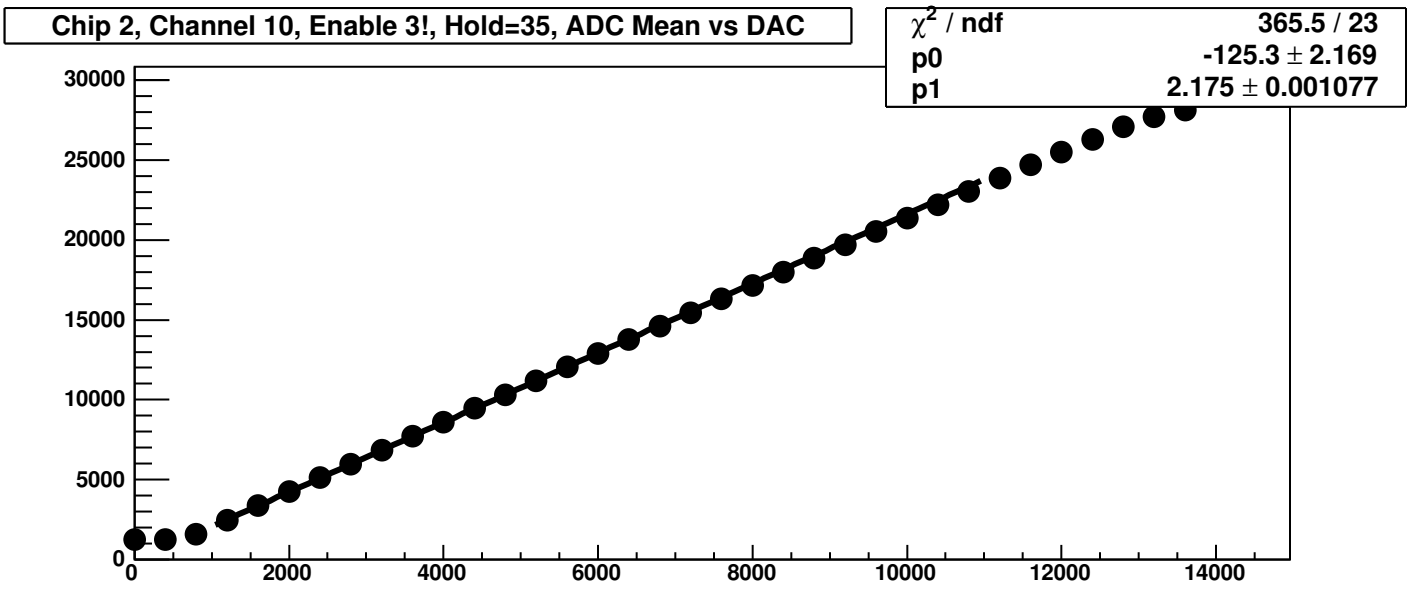
Chip 2, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

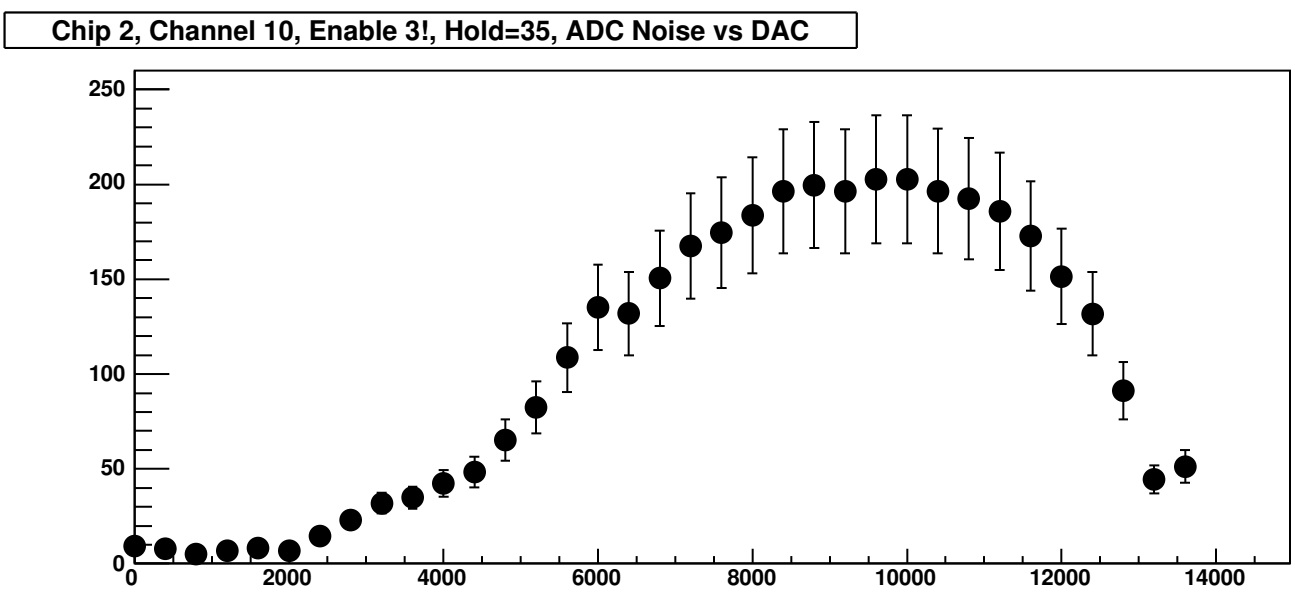


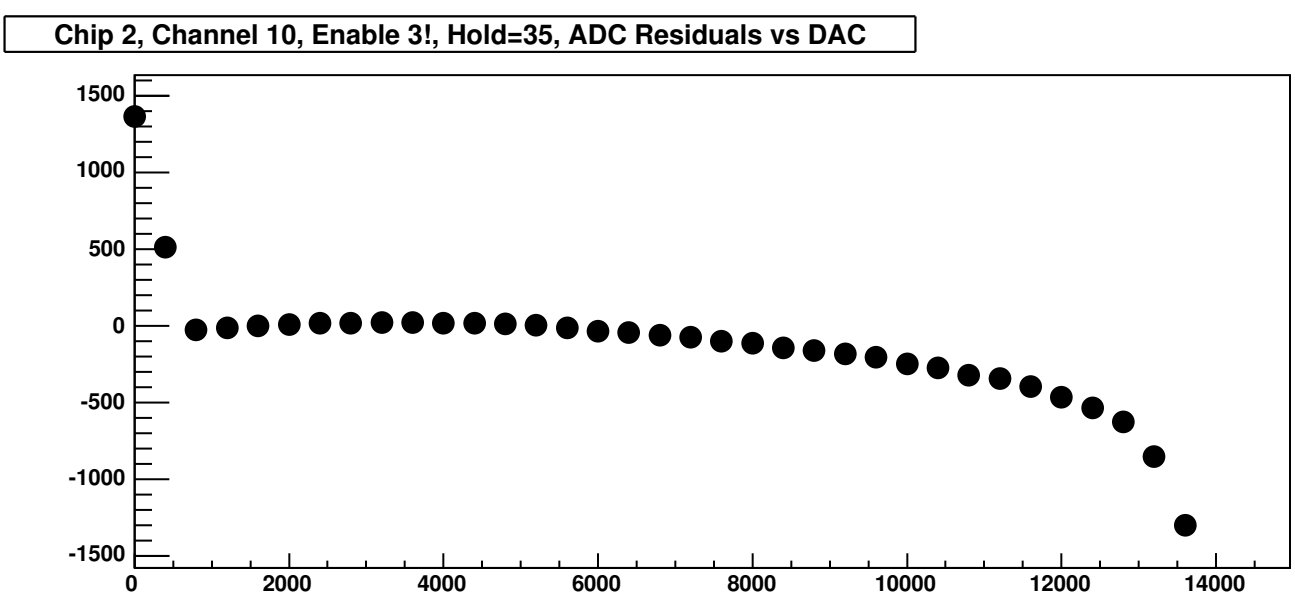

Chip 2, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 45.24 / 23
p0 -185.6 ± 0.778
p1 0.01065 ± 0.0001198
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

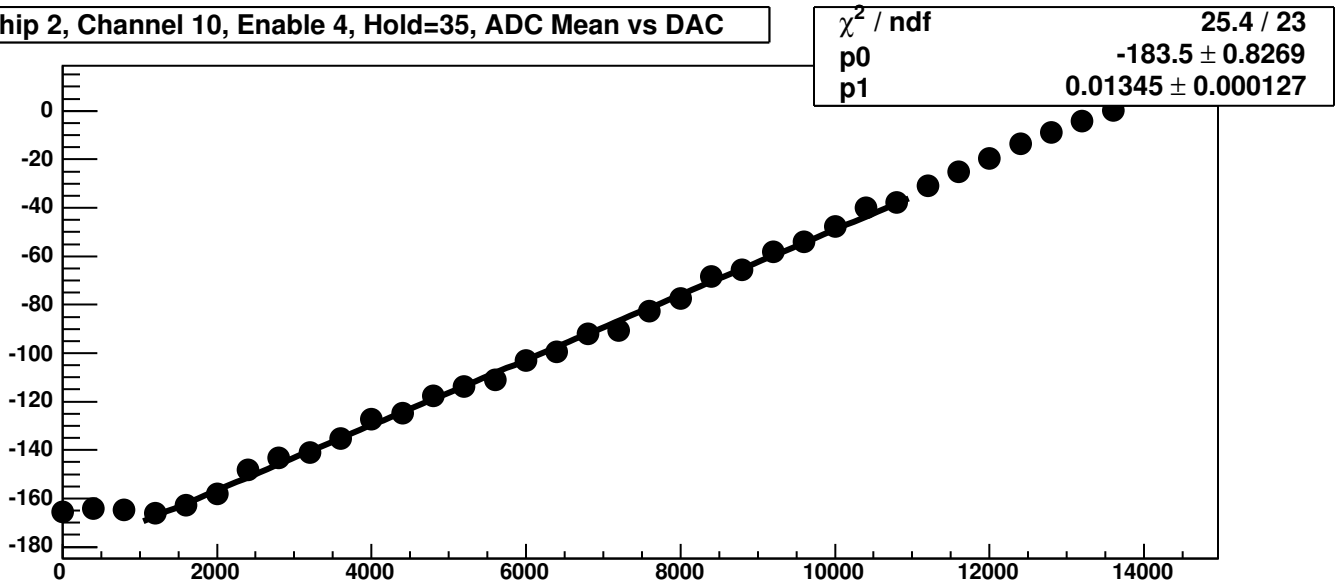

Chip 2, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 365.5 / 23
p0 -125.3 ± 2.169
p1 2.175 ± 0.001077
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

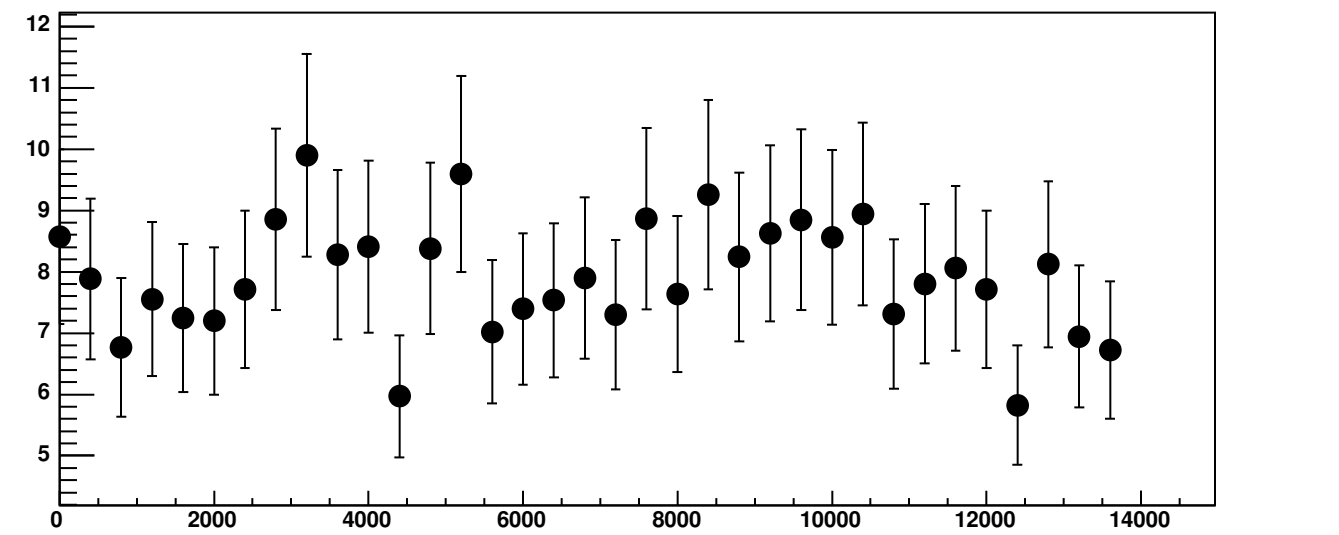

Chip 2, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

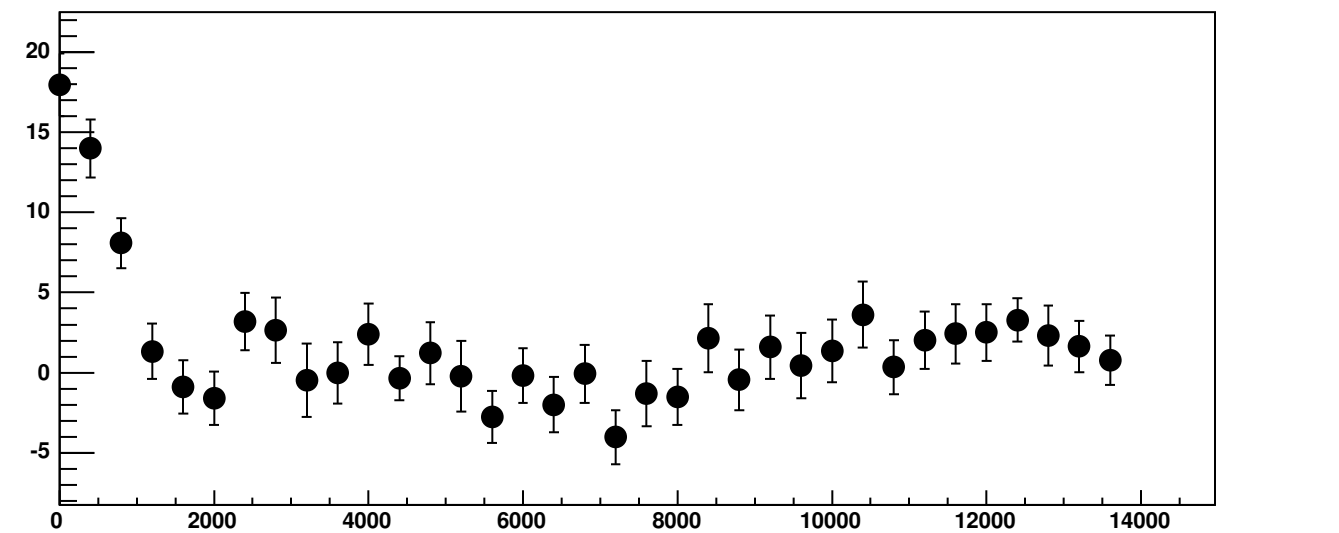
Chip 2, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



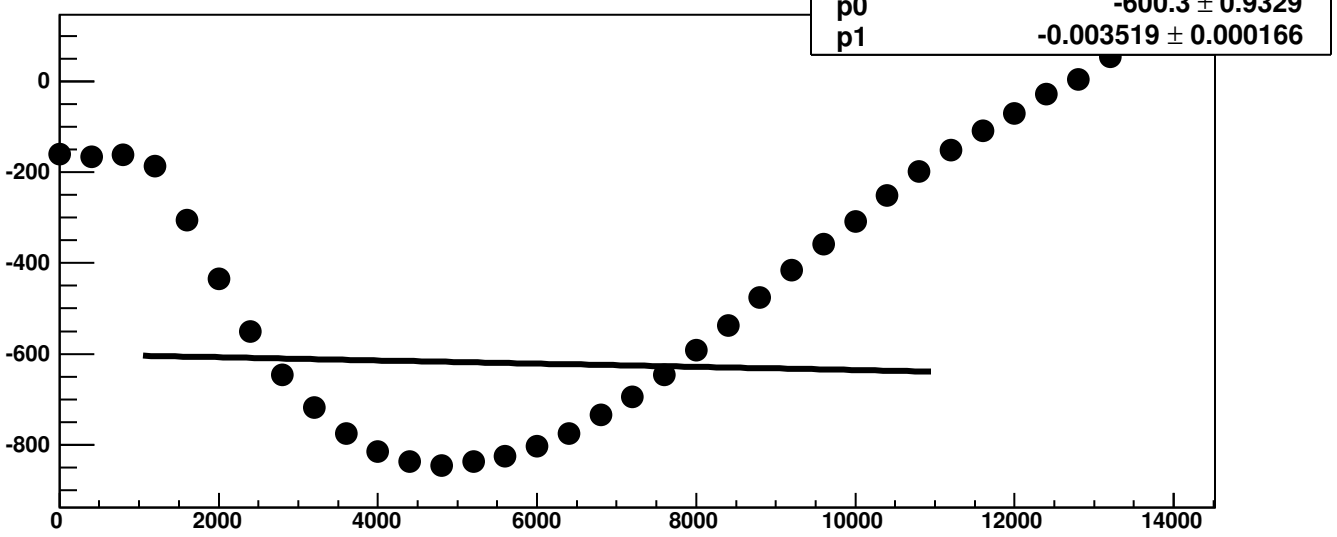
Chip 2, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



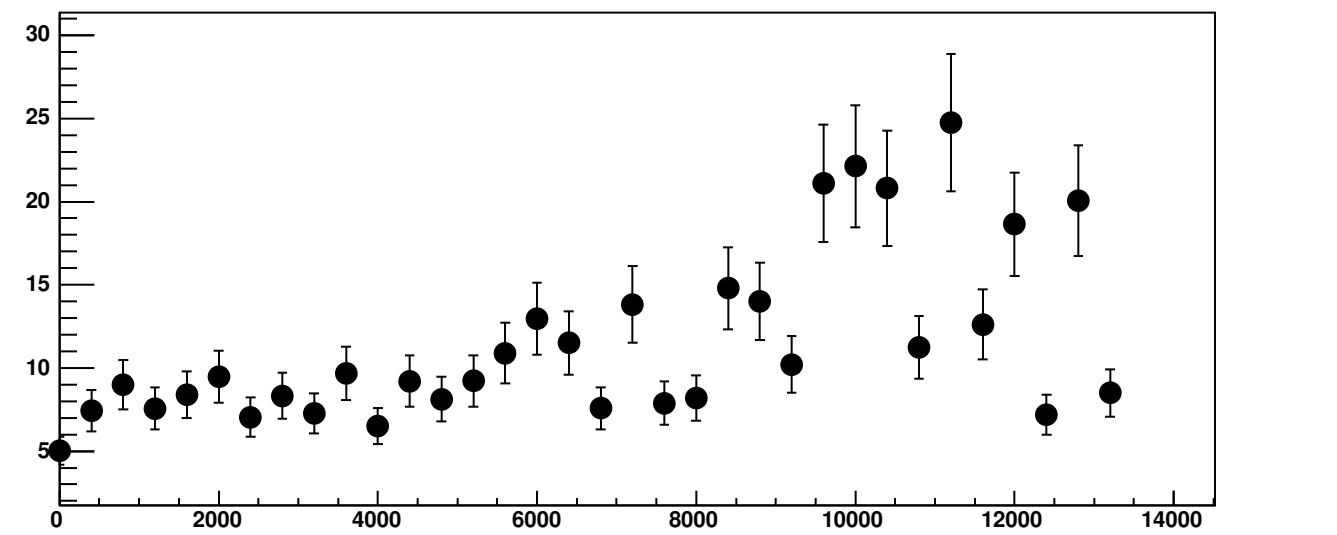
Chip 2, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



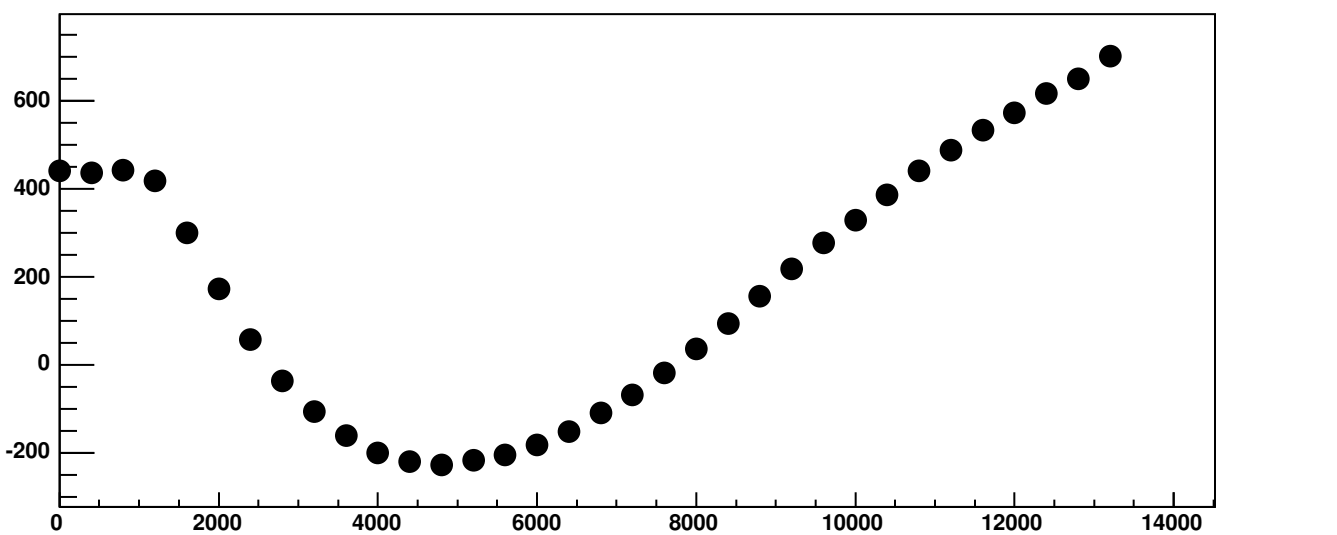
Chip 2, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

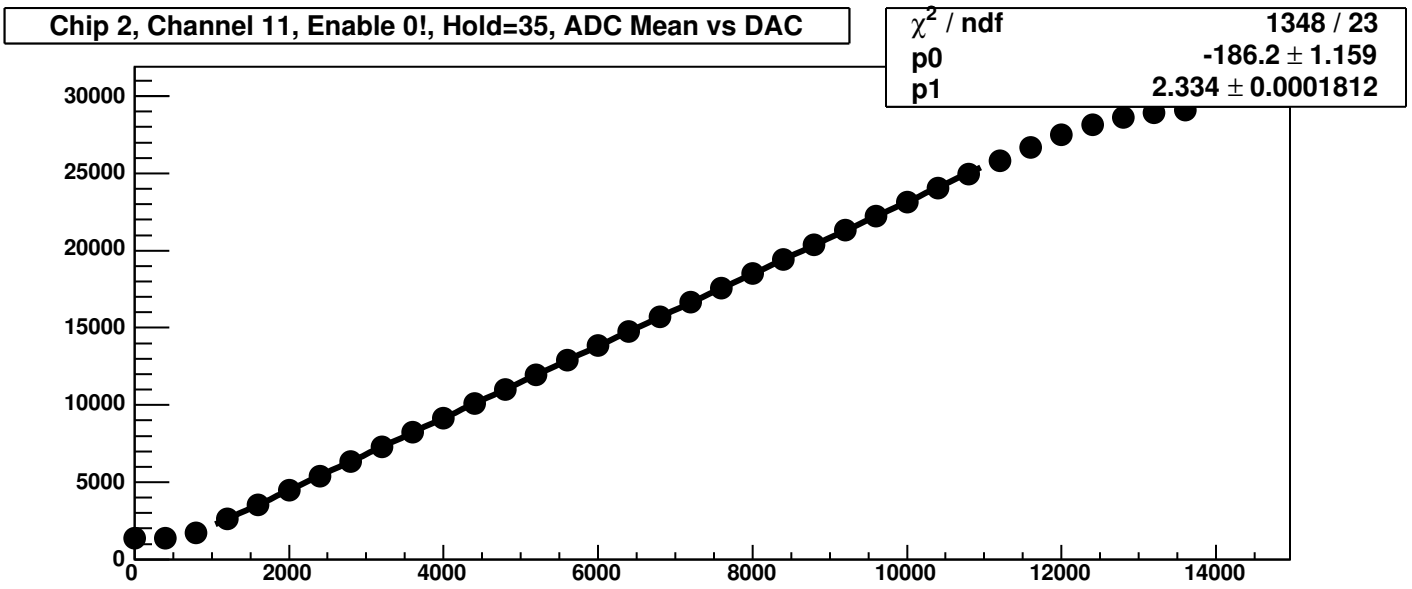


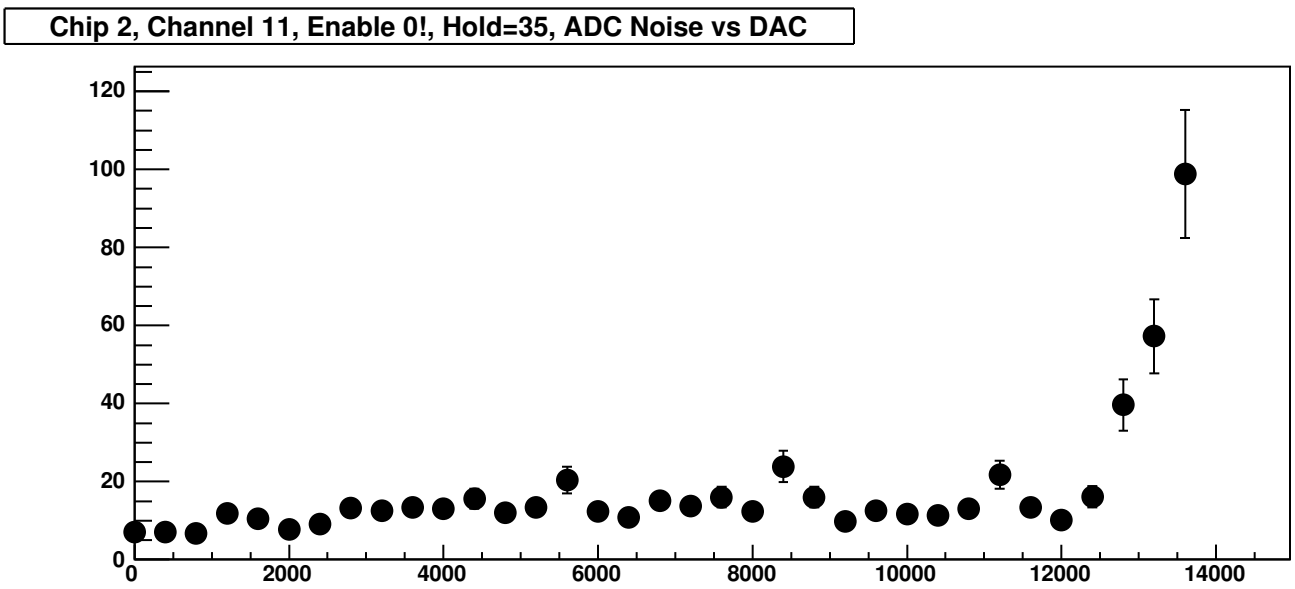
Chip 2, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

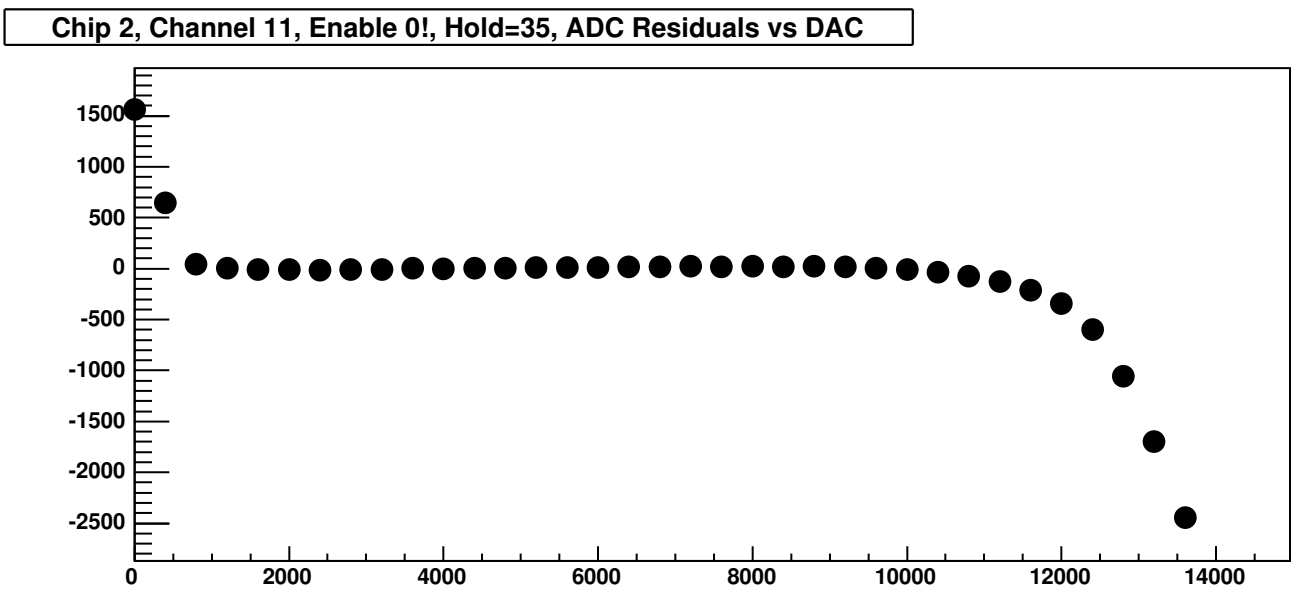


Chip 2, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

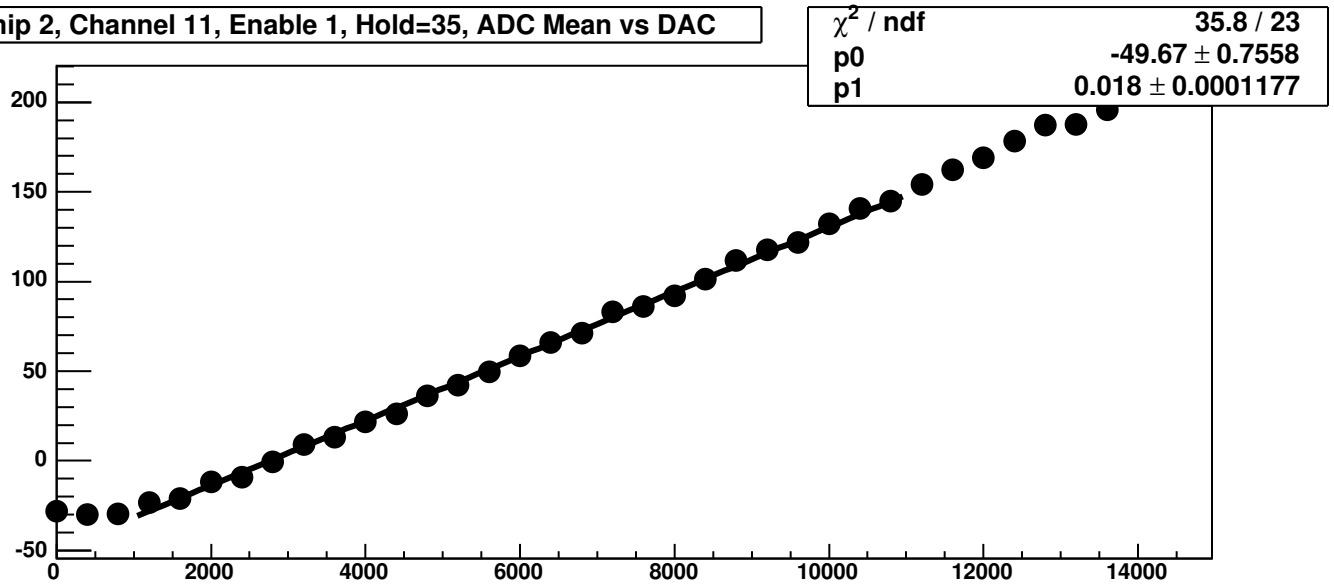



Chip 2, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1348 / 23
p0 -186.2 ± 1.159
p1 2.334 ± 0.0001812
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

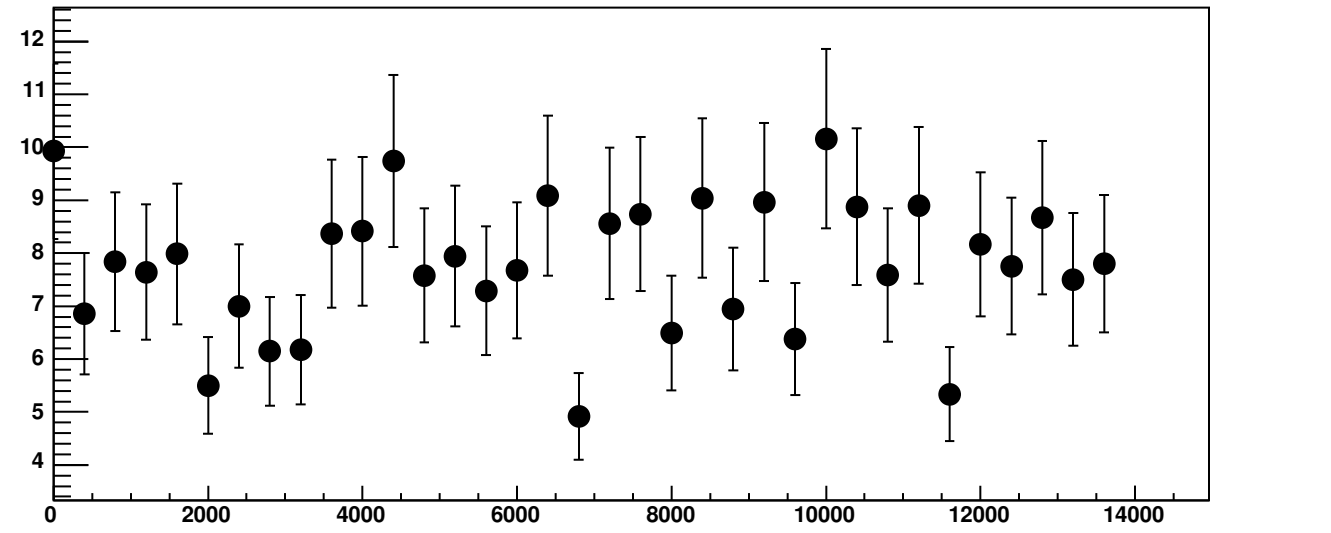

Chip 2, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

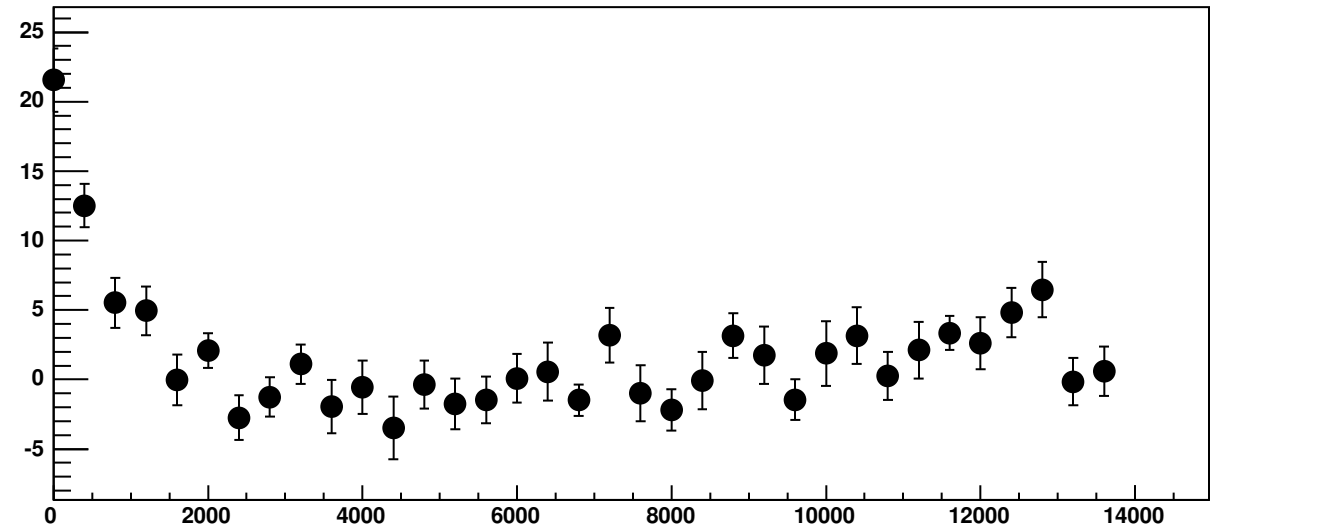
Chip 2, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC



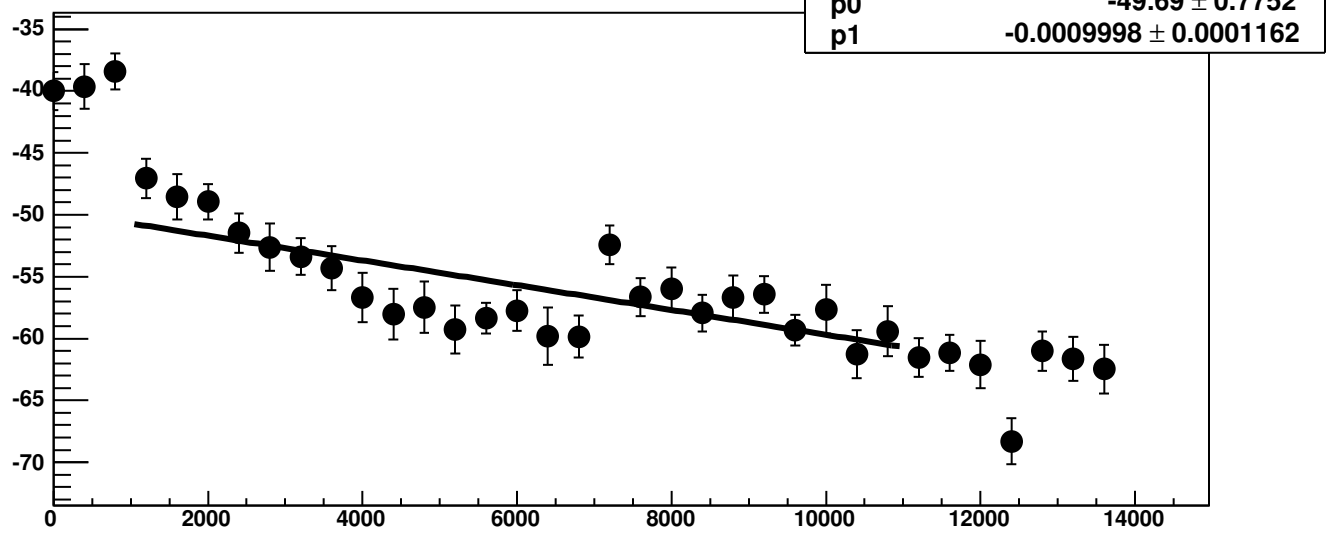
Chip 2, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC



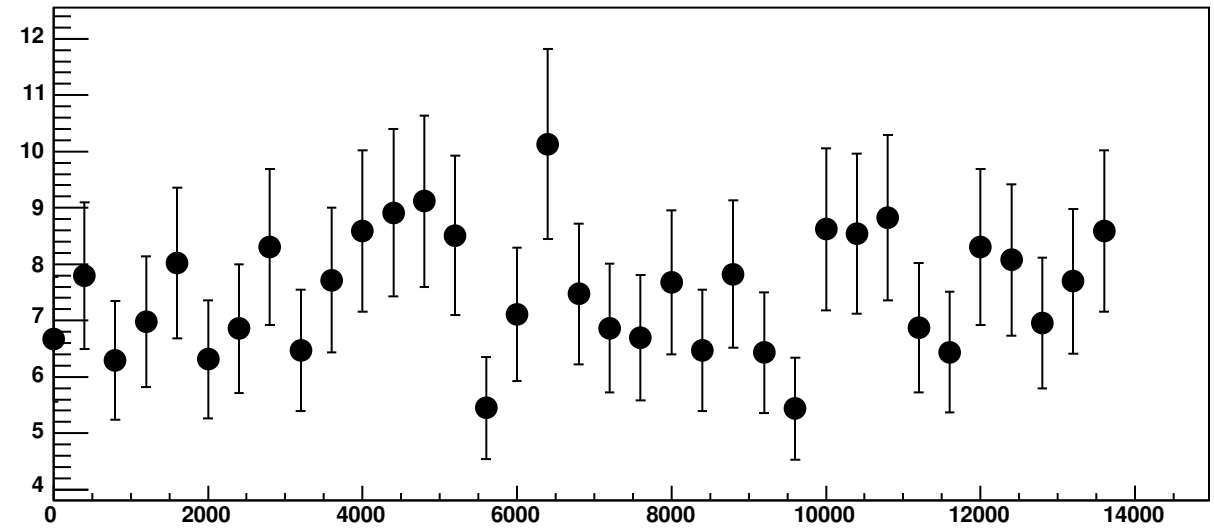
Chip 2, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC



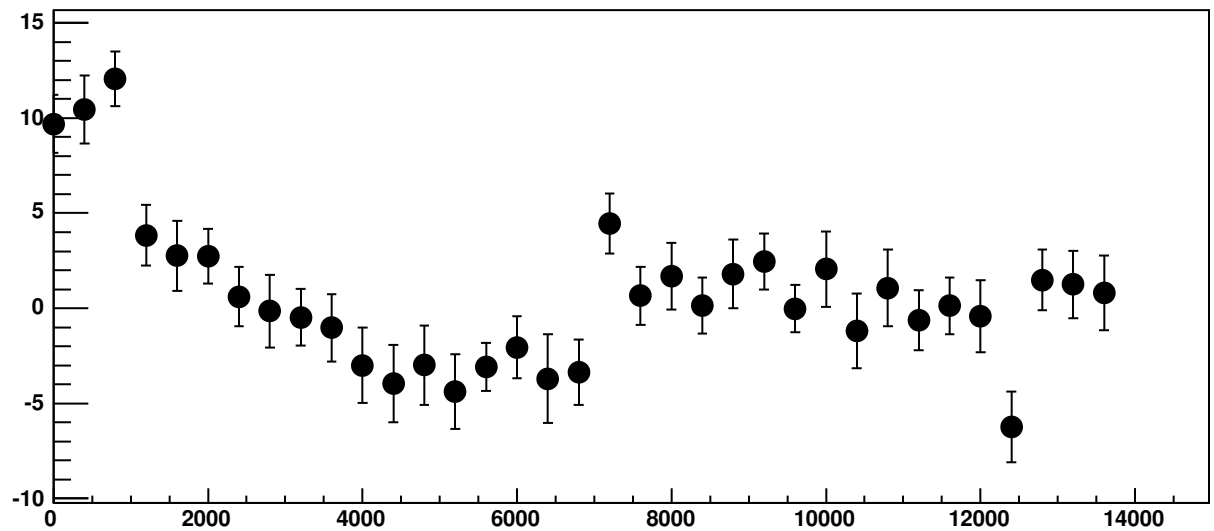
Chip 2, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

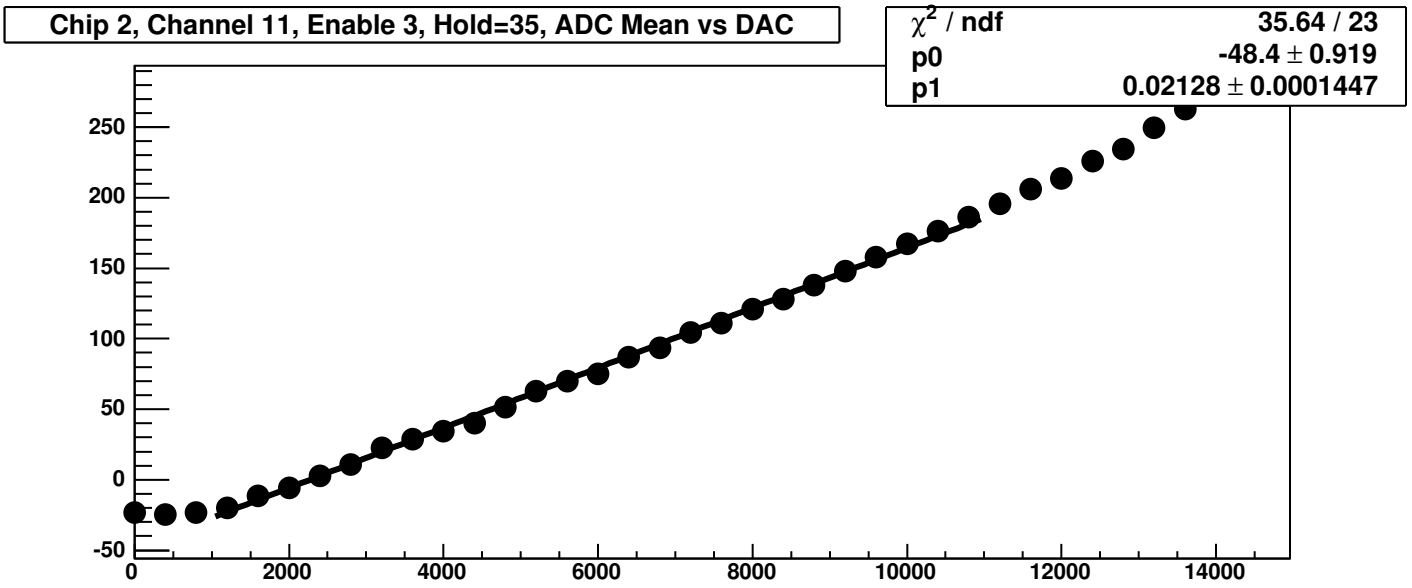


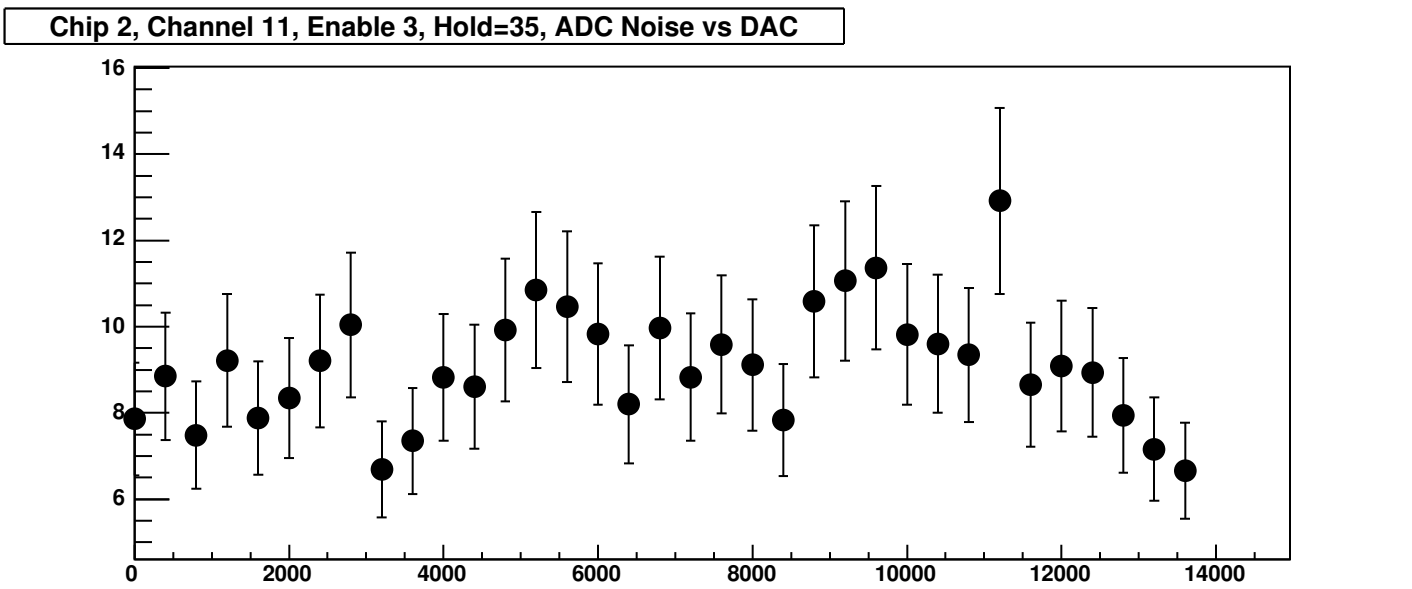
Chip 2, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

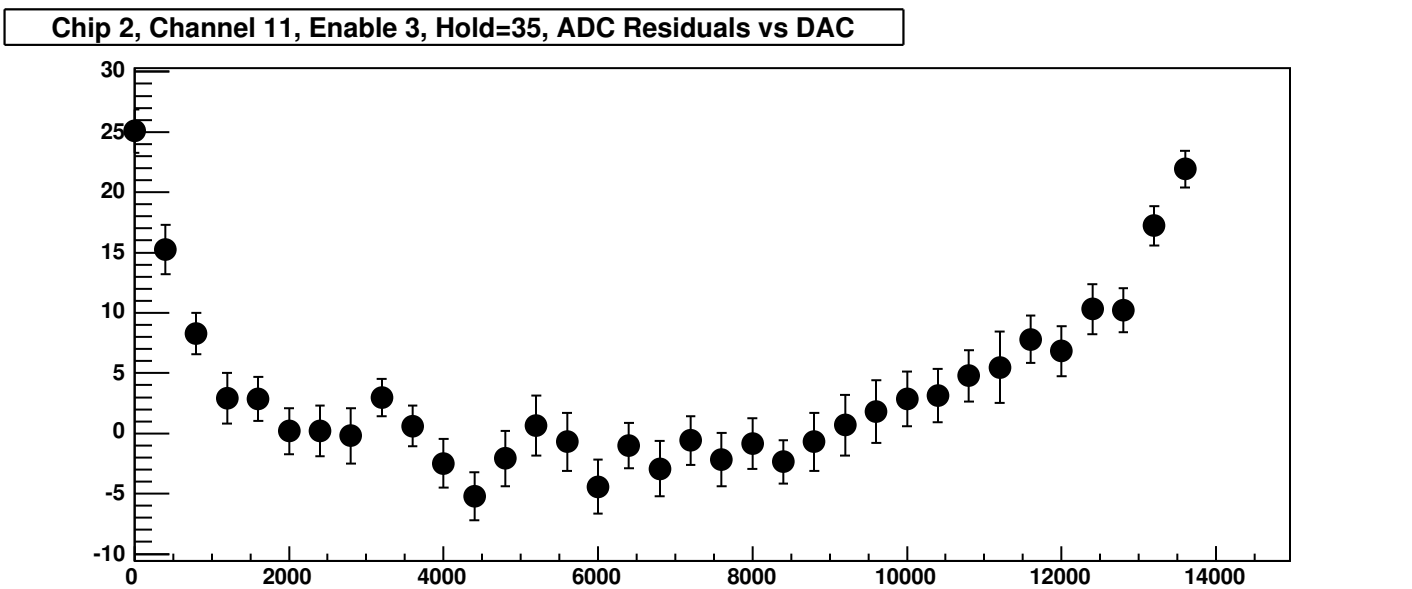


Chip 2, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

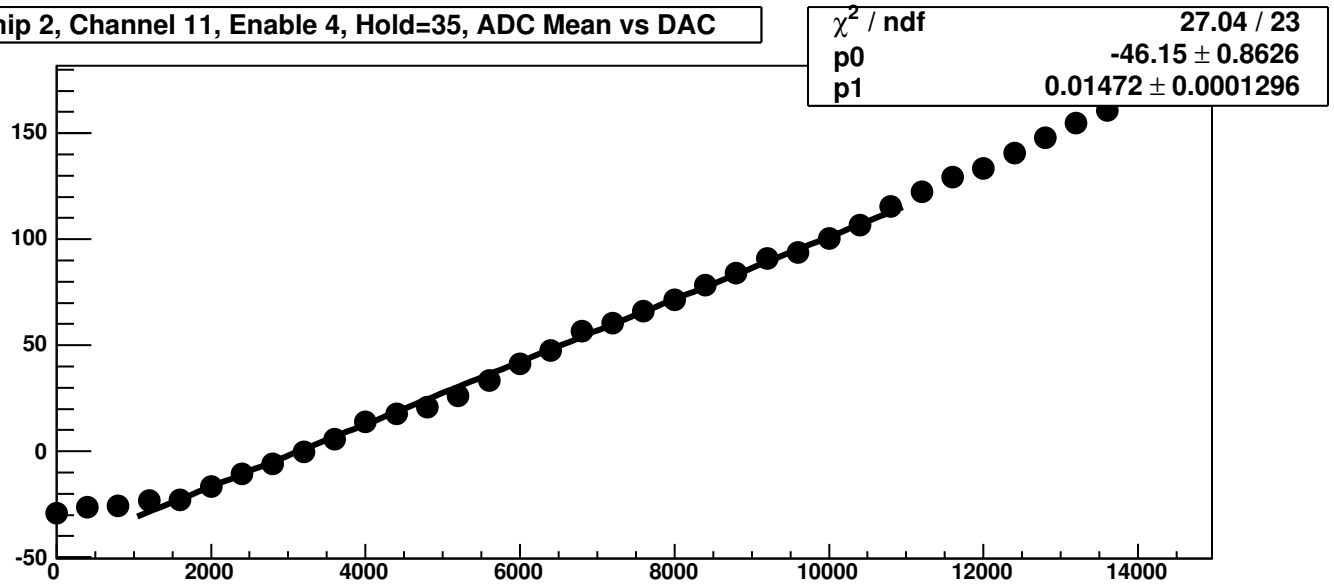



Chip 2, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 35.64 / 23
p0 -48.4 ± 0.919
p1 0.02128 ± 0.0001447
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

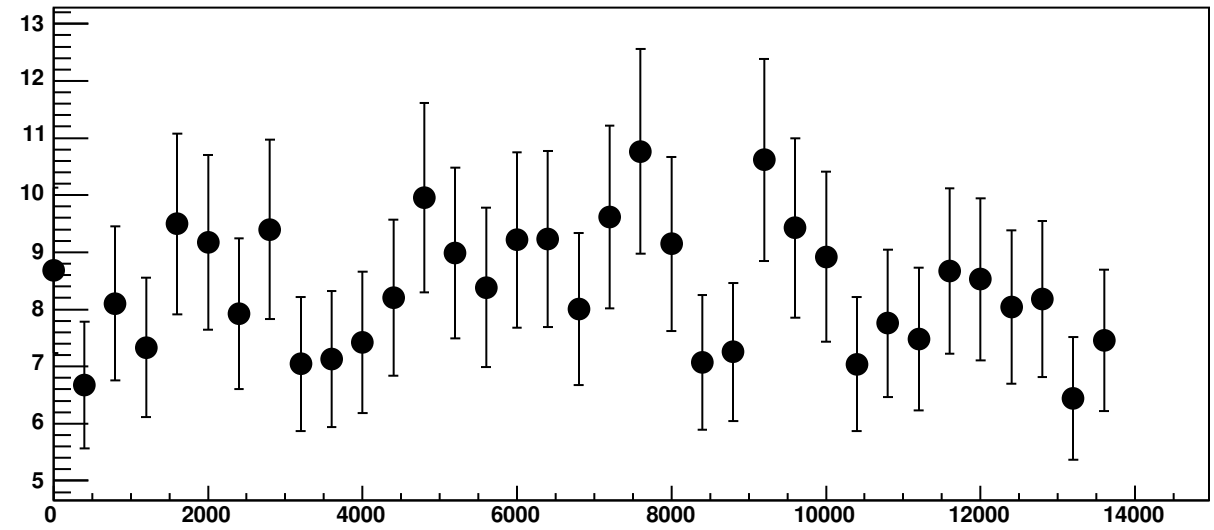

Chip 2, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

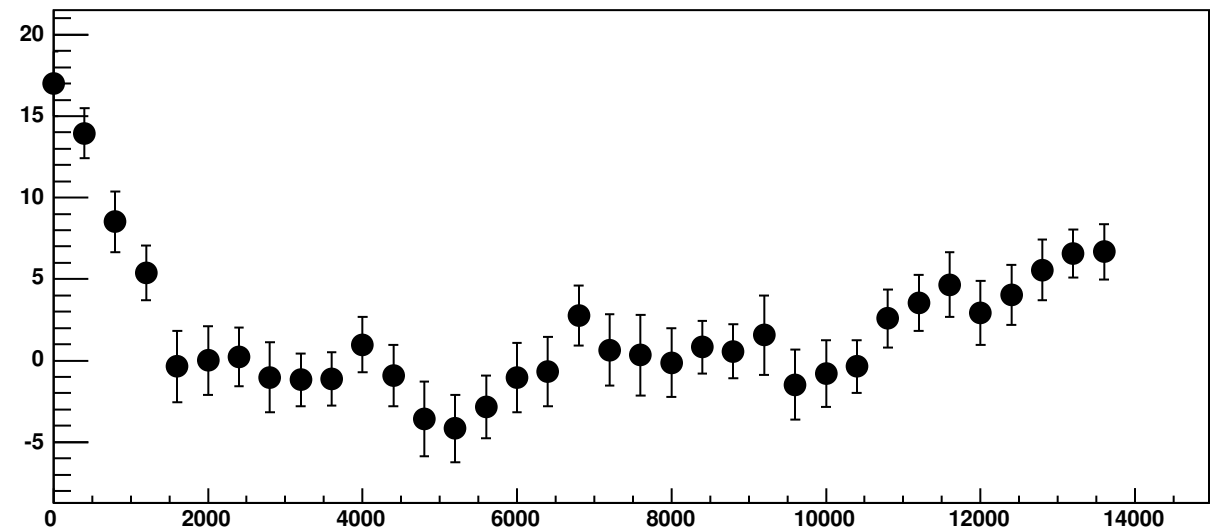
Chip 2, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

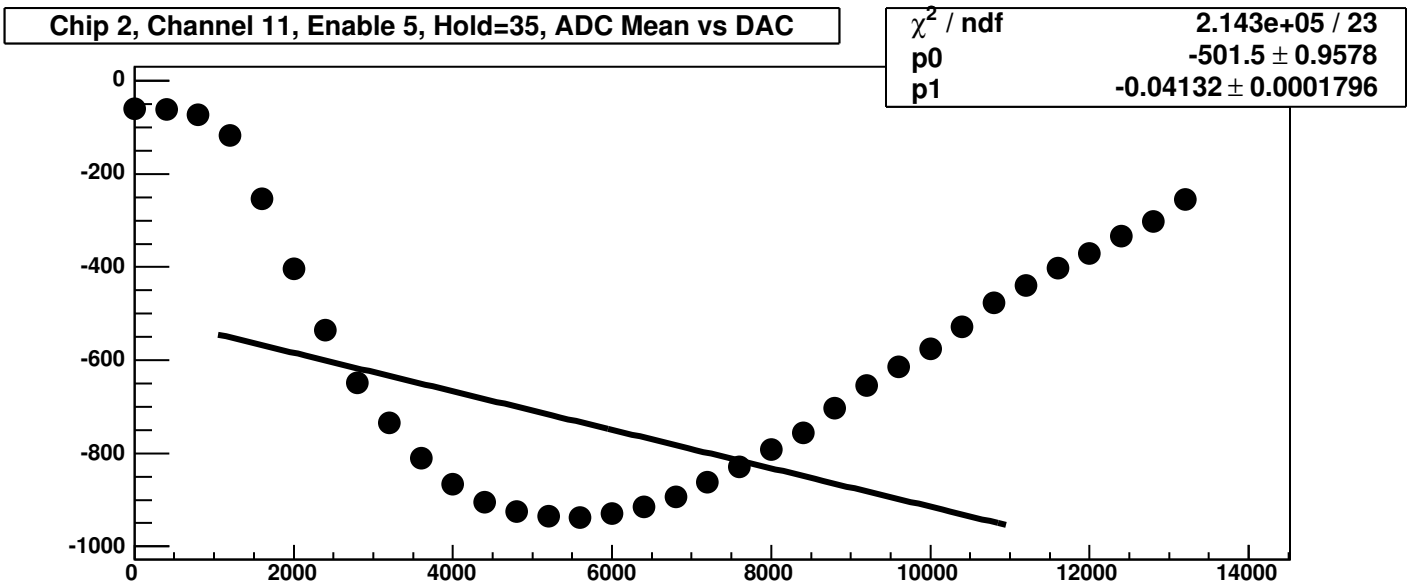


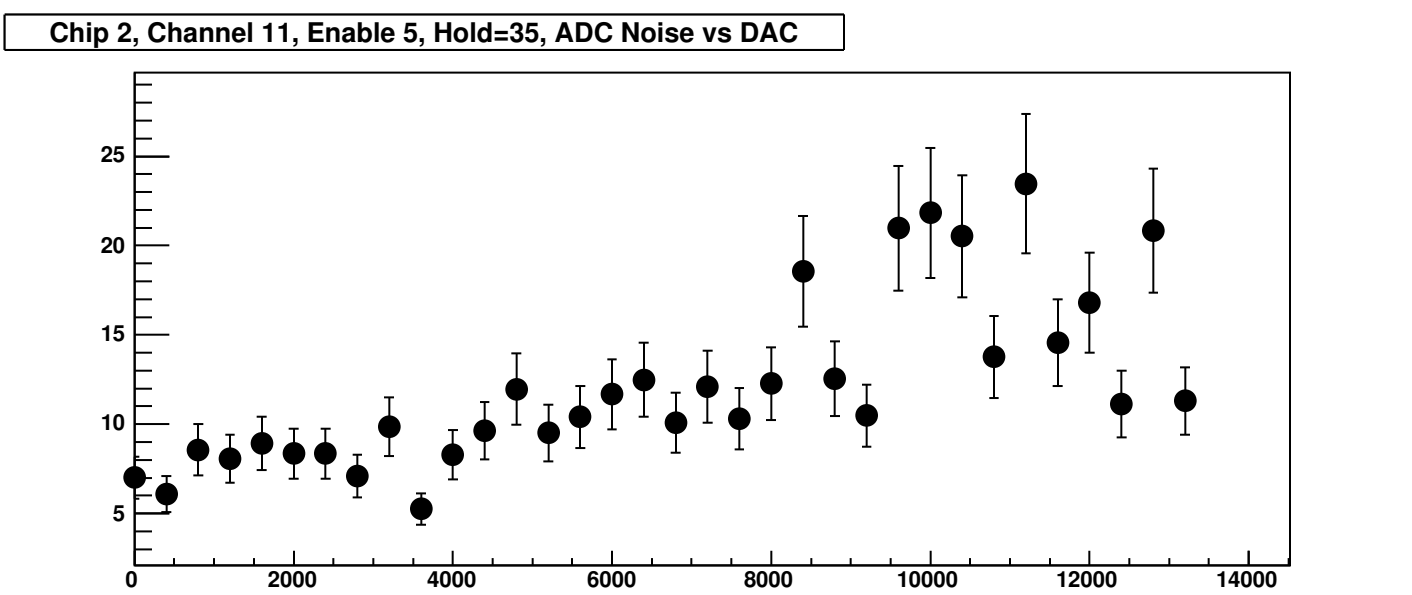
Chip 2, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

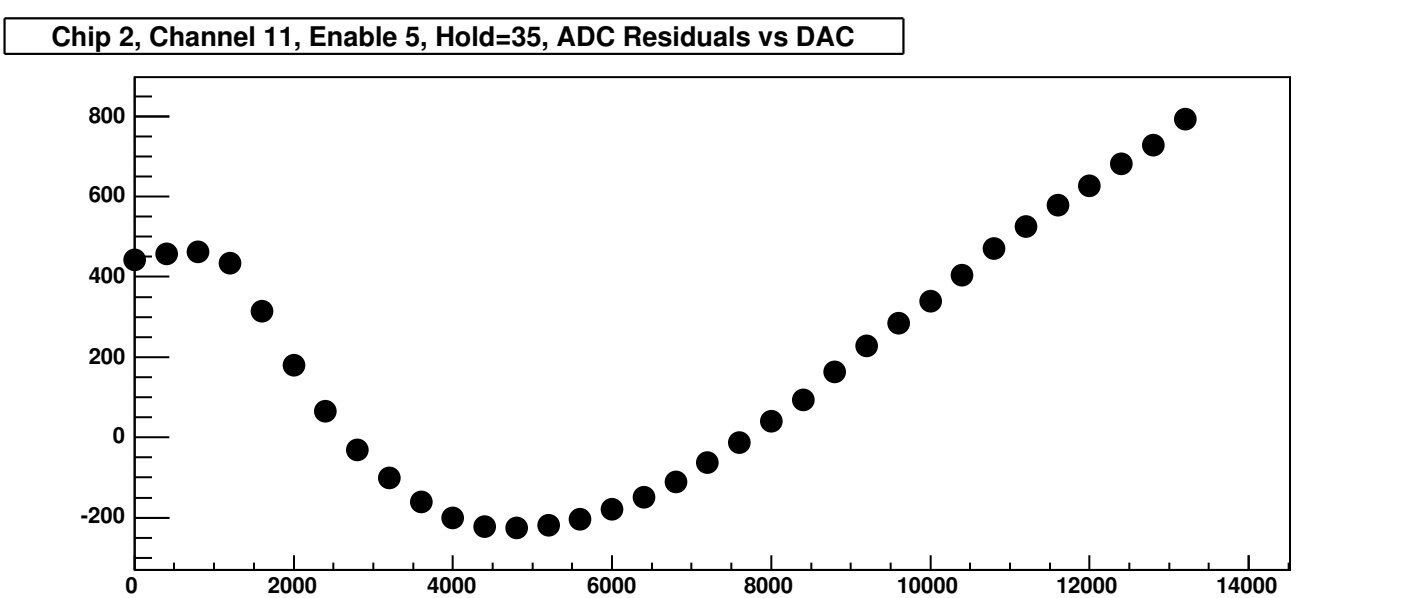


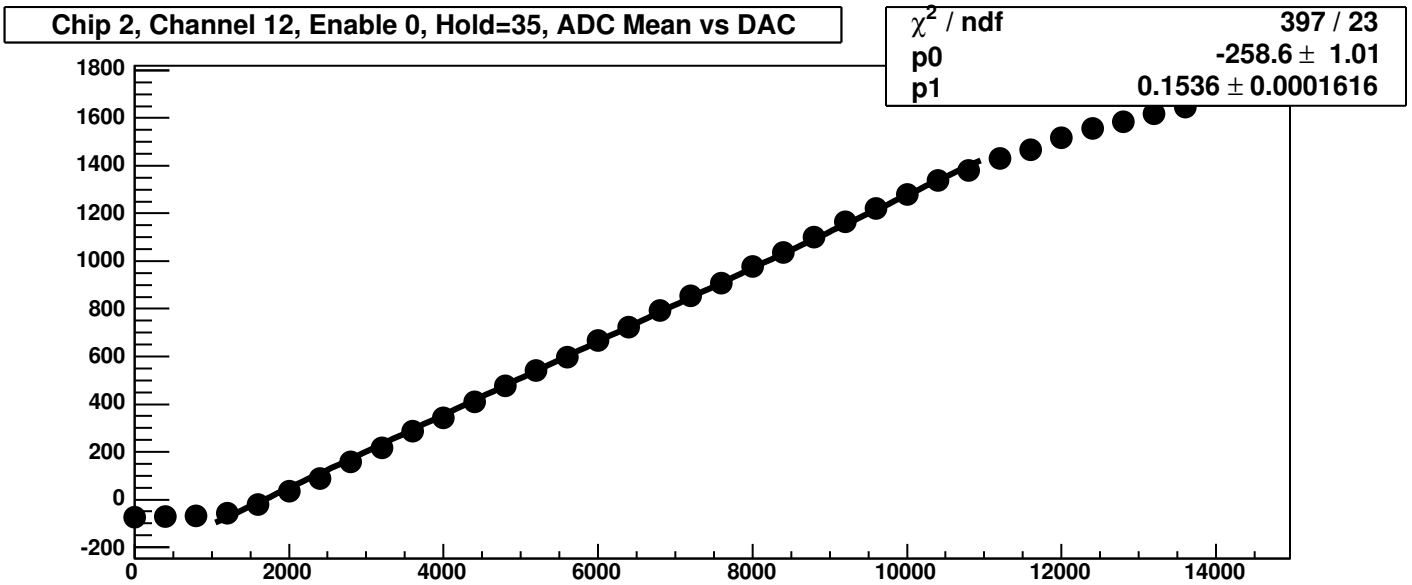
Chip 2, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

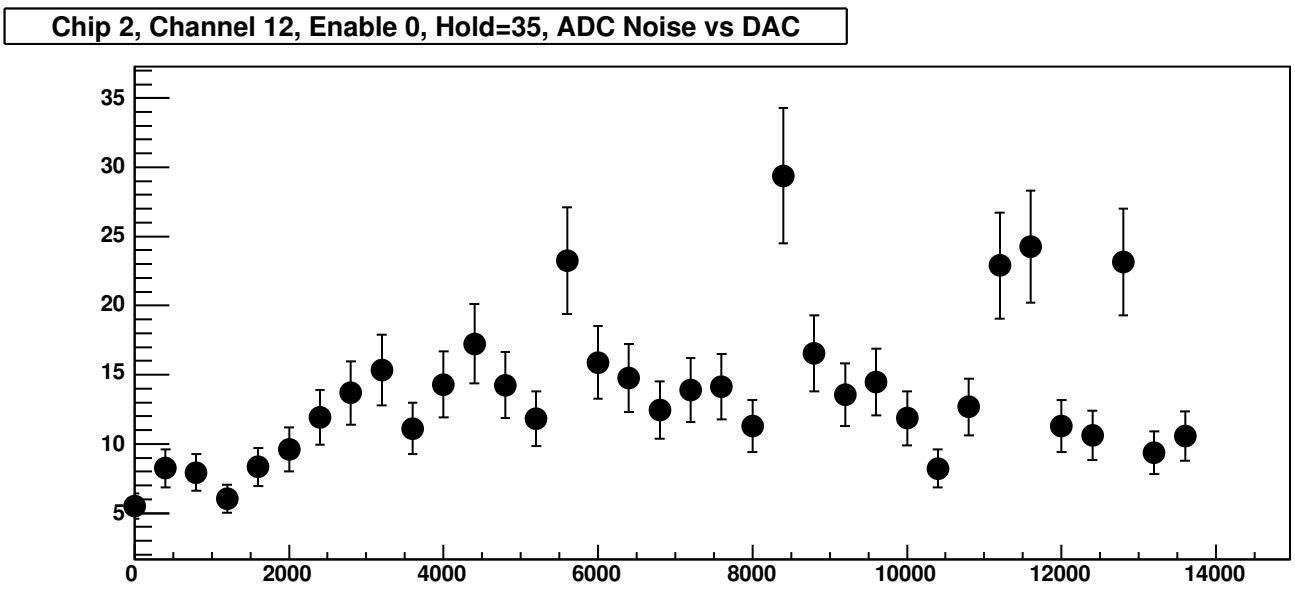


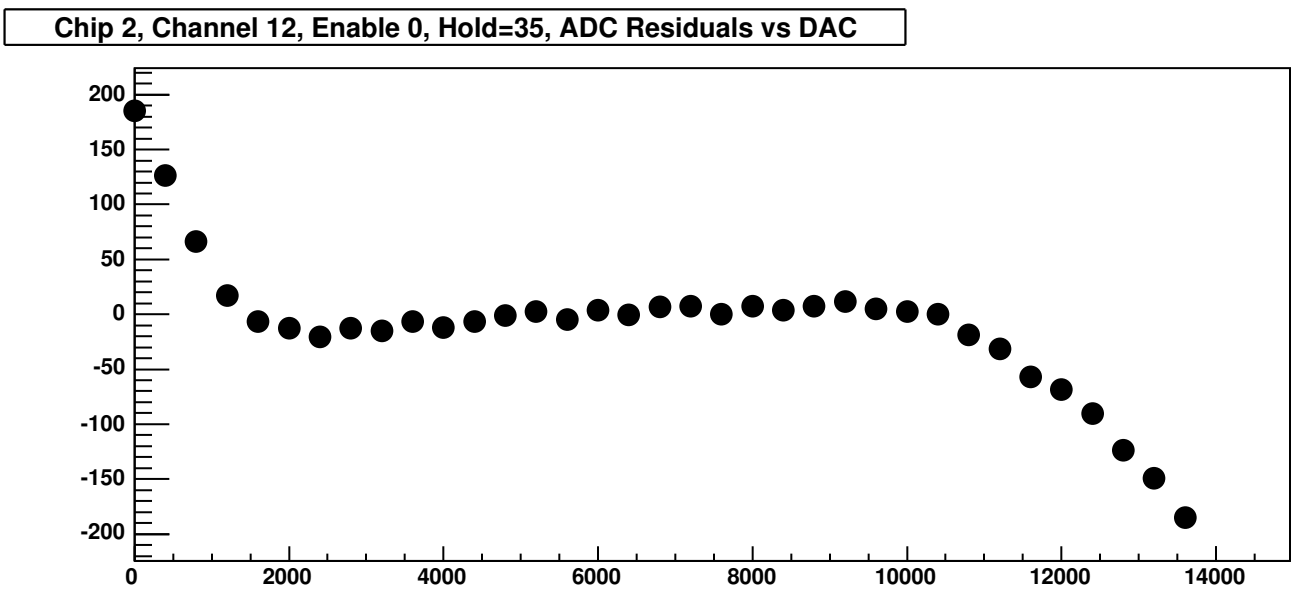

Chip 2, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2.143e+05 / 23
p0 -501.5 ± 0.9578
p1 -0.04132 ± 0.0001796
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

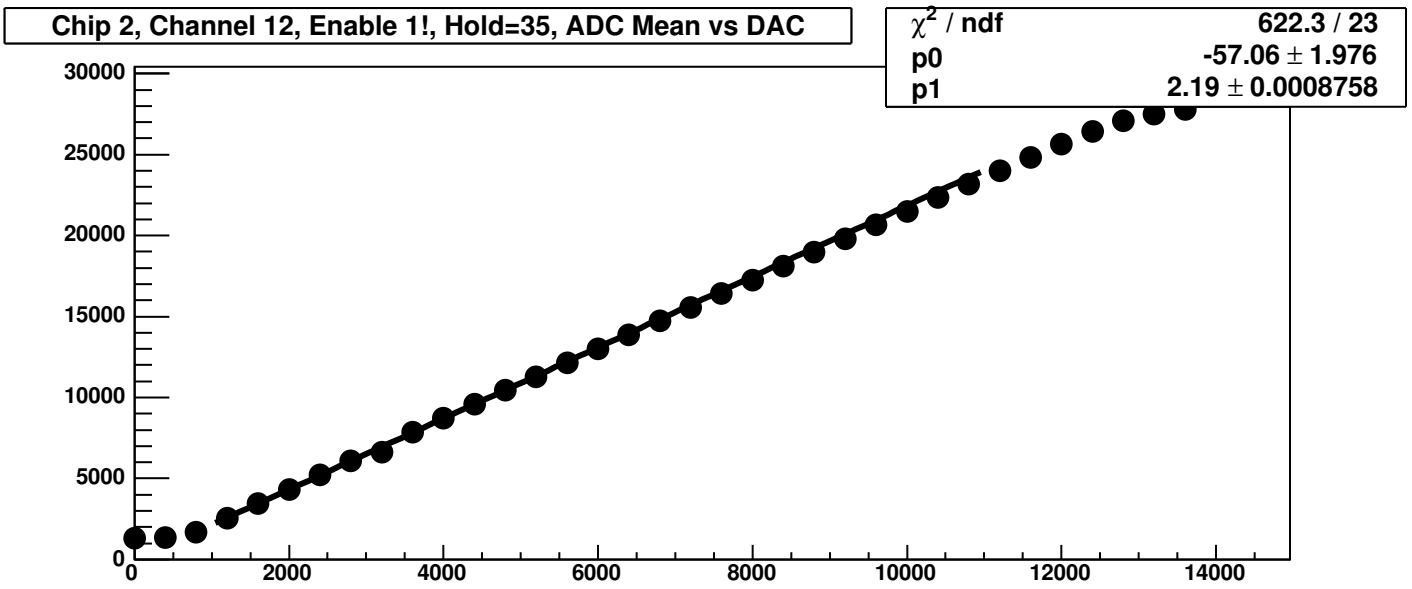

Chip 2, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

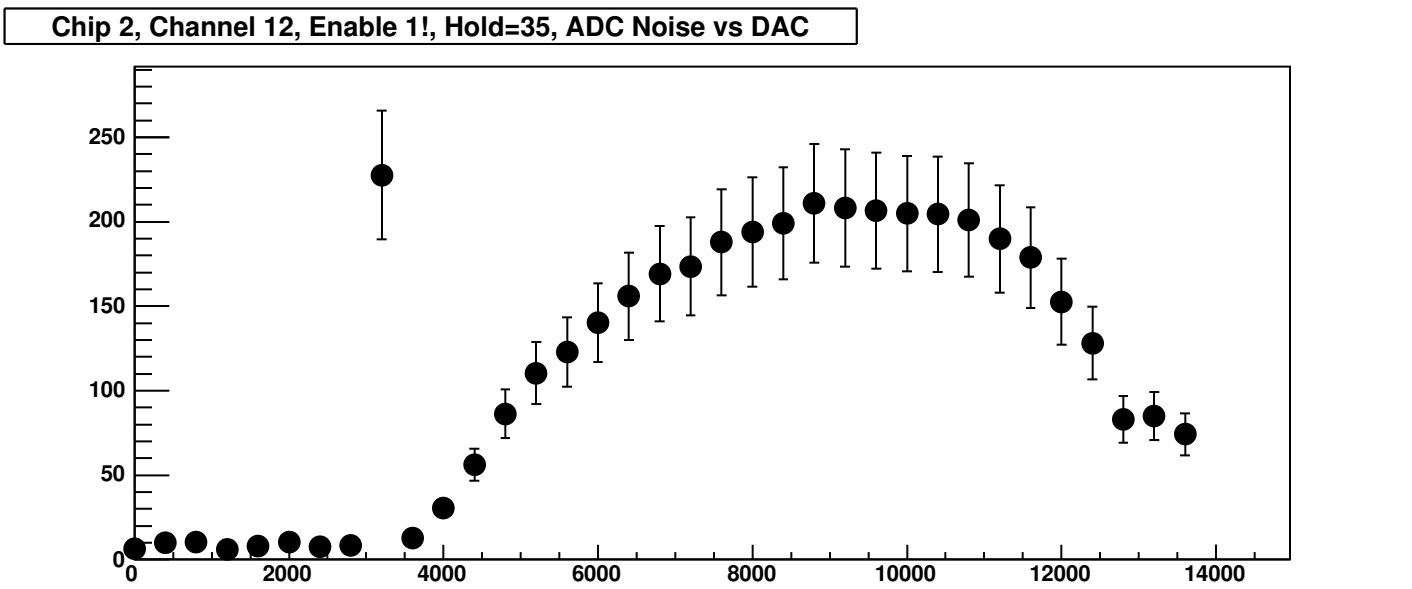

Chip 2, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

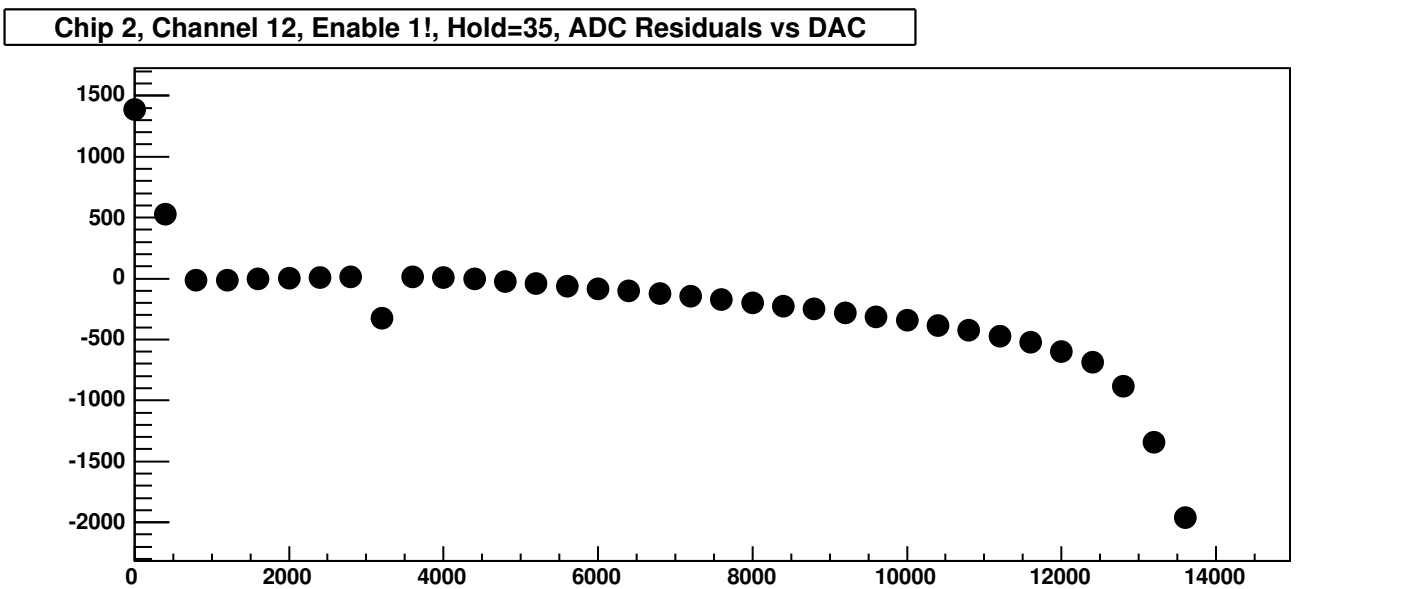

Chip 2, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 397 / 23
p0 -258.6 ± 1.01
p1 0.1536 ± 0.0001616
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

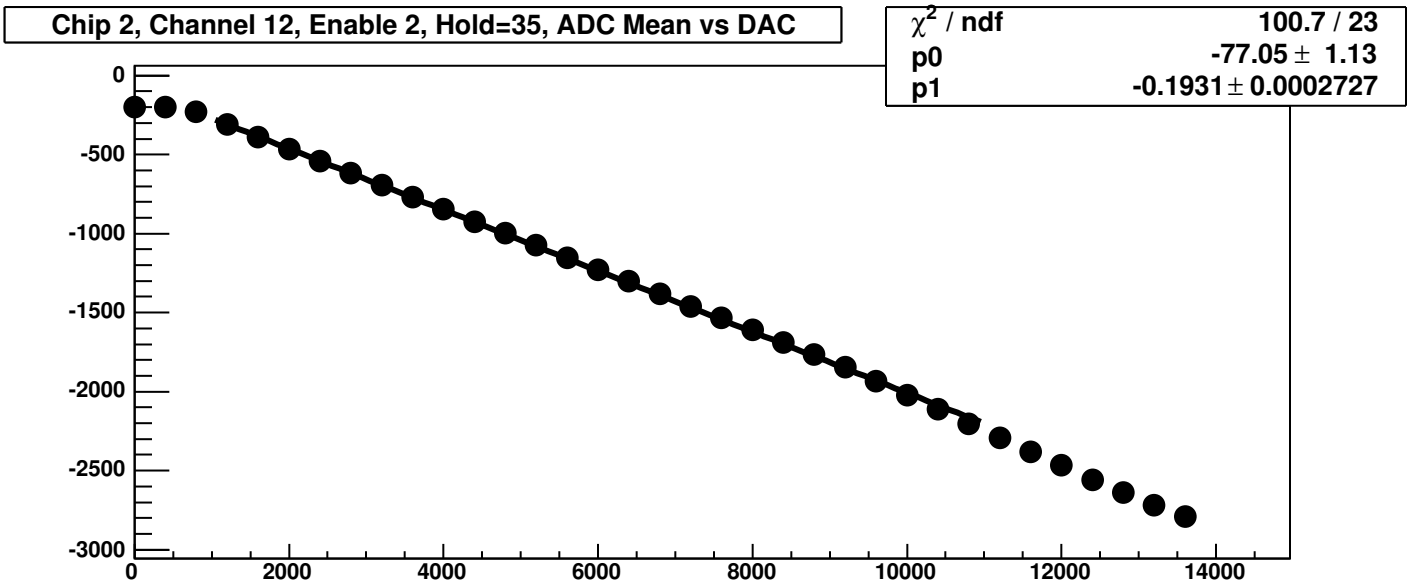

Chip 2, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

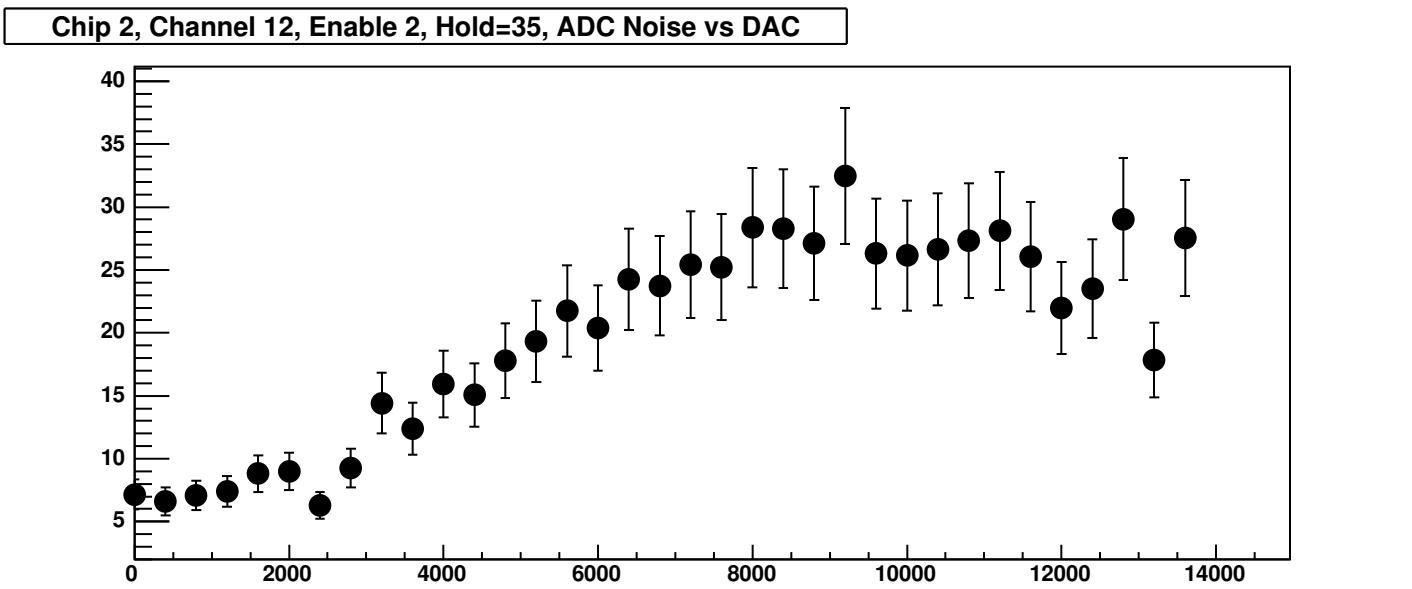

Chip 2, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

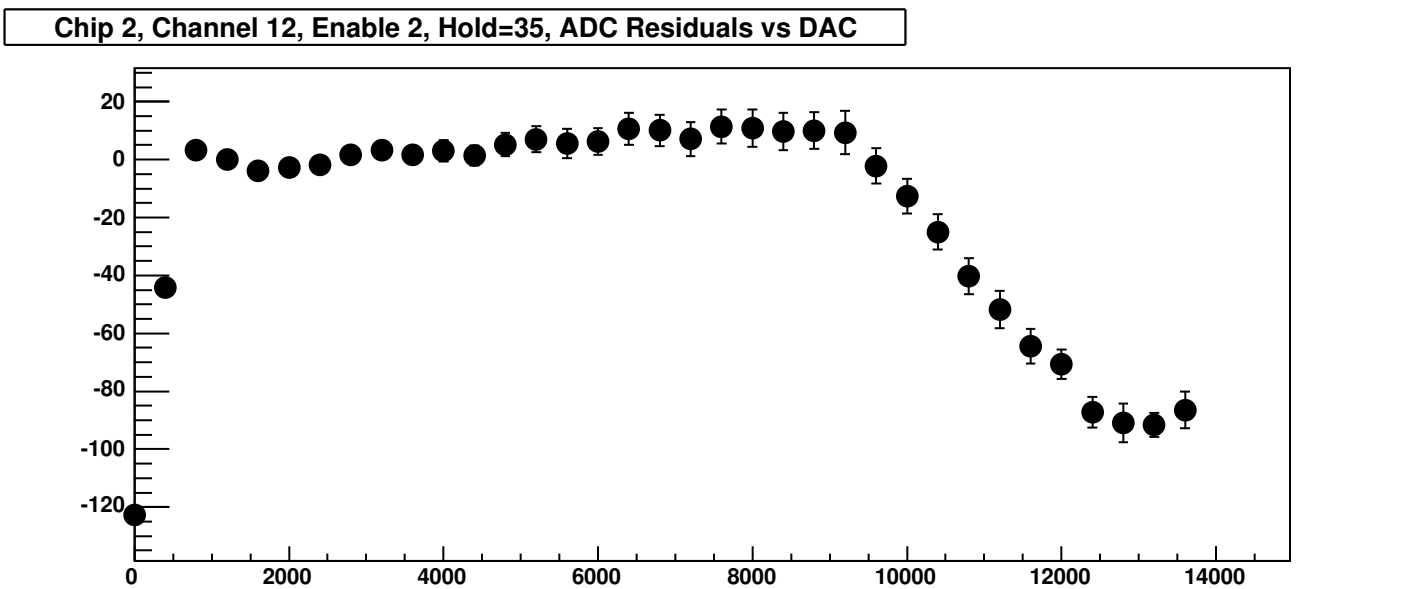

Chip 2, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 622.3 / 23
p0 -57.06 ± 1.976
p1 2.19 ± 0.0008758
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

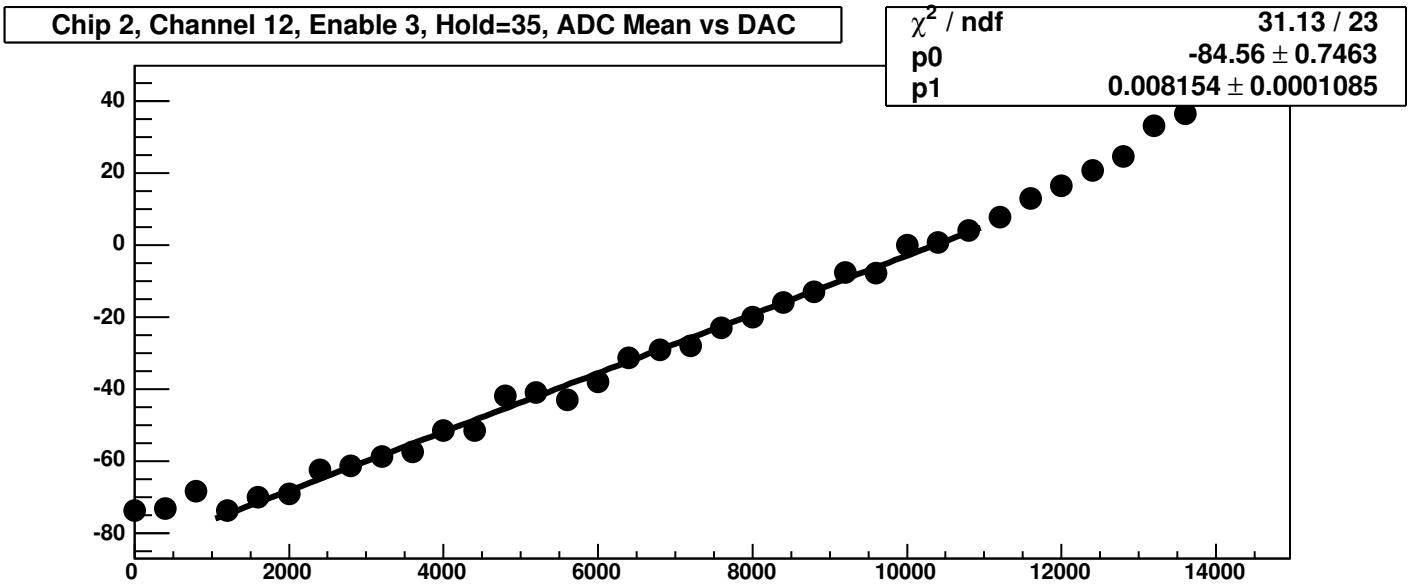

Chip 2, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

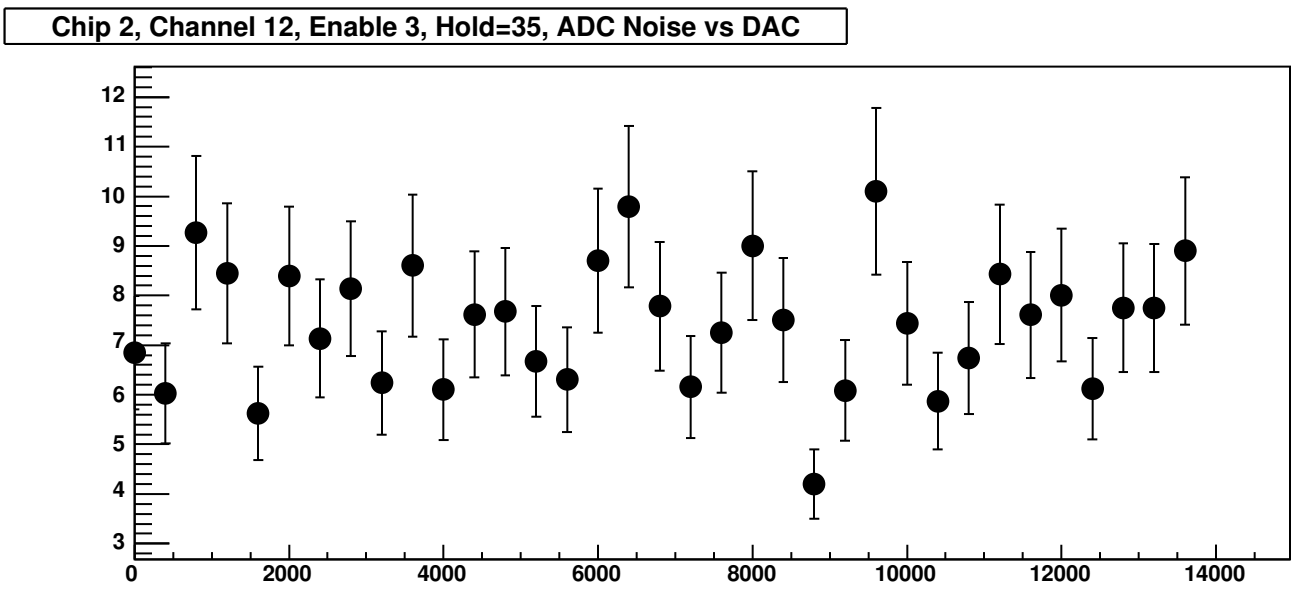

Chip 2, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

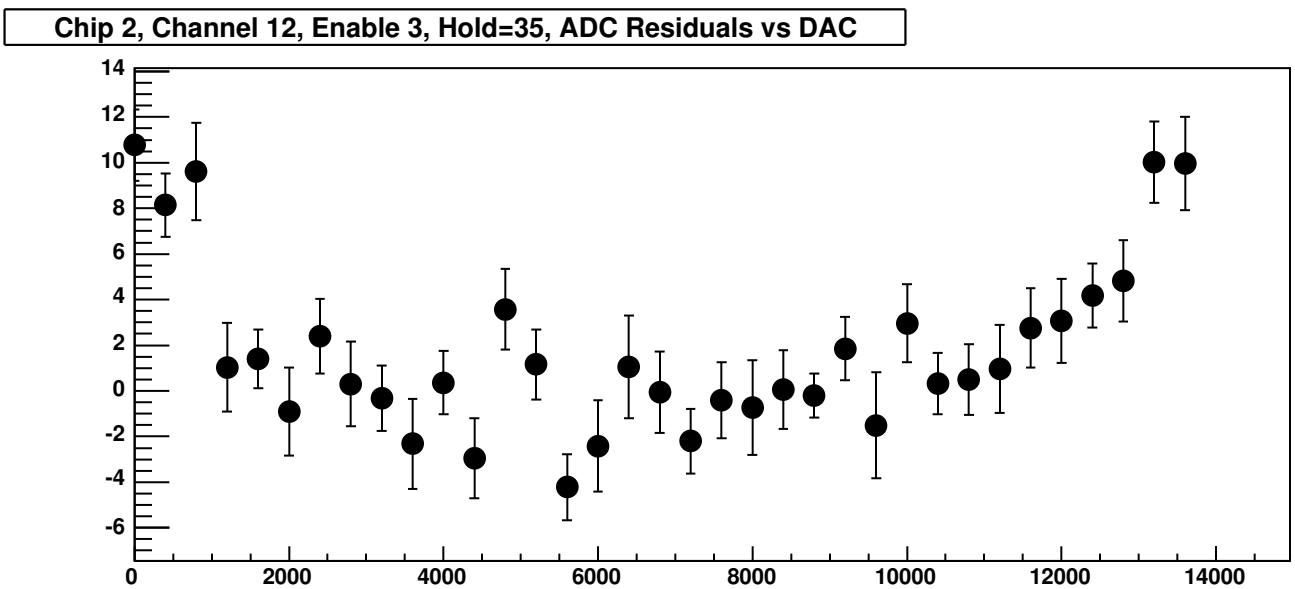

Chip 2, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 100.7 / 23
p0 -77.05 ± 1.13
p1 -0.1931 ± 0.0002727
0
-500
-1000
-1500
-2000
-2500
-3000
0
2000
4000
6000
8000
10000
12000
14000

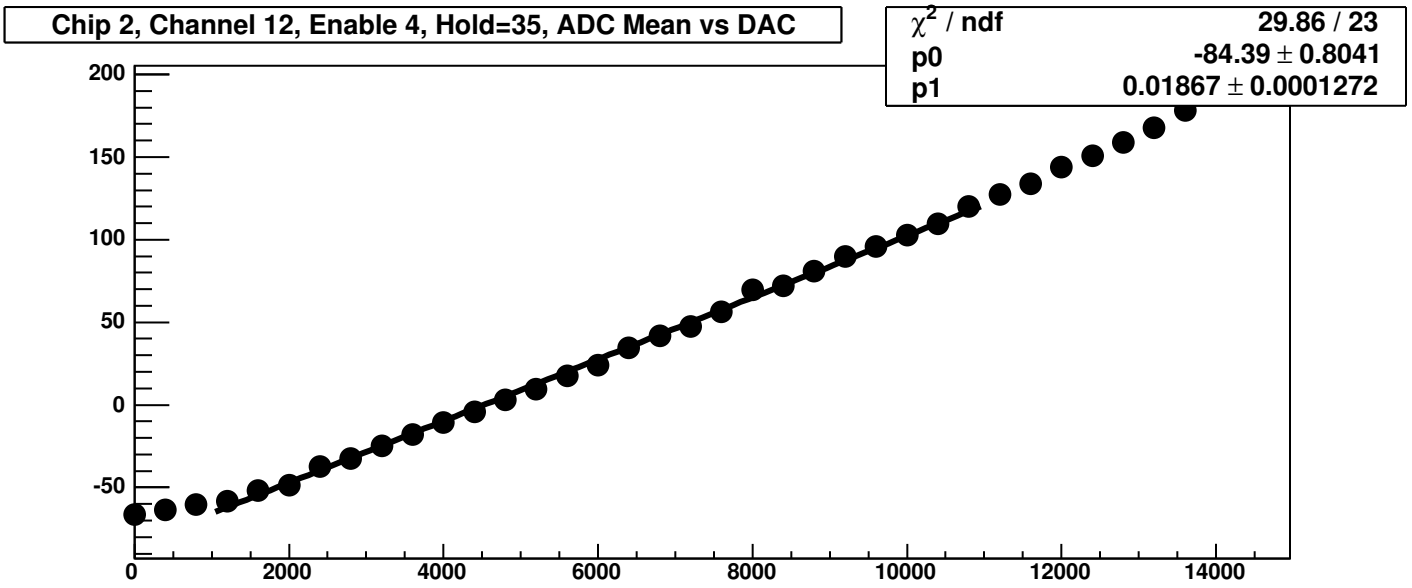

Chip 2, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

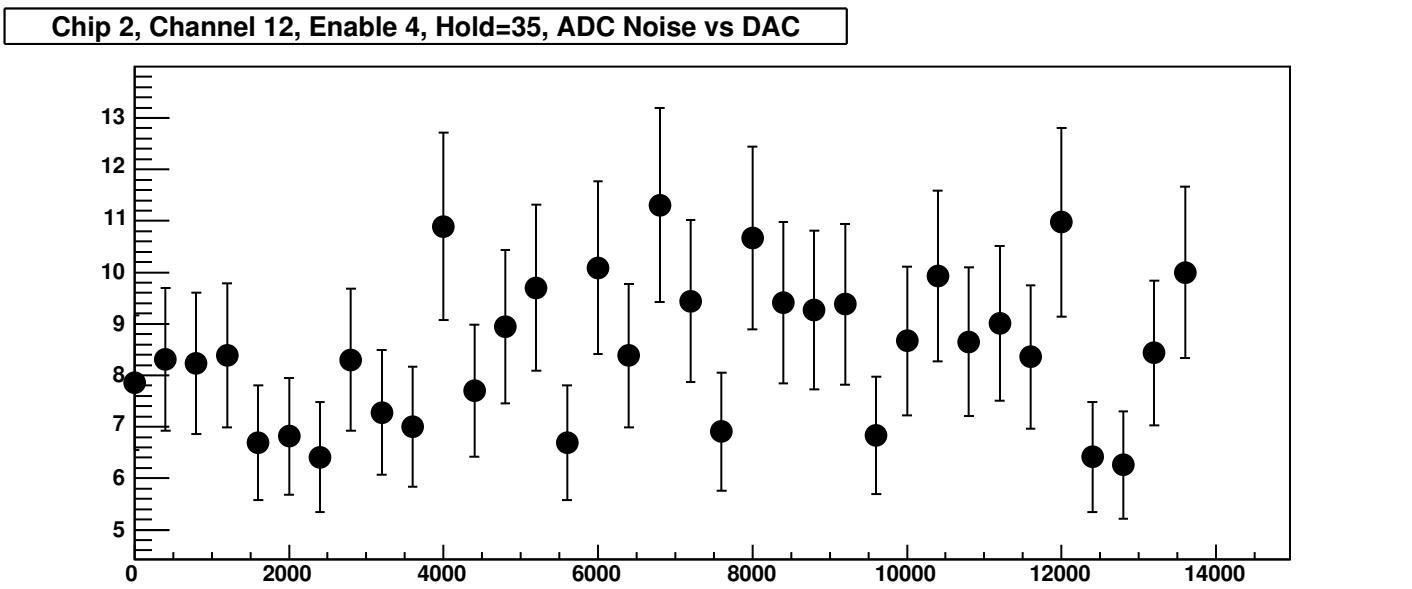

Chip 2, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
2000
4000
6000
8000
10000
12000
14000

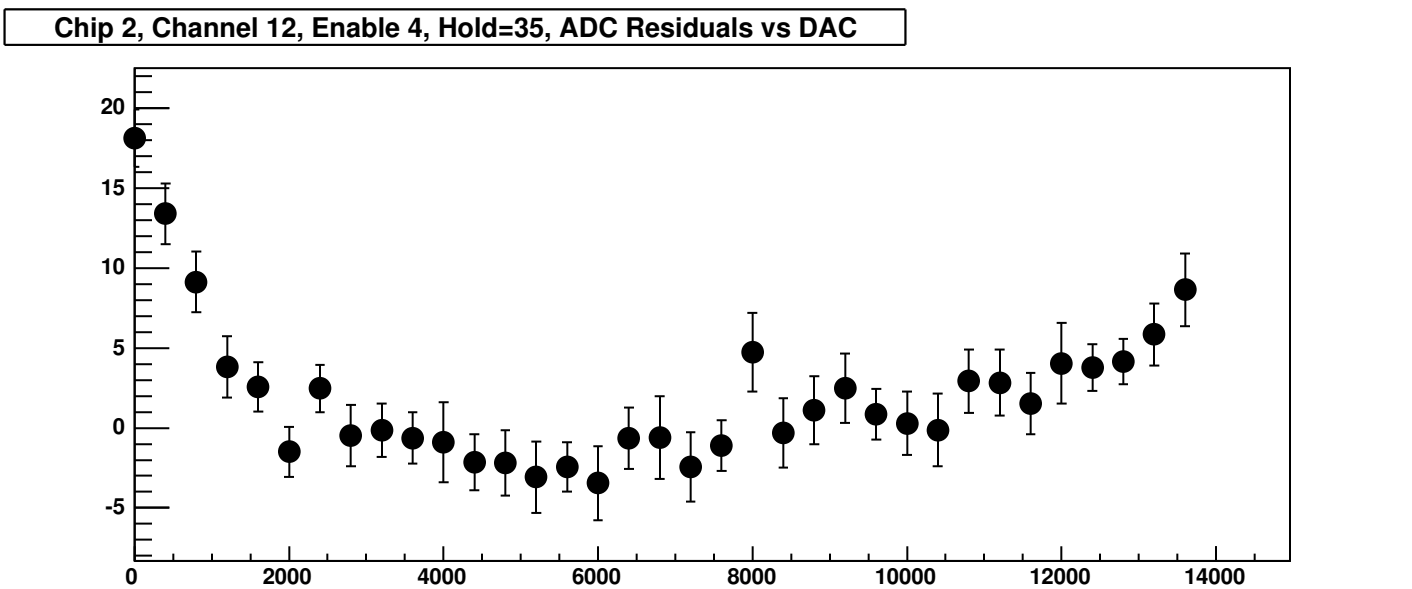

Chip 2, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.13 / 23
p0 -84.56 ± 0.7463
p1 0.008154 ± 0.0001085
40
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000

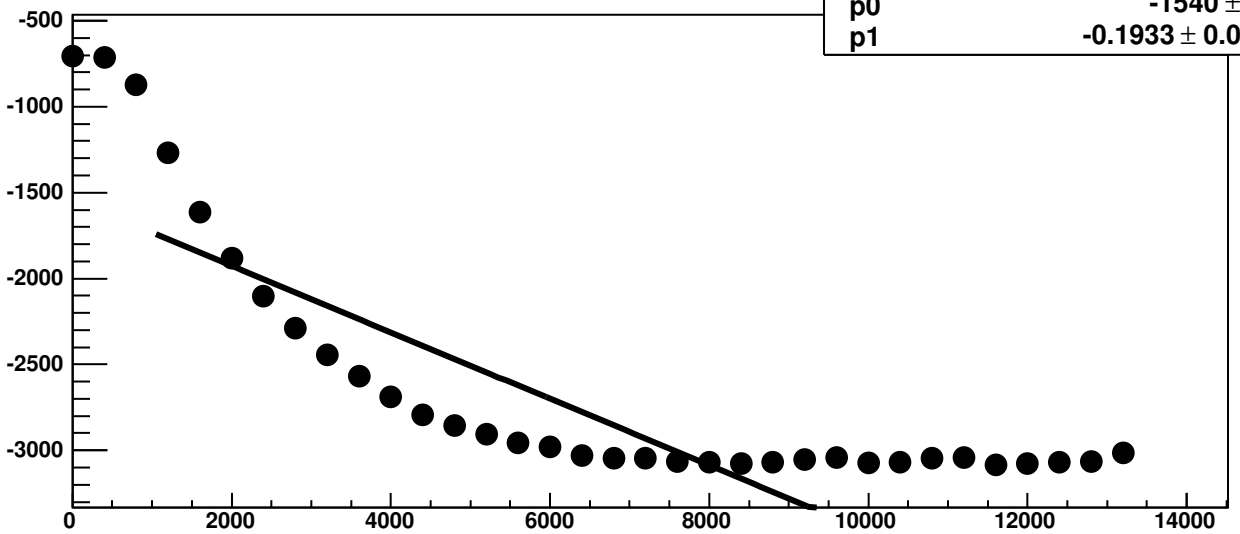

Chip 2, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.86 / 23
p0 -84.39 ± 0.8041
p1 0.01867 ± 0.0001272
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

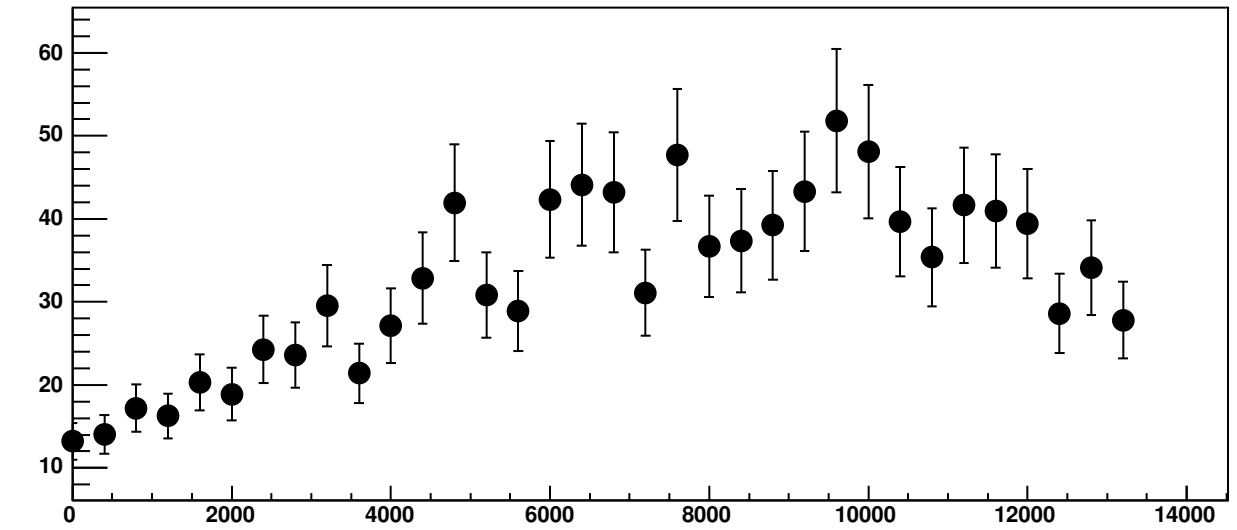

Chip 2, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

Chip 2, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

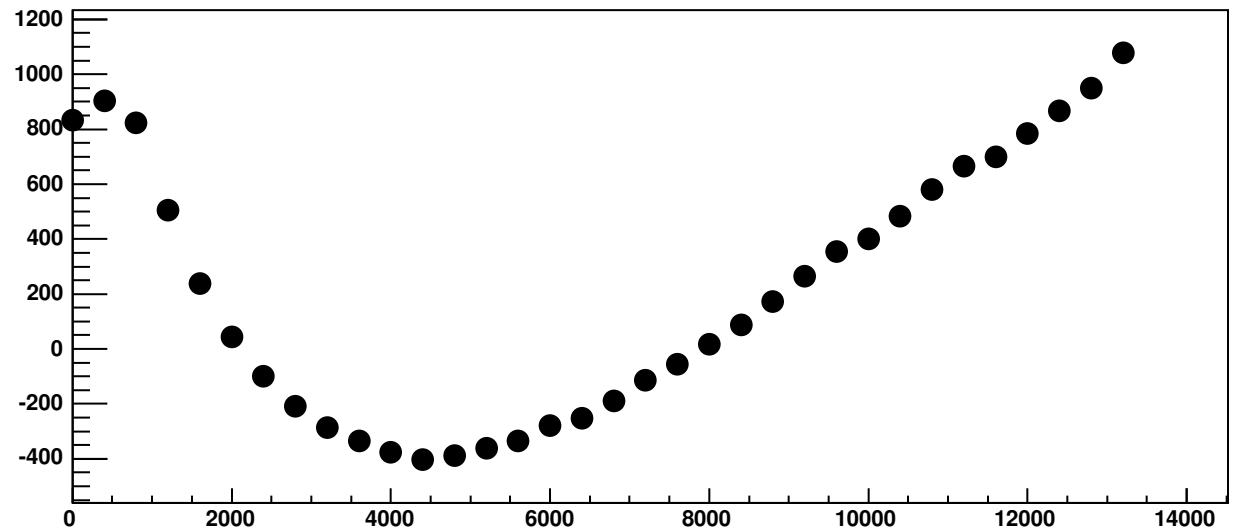
χ^2 / ndf	5.593e+04 / 23
p0	-1540 ± 2.463
p1	-0.1933 ± 0.000473

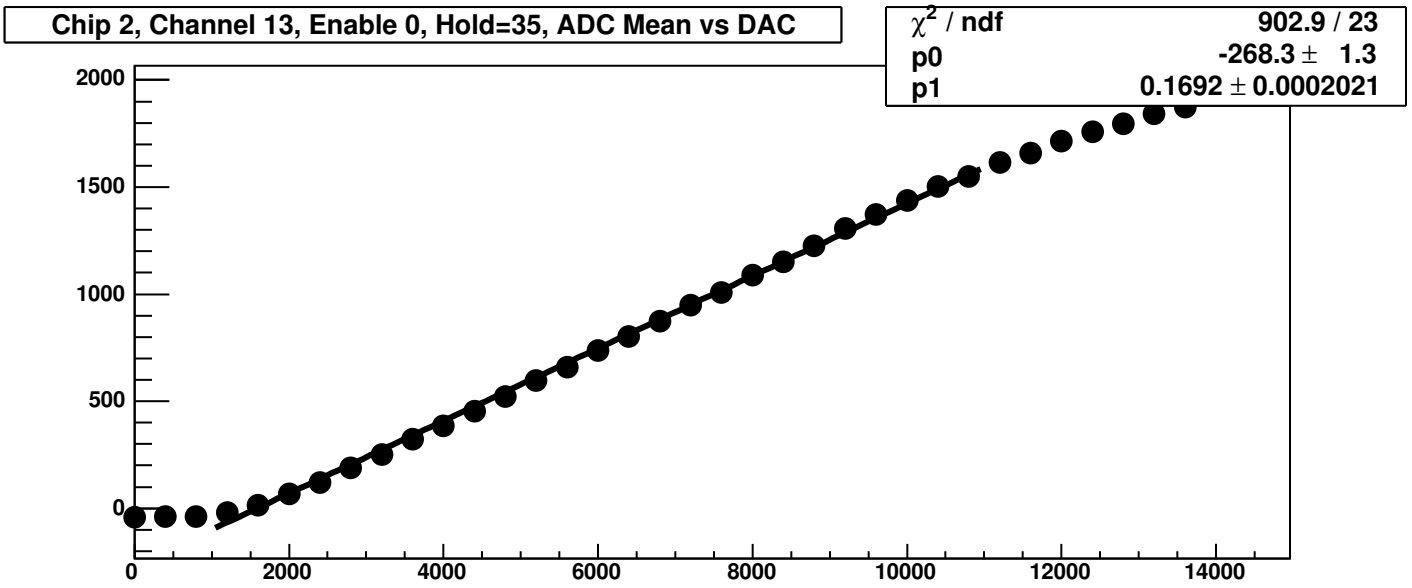


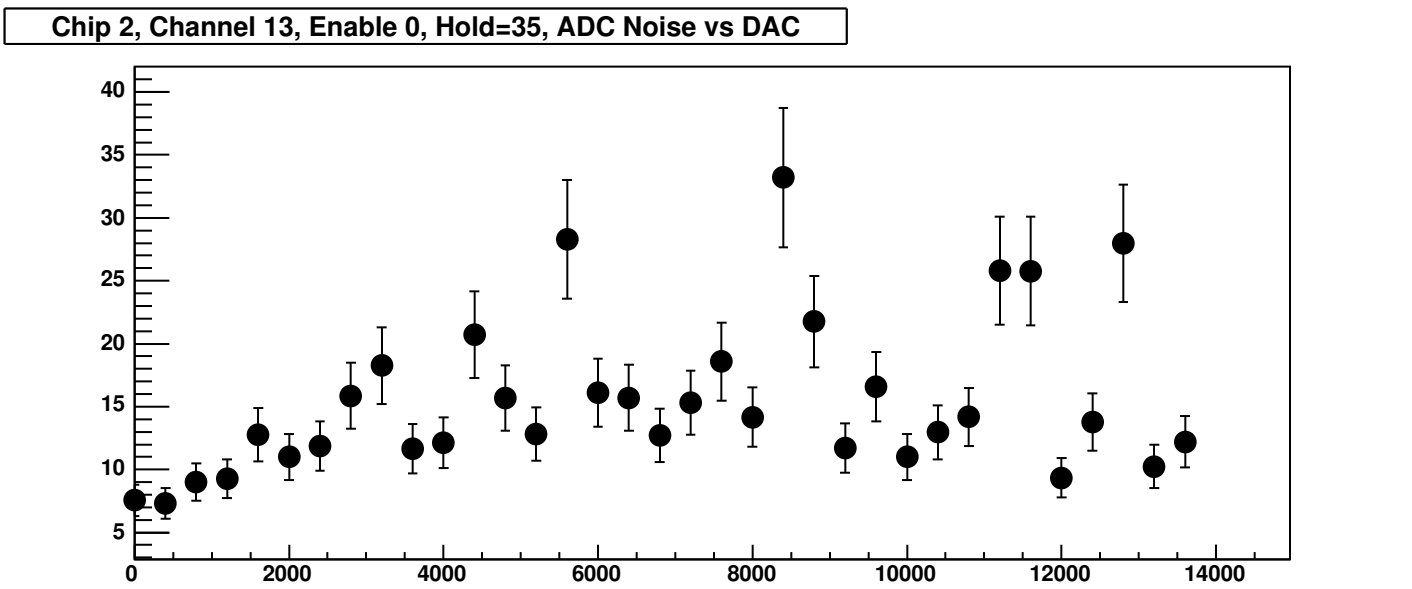
Chip 2, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

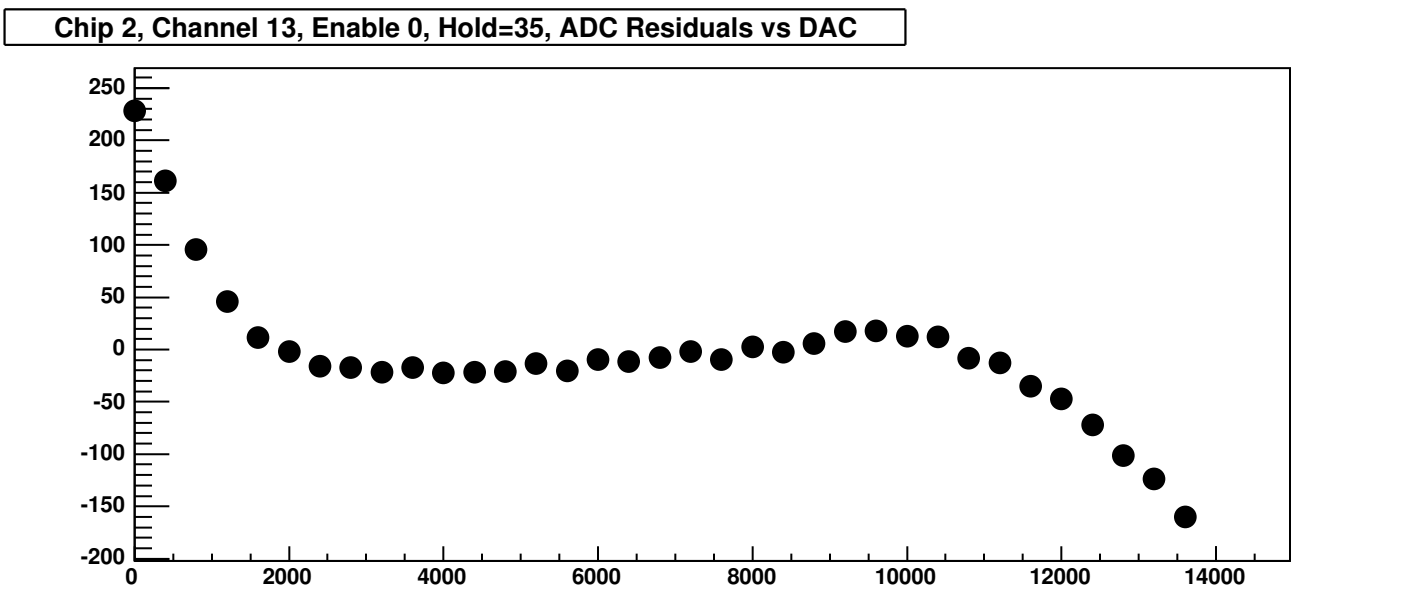


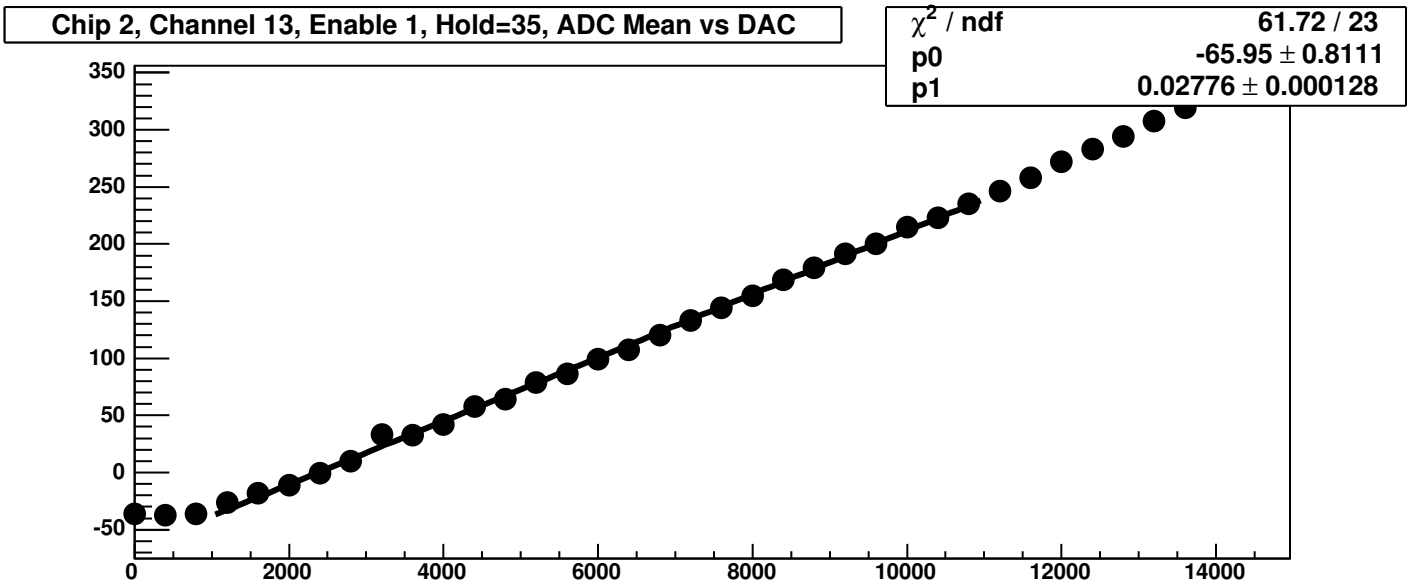
Chip 2, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

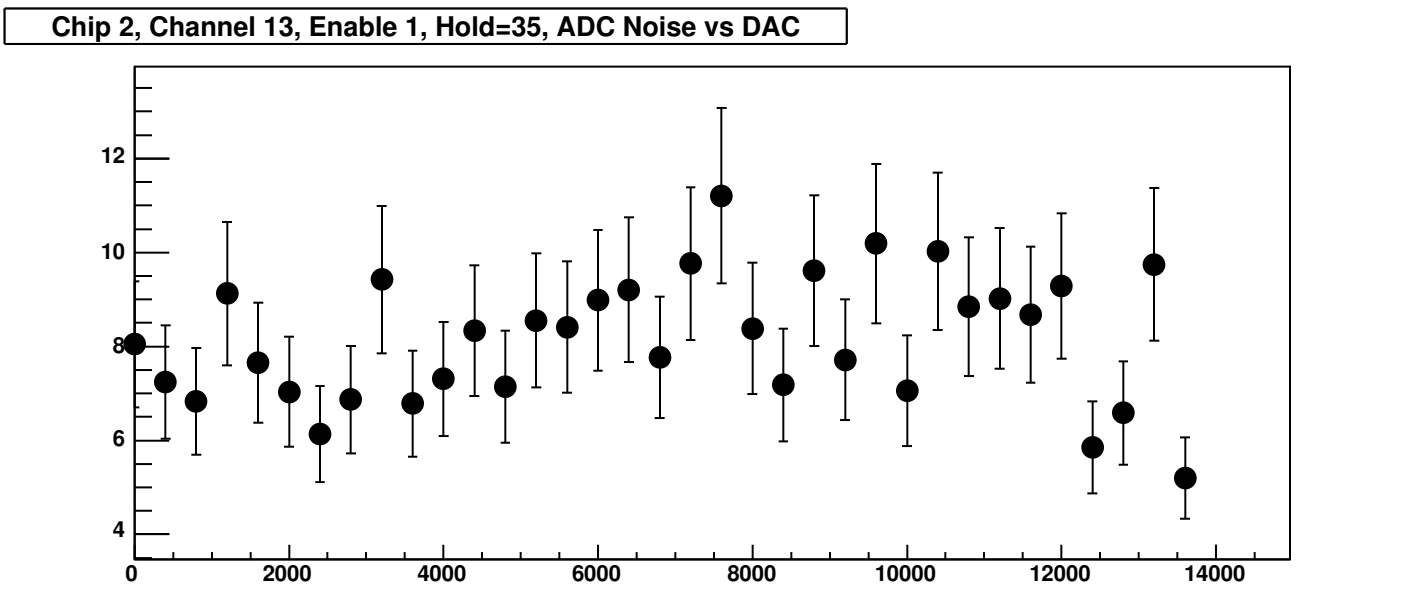


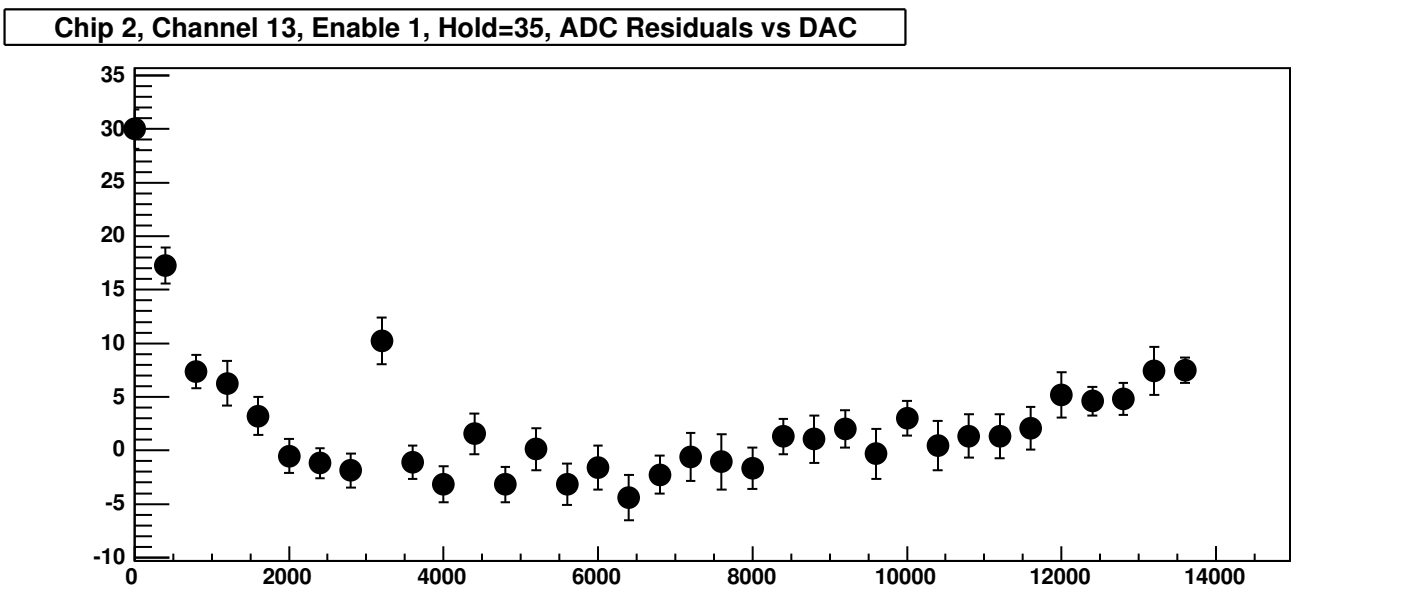

Chip 2, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 902.9 / 23
p0 -268.3 ± 1.3
p1 0.1692 ± 0.0002021
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

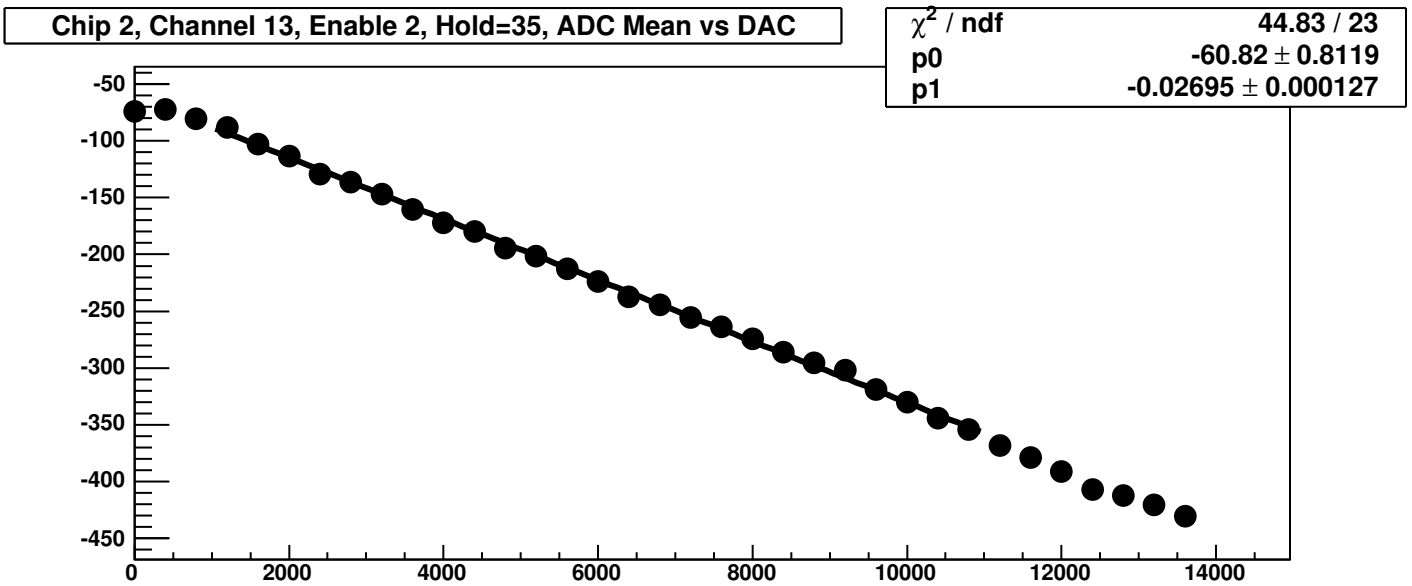

Chip 2, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

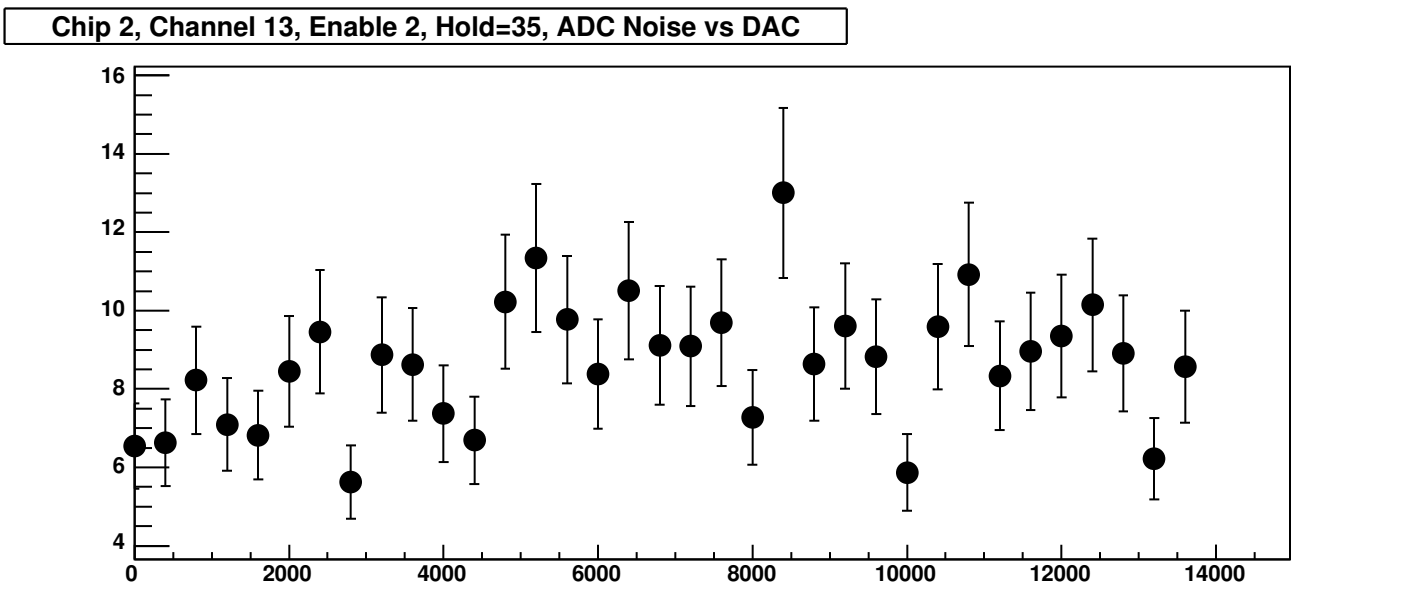

Chip 2, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

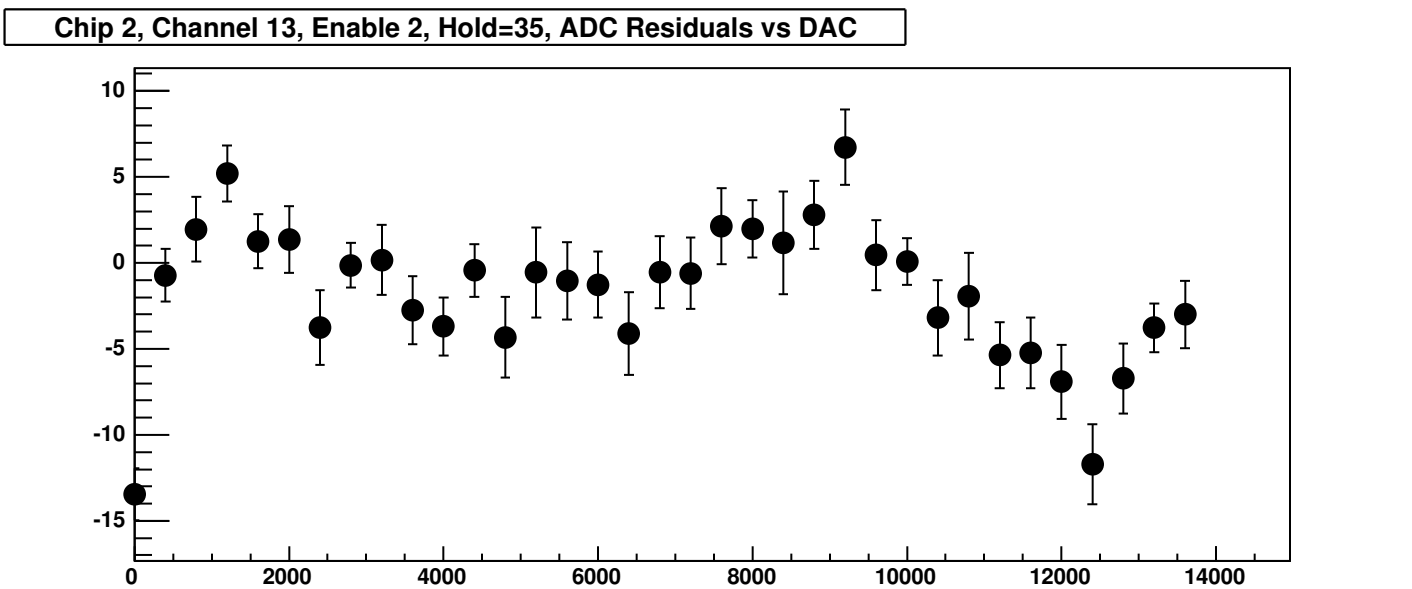

Chip 2, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 61.72 / 23
p0 -65.95 ± 0.8111
p1 0.02776 ± 0.000128
350
300
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

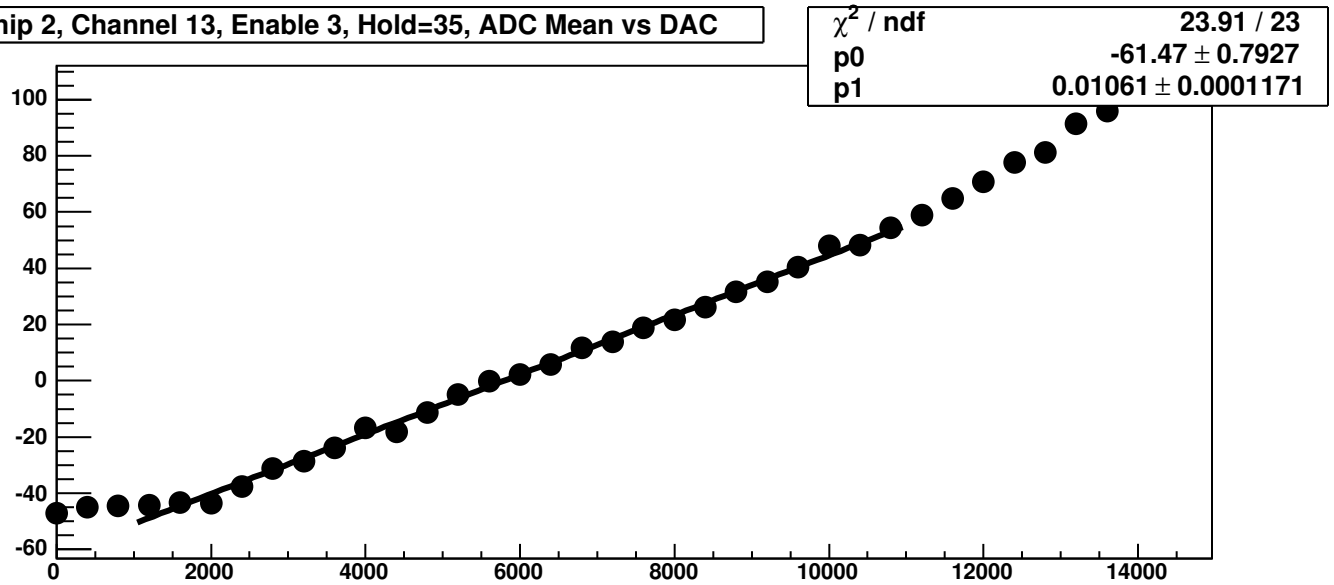

Chip 2, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 44.83 / 23
p0 -60.82 ± 0.8119
p1 -0.02695 ± 0.000127
-50
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

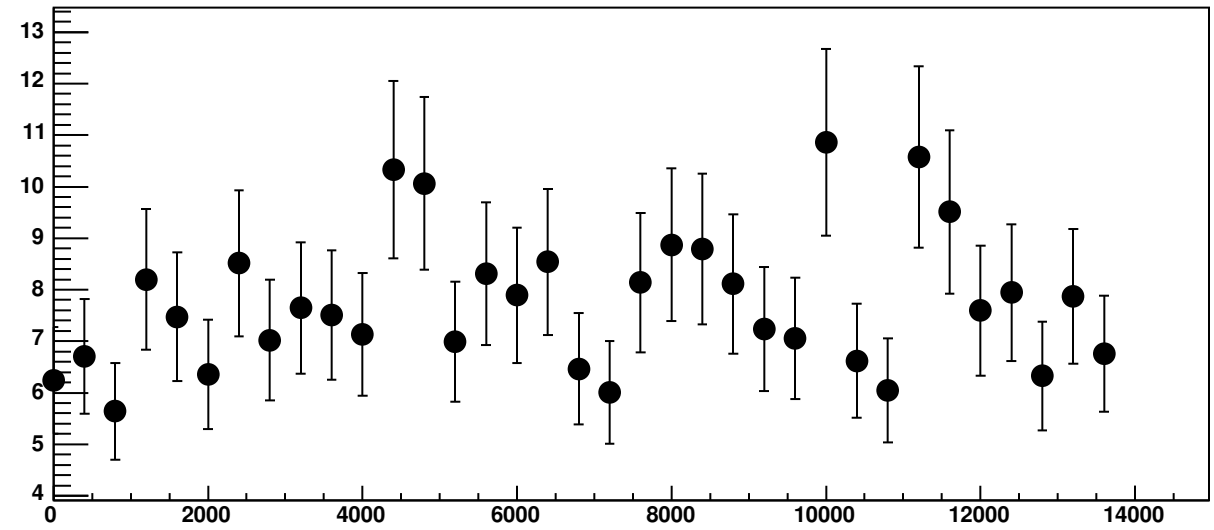

Chip 2, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

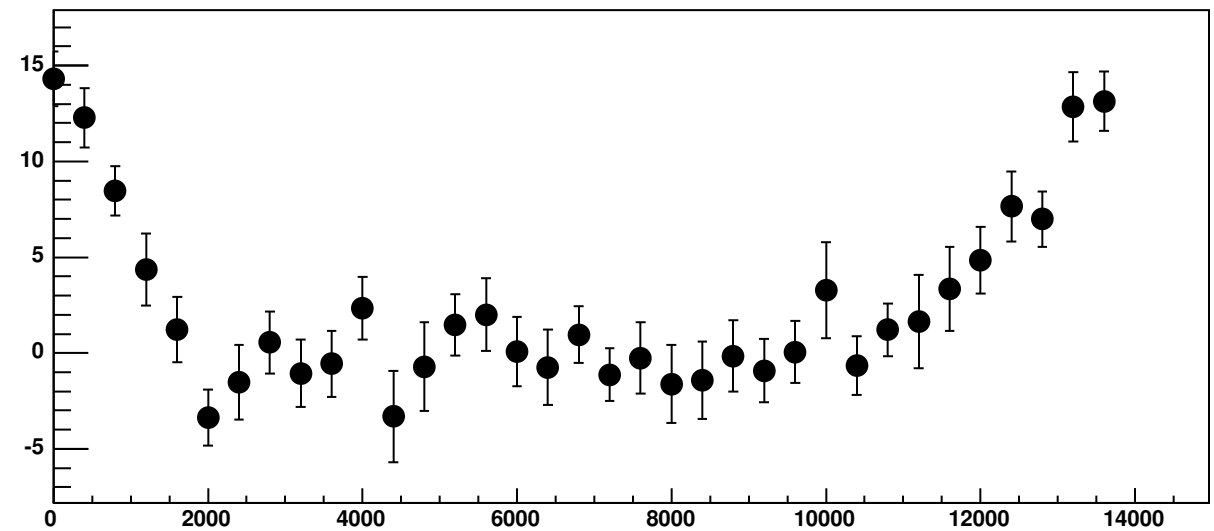
Chip 2, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

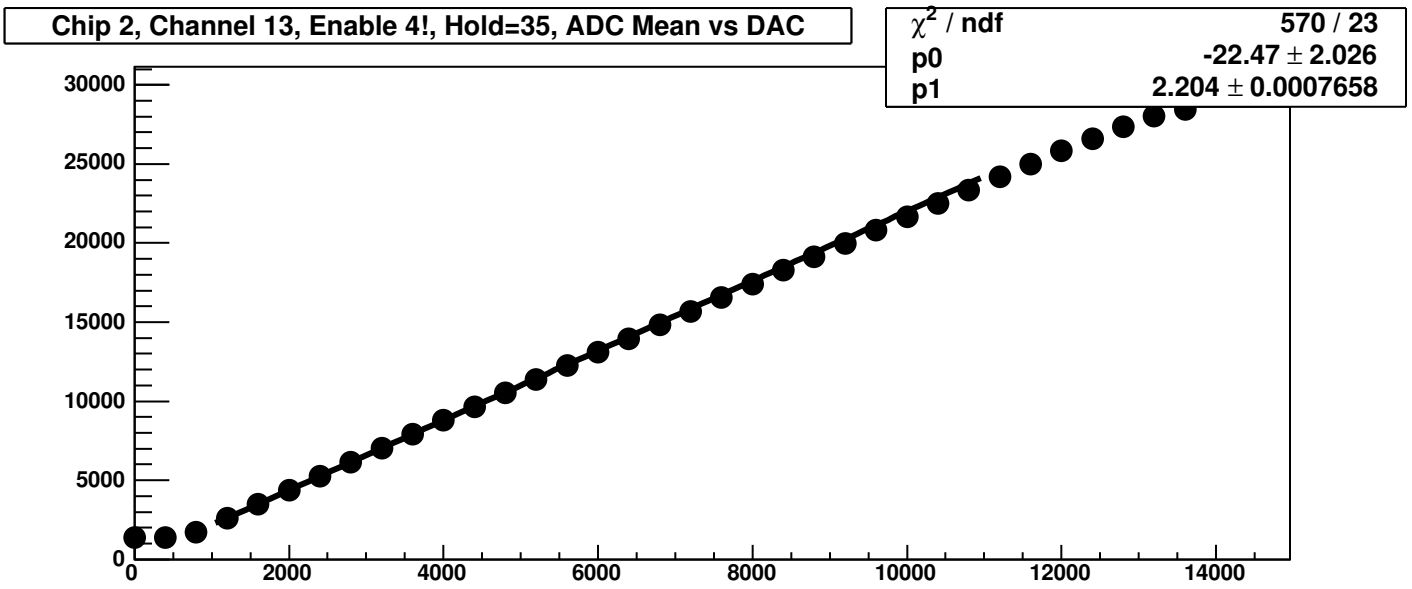


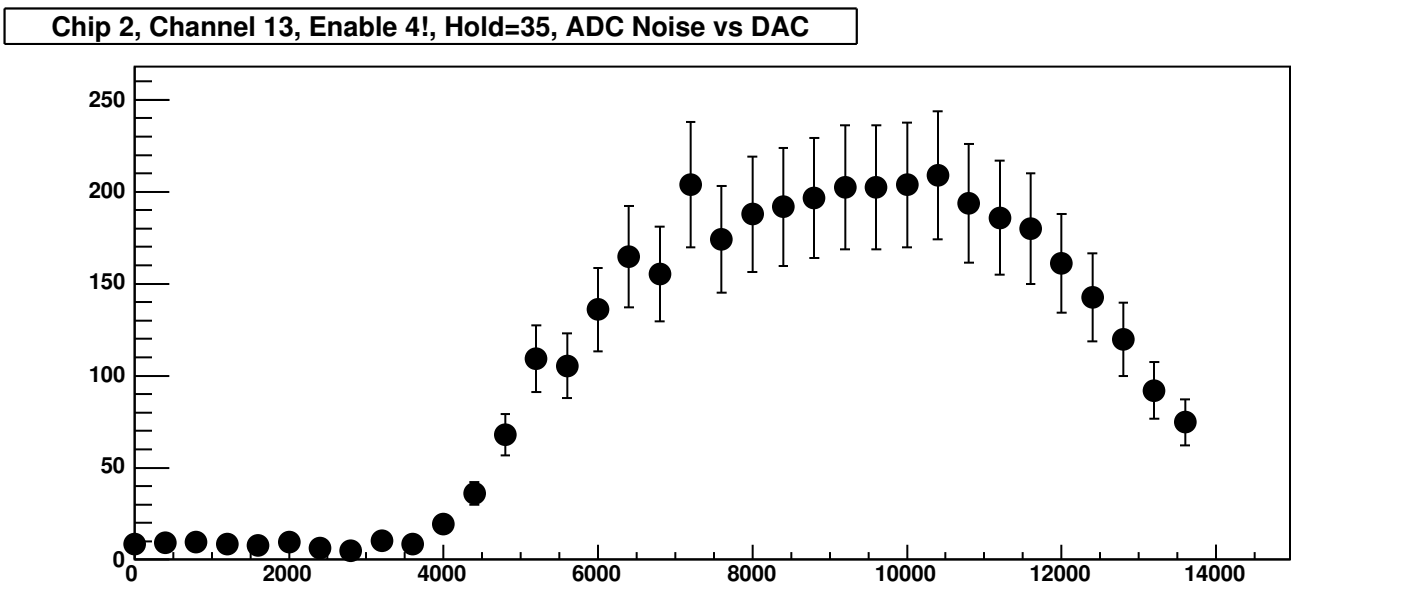
Chip 2, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

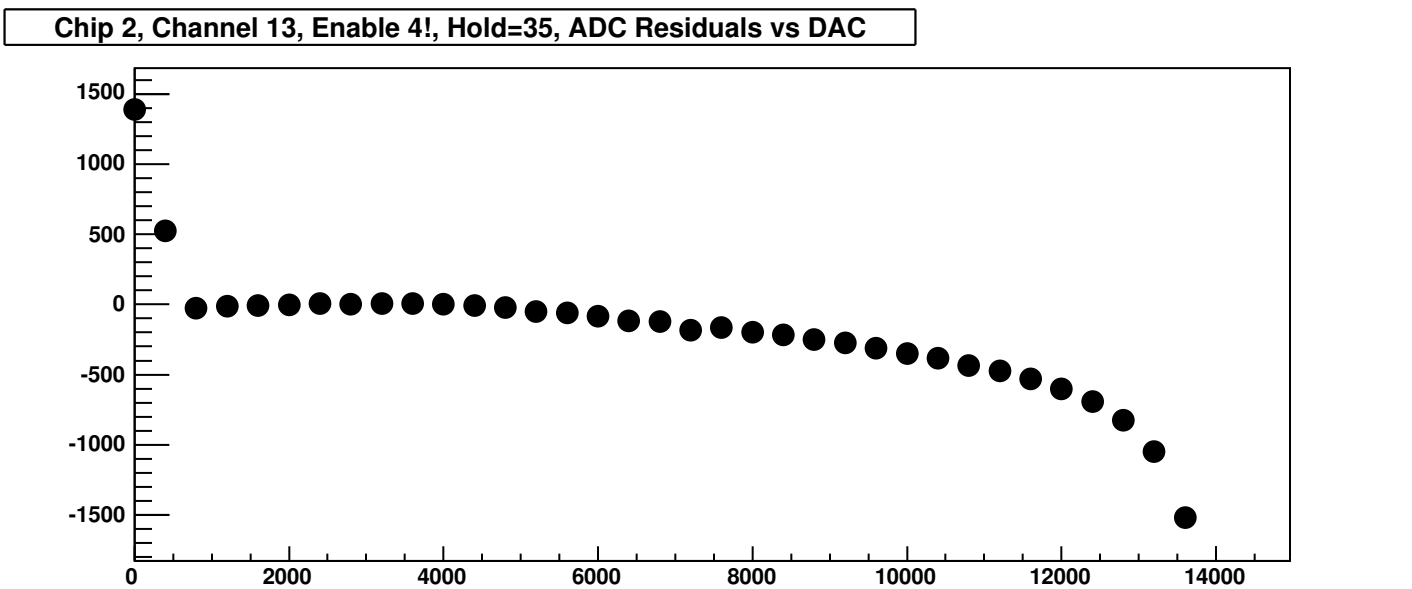


Chip 2, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC



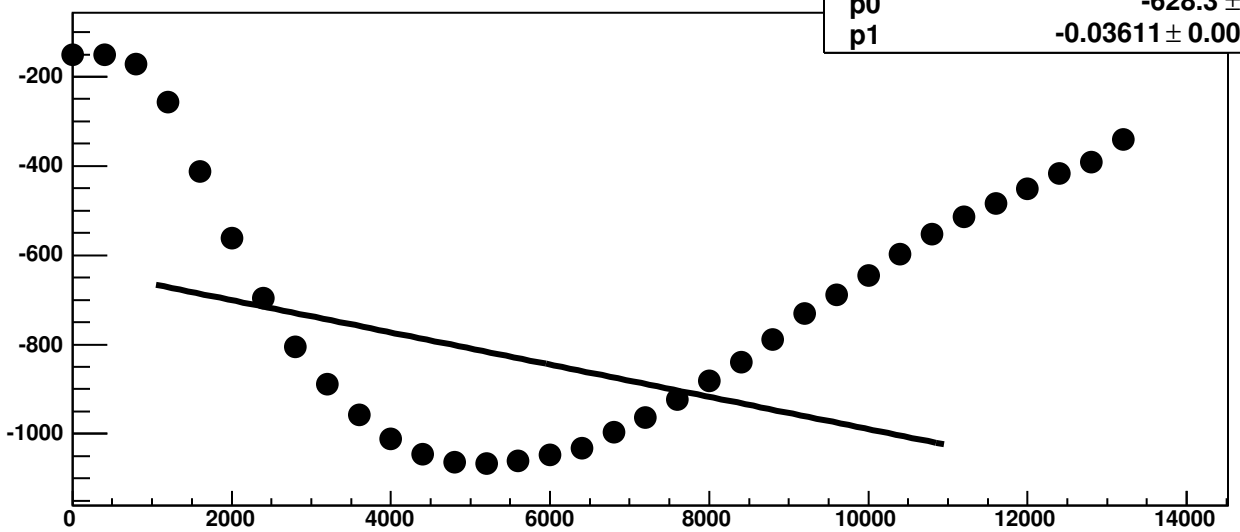

Chip 2, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 570 / 23
p0 -22.47 ± 2.026
p1 2.204 ± 0.0007658
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

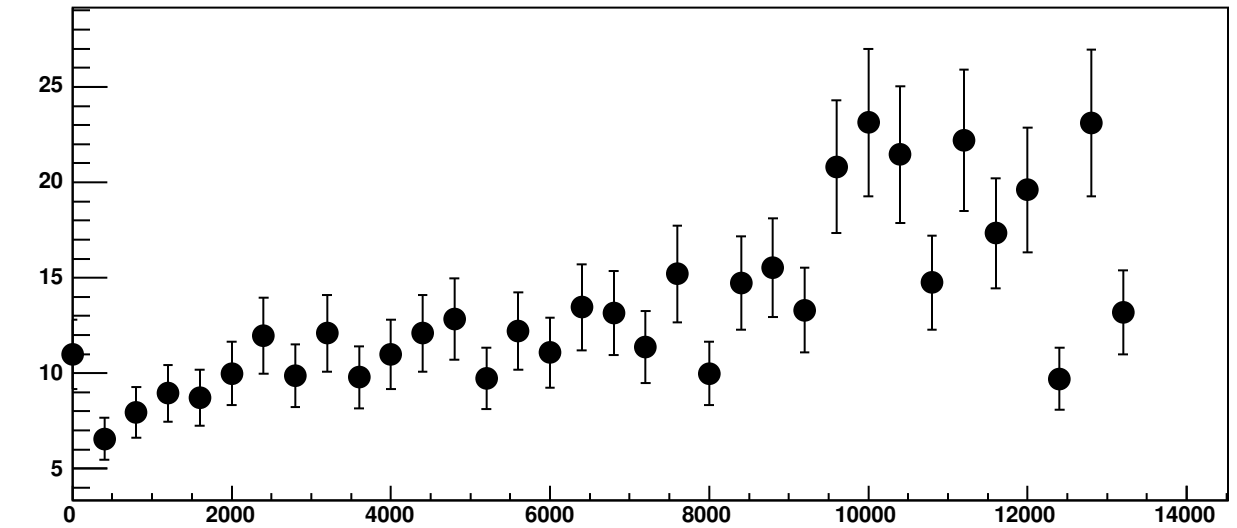

Chip 2, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

Chip 2, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

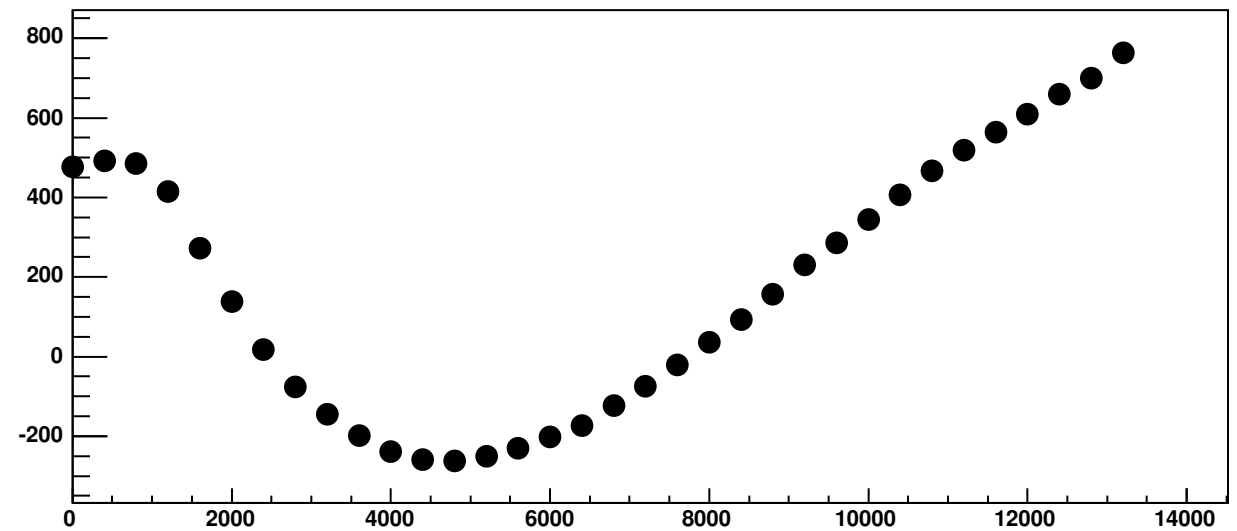
χ^2 / ndf	1.742e+05 / 23
p0	-628.3 ± 1.147
p1	-0.03611 ± 0.0002014

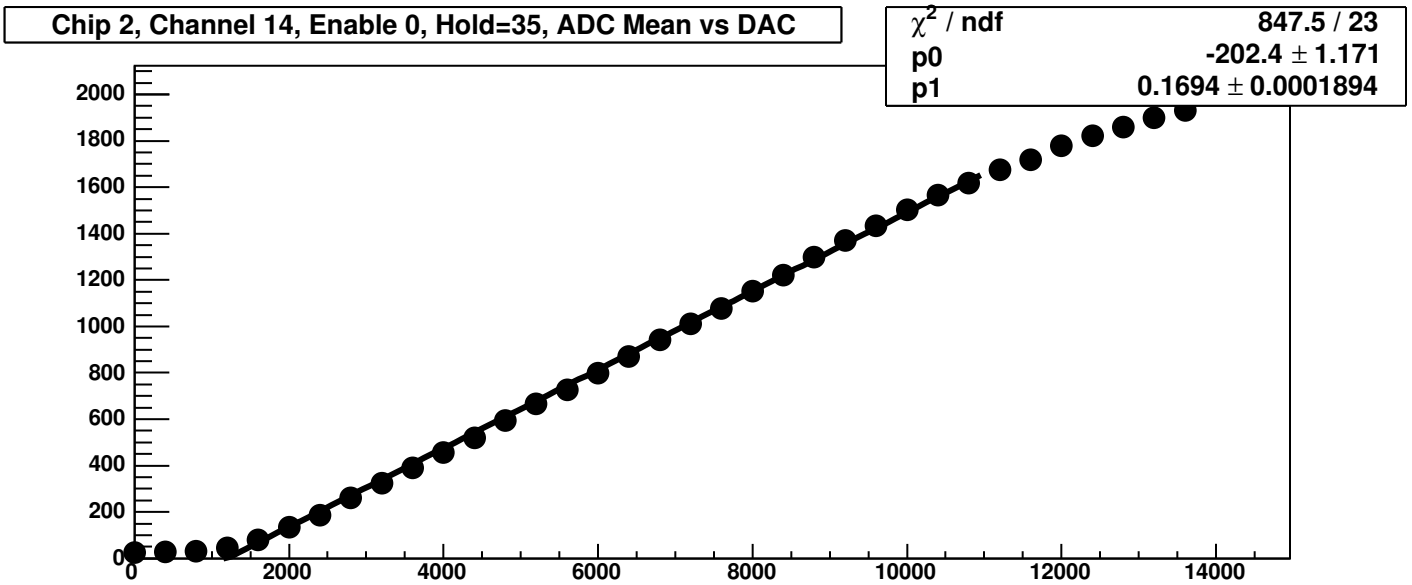


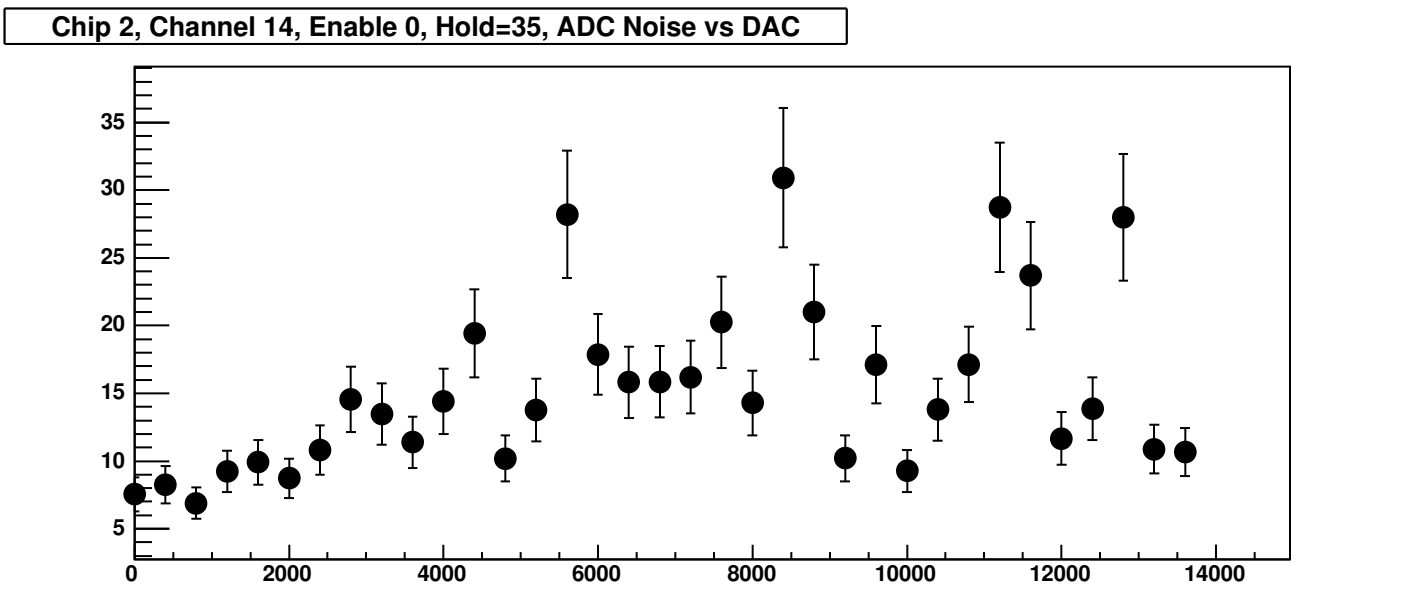
Chip 2, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

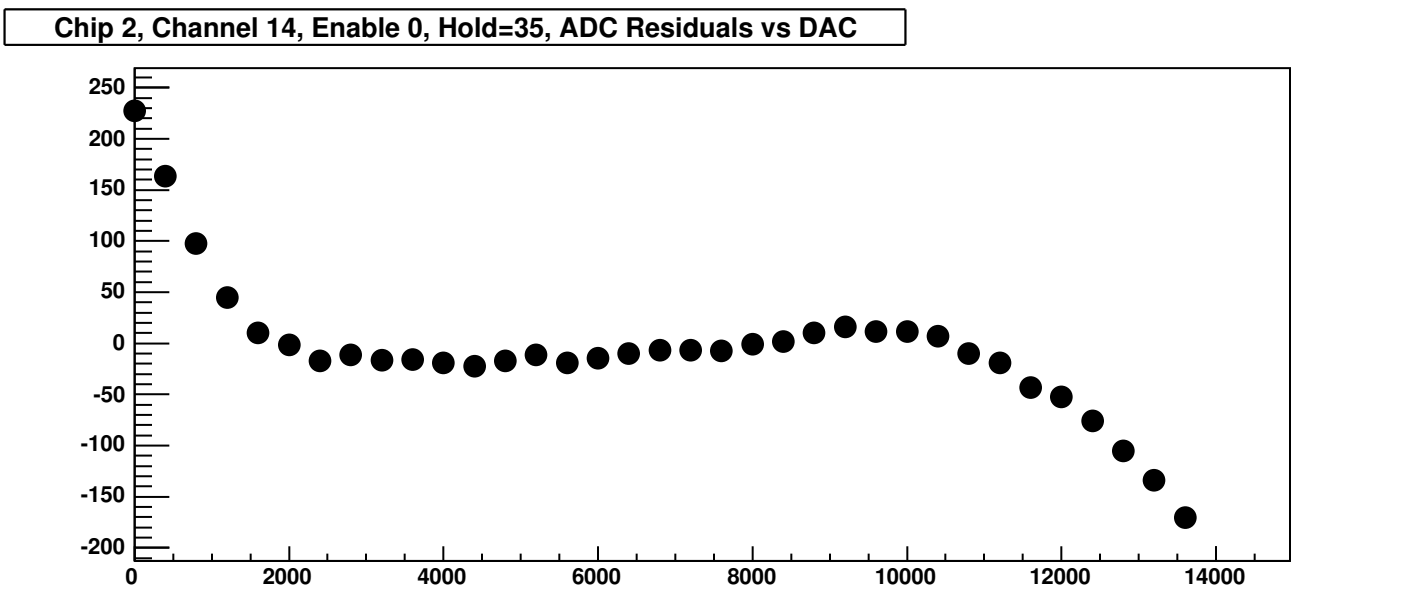


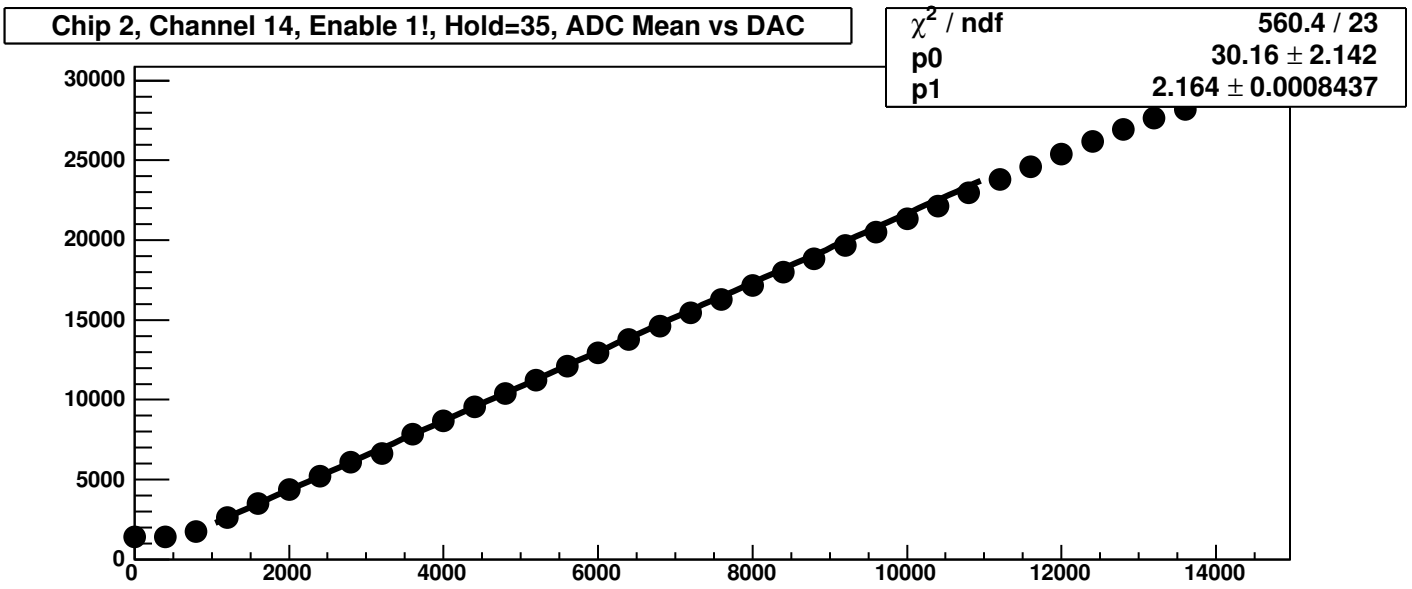
Chip 2, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

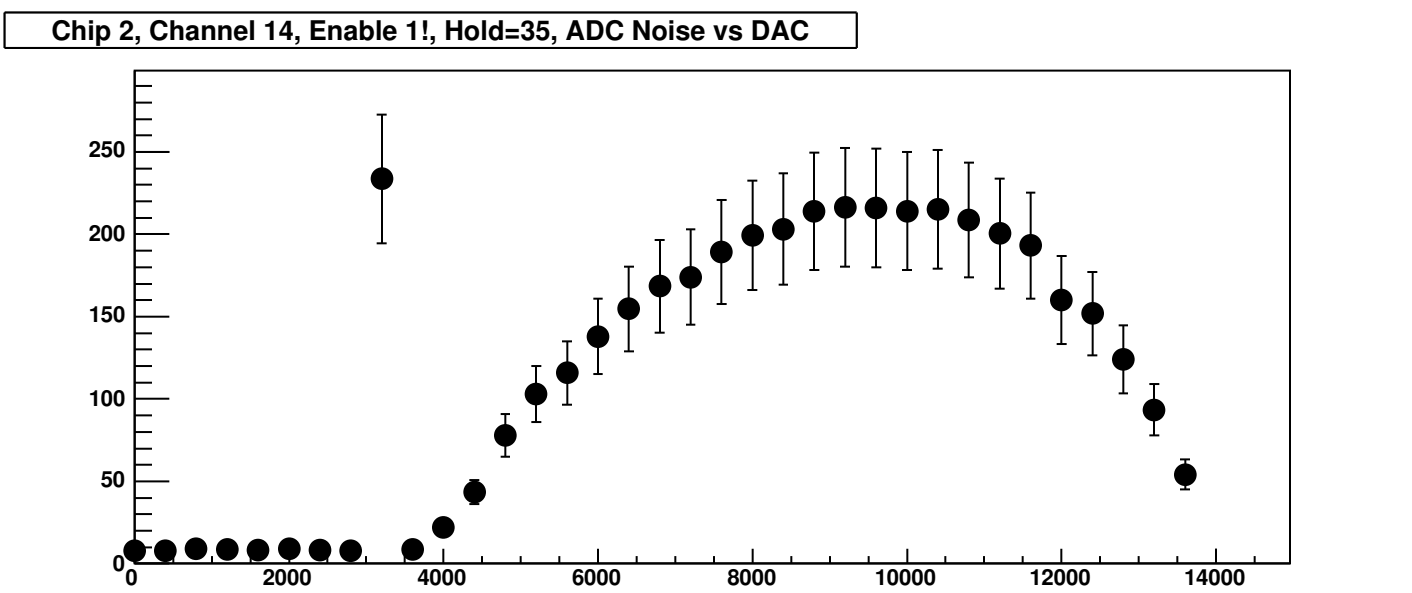


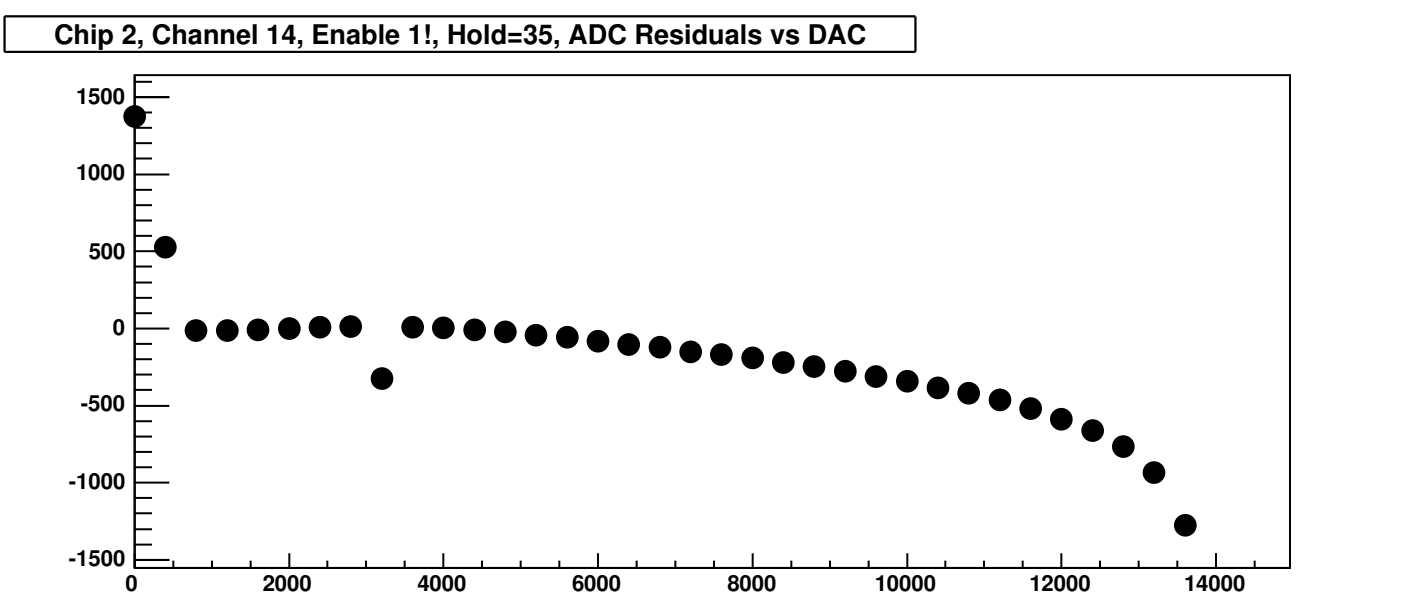

Chip 2, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 847.5 / 23
p0 -202.4 ± 1.171
p1 0.1694 ± 0.0001894
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

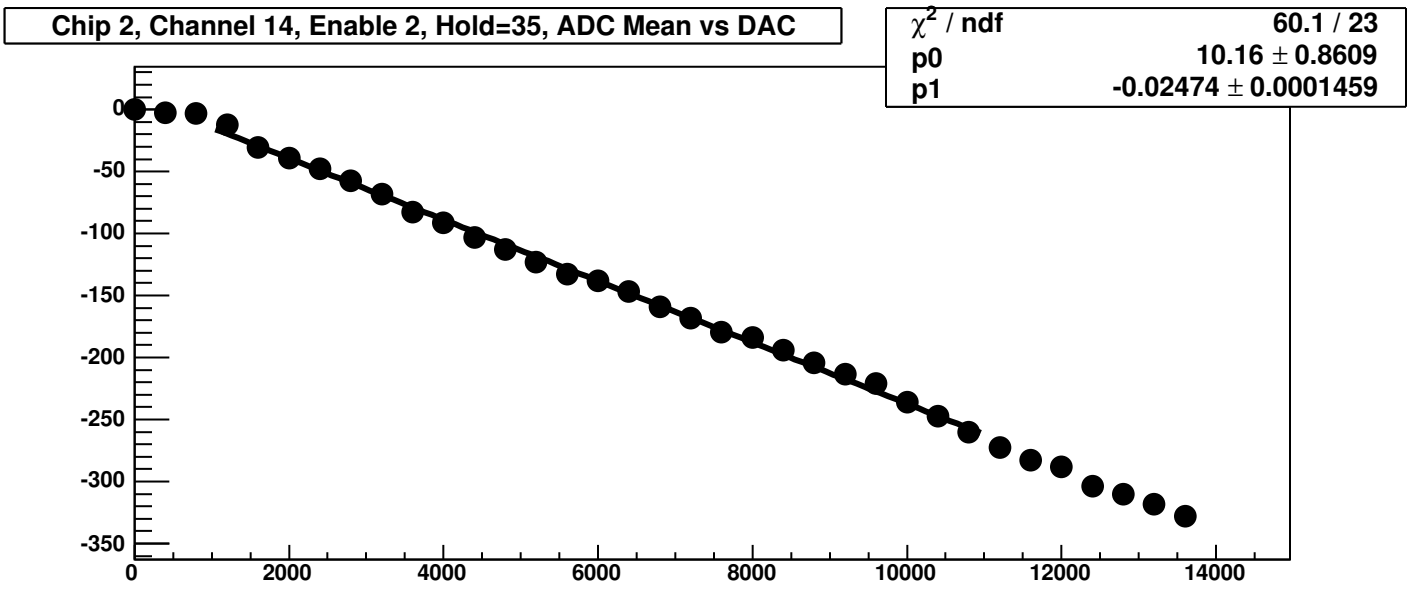

Chip 2, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

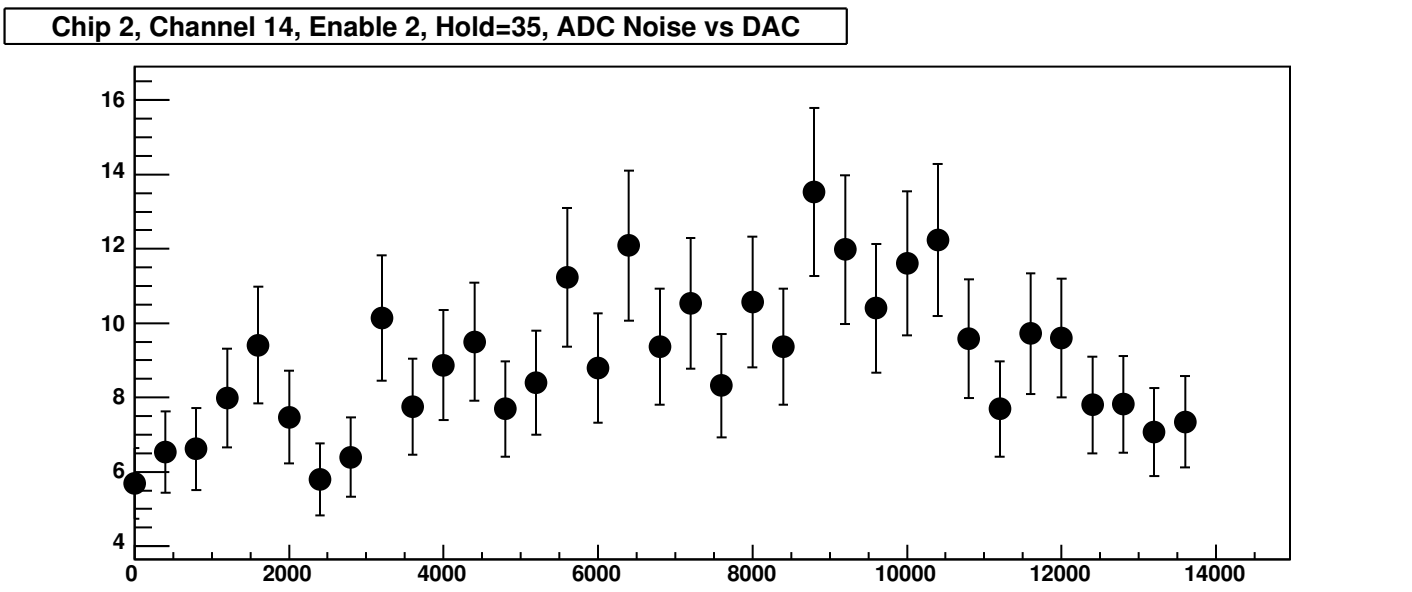

Chip 2, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

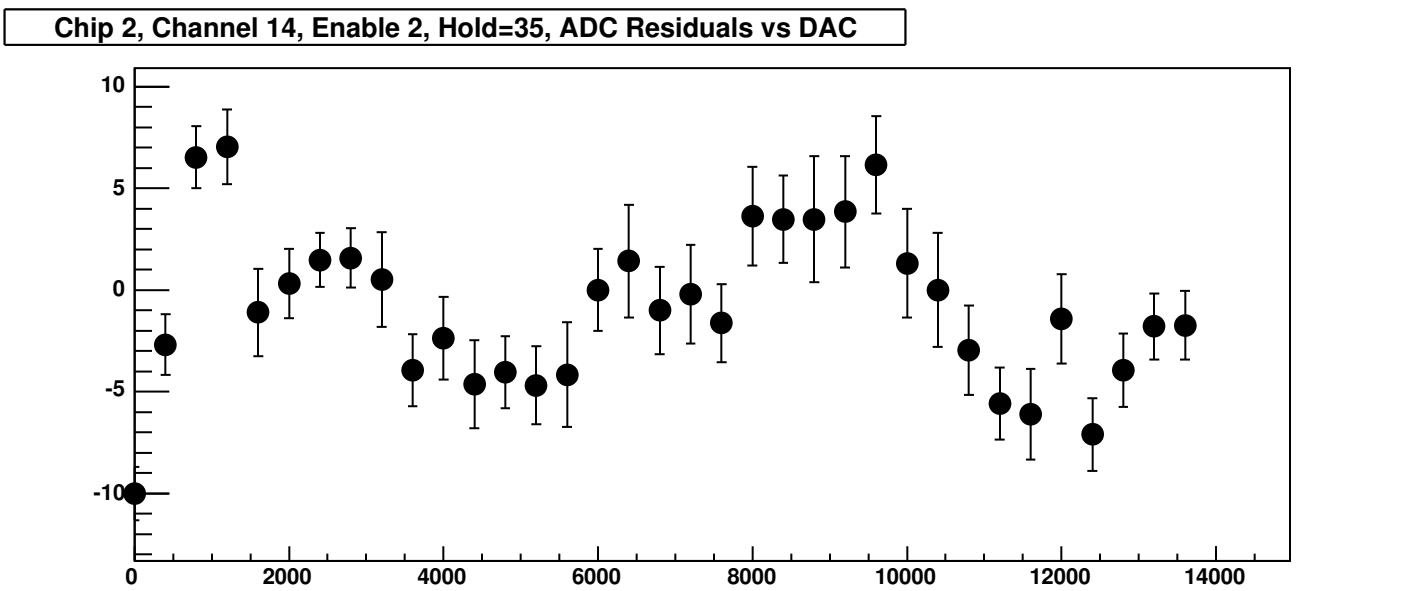

Chip 2, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 560.4 / 23
p0 30.16 ± 2.142
p1 2.164 ± 0.0008437
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

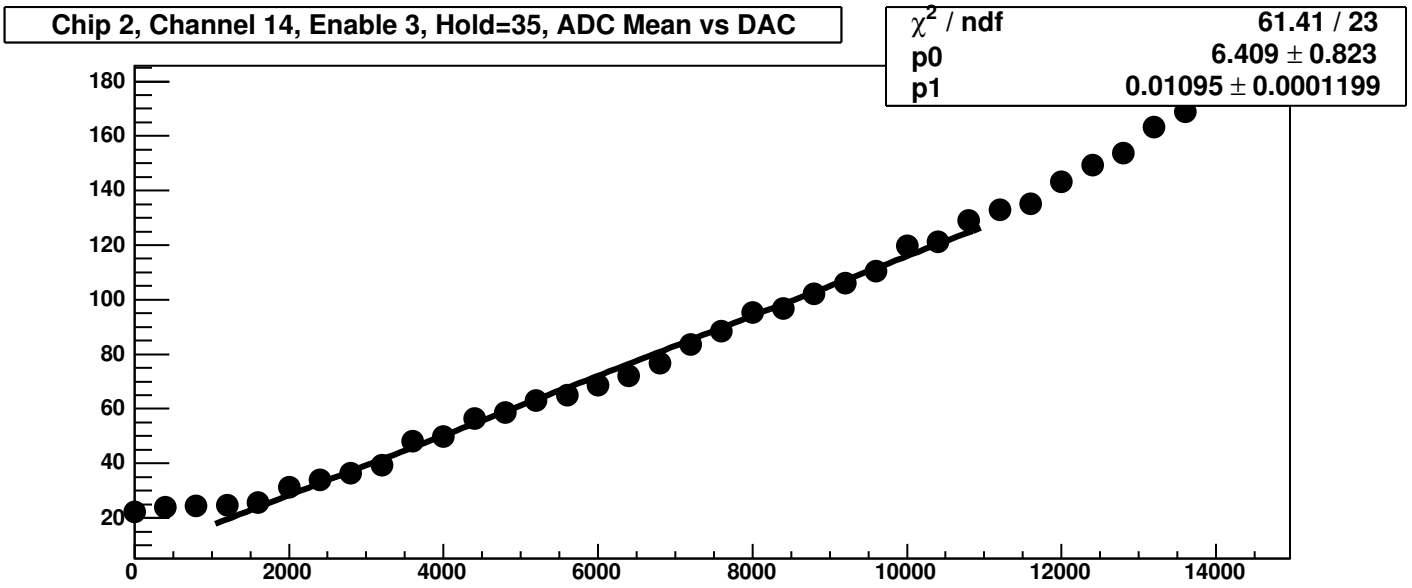

Chip 2, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

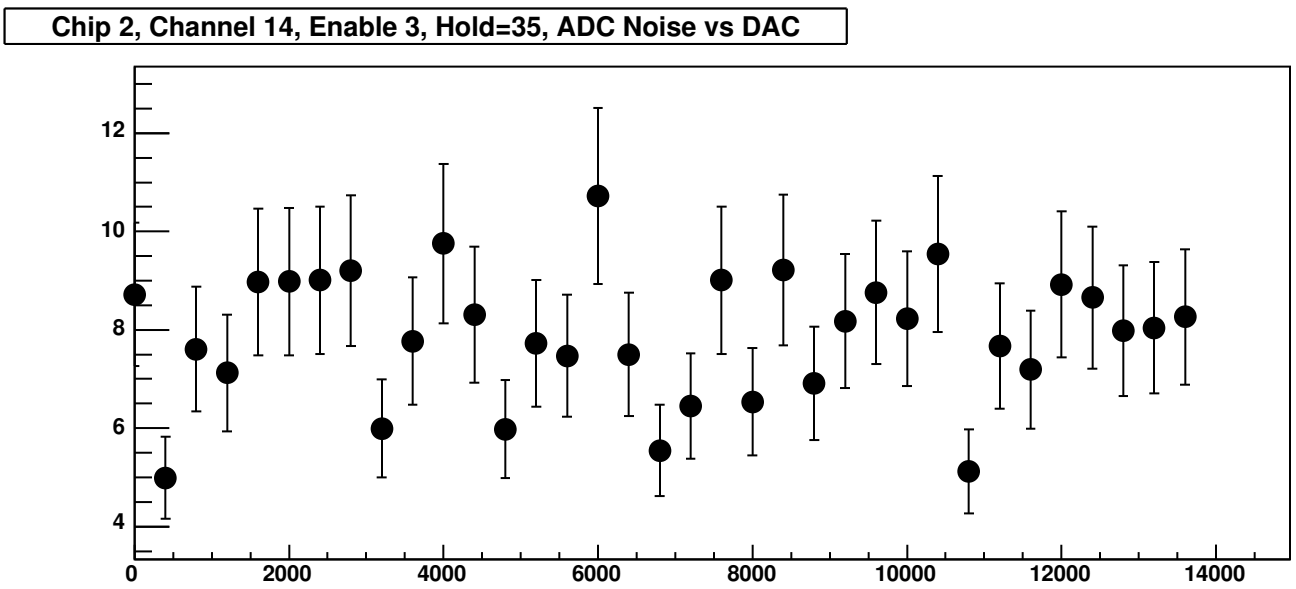

Chip 2, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

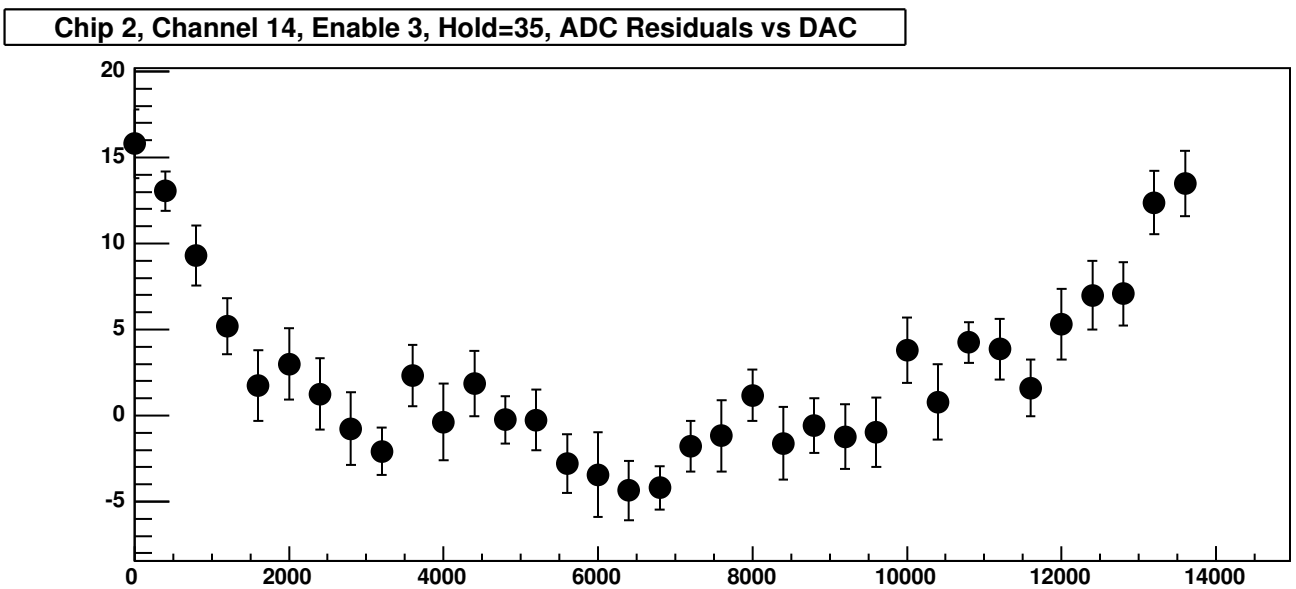

Chip 2, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 60.1 / 23
p0 10.16 ± 0.8609
p1 -0.02474 ± 0.0001459
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

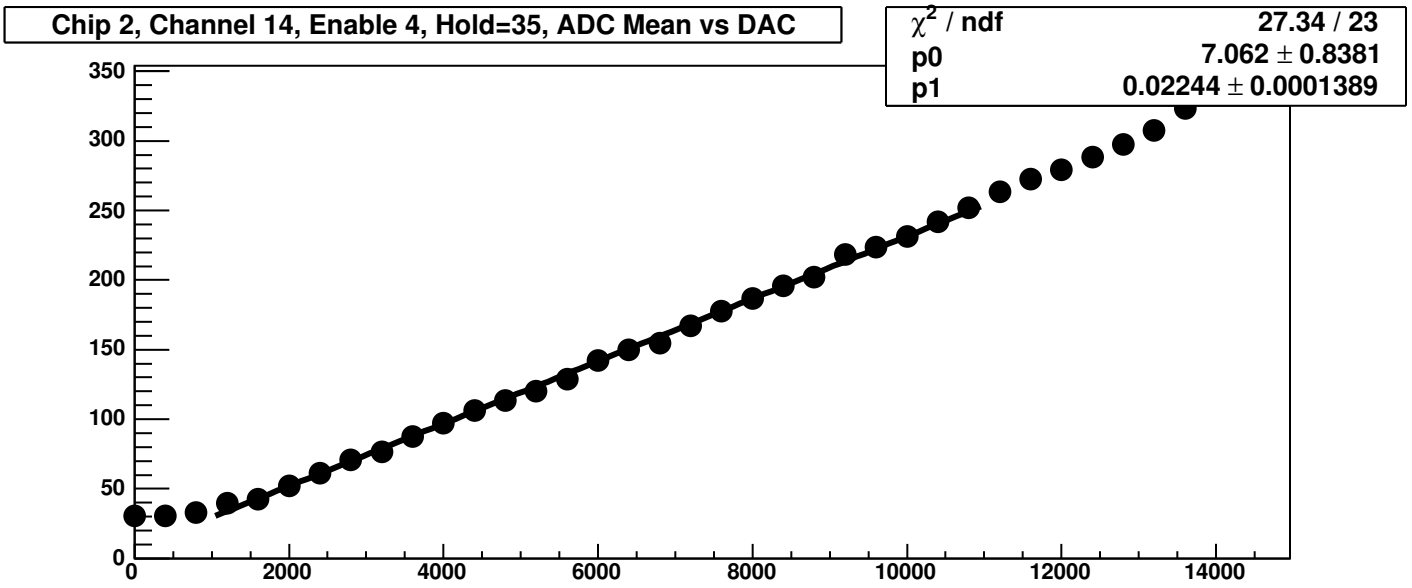

Chip 2, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

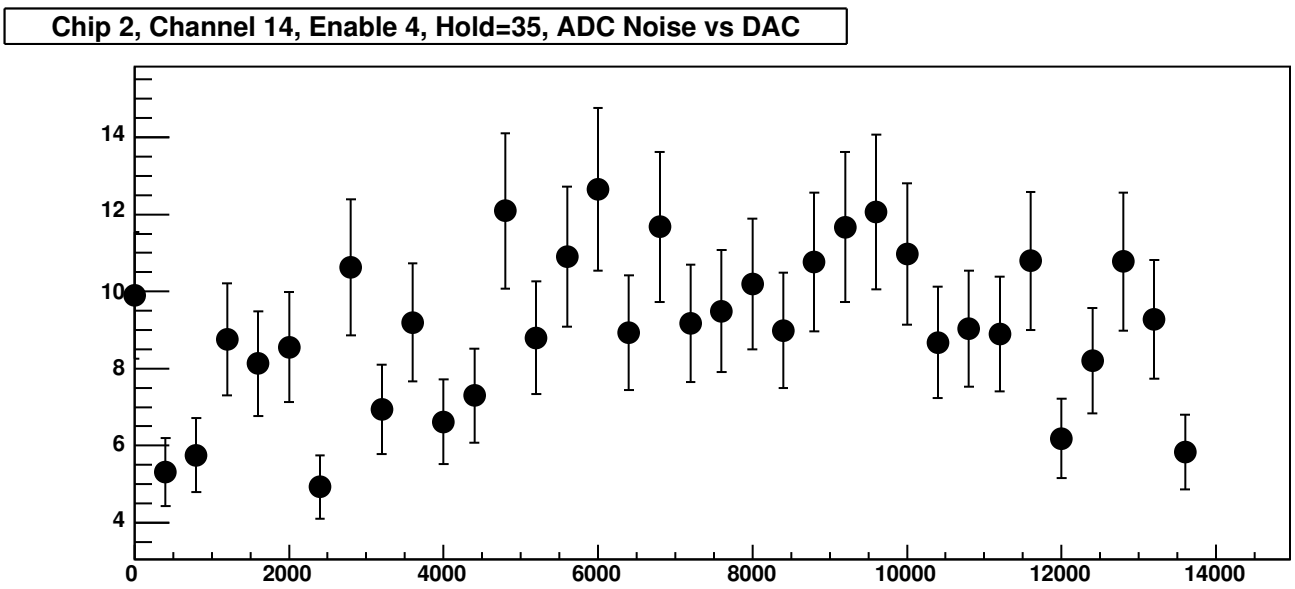

Chip 2, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

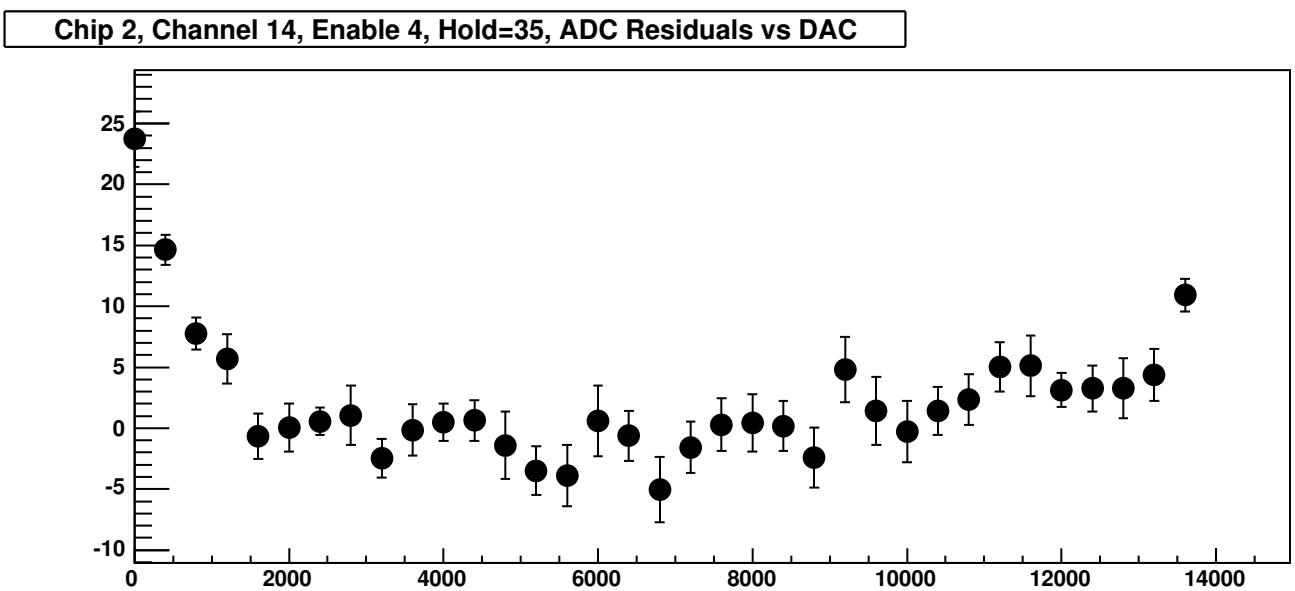

Chip 2, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 61.41 / 23
p0 6.409 ± 0.823
p1 0.01095 ± 0.0001199
180
160
140
120
100
80
60
40
20
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

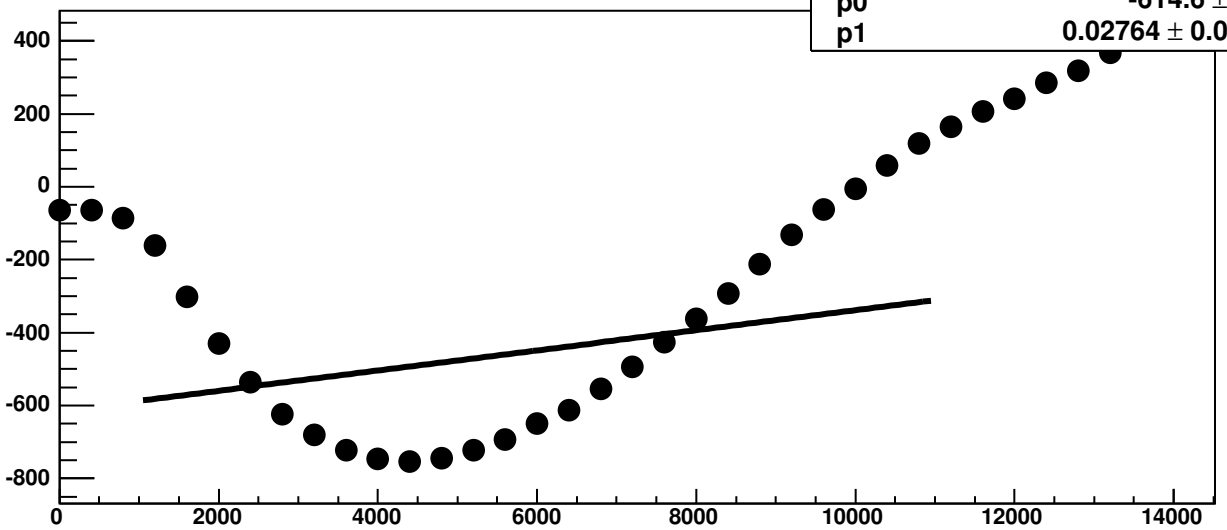

Chip 2, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.34 / 23
p0 7.062 ± 0.8381
p1 0.02244 ± 0.0001389
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

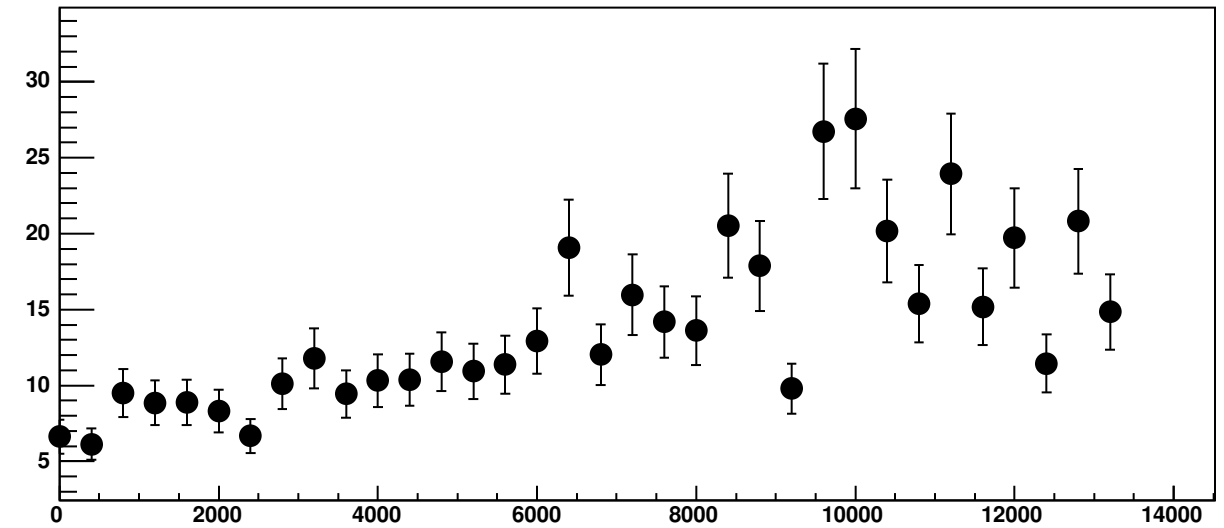

Chip 2, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

Chip 2, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

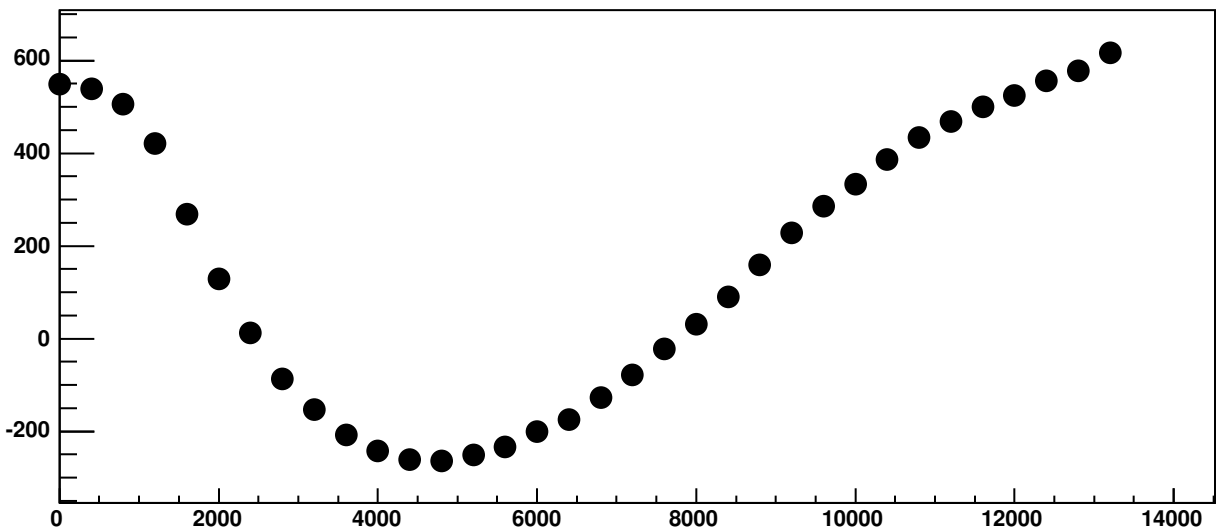
χ^2 / ndf	1.772e+05 / 23
p0	-614.6 ± 1.046
p1	0.02764 ± 0.000197

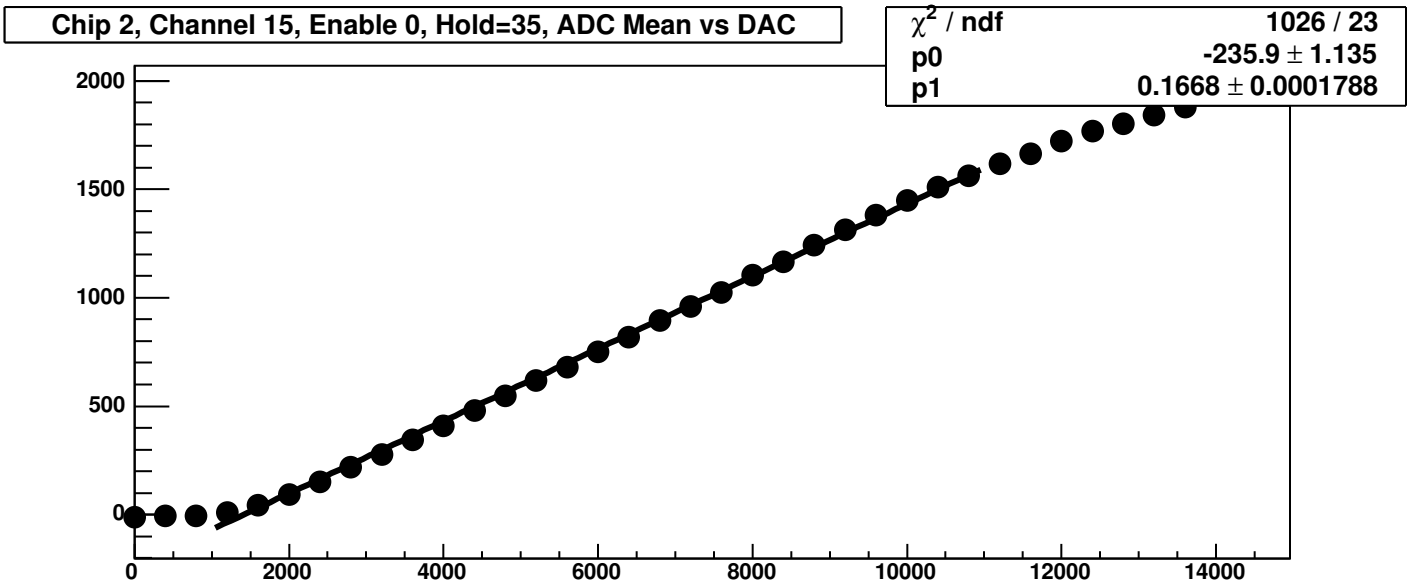


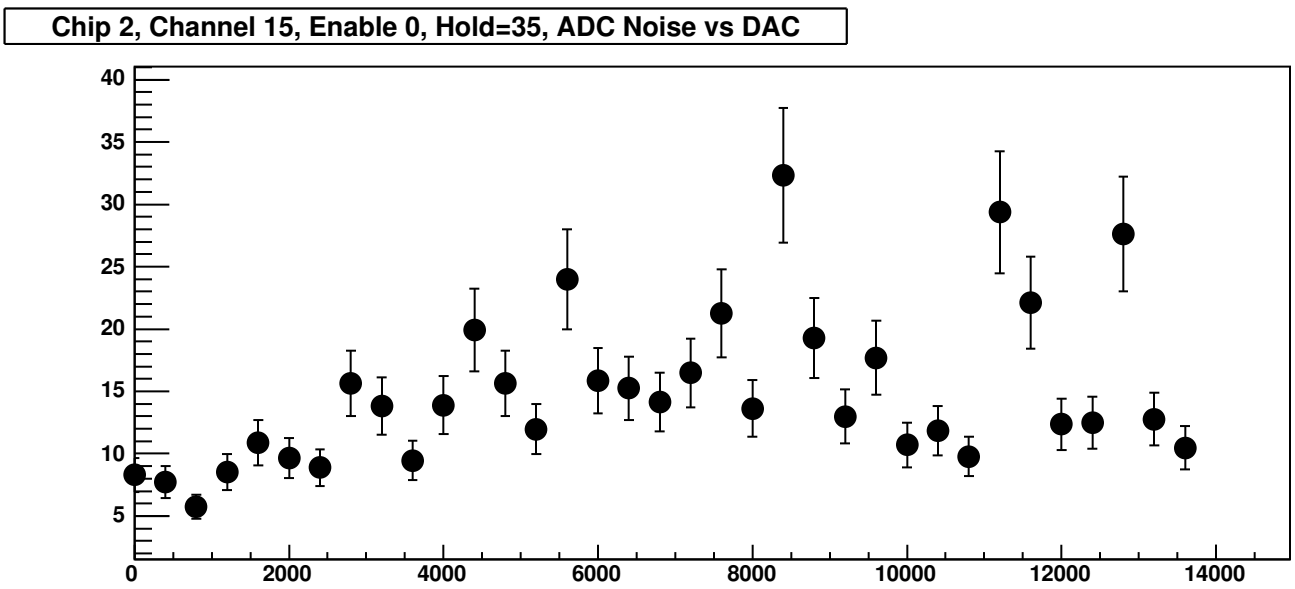
Chip 2, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC

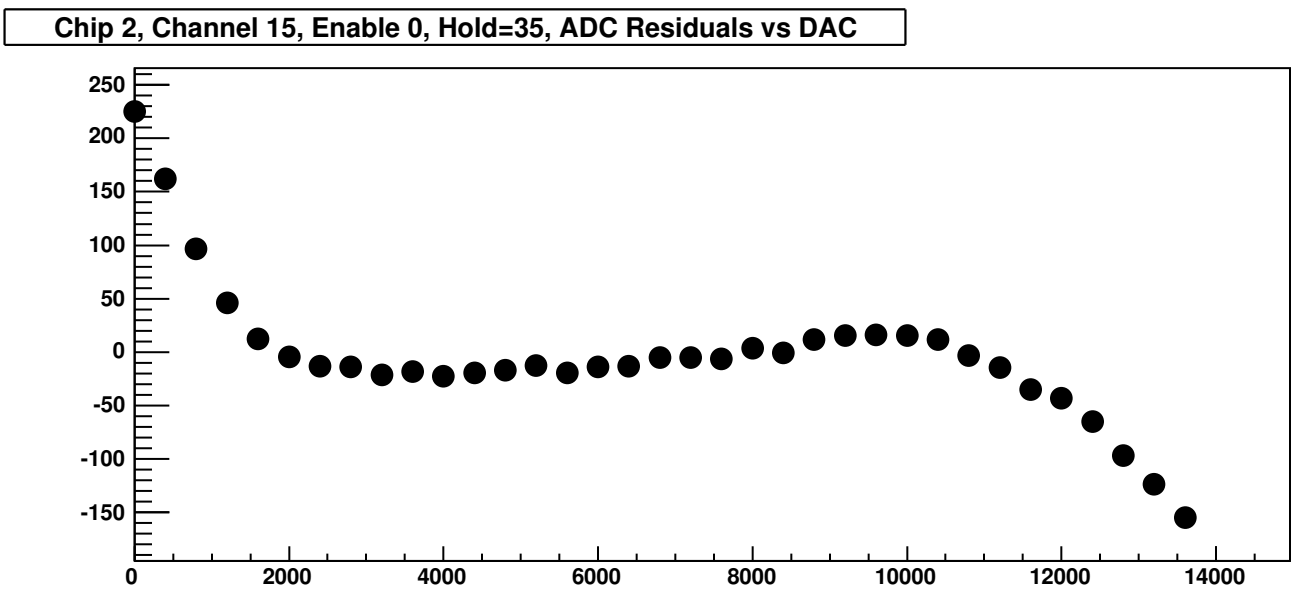


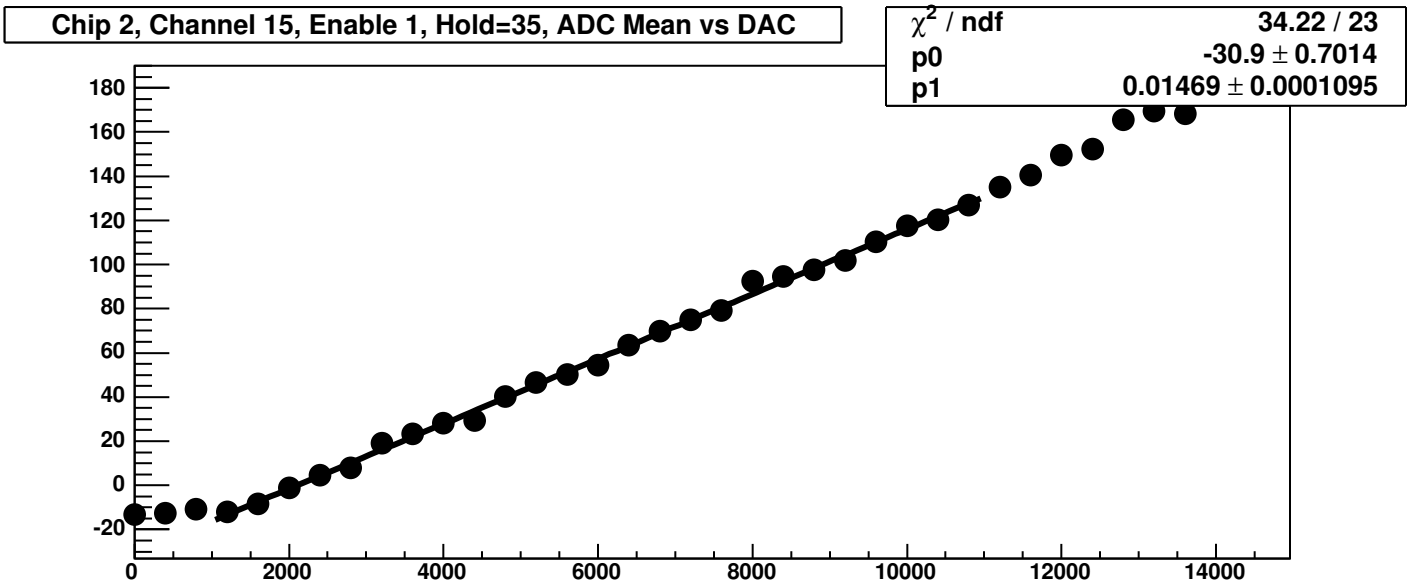
Chip 2, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

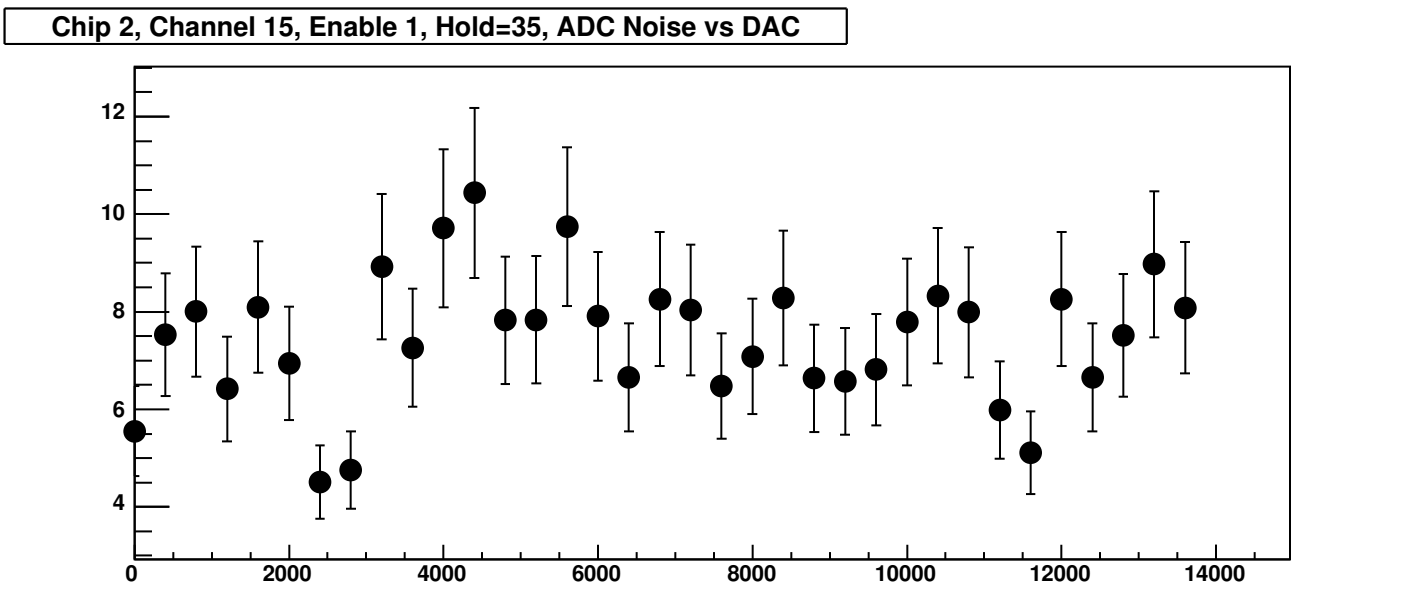


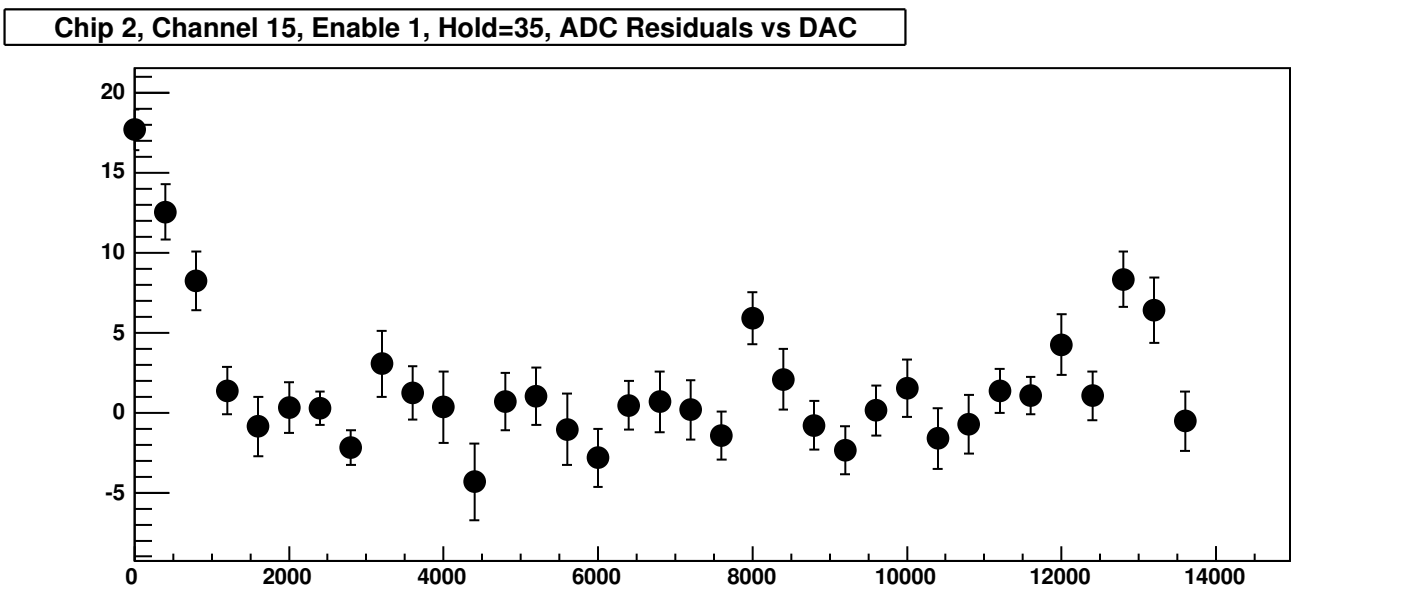

Chip 2, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1026 / 23
p0 -235.9 ± 1.135
p1 0.1668 ± 0.0001788
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

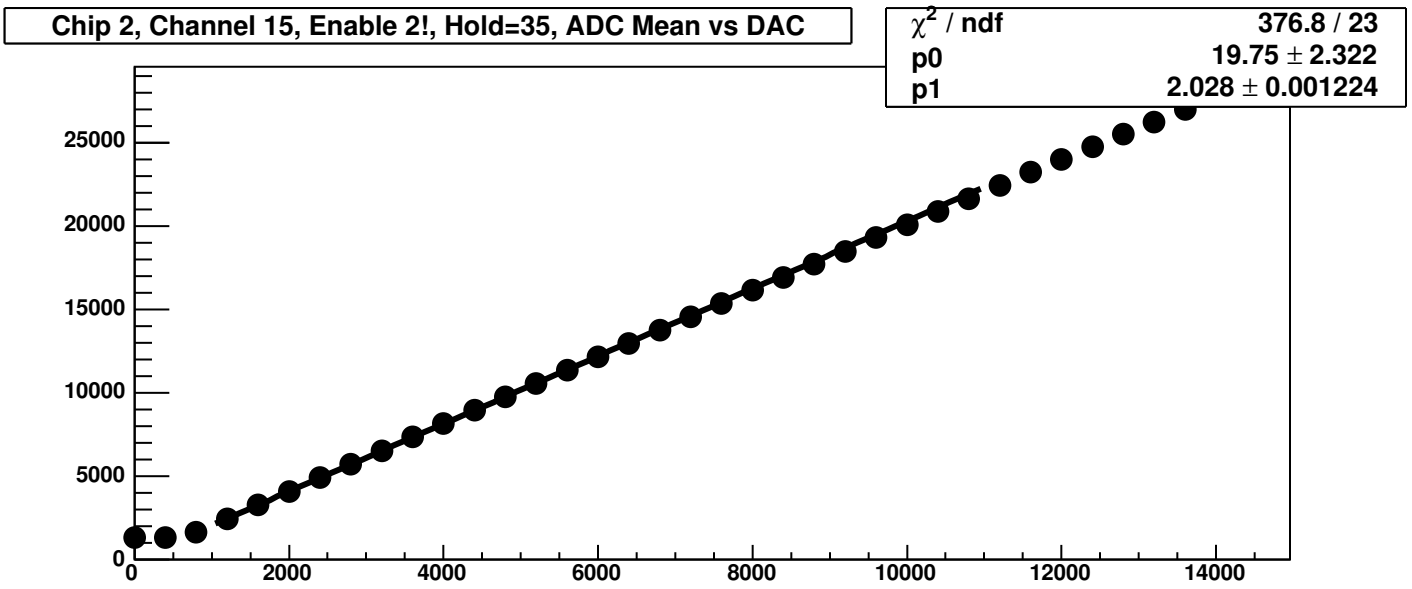

Chip 2, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

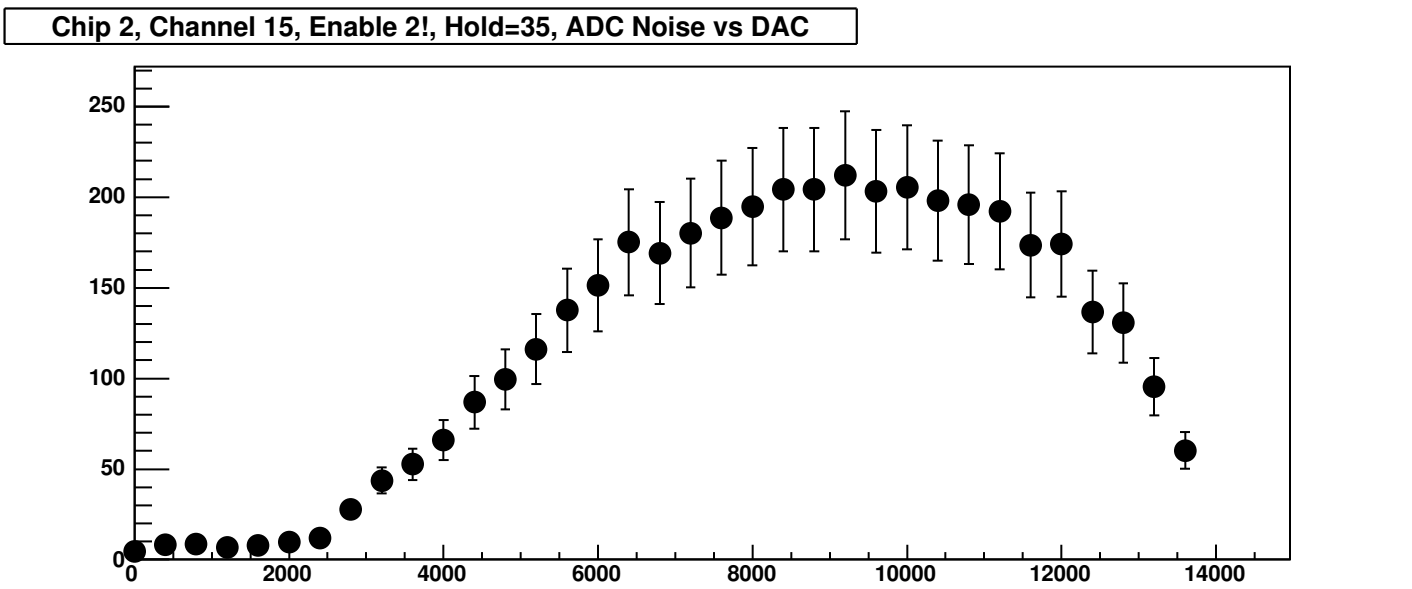

Chip 2, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

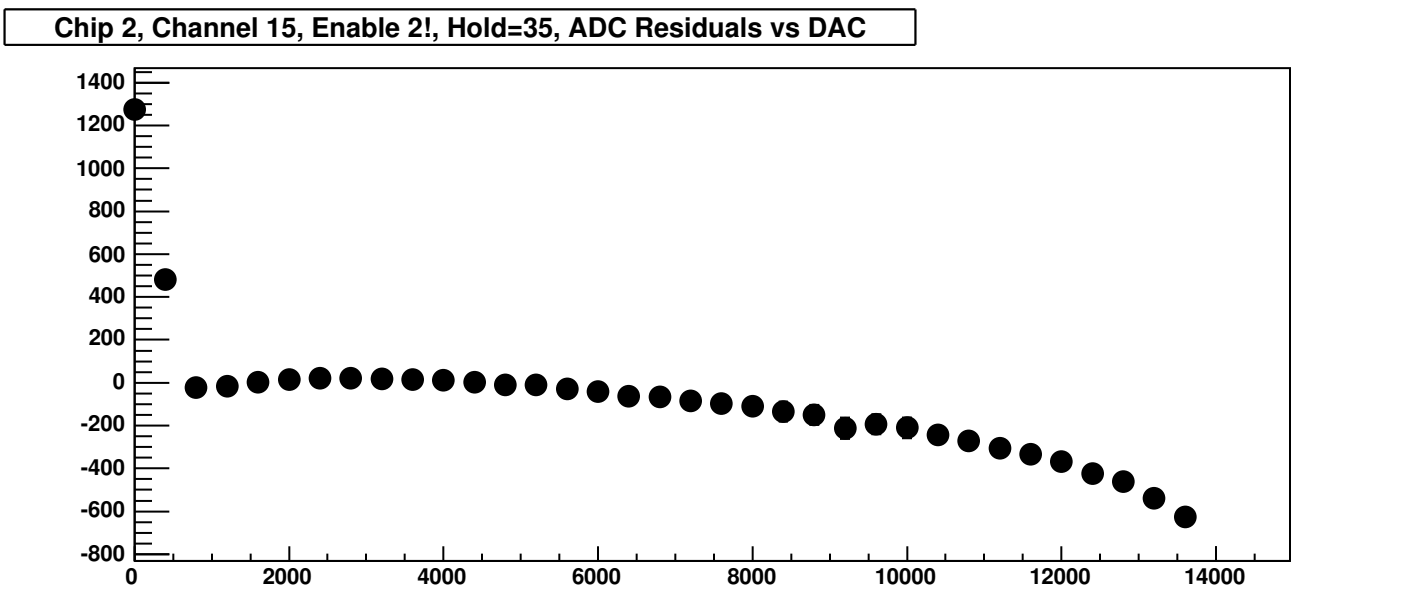

Chip 2, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 34.22 / 23
p0 -30.9 ± 0.7014
p1 0.01469 ± 0.0001095
180
160
140
120
100
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000

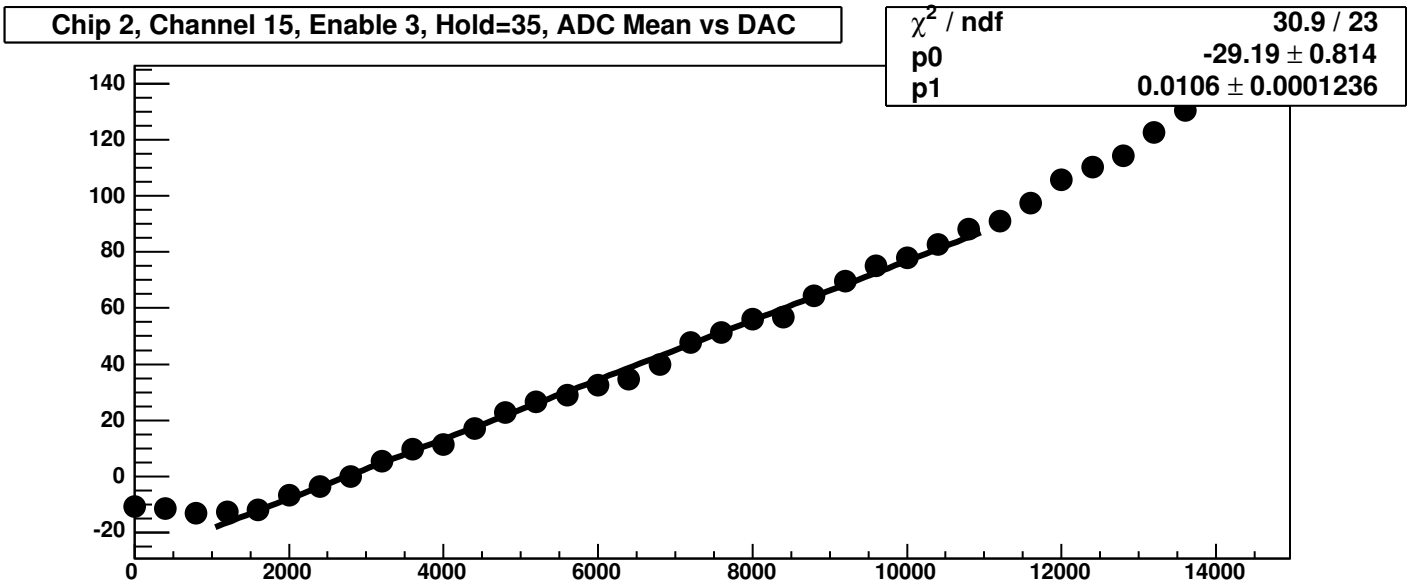

Chip 2, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

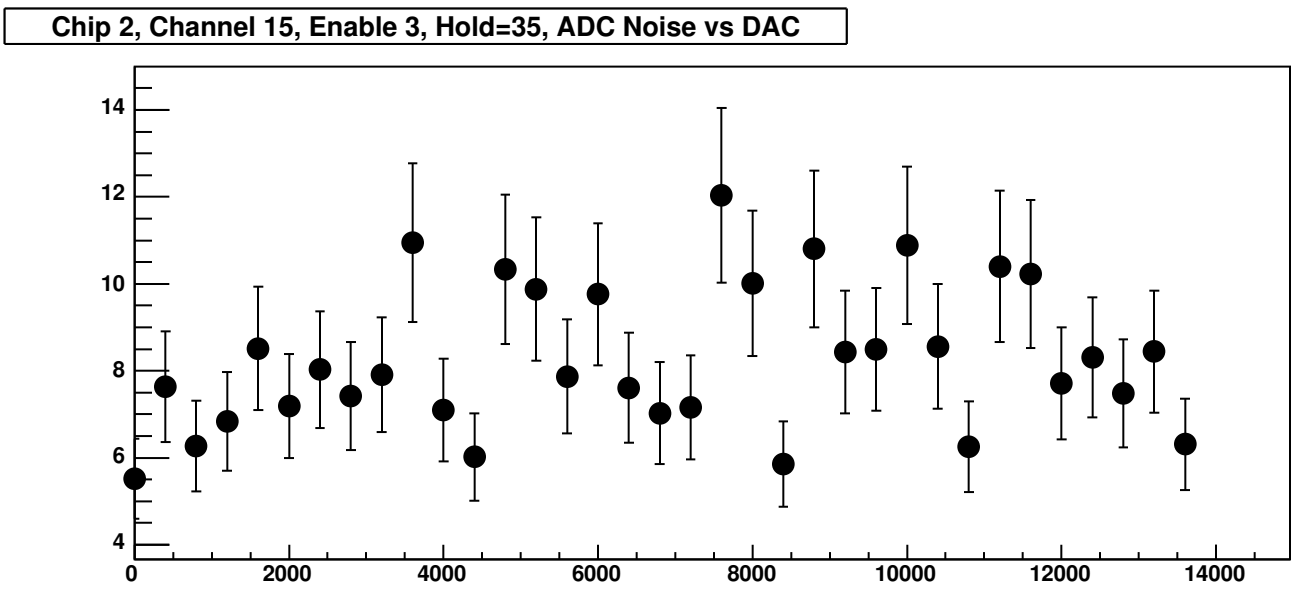

Chip 2, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

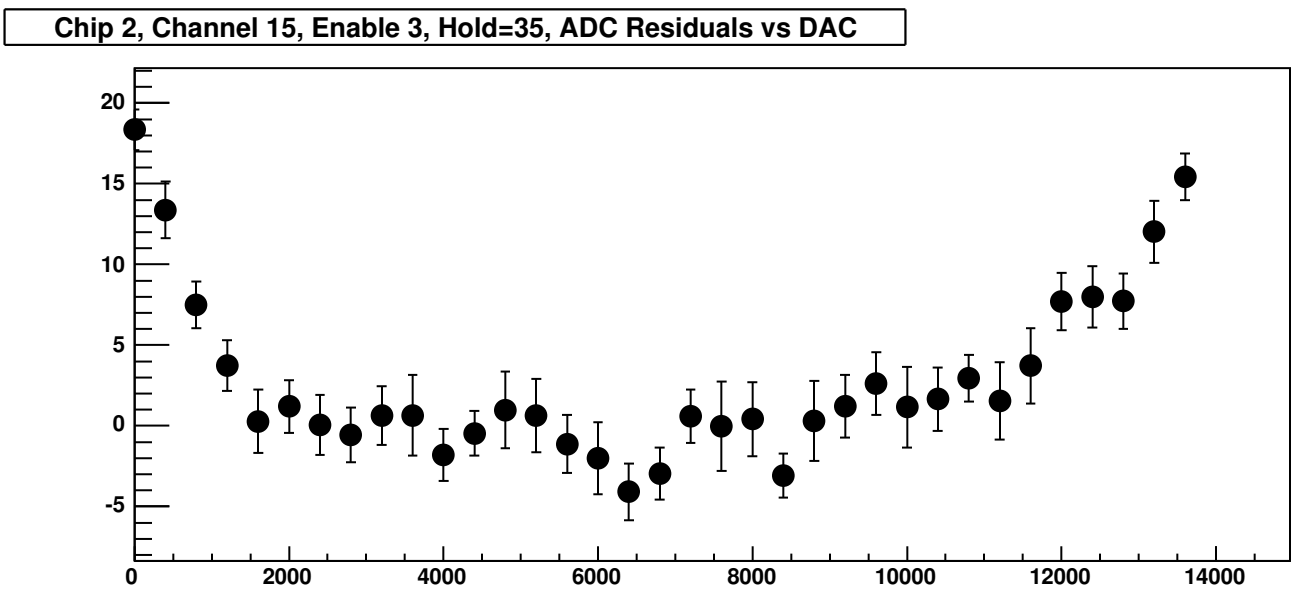

Chip 2, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 376.8 / 23
p0 19.75 ± 2.322
p1 2.028 ± 0.001224
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

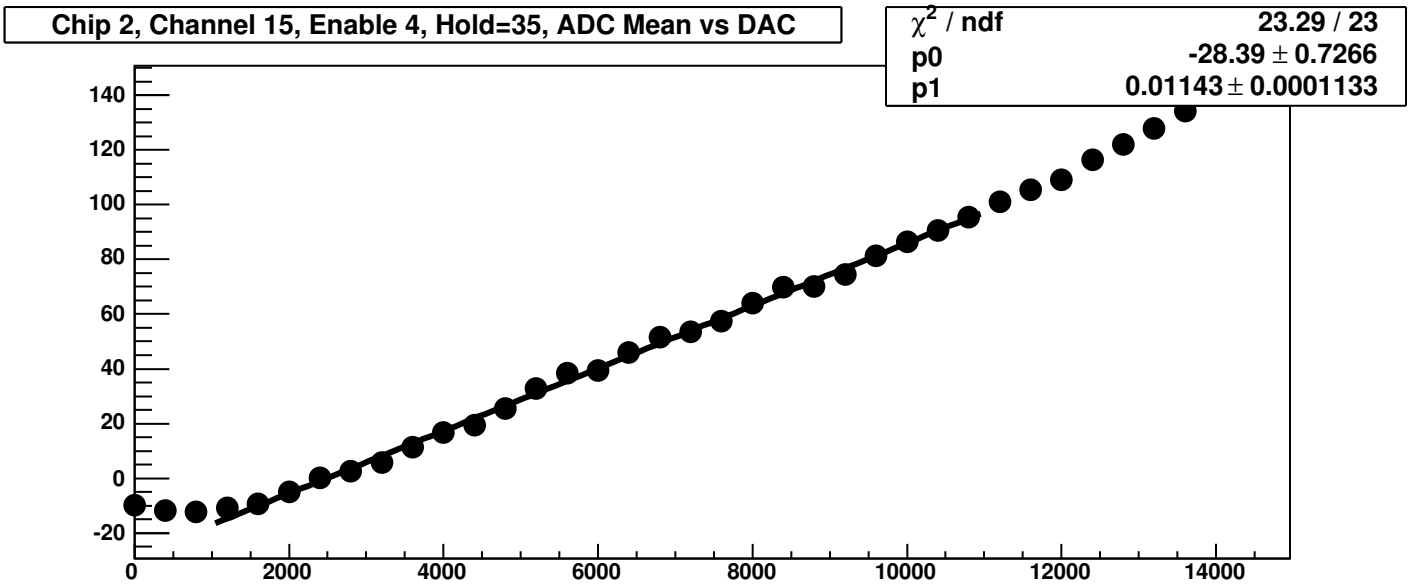

Chip 2, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

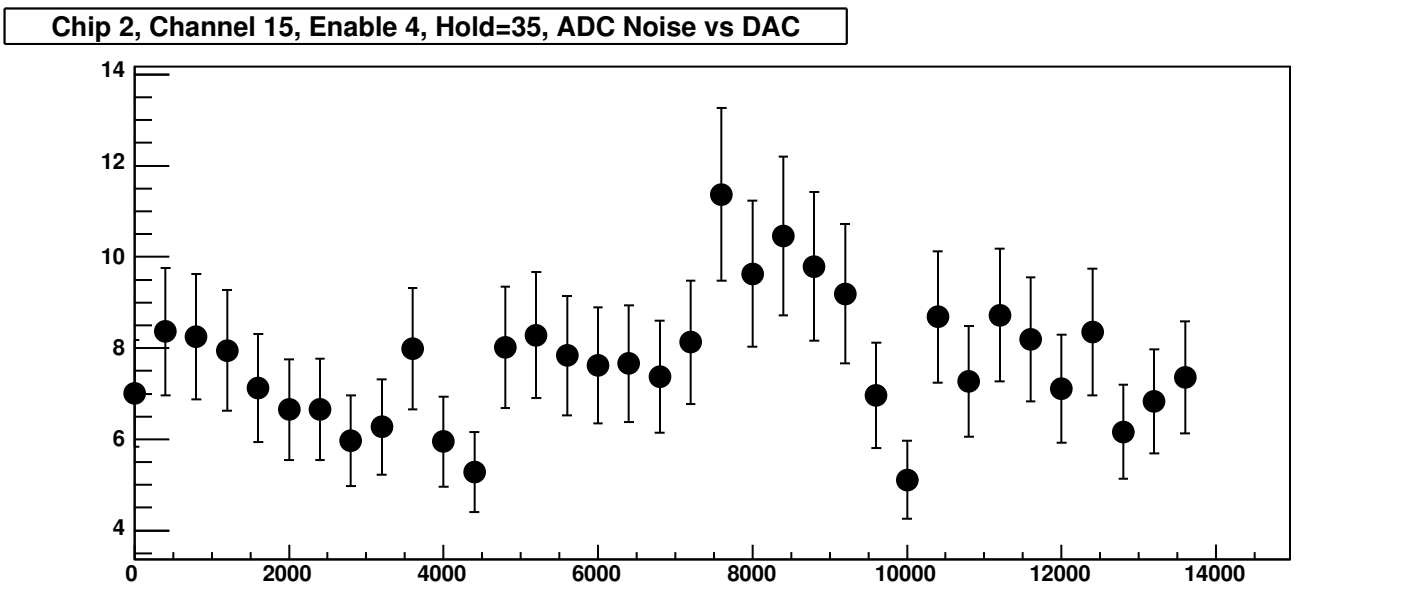

Chip 2, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

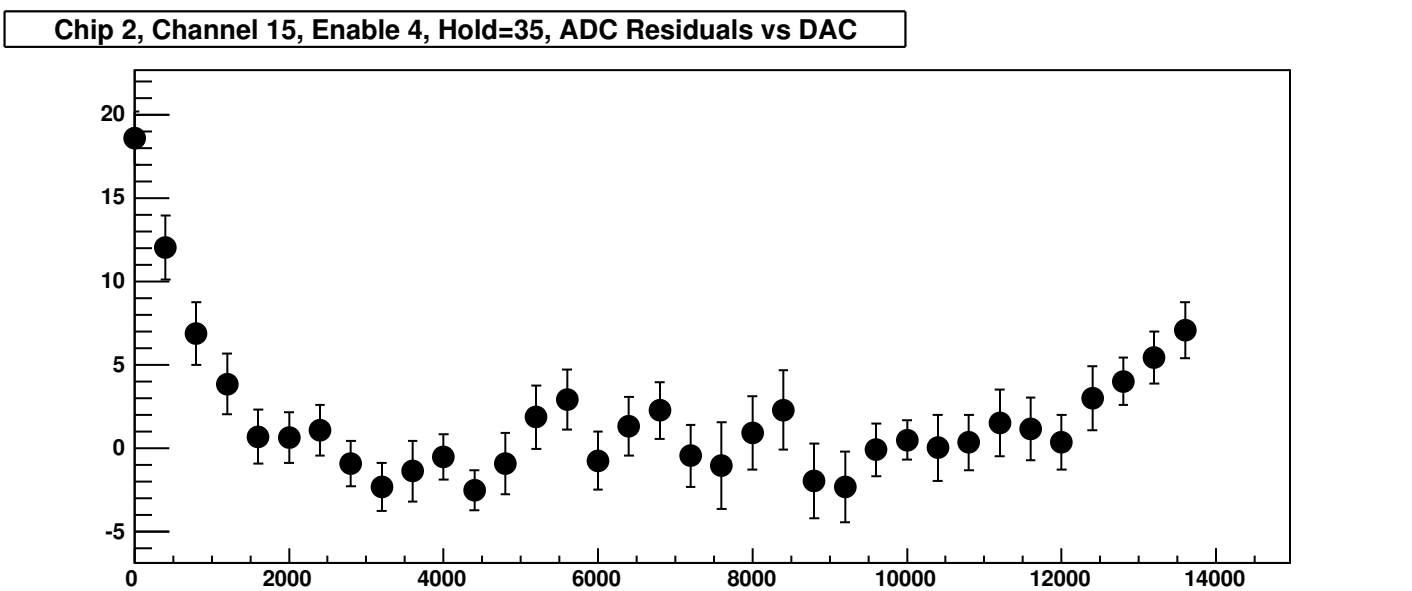

Chip 2, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 30.9 / 23
p0 -29.19 ± 0.814
p1 0.0106 ± 0.0001236
140
120
100
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

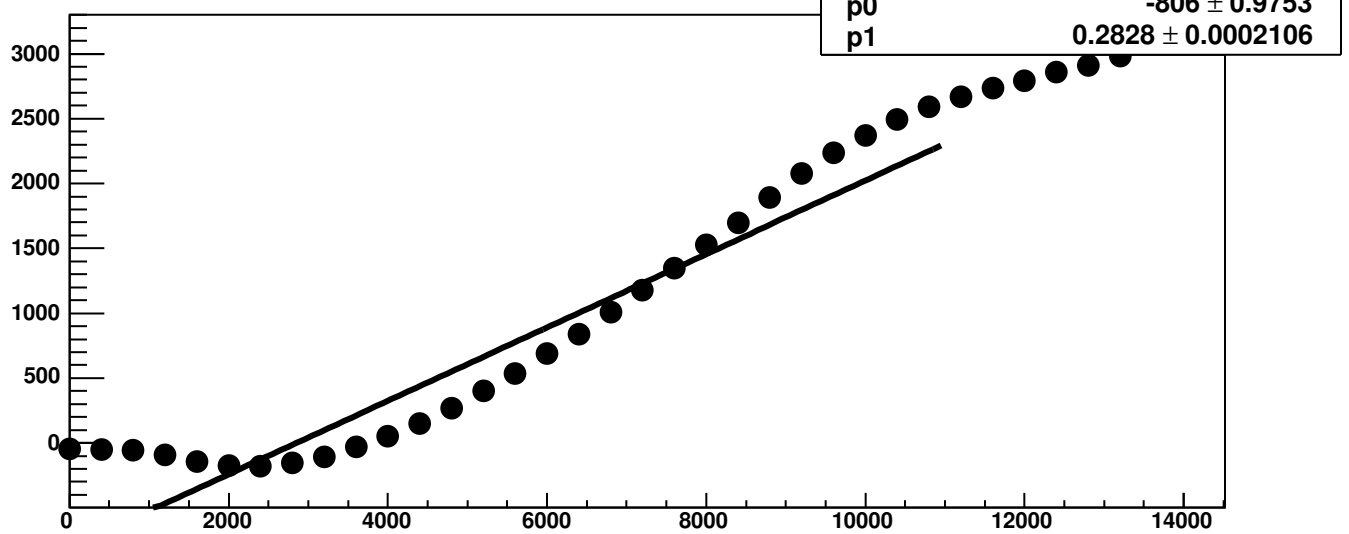

Chip 2, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 23.29 / 23
p0 -28.39 ± 0.7266
p1 0.01143 ± 0.0001133
140
120
100
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000

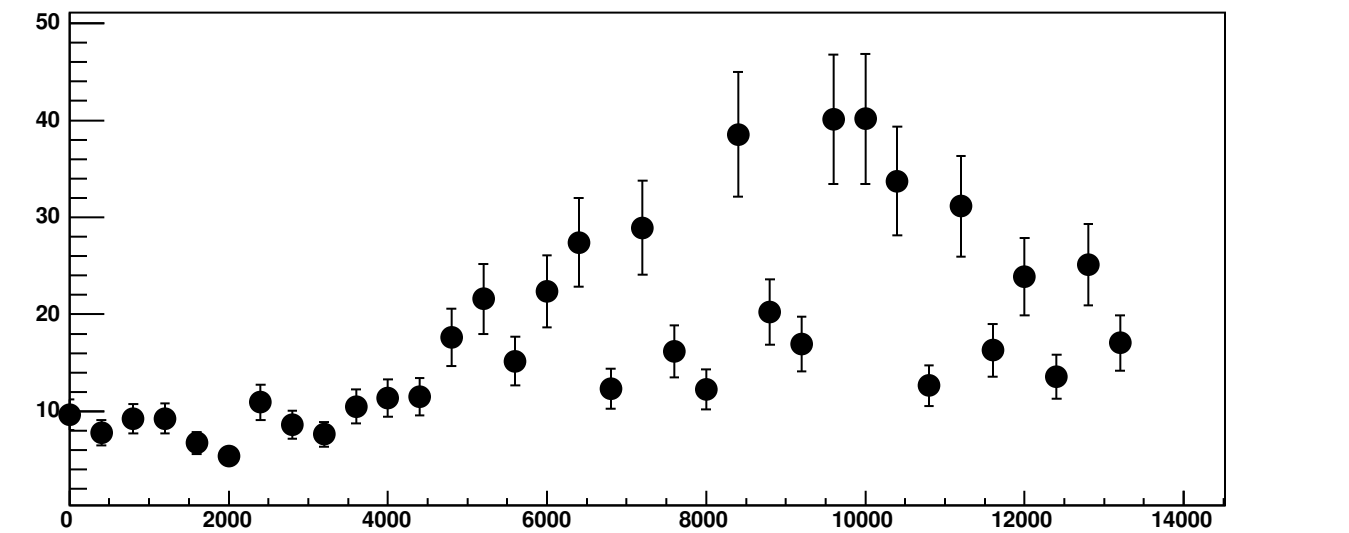

Chip 2, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

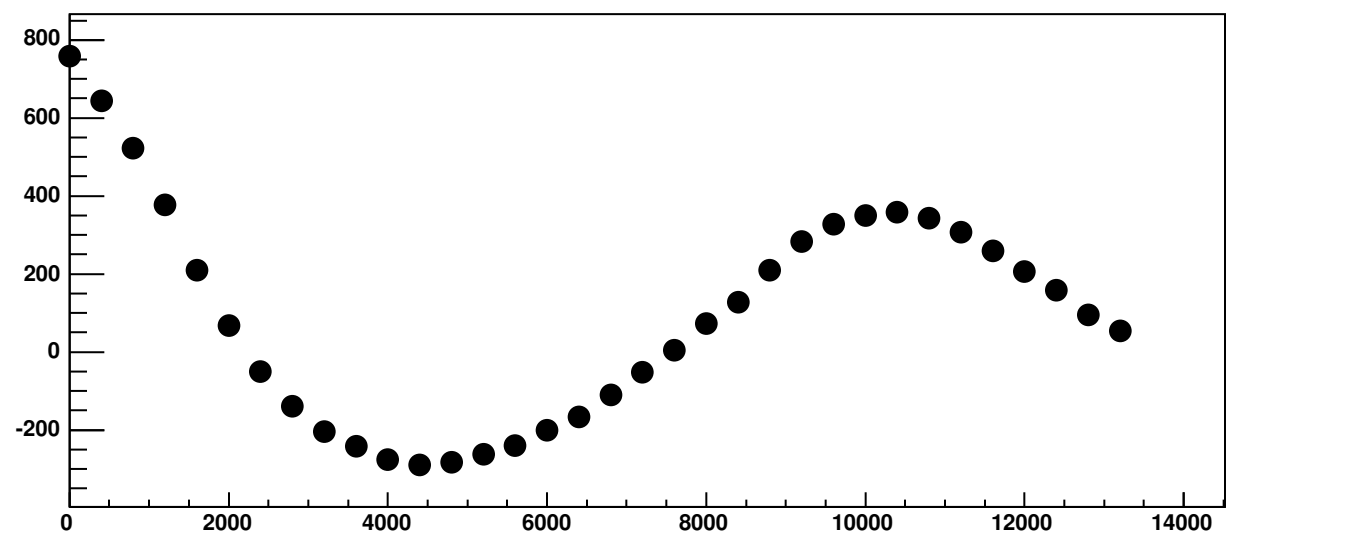
Chip 2, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC

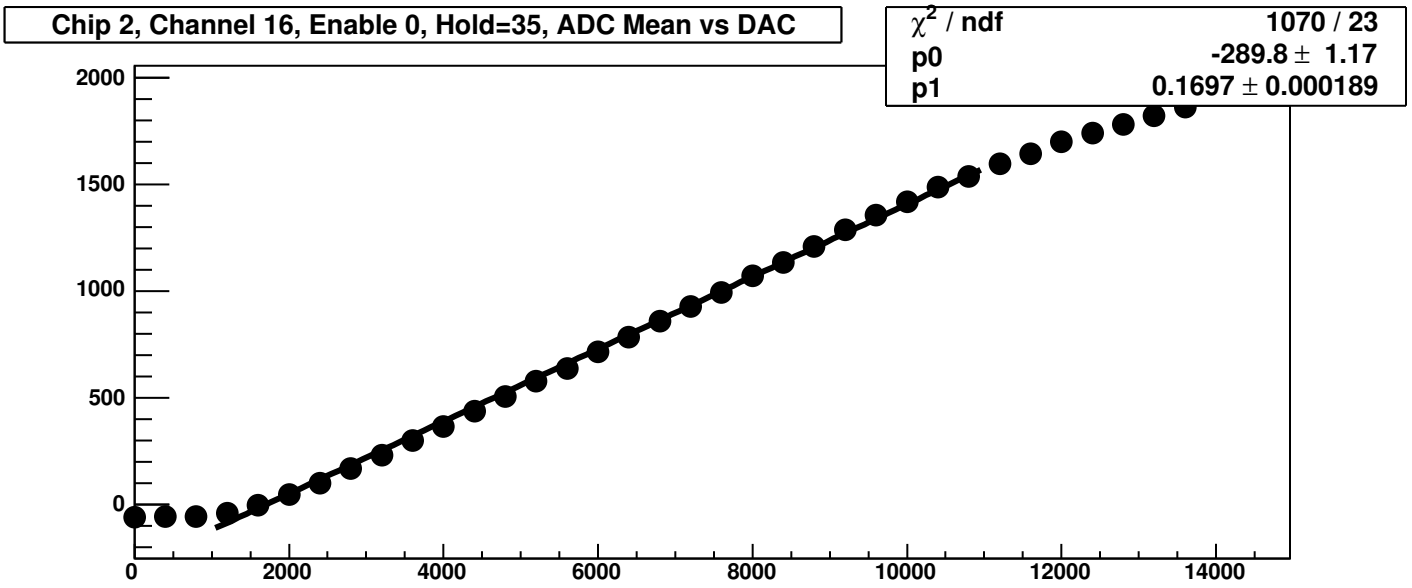


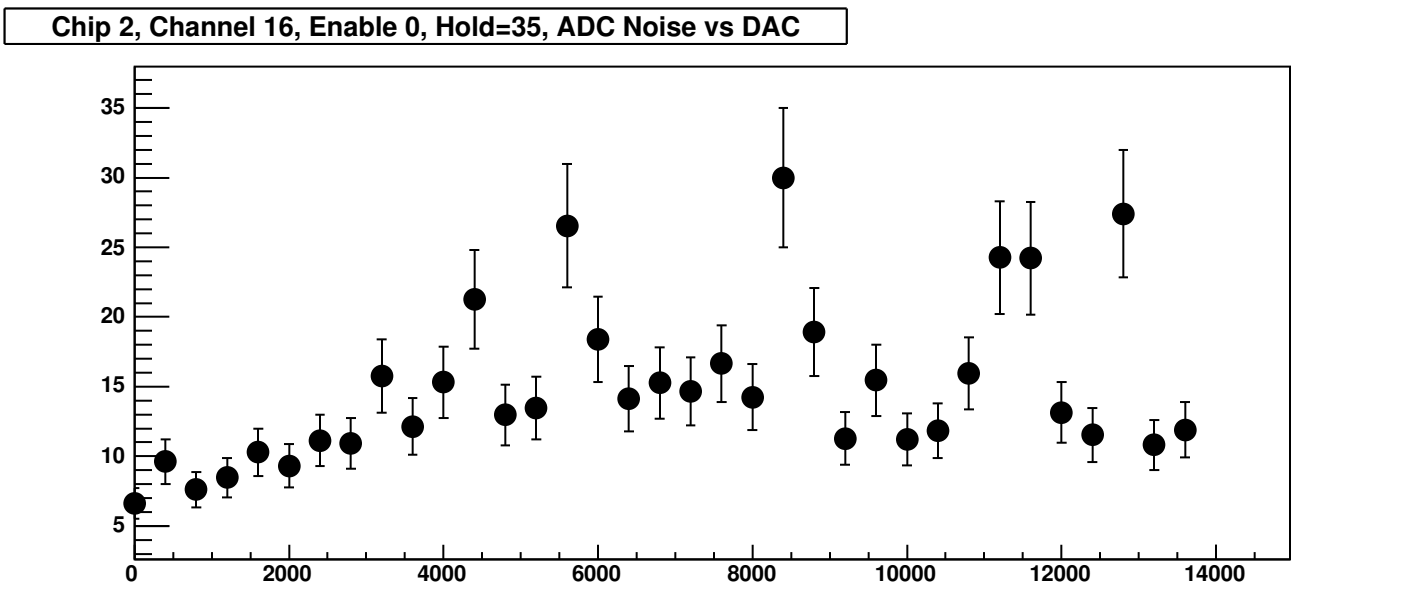
Chip 2, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC

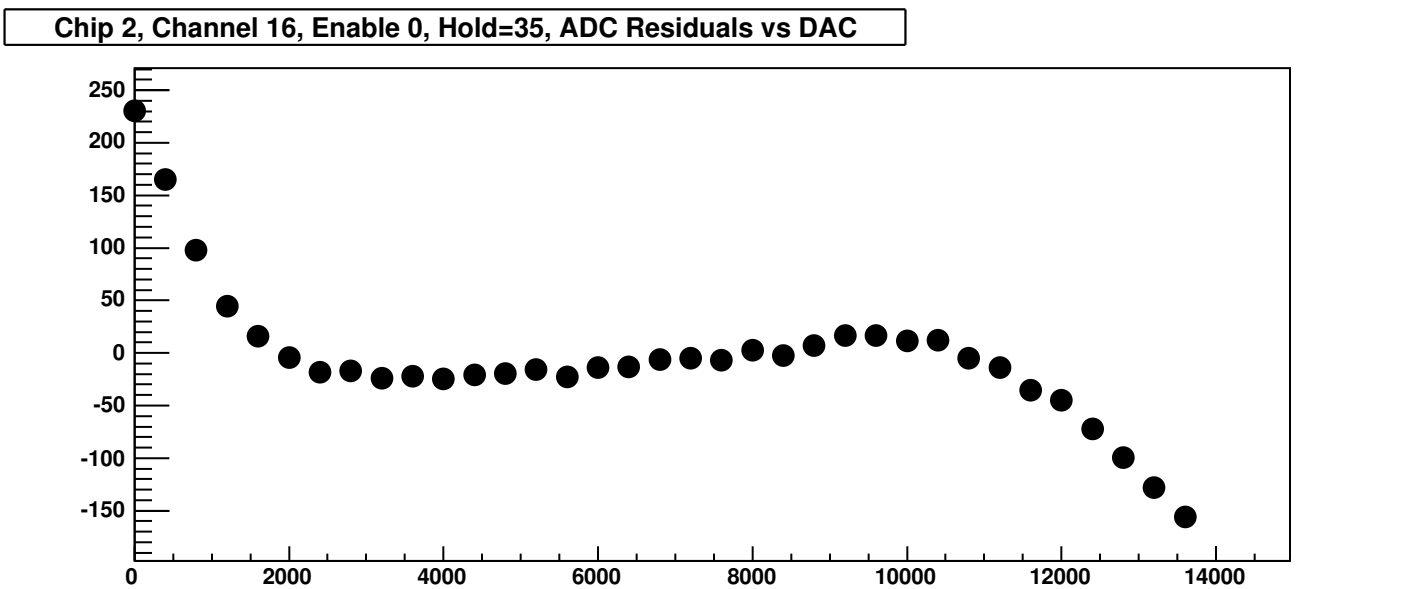


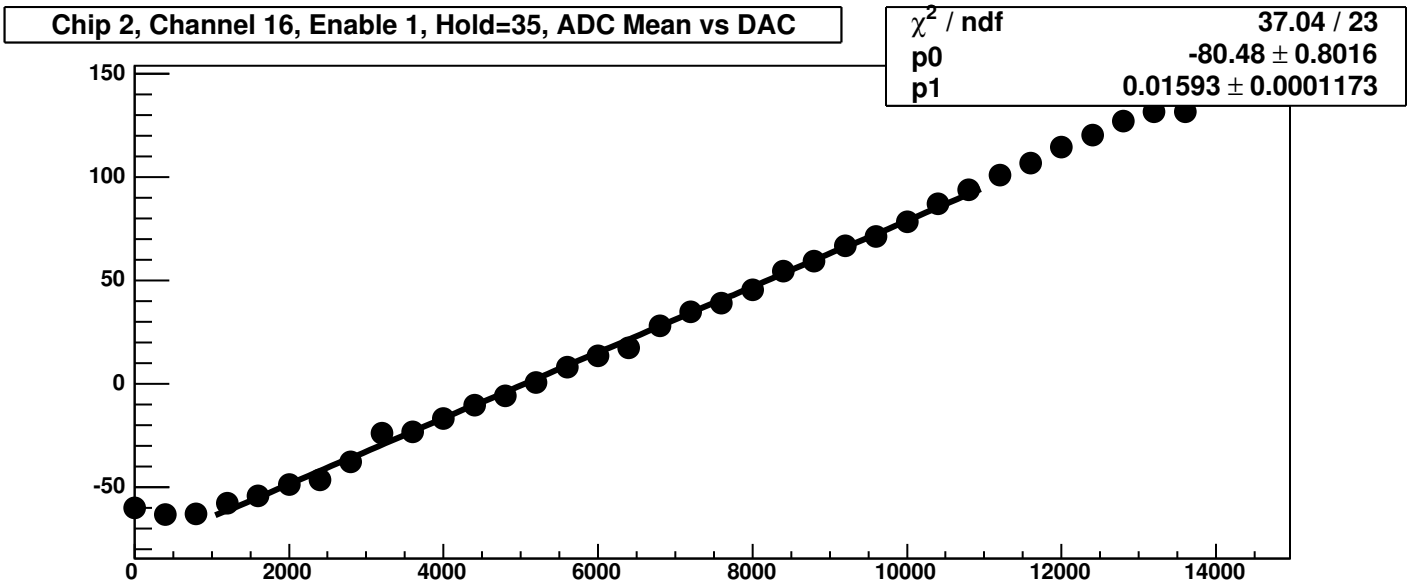
Chip 2, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC

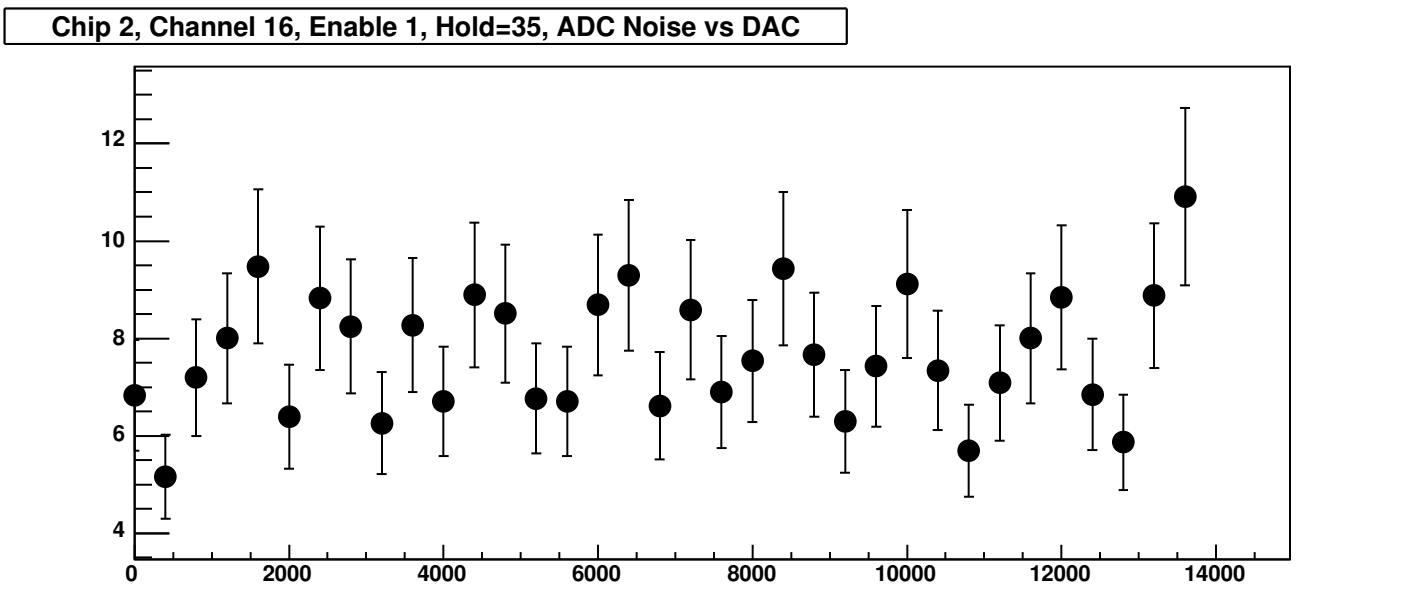


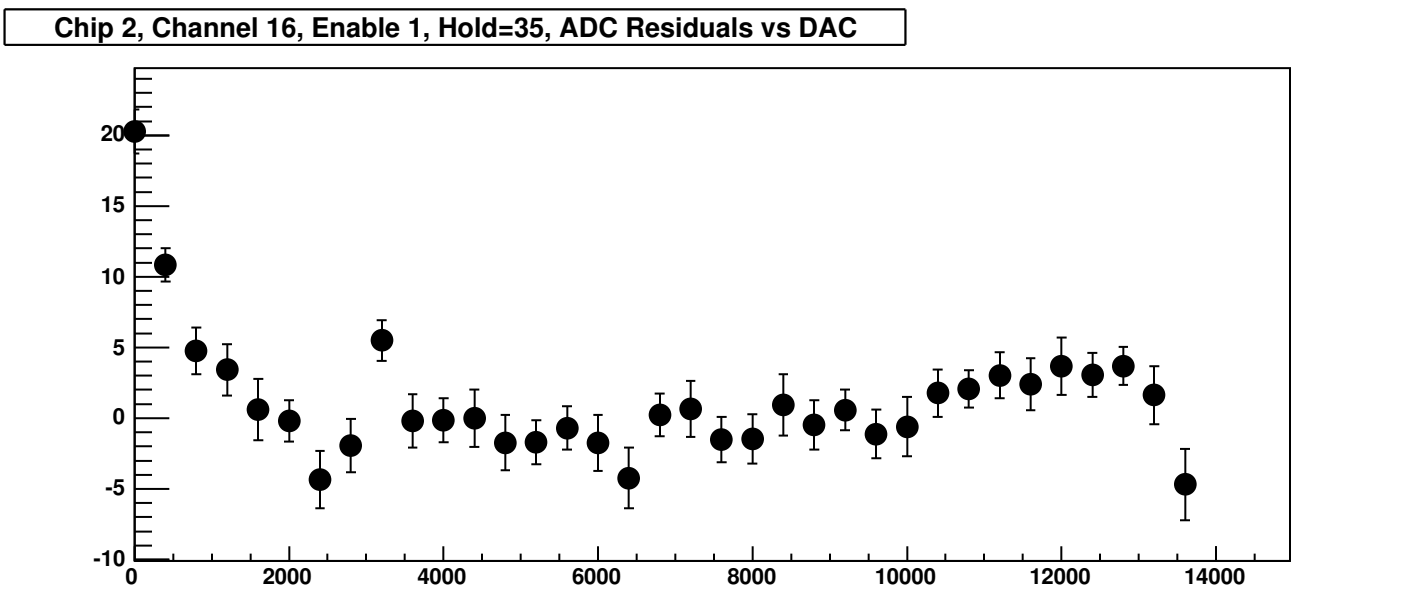

Chip 2, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1070 / 23
p0 -289.8 ± 1.17
p1 0.1697 ± 0.000189
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

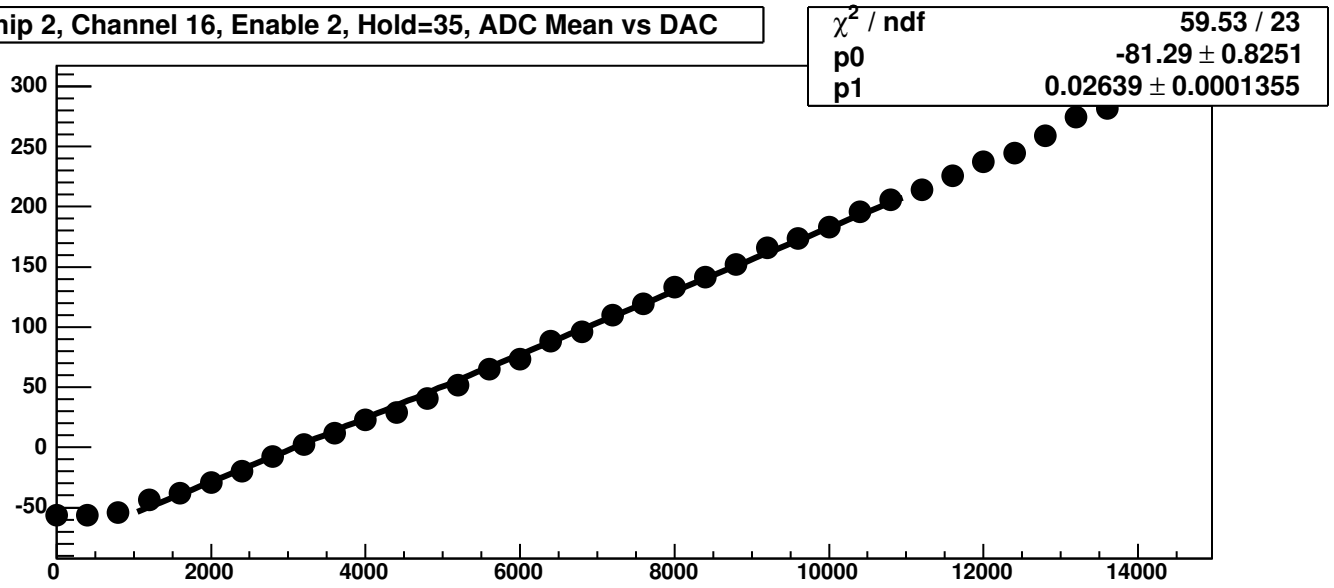

Chip 2, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 37.04 / 23
p0 -80.48 ± 0.8016
p1 0.01593 ± 0.0001173
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

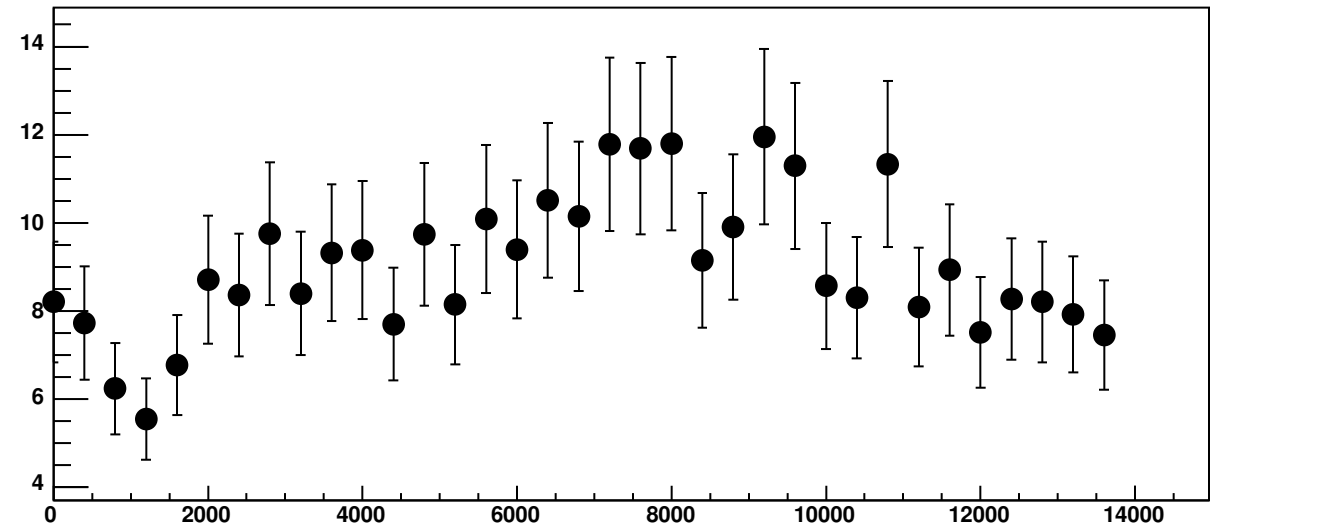

Chip 2, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

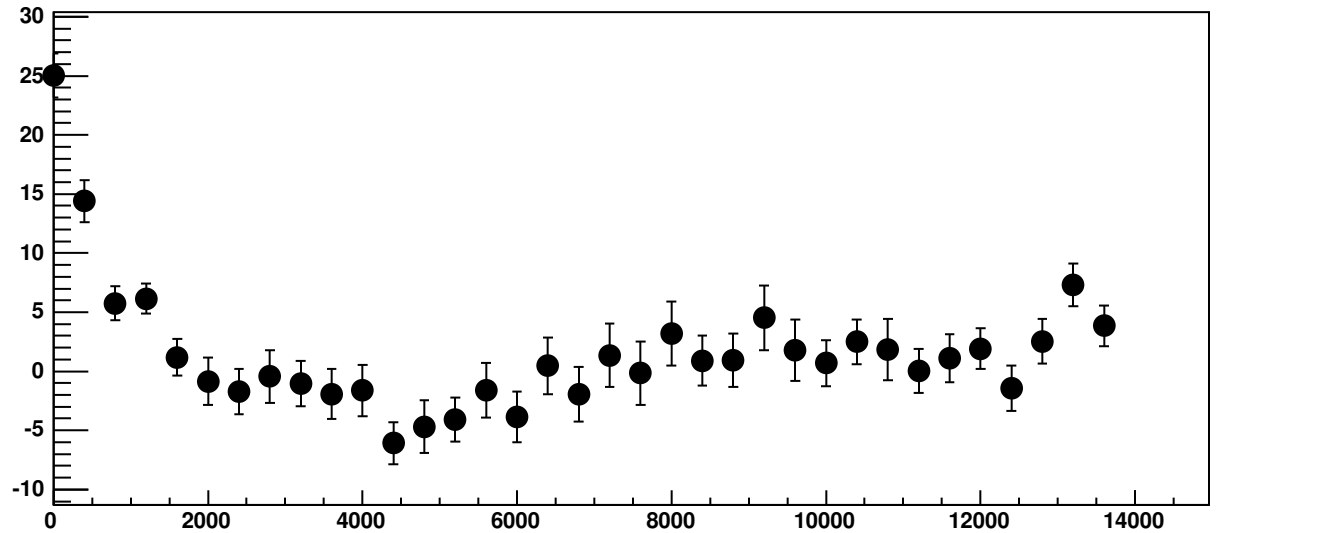
Chip 2, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC



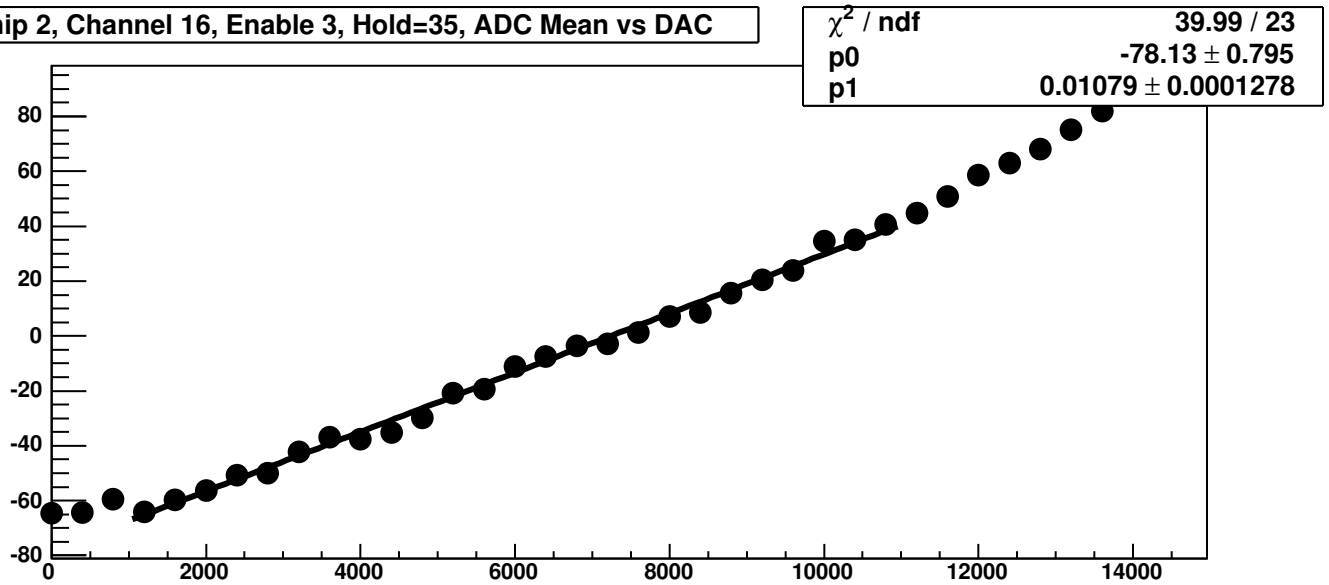
Chip 2, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC



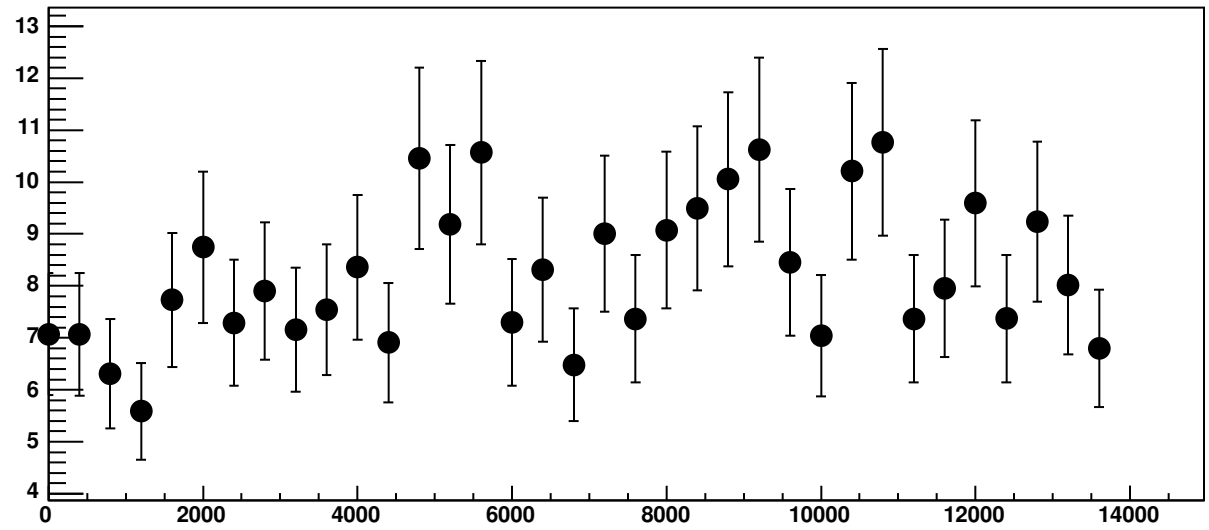
Chip 2, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC



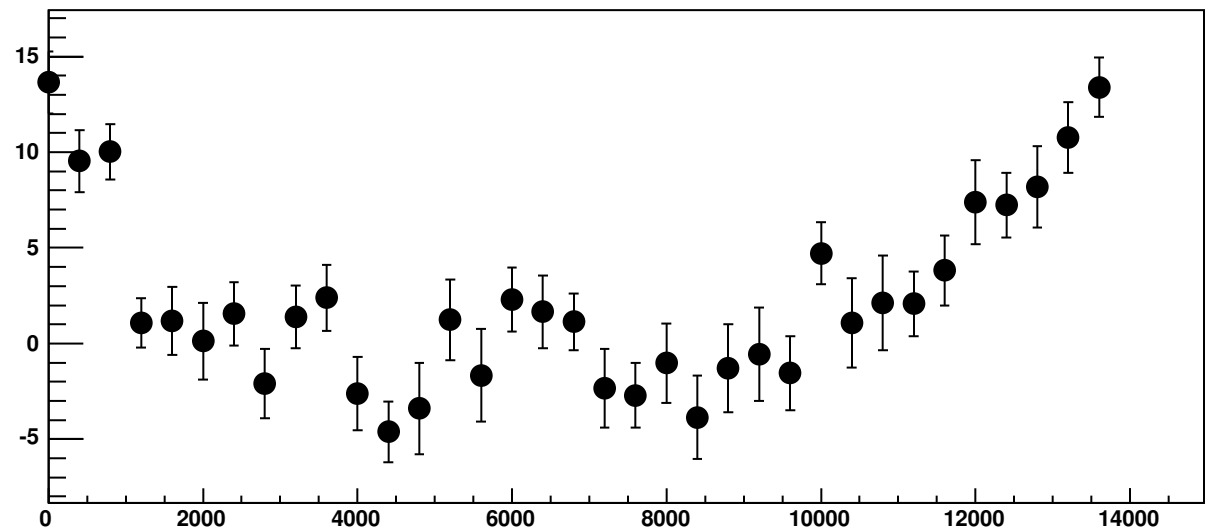
Chip 2, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

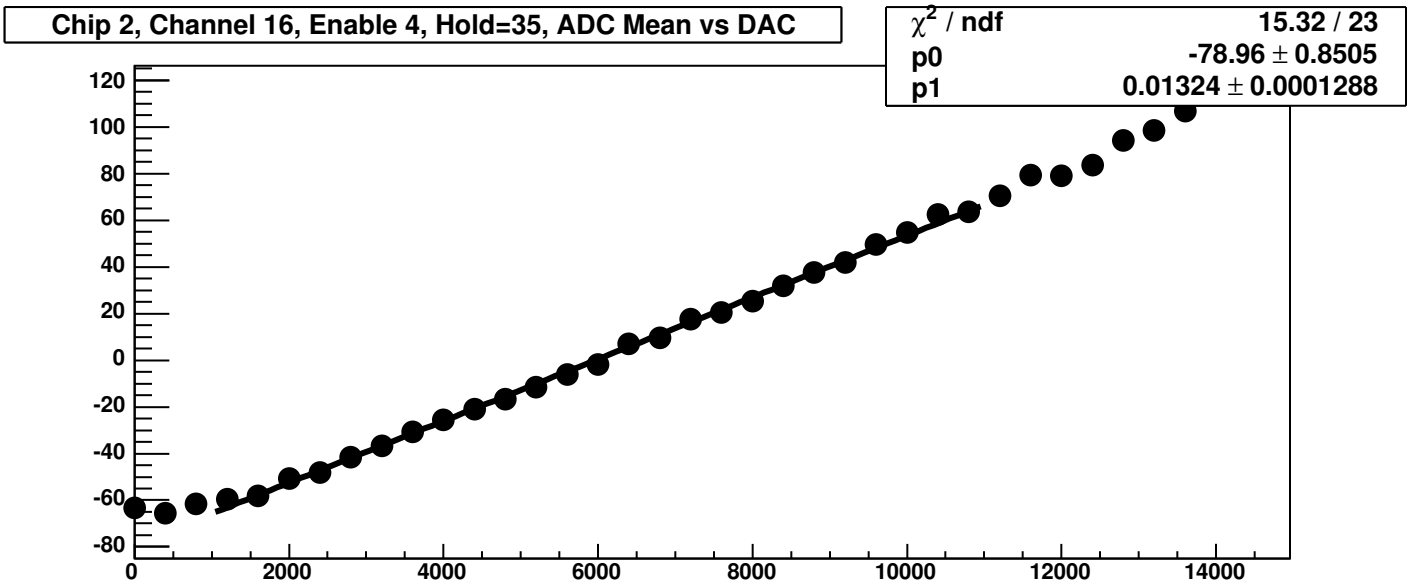


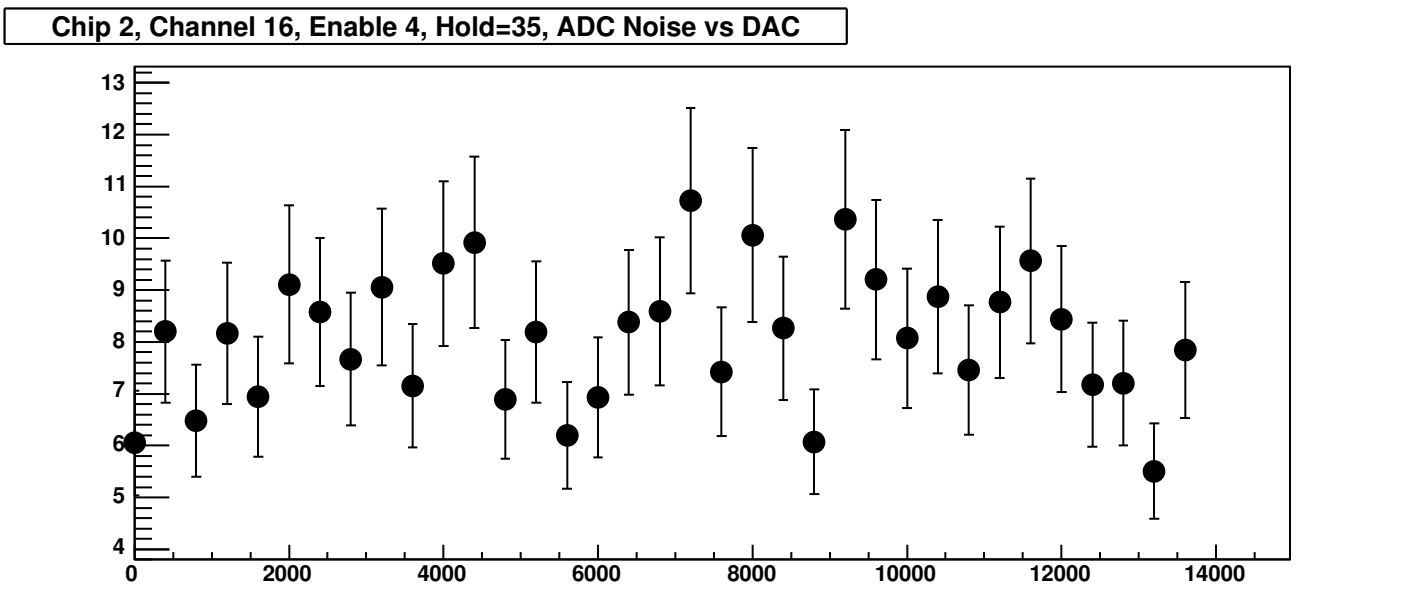
Chip 2, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC

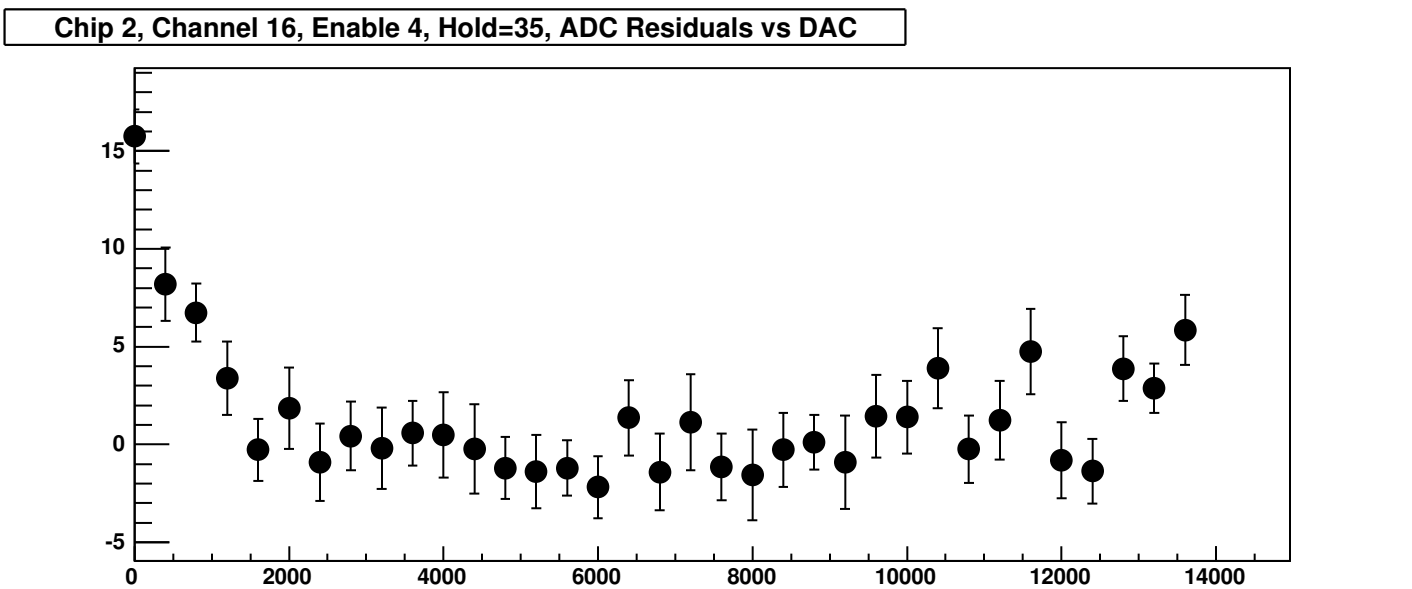


Chip 2, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

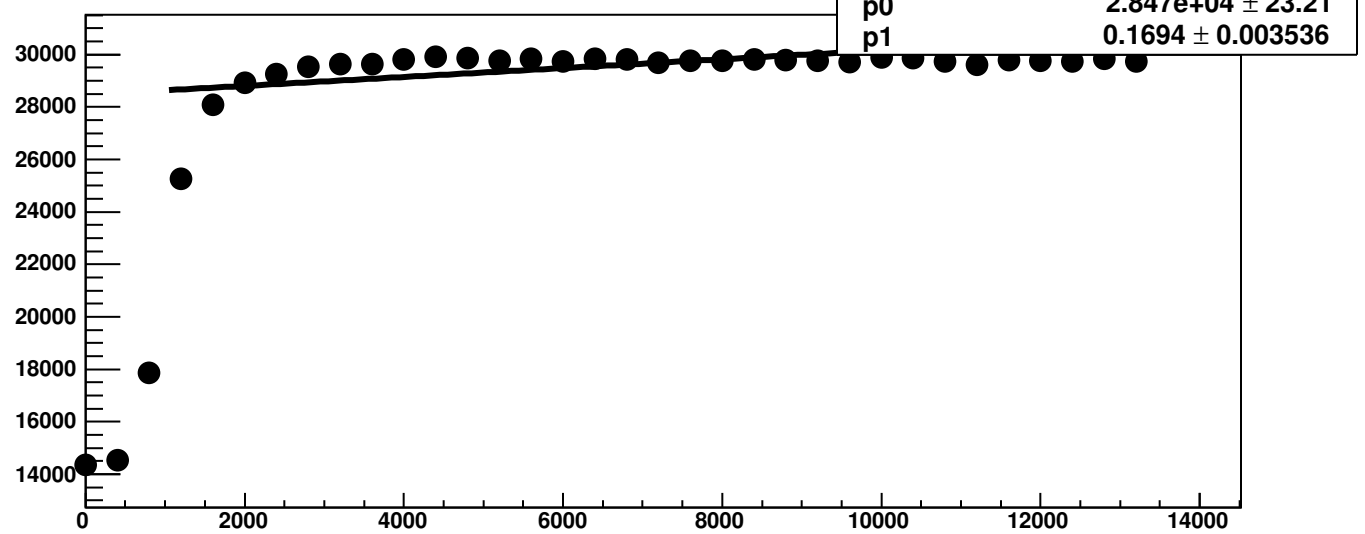



Chip 2, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 15.32 / 23
p0 -78.96 ± 0.8505
p1 0.01324 ± 0.0001288
120
100
80
60
40
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000

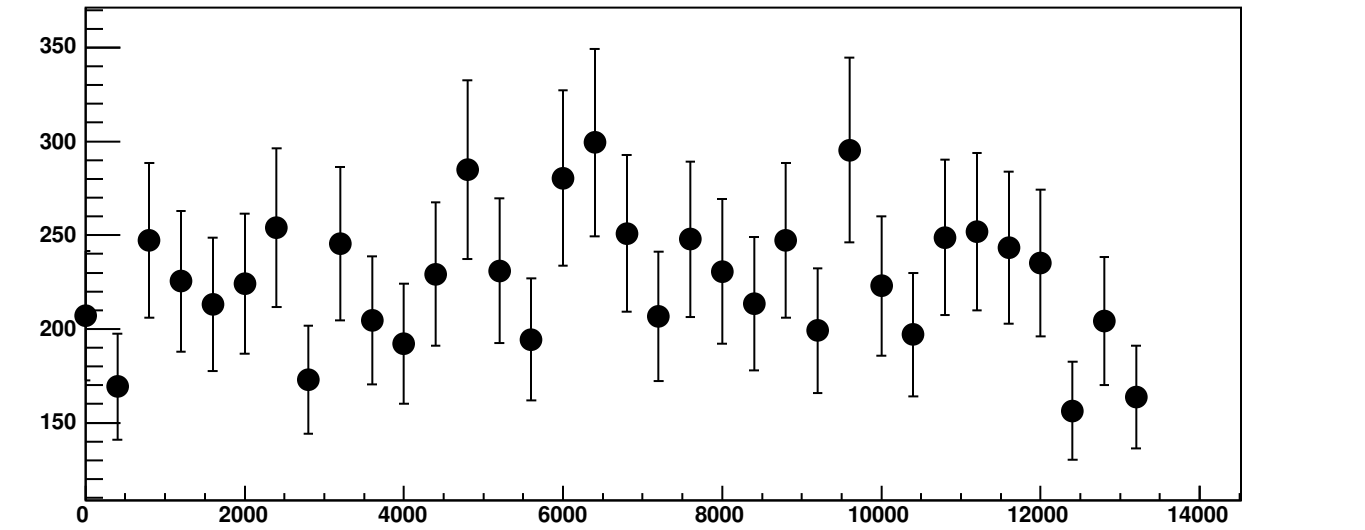

Chip 2, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

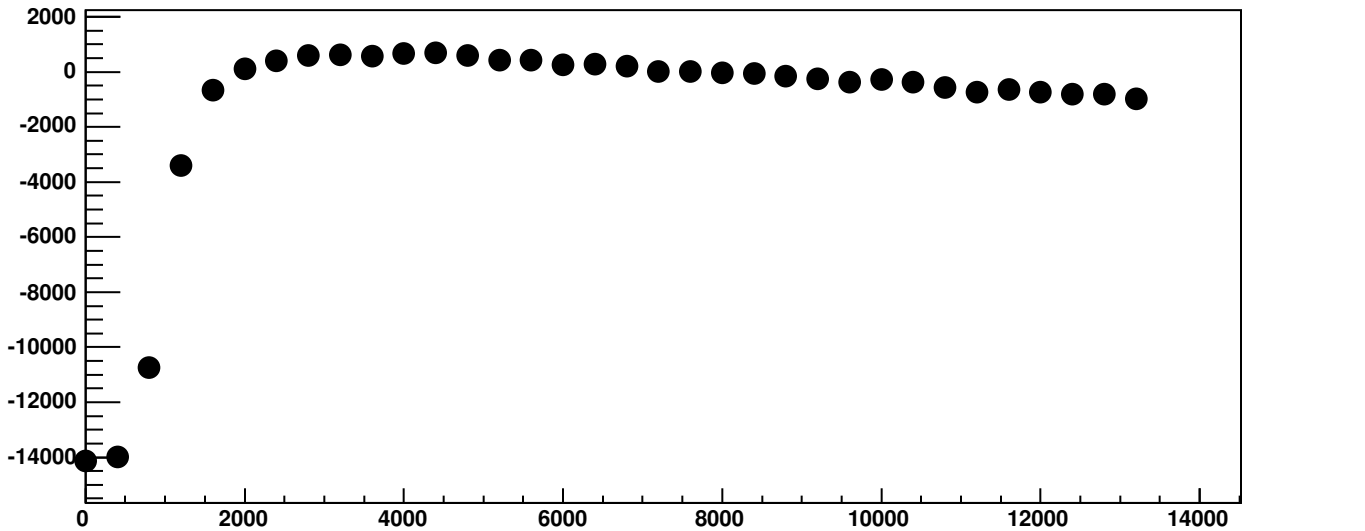
Chip 2, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC

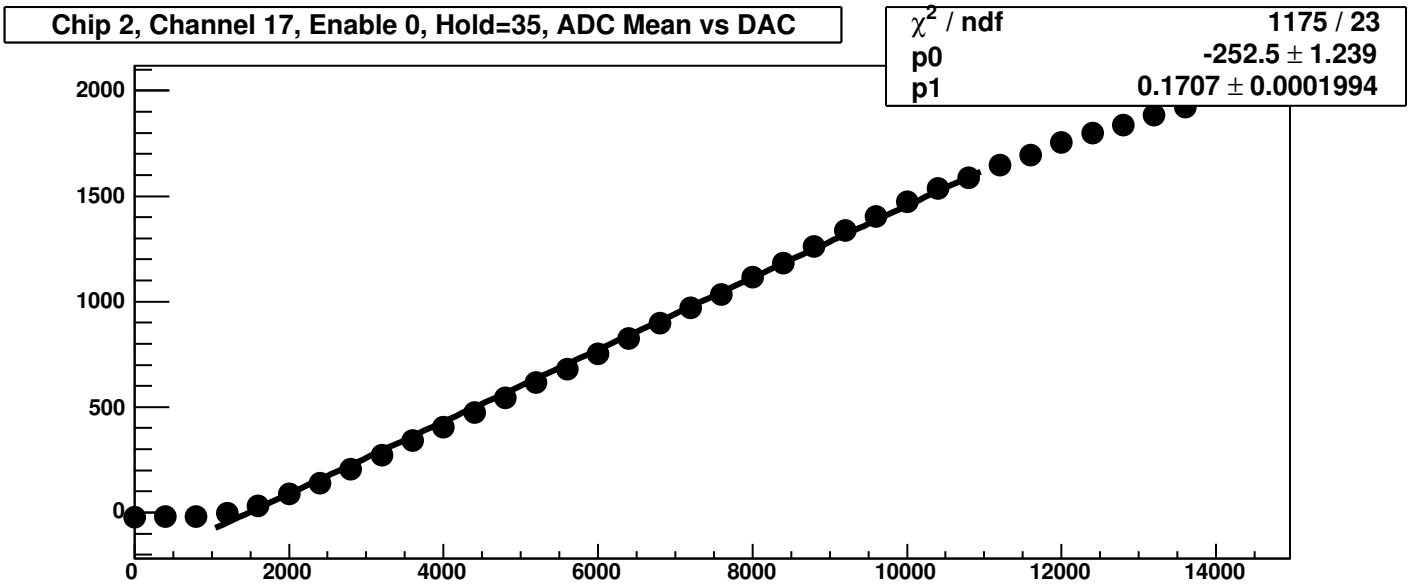


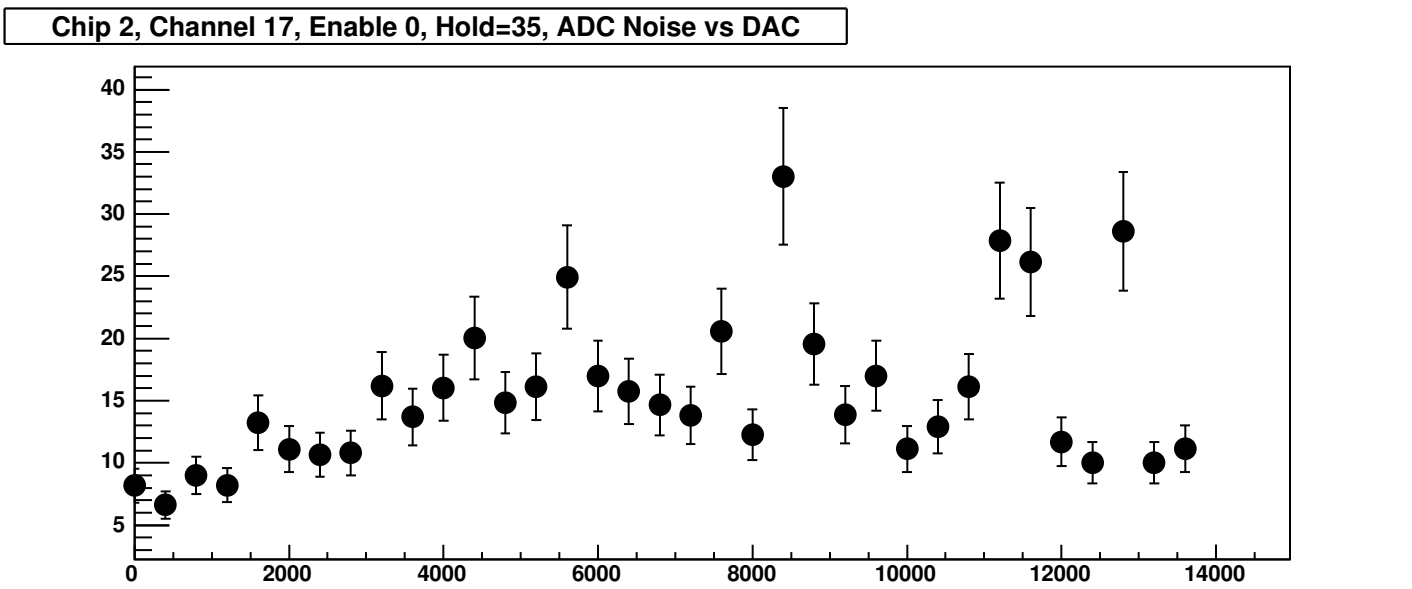
Chip 2, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC

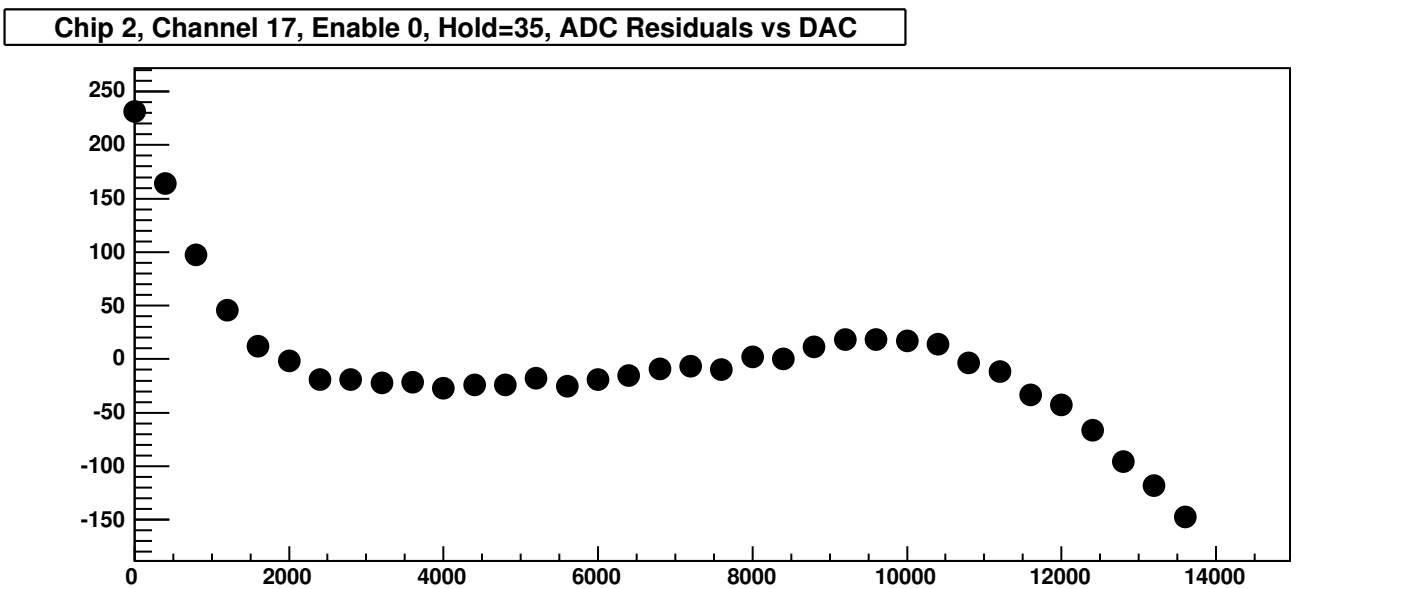


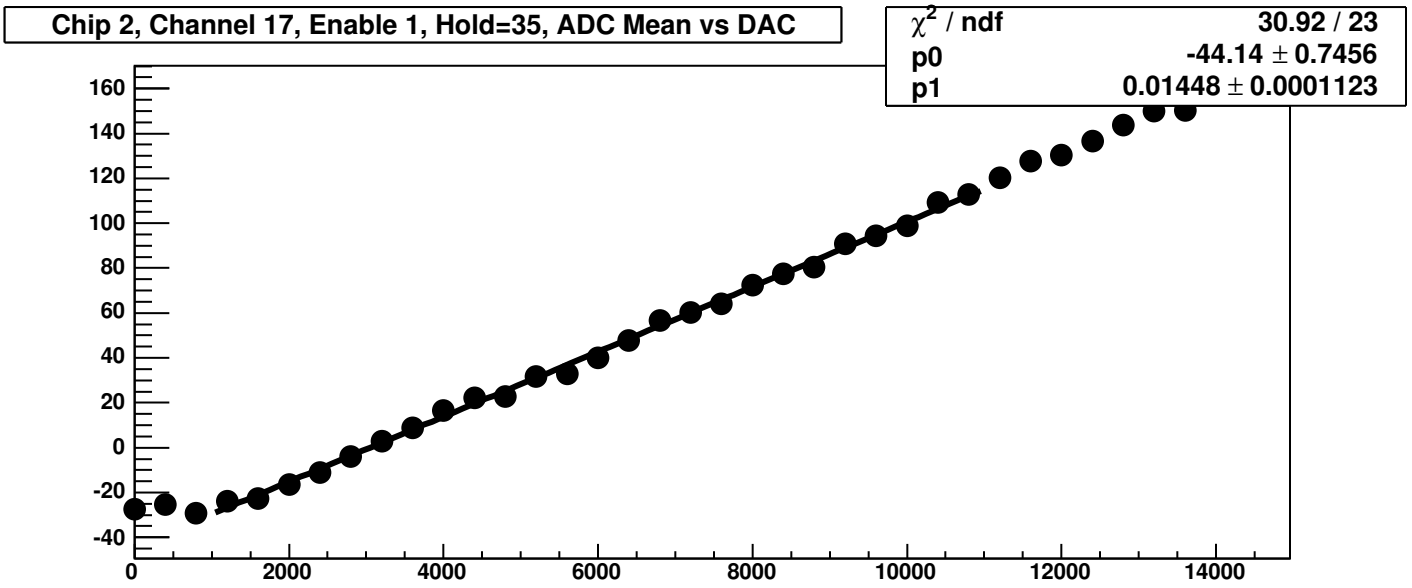
Chip 2, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC

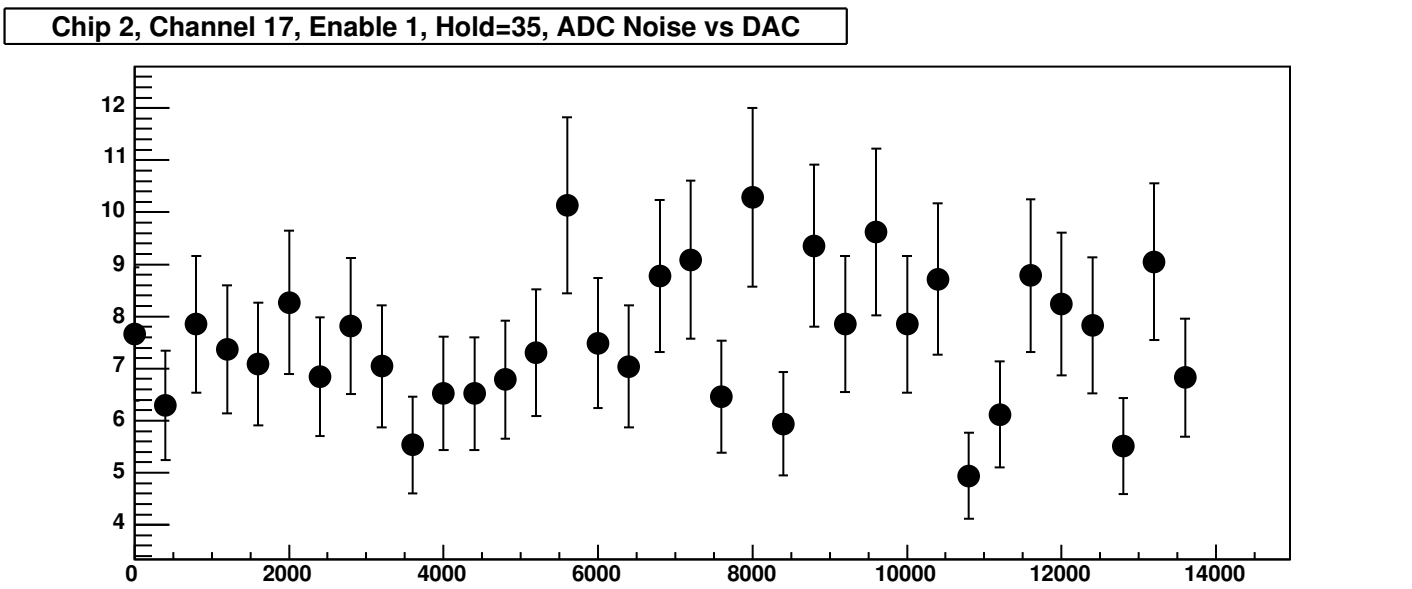


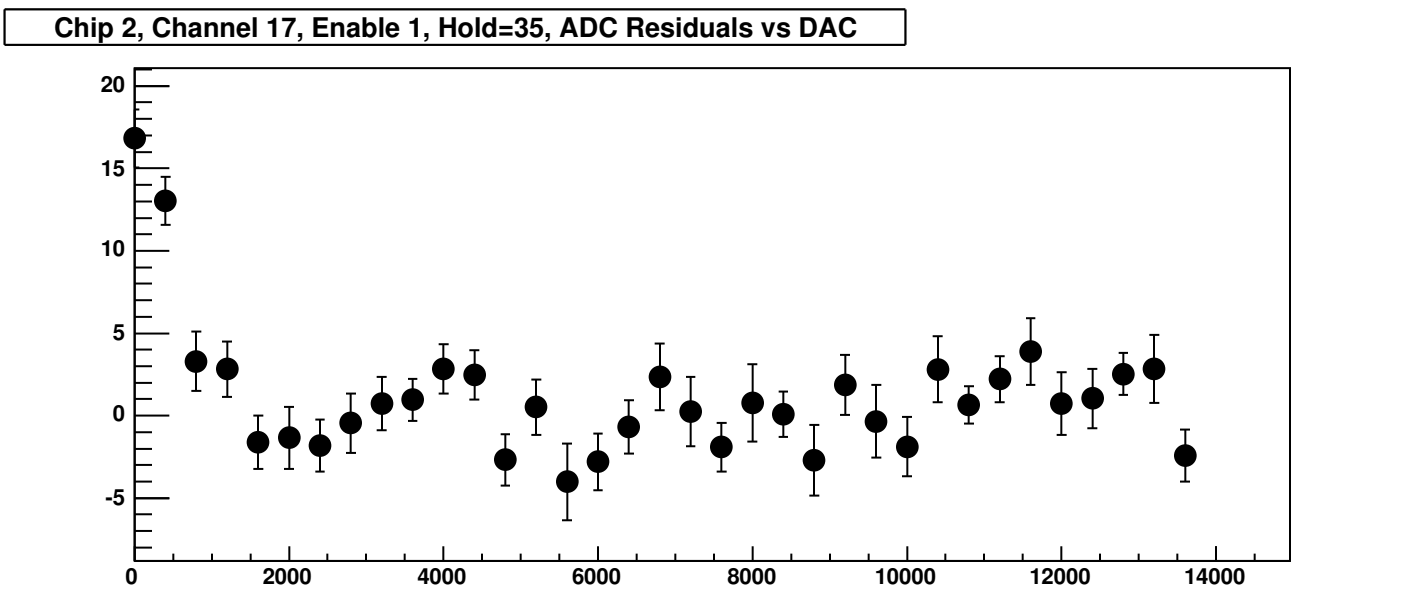

Chip 2, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1175 / 23
p0 -252.5 ± 1.239
p1 0.1707 ± 0.0001994
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

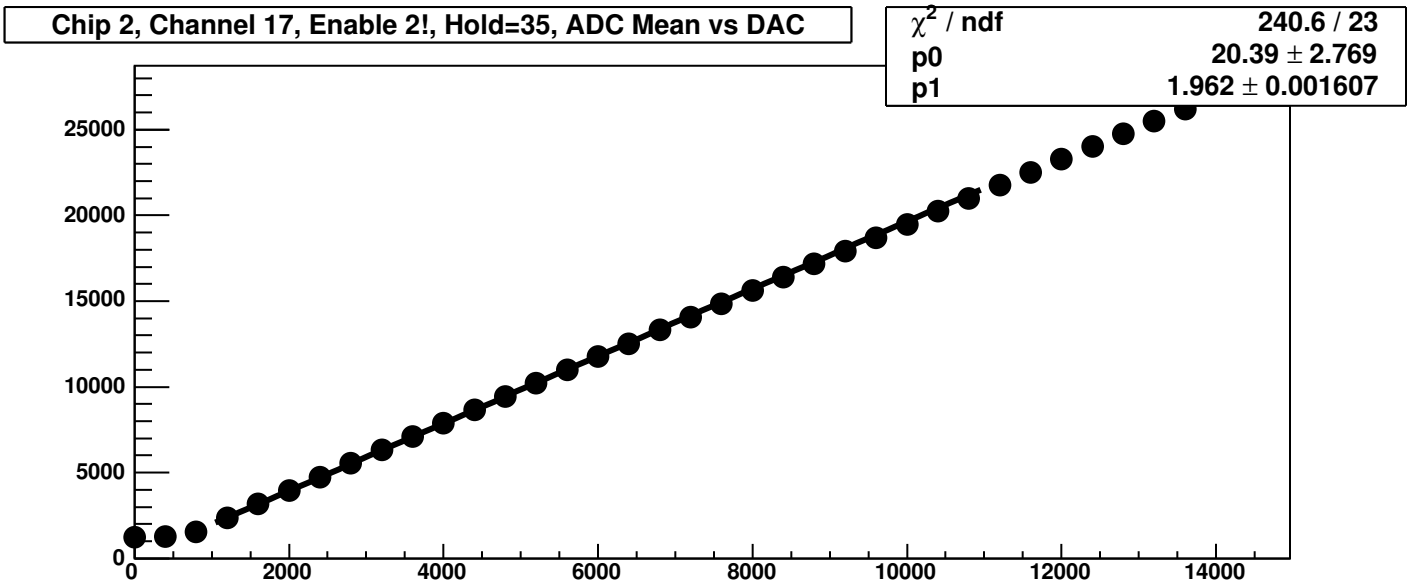

Chip 2, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

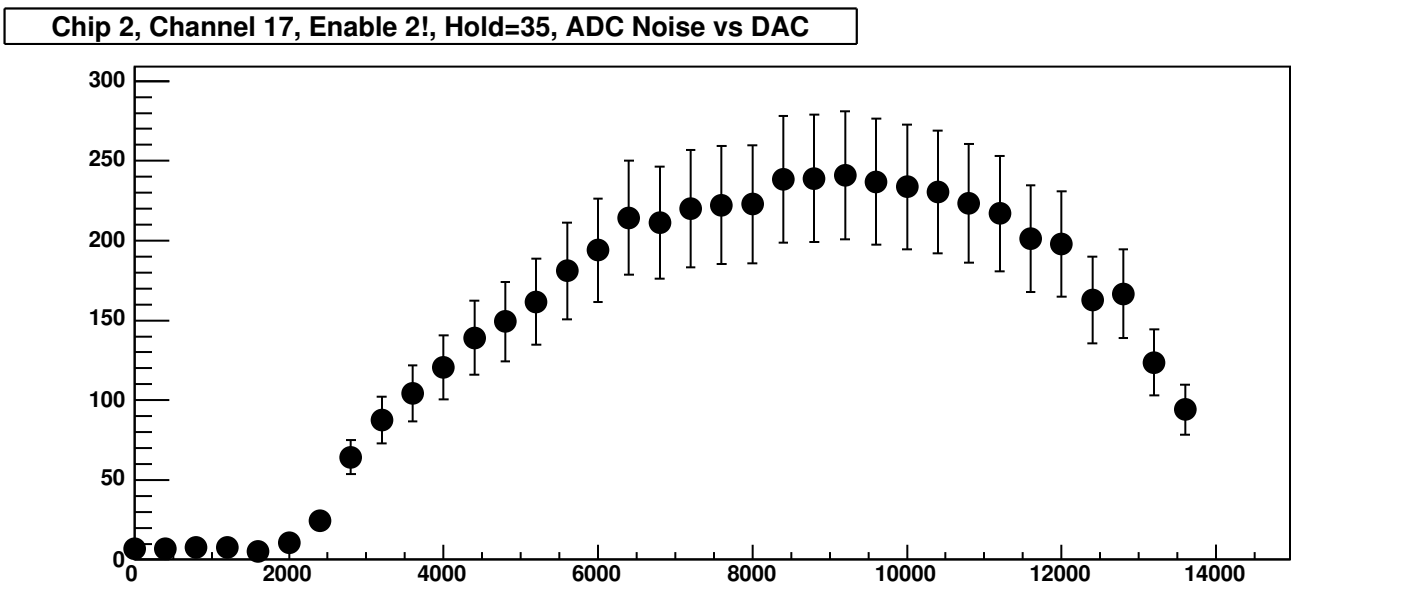

Chip 2, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

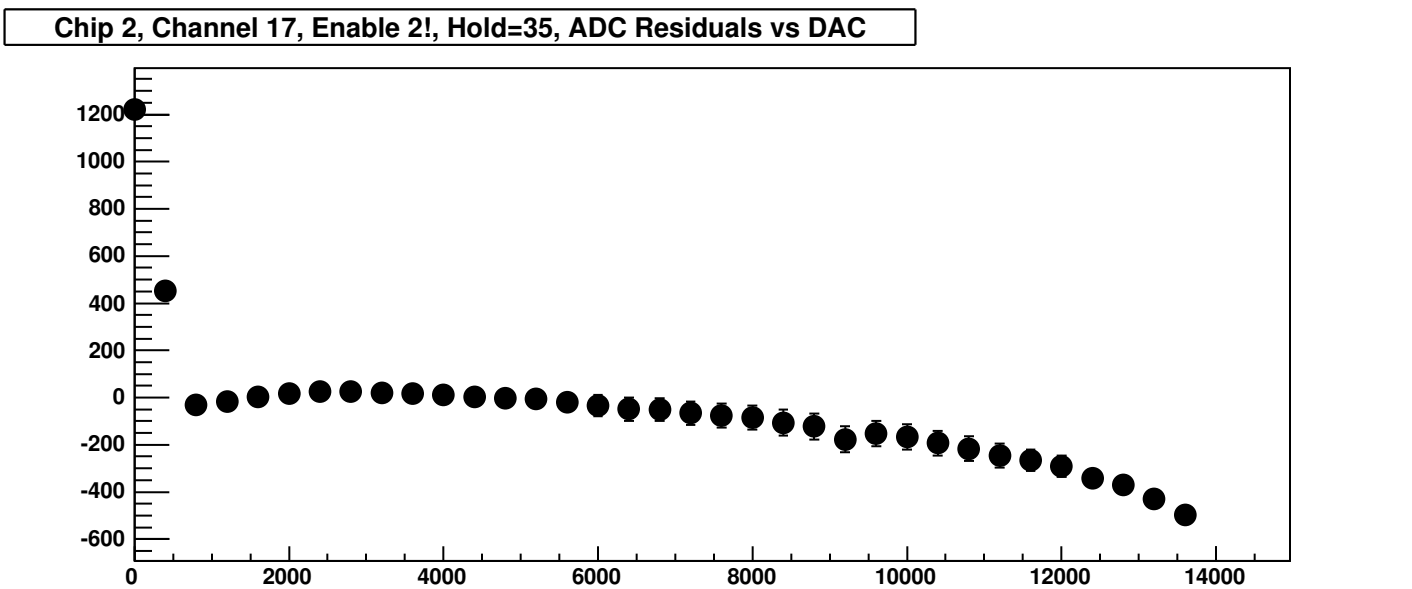

Chip 2, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 30.92 / 23
p0 -44.14 ± 0.7456
p1 0.01448 ± 0.0001123
160
140
120
100
80
60
40
20
0
-20
-40
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

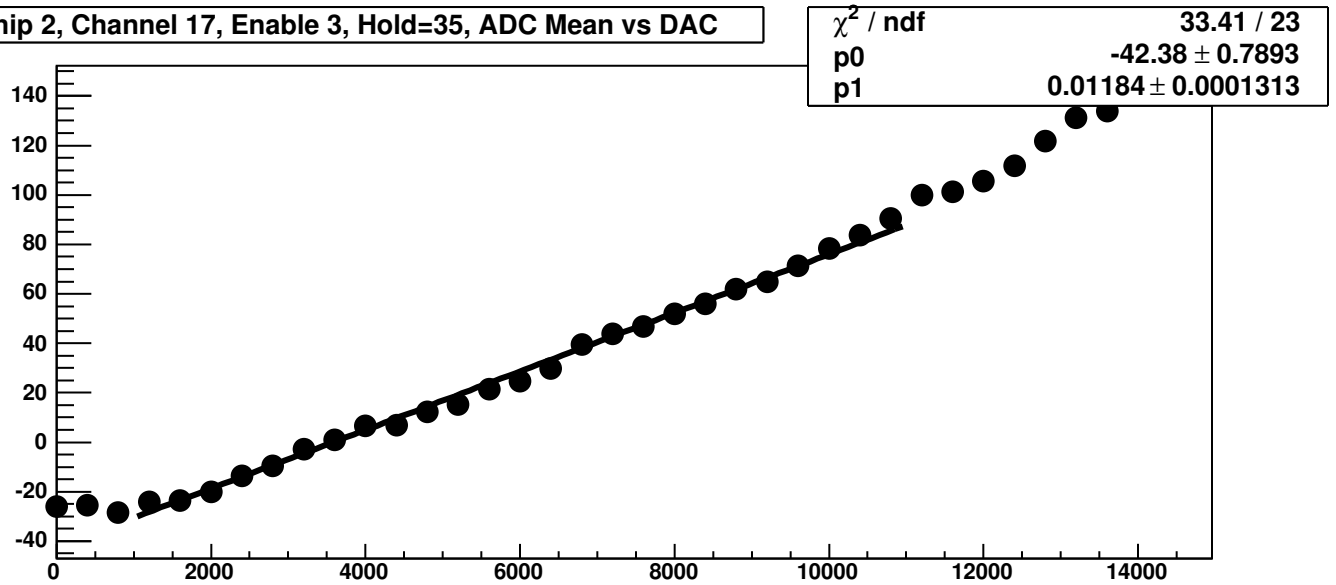

Chip 2, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 240.6 / 23
p0 20.39 ± 2.769
p1 1.962 ± 0.001607
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

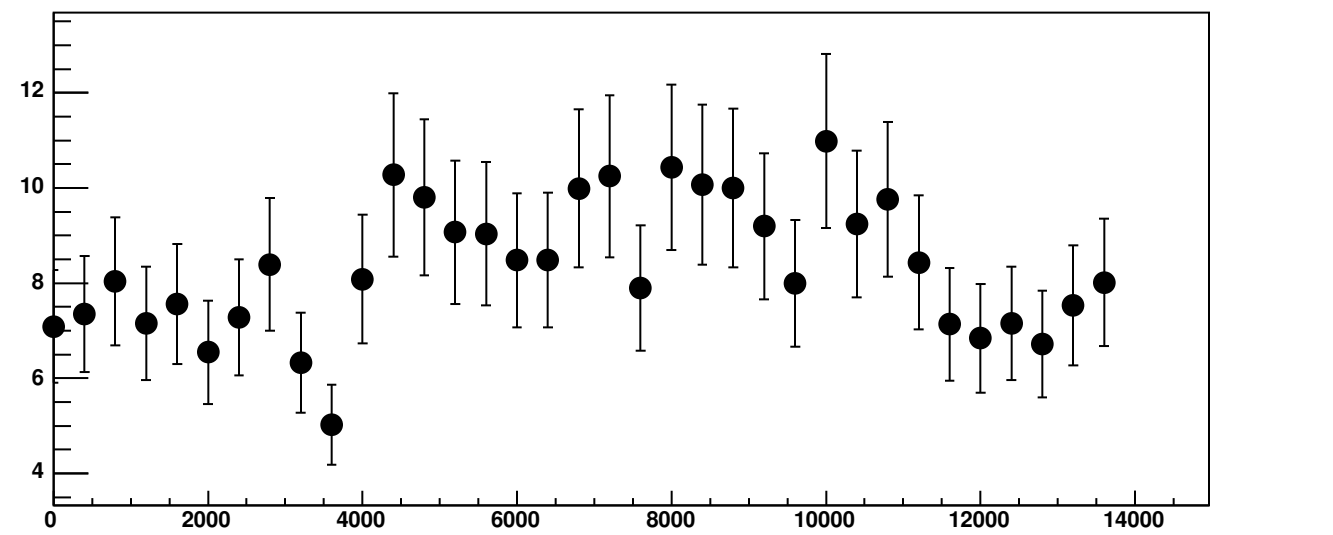

Chip 2, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

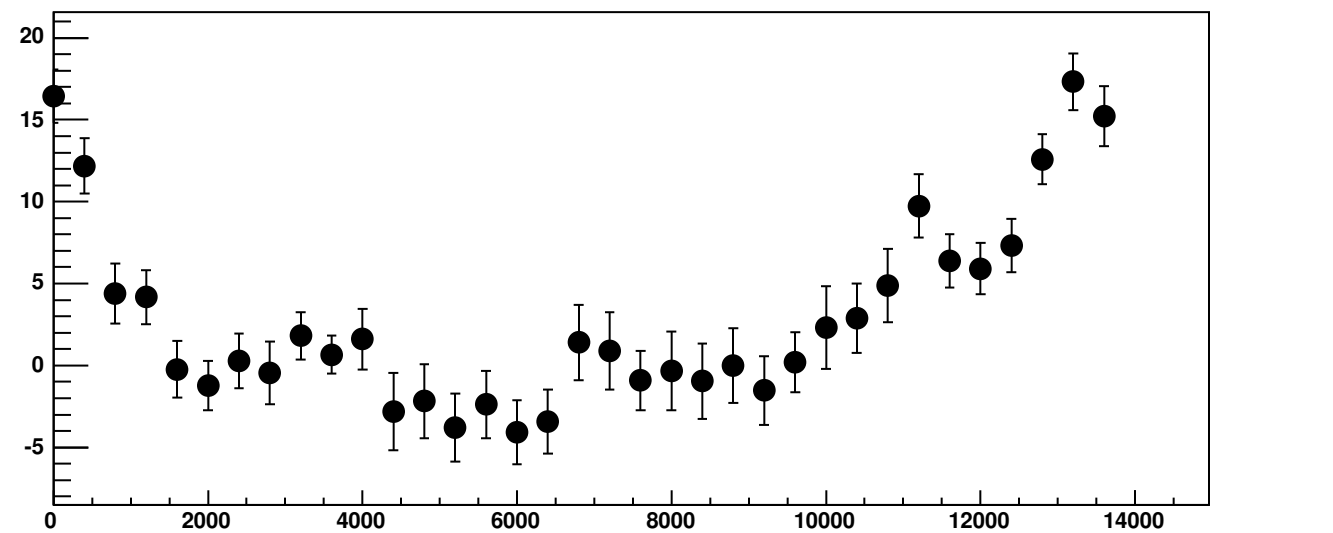
Chip 2, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



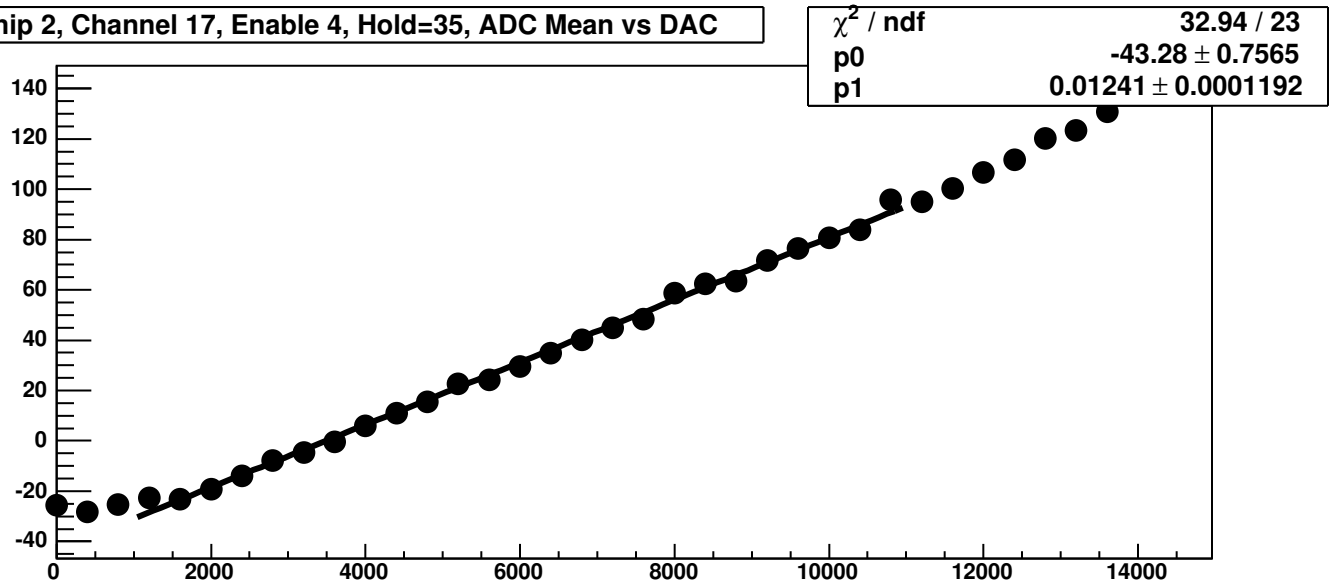
Chip 2, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



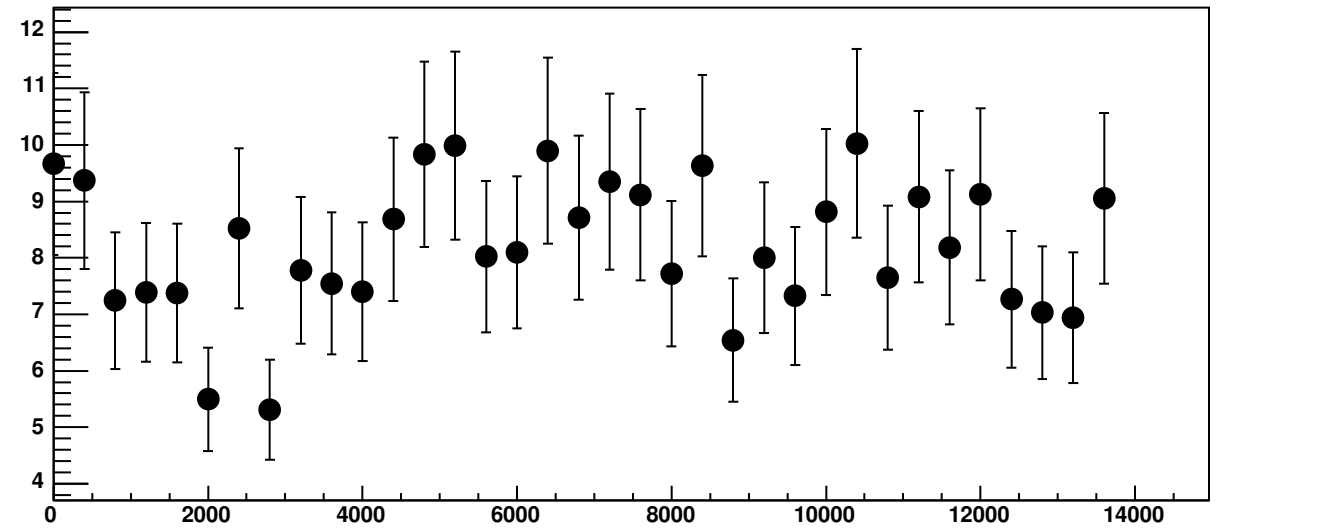
Chip 2, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



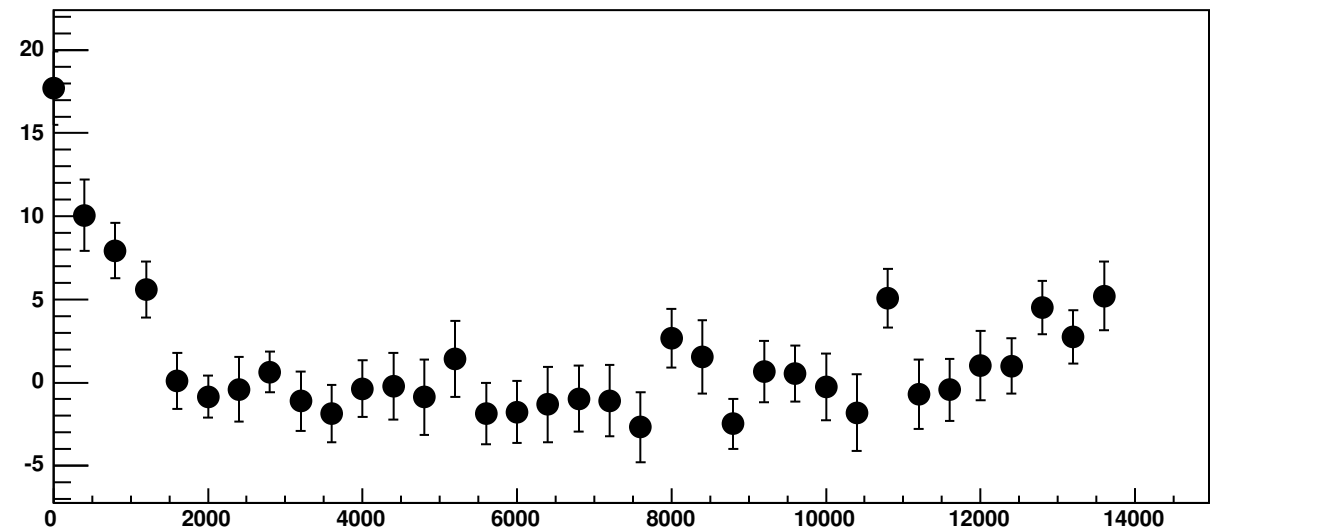
Chip 2, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

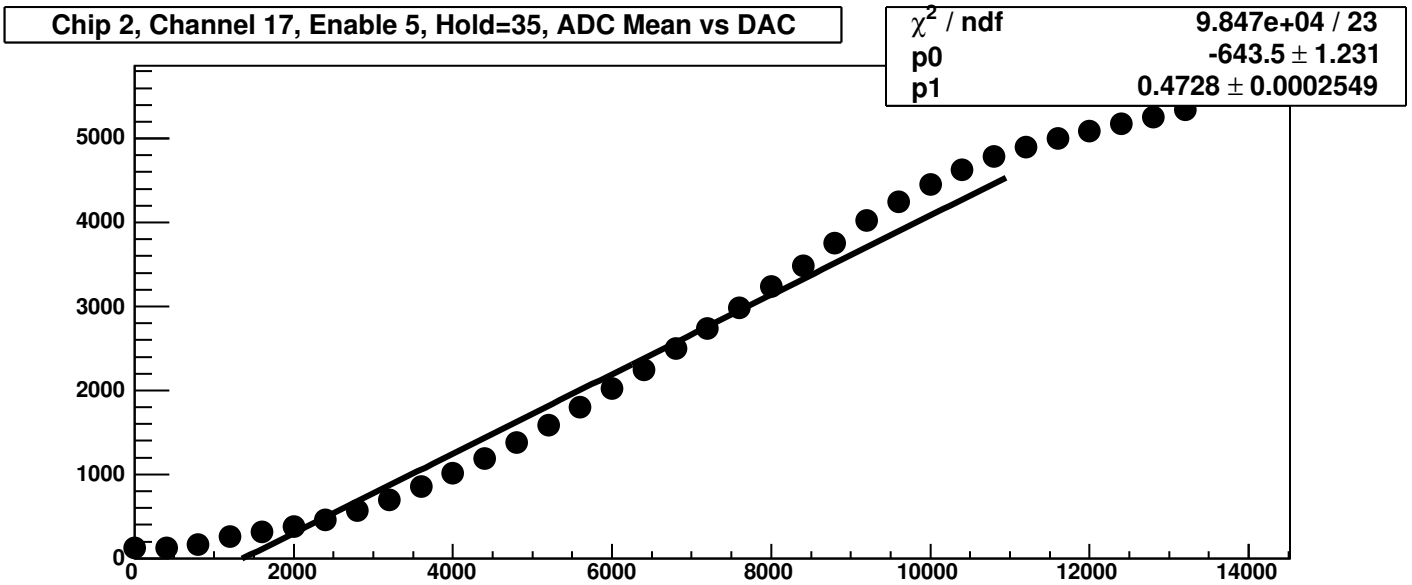


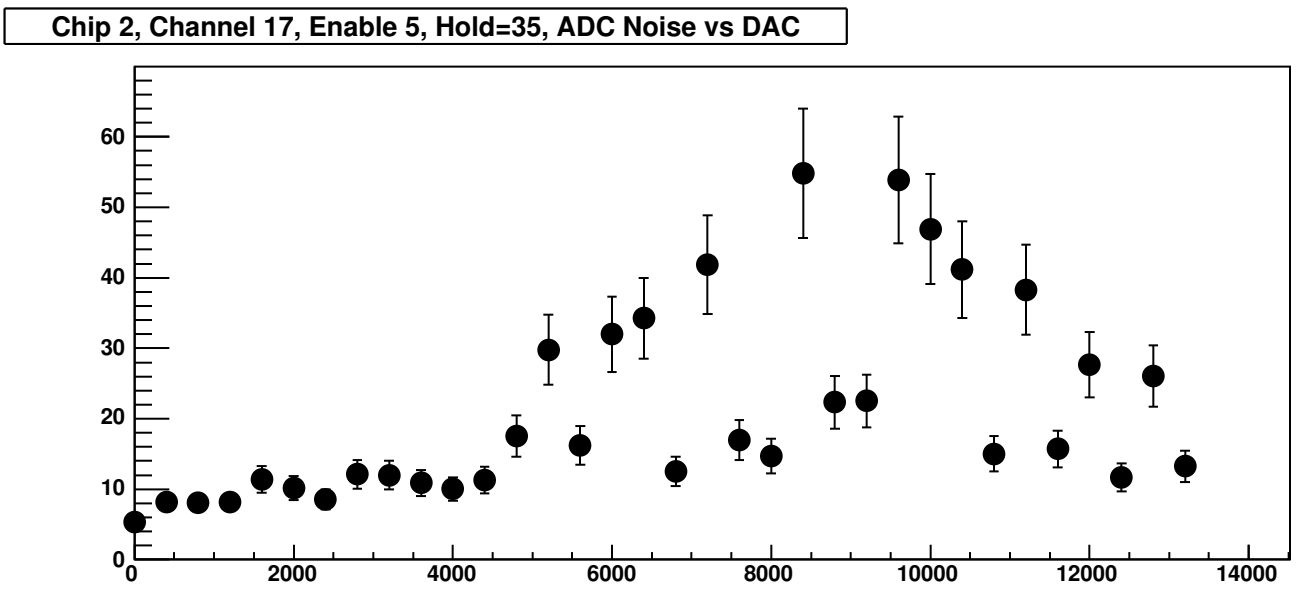
Chip 2, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

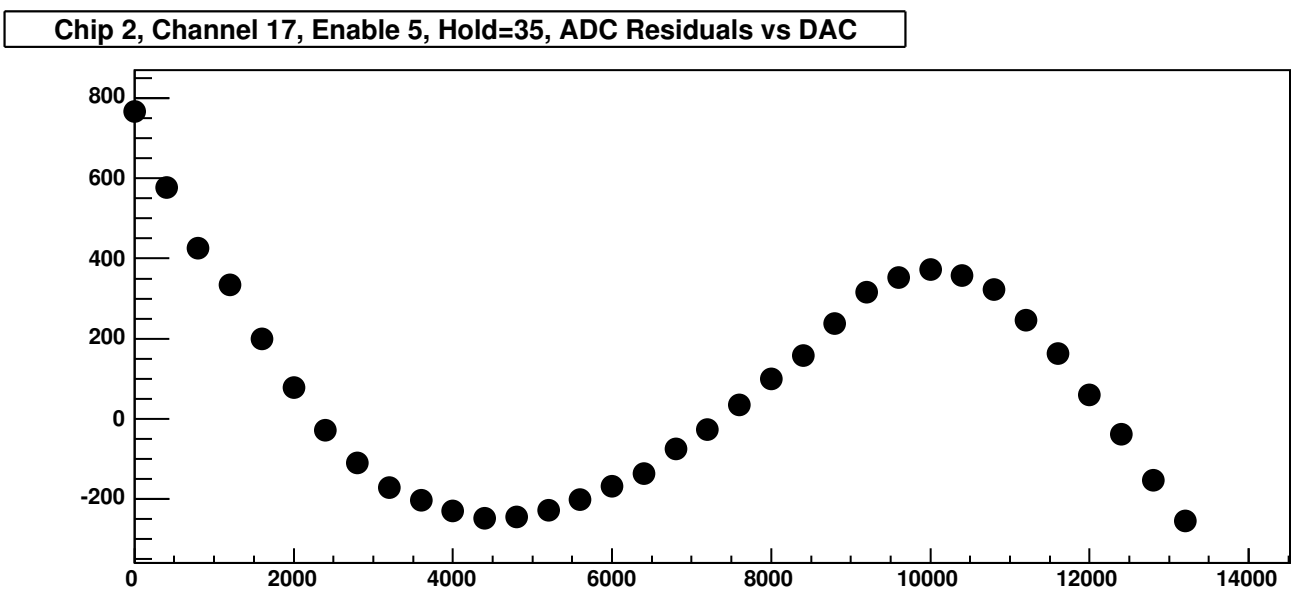


Chip 2, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

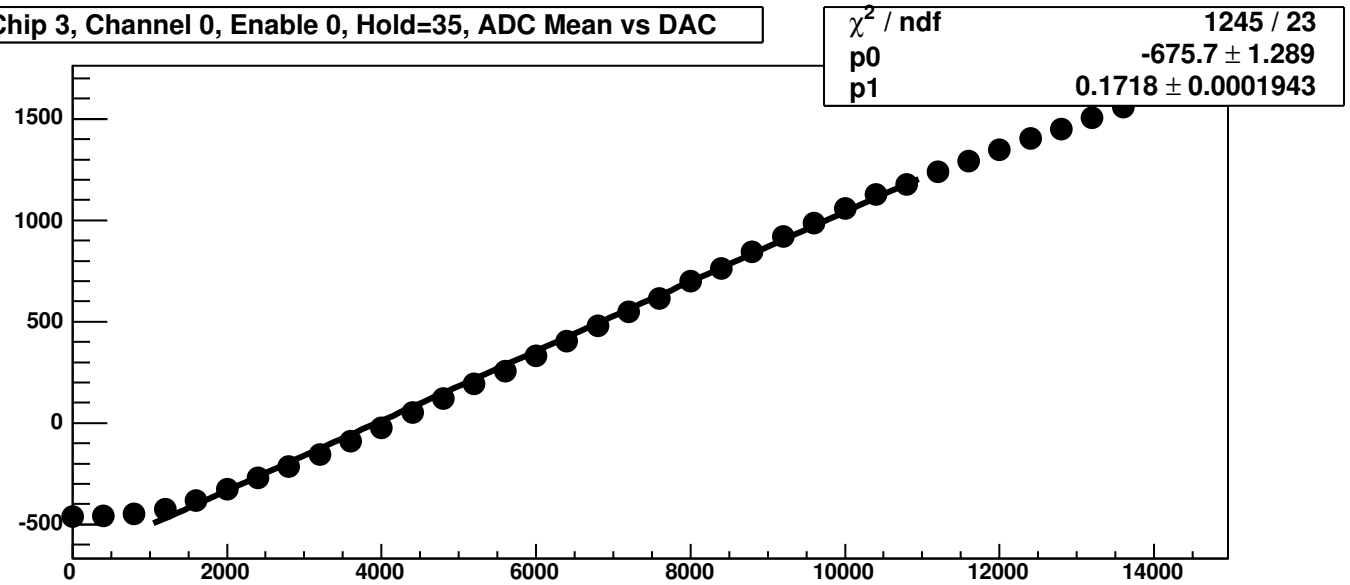



Chip 2, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 9.847e+04 / 23
p0 -643.5 ± 1.231
p1 0.4728 ± 0.0002549
0
1000
2000
3000
4000
5000
0
2000
4000
6000
8000
10000
12000
14000

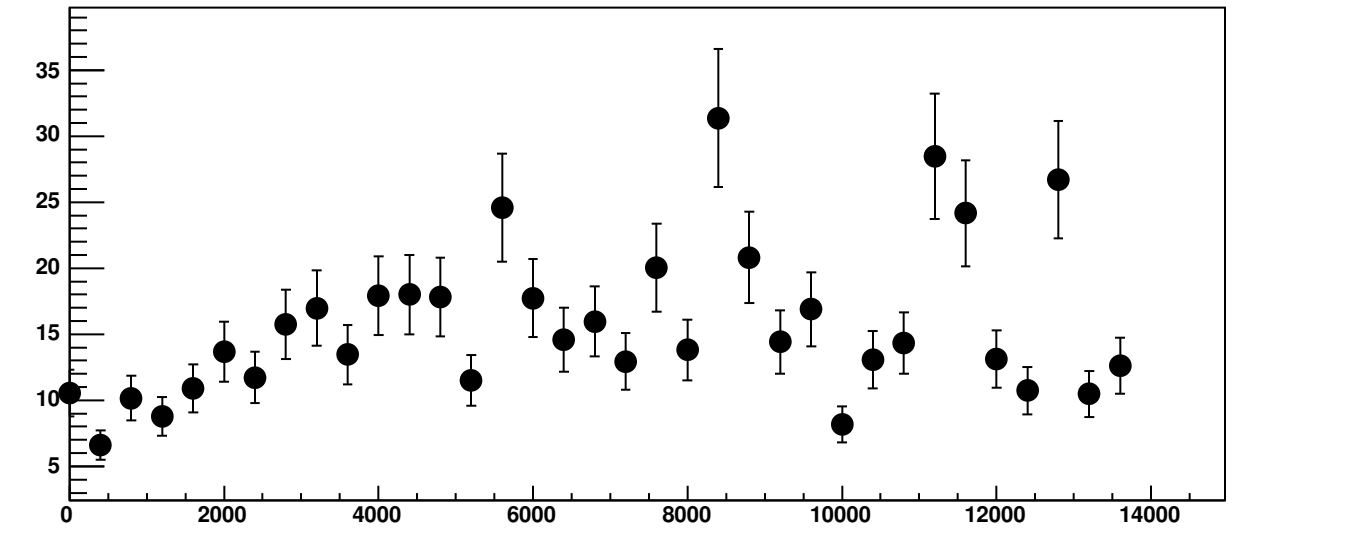

Chip 2, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
0
10
20
30
40
50
60
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
-200
0
200
400
600
800
0
2000
4000
6000
8000
10000
12000
14000

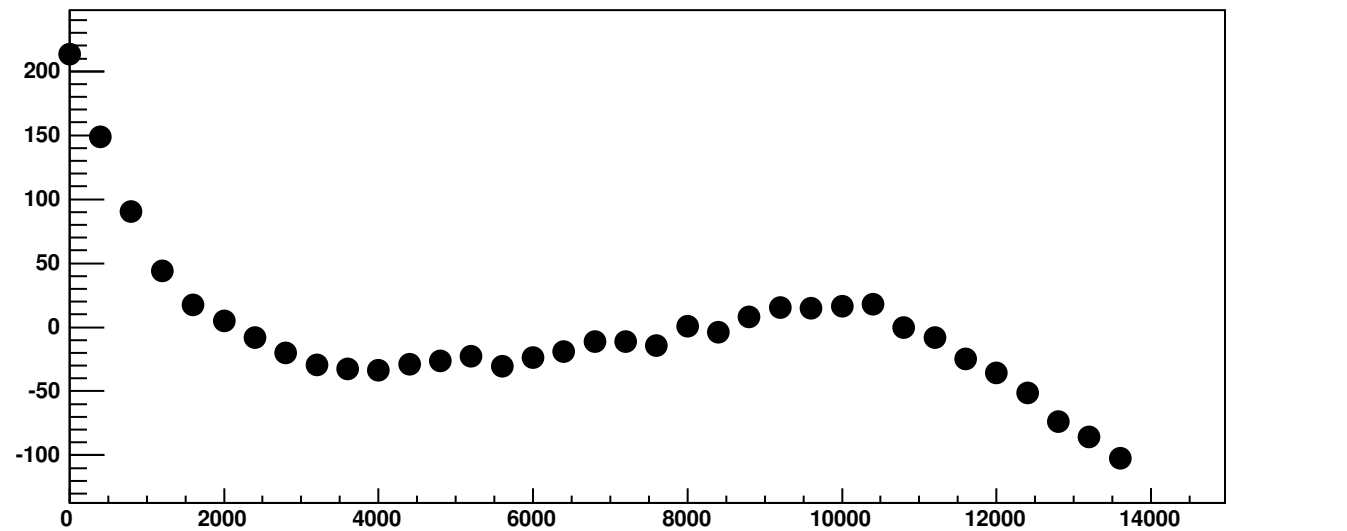
Chip 3, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

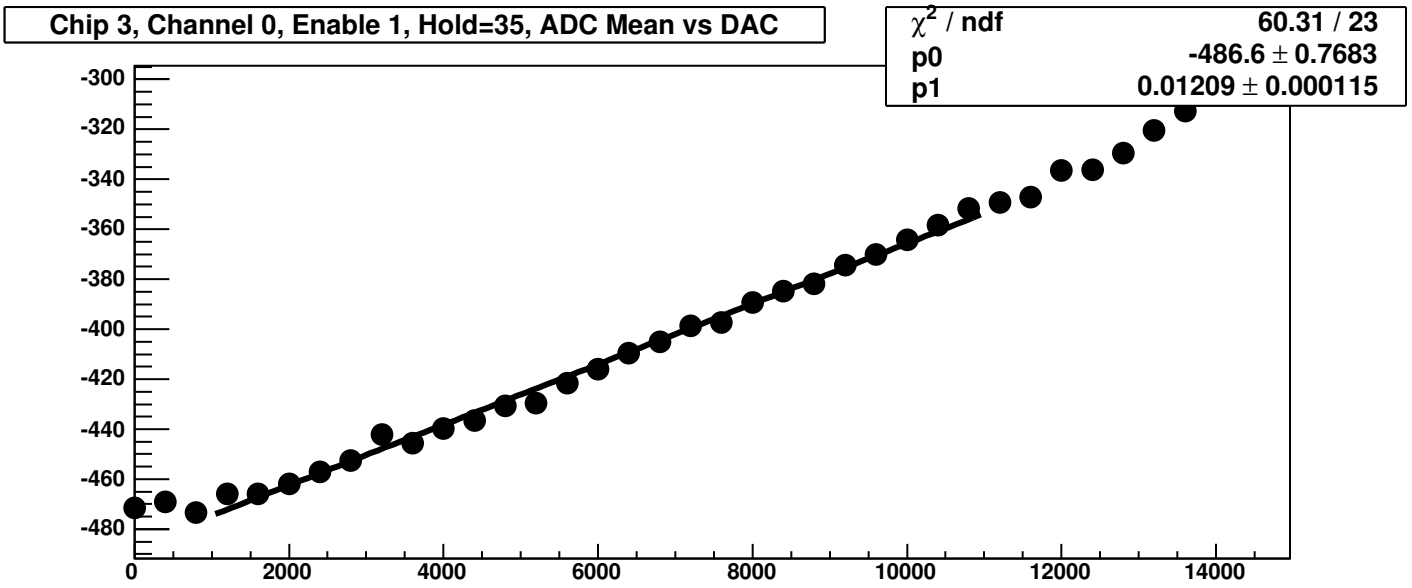


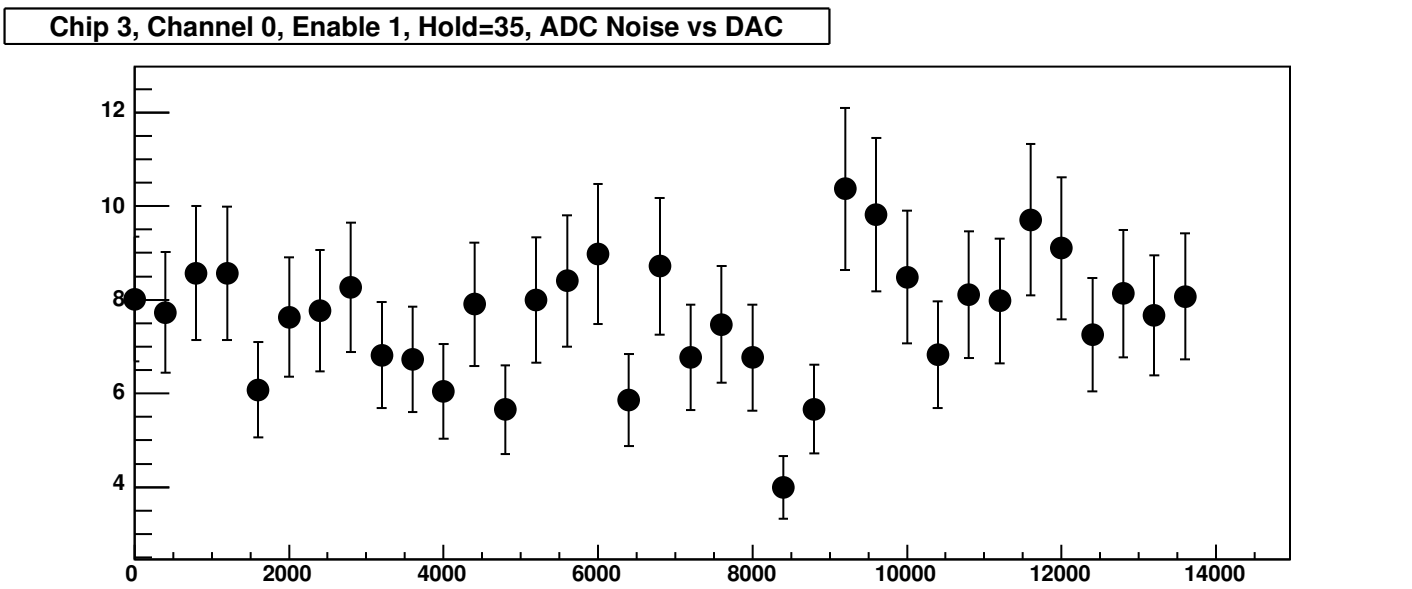
Chip 3, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC

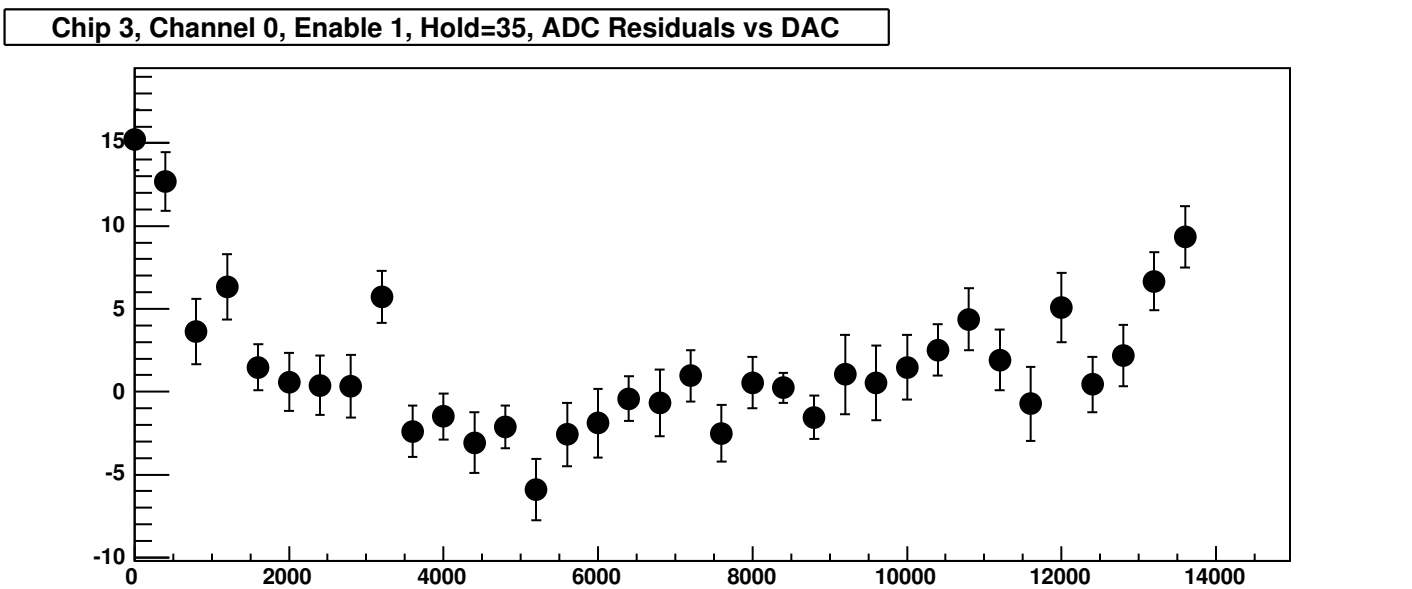


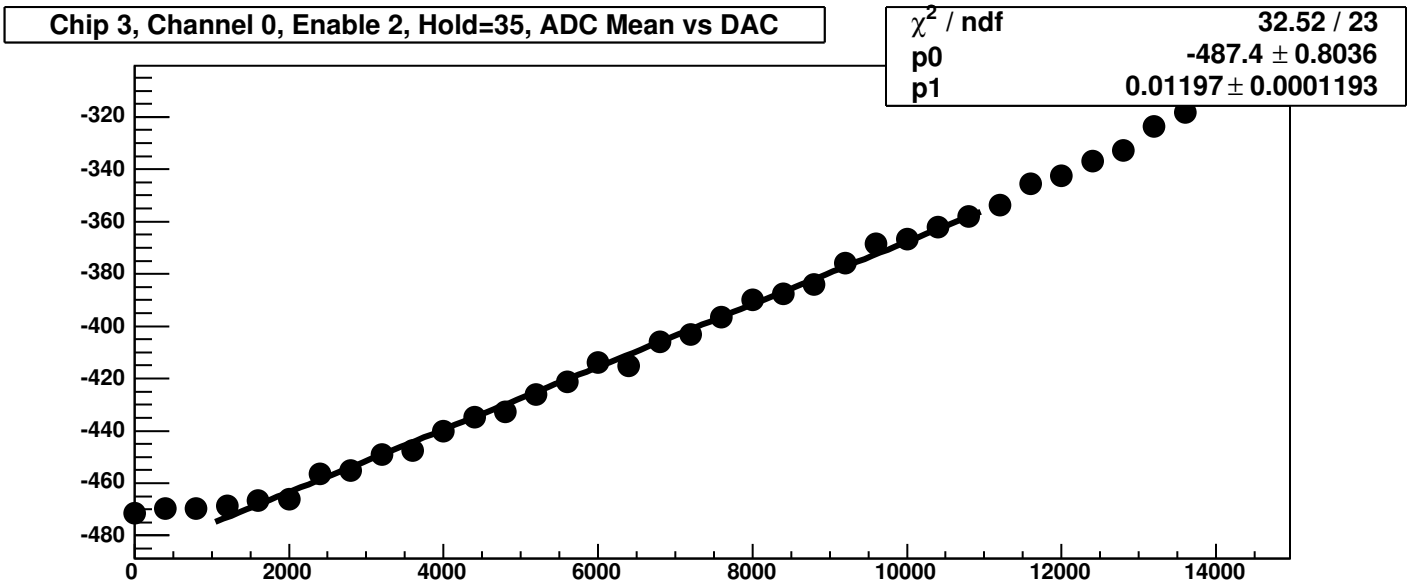
Chip 3, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

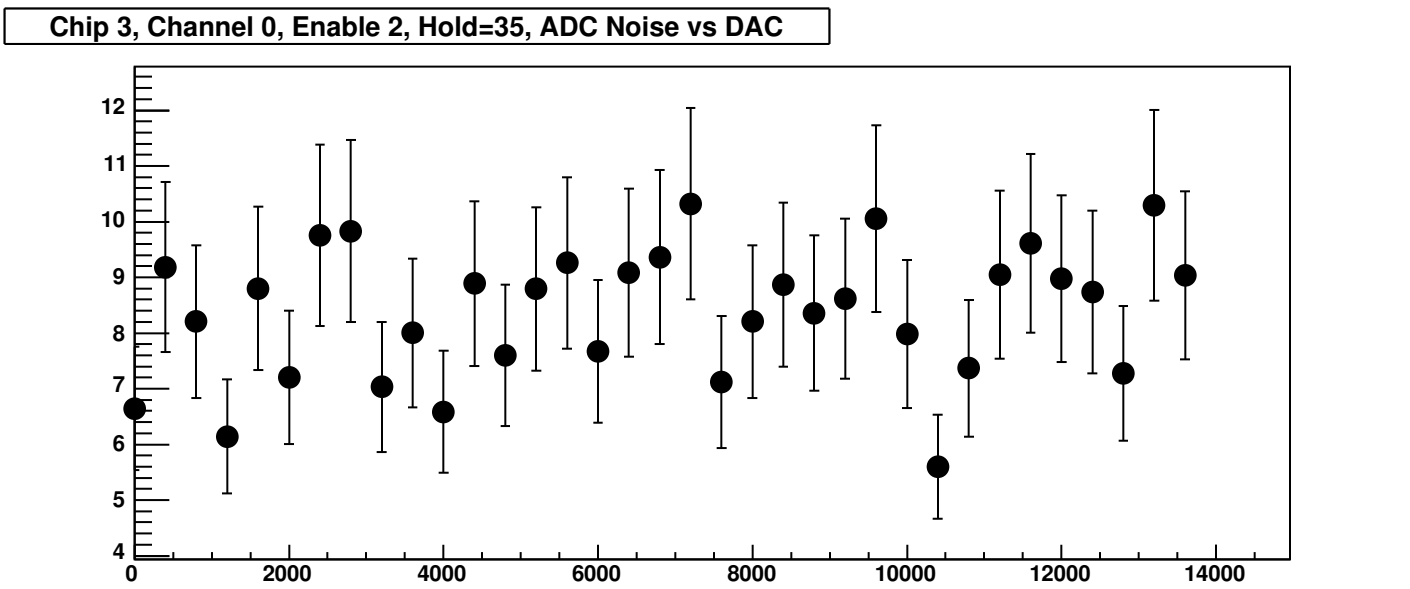


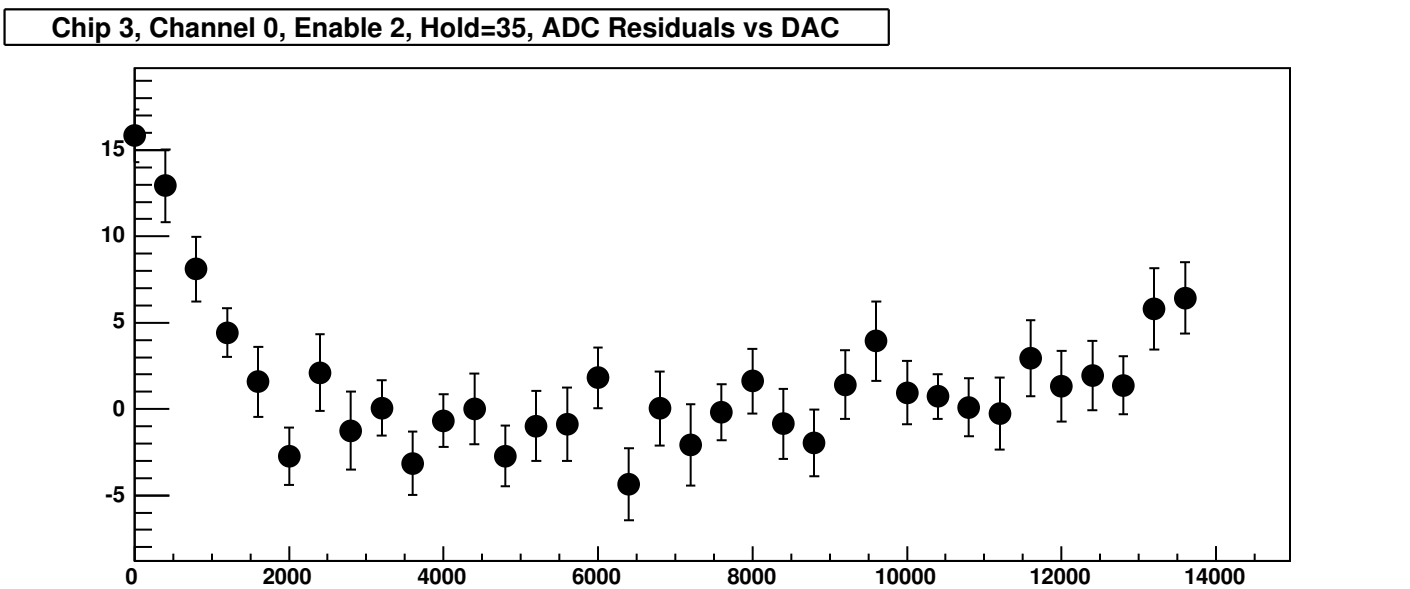

Chip 3, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 60.31 / 23
p0 -486.6 ± 0.7683
p1 0.01209 ± 0.000115
-300
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
2000
4000
6000
8000
10000
12000
14000

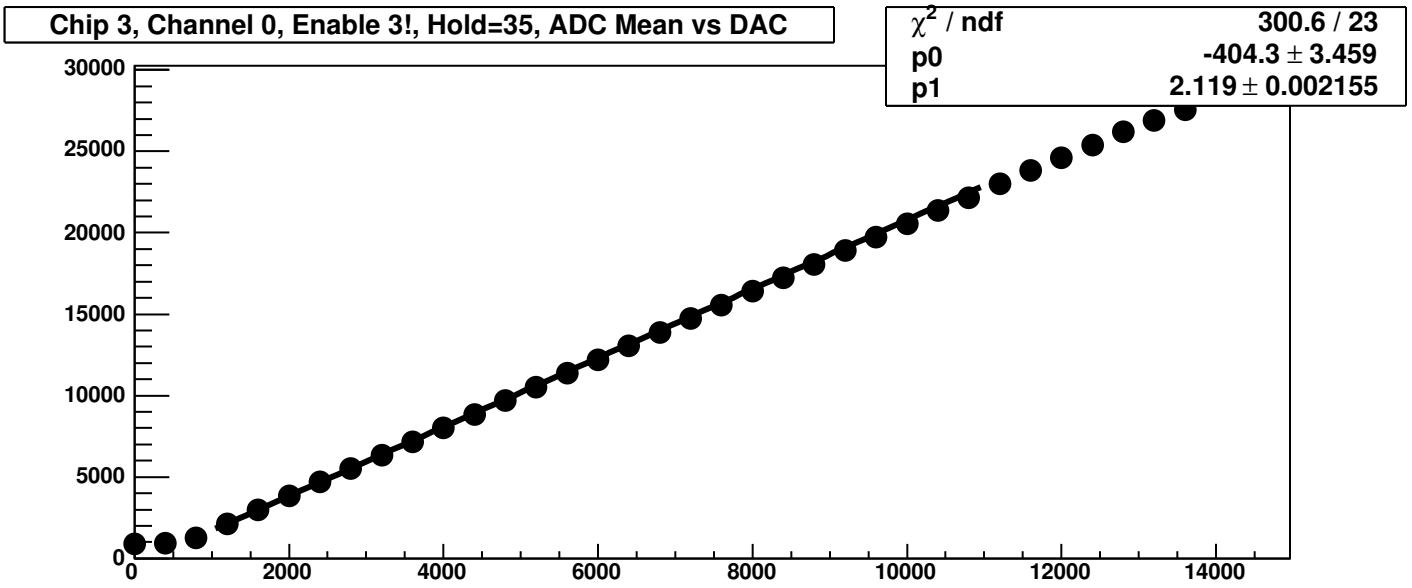

Chip 3, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

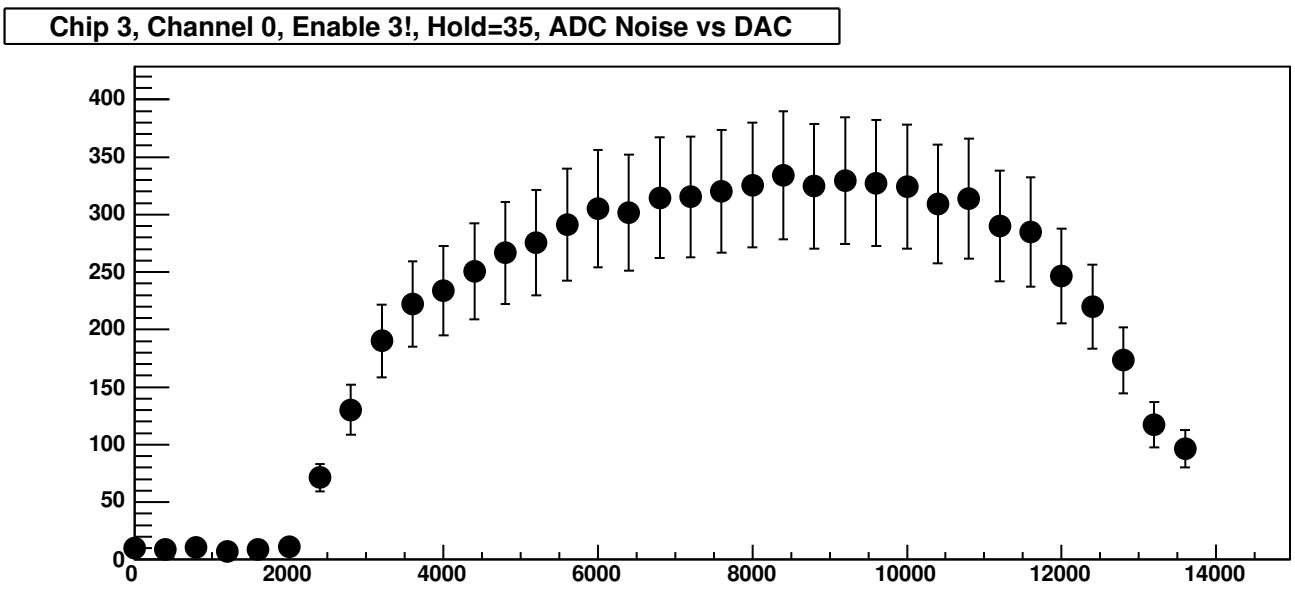

Chip 3, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

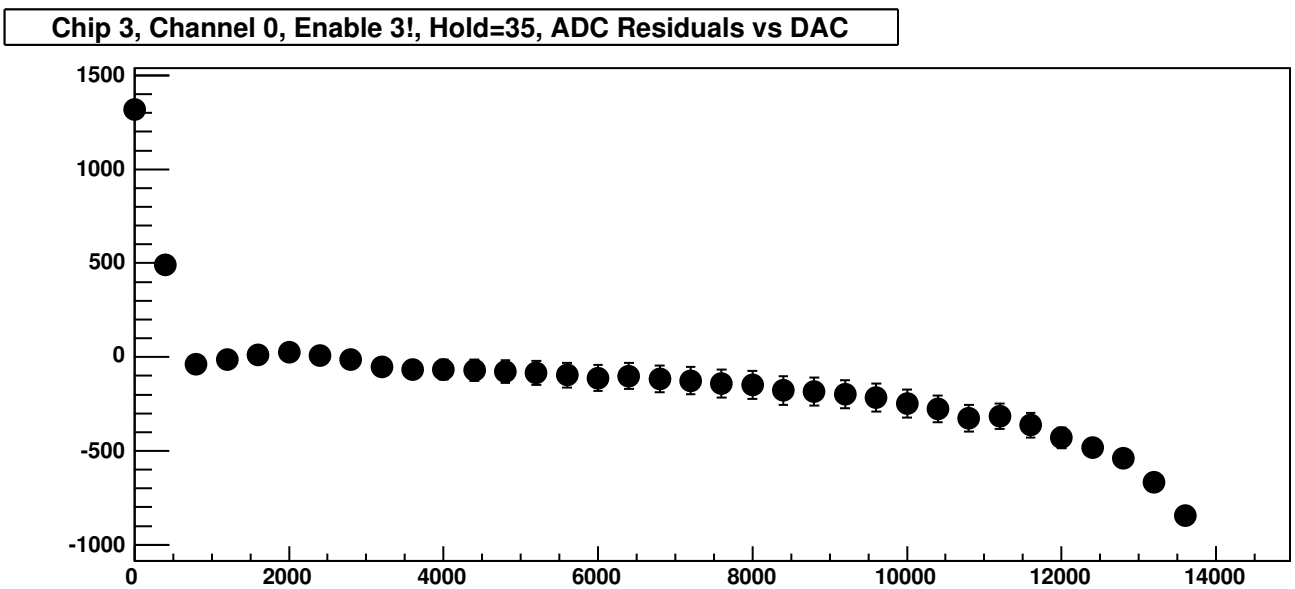

Chip 3, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 32.52 / 23
p0 -487.4 ± 0.8036
p1 0.01197 ± 0.0001193
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
2000
4000
6000
8000
10000
12000
14000

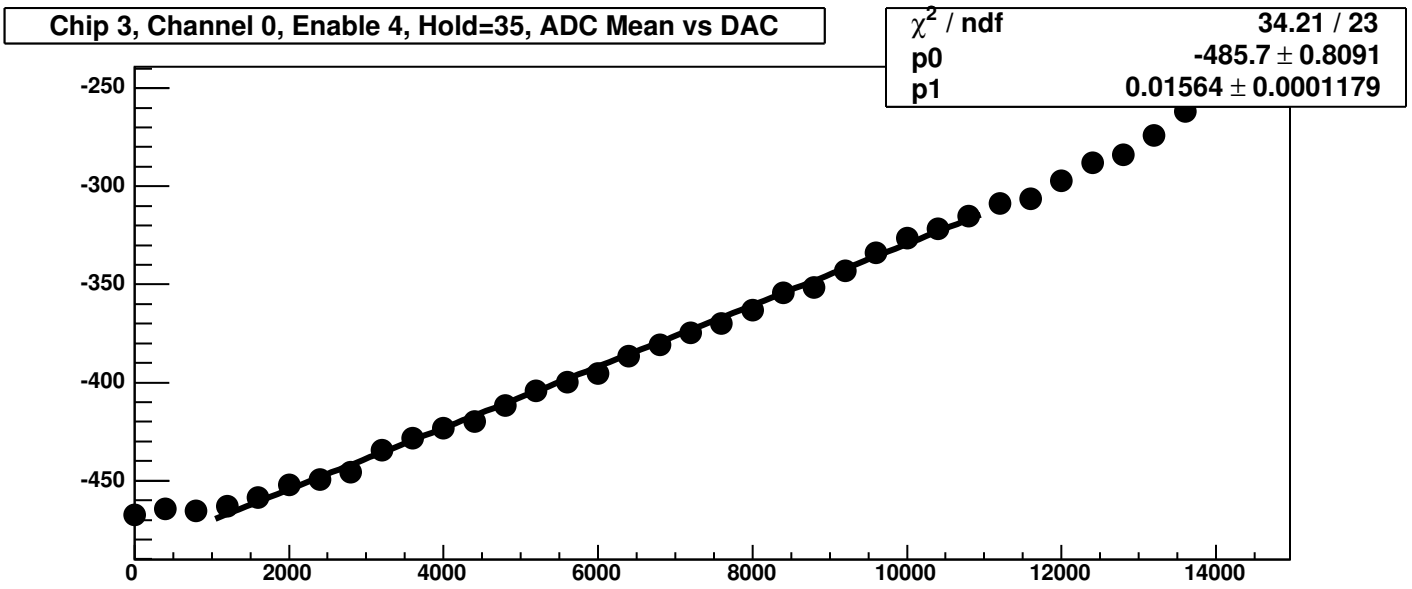

Chip 3, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

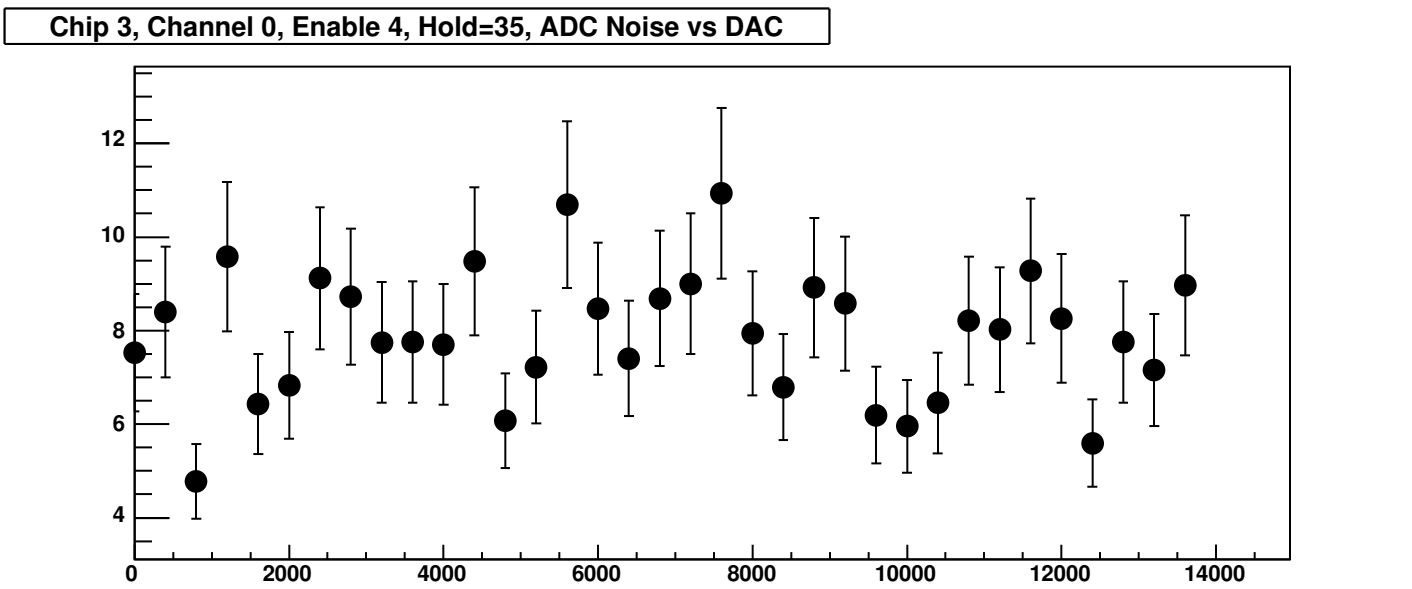

Chip 3, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

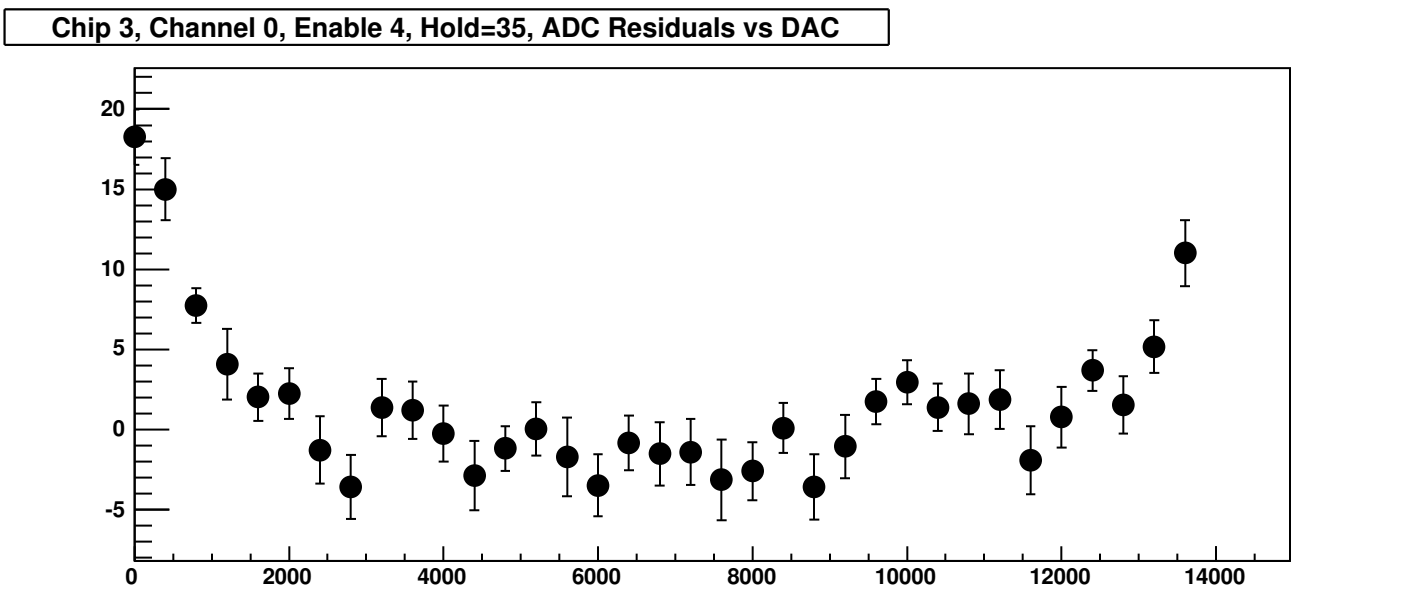

Chip 3, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 300.6 / 23
p0 -404.3 ± 3.459
p1 2.119 ± 0.002155
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

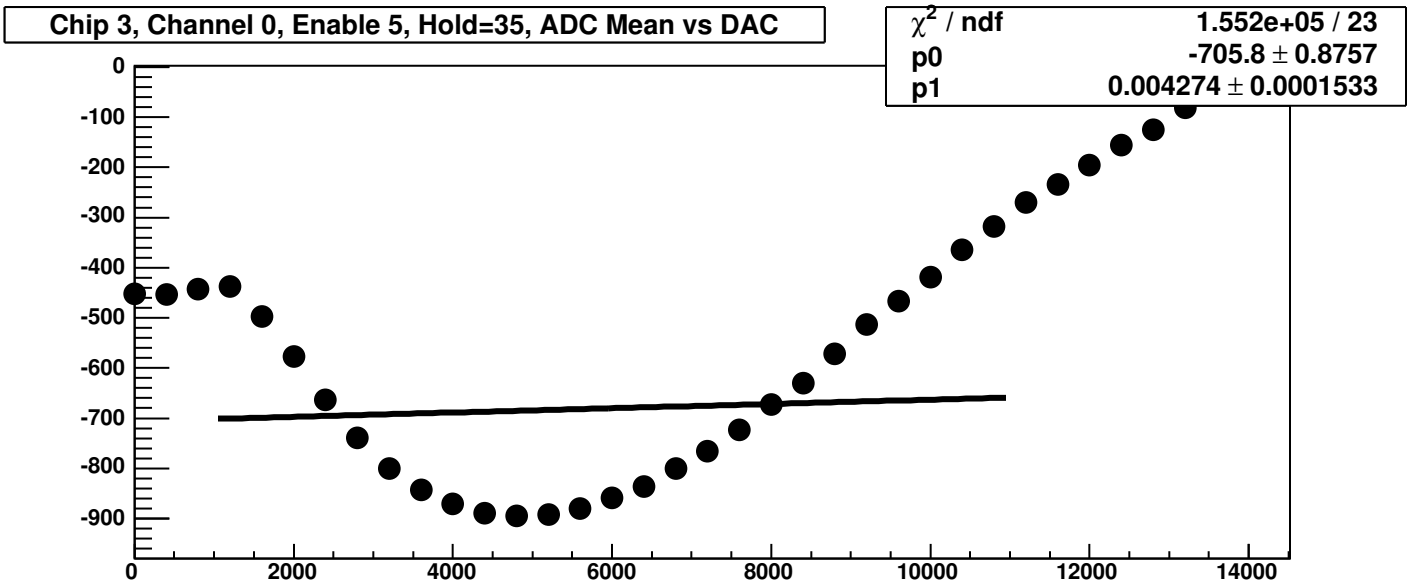

Chip 3, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

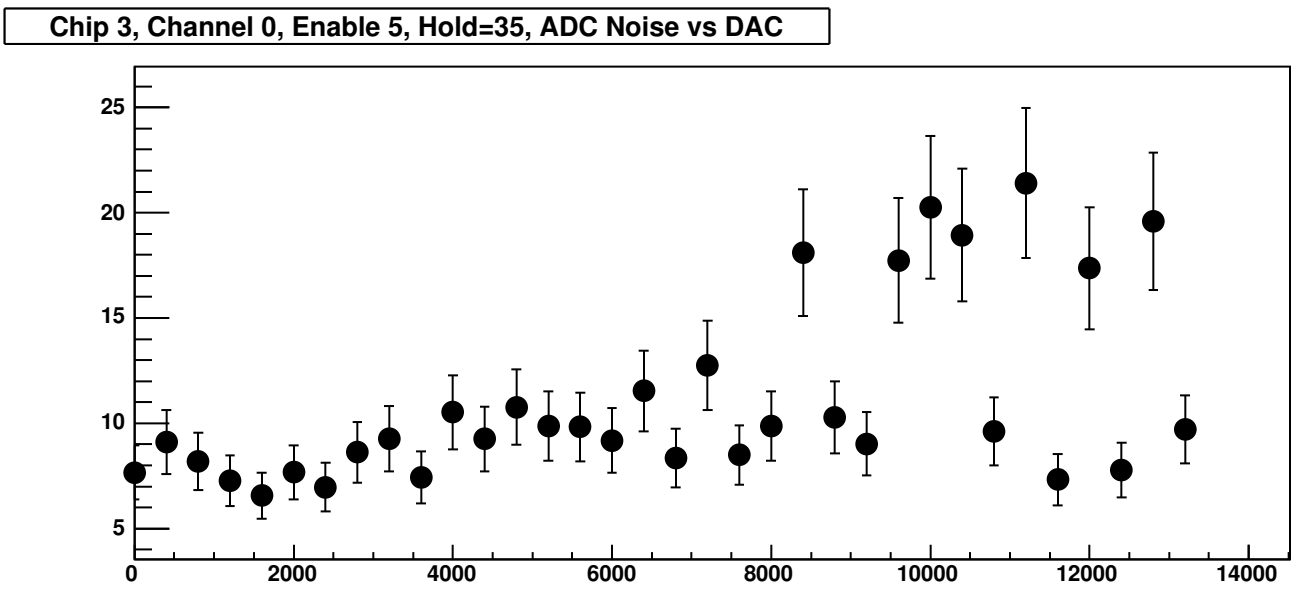

Chip 3, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

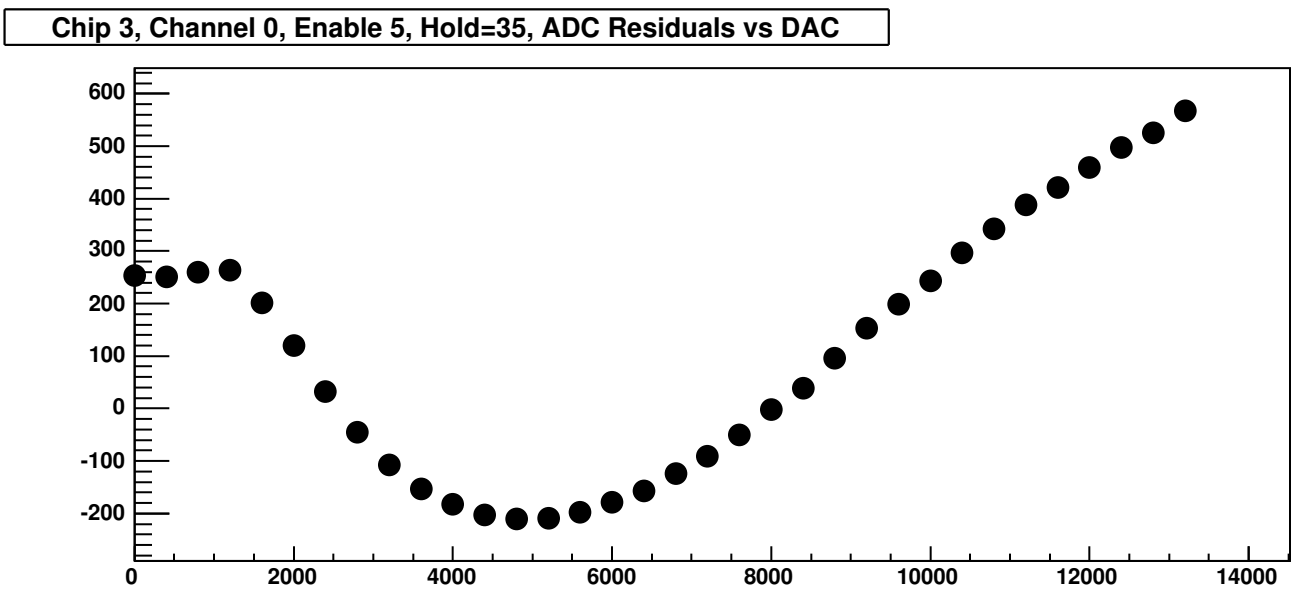

Chip 3, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 34.21 / 23
p0 -485.7 ± 0.8091
p1 0.01564 ± 0.0001179
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

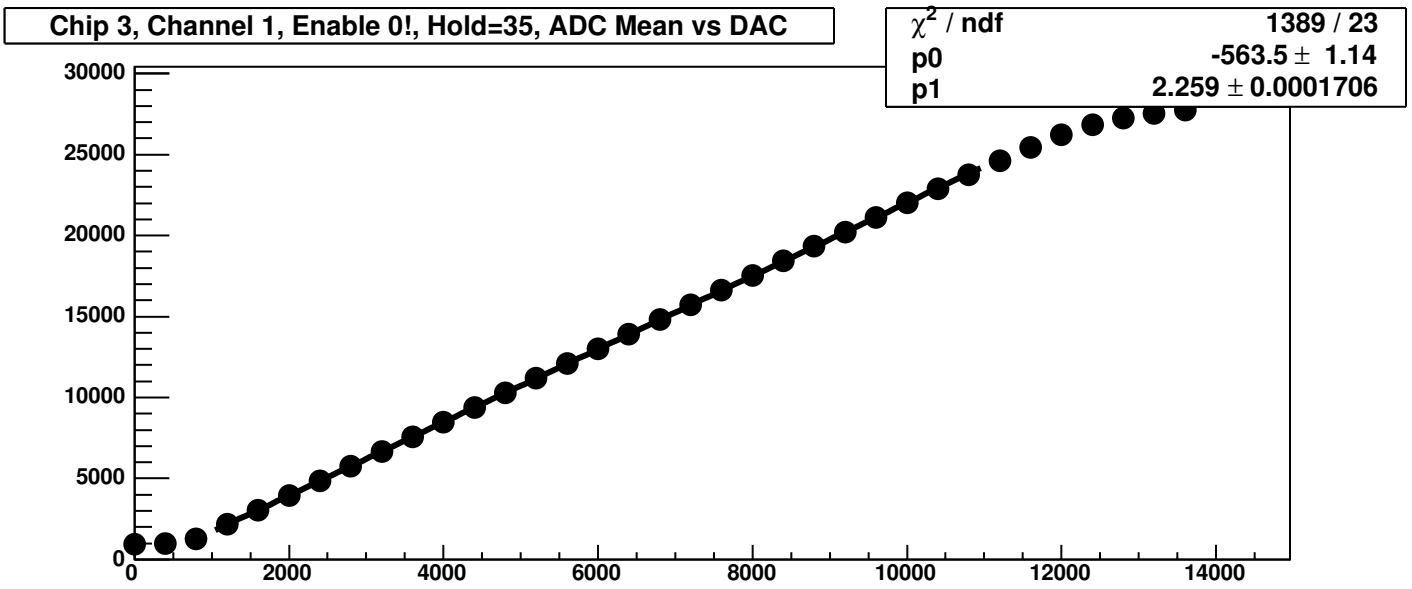

Chip 3, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

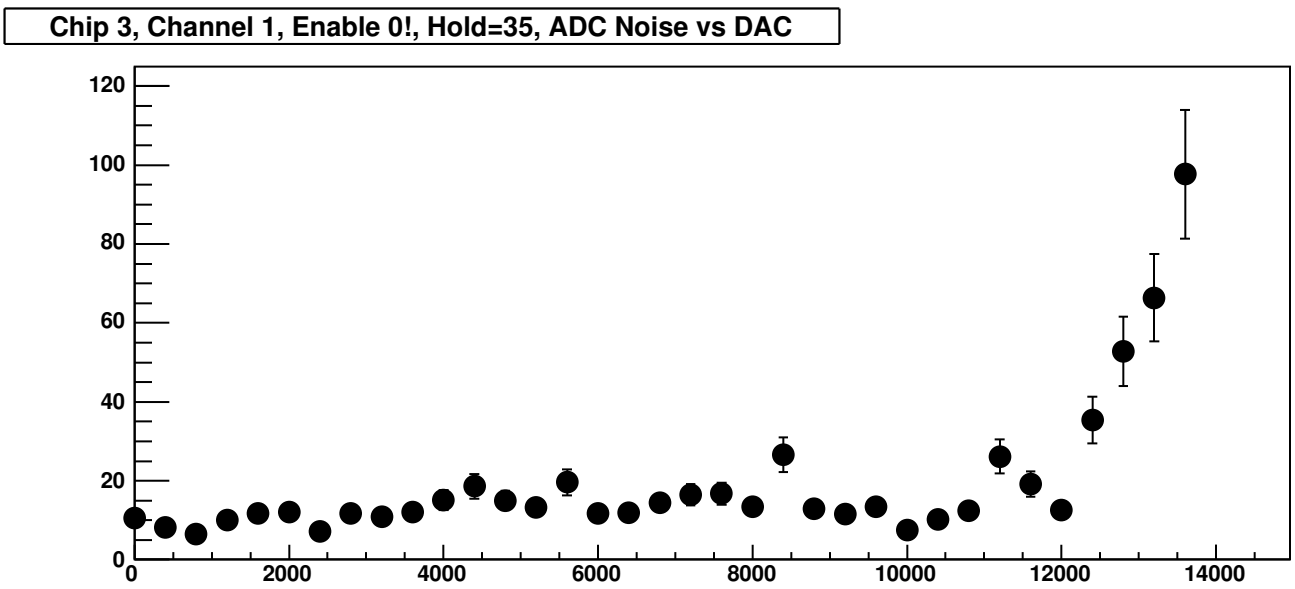

Chip 3, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

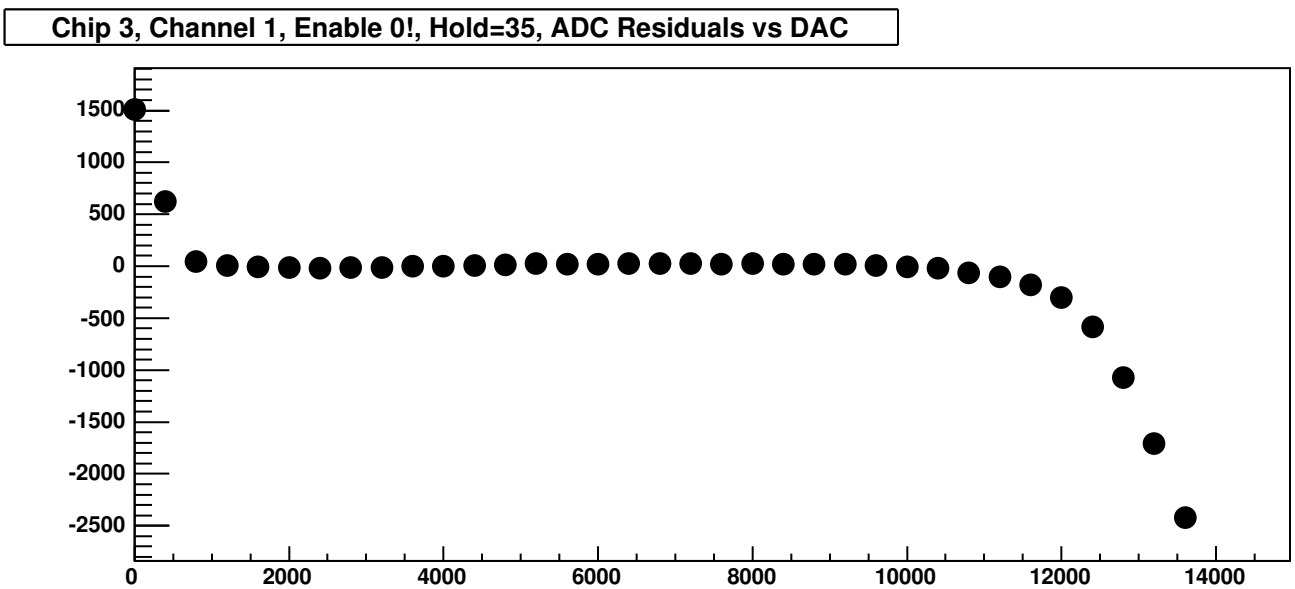

Chip 3, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.552e+05 / 23
p0 -705.8 ± 0.8757
p1 0.004274 ± 0.0001533
0
-100
-200
-300
-400
-500
-600
-700
-800
-900
0
2000
4000
6000
8000
10000
12000
14000

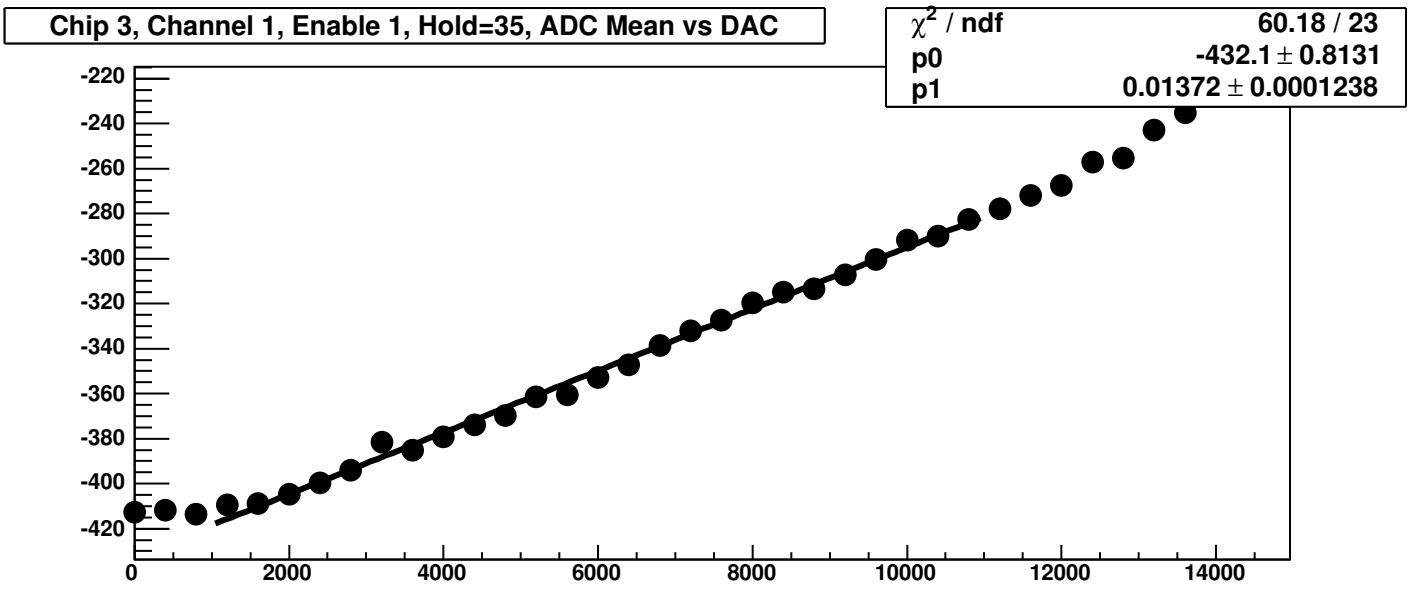

Chip 3, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

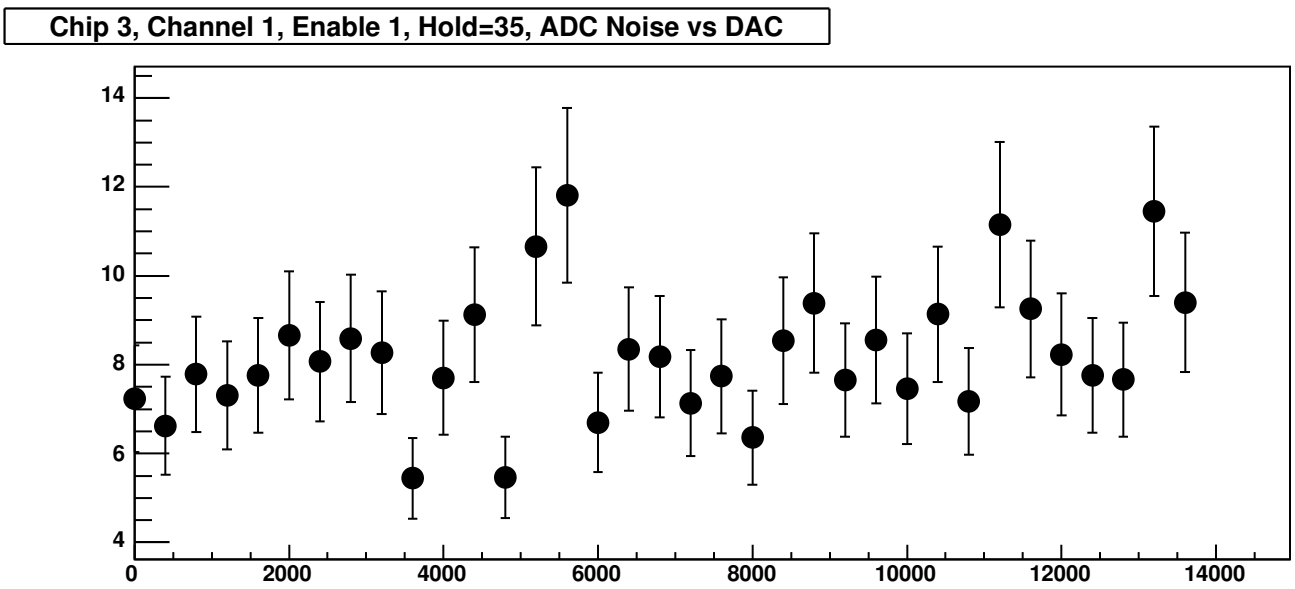

Chip 3, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

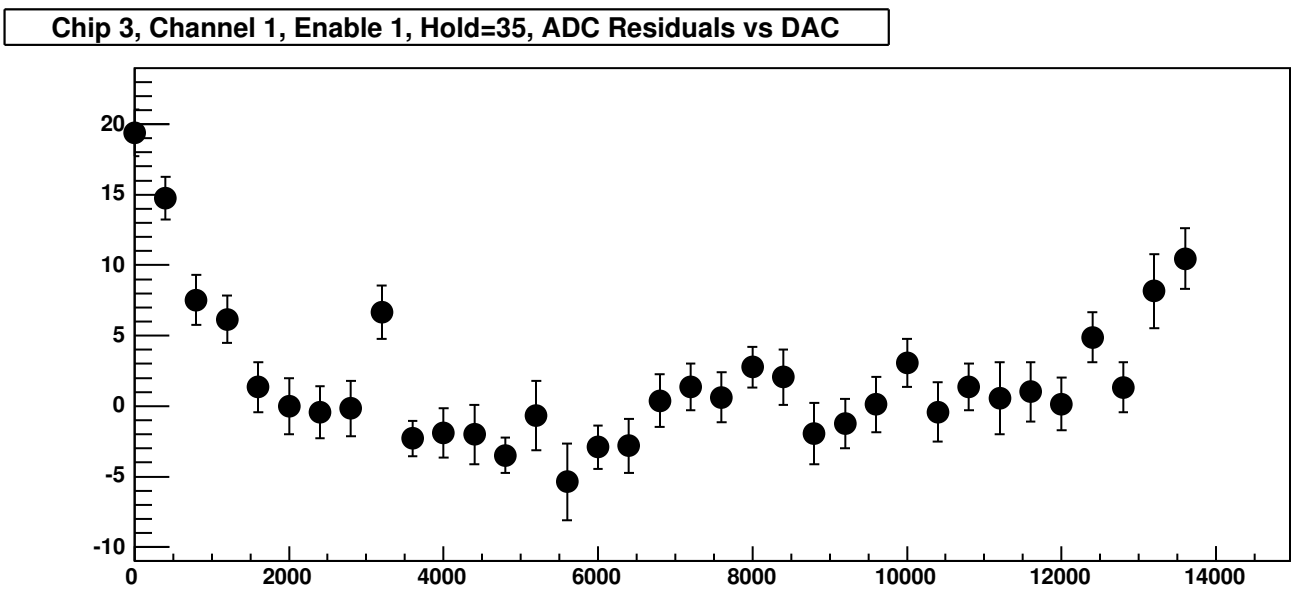

Chip 3, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1389 / 23
p0 -563.5 ± 1.14
p1 2.259 ± 0.0001706
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

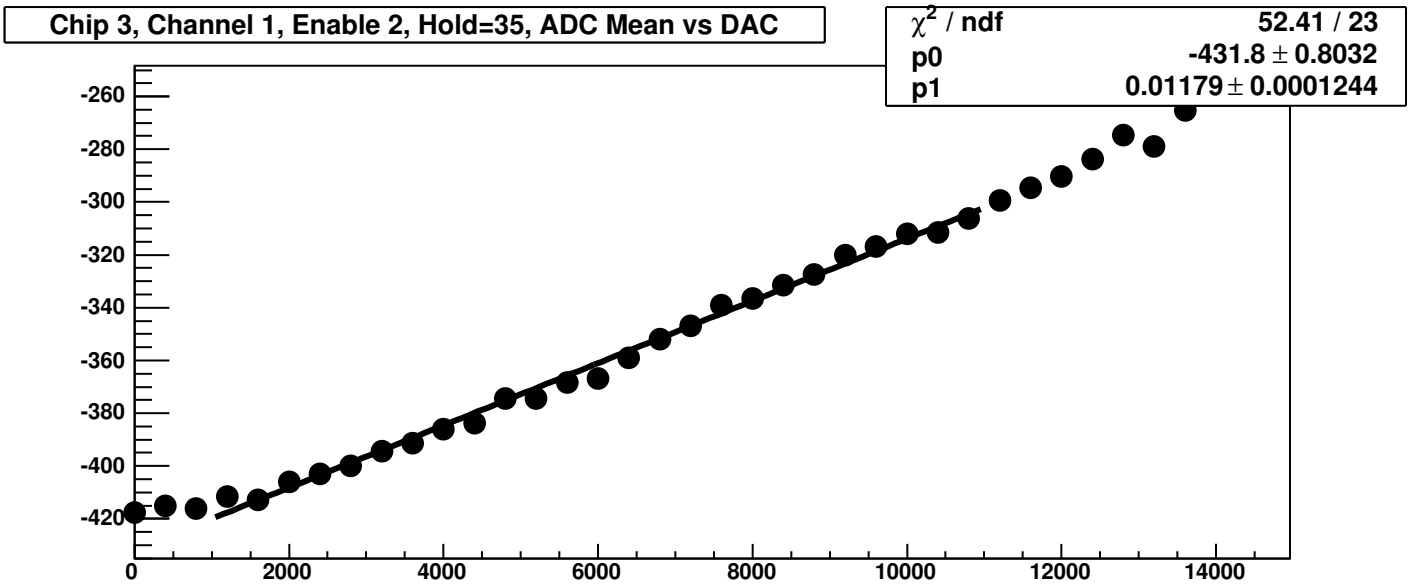

Chip 3, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

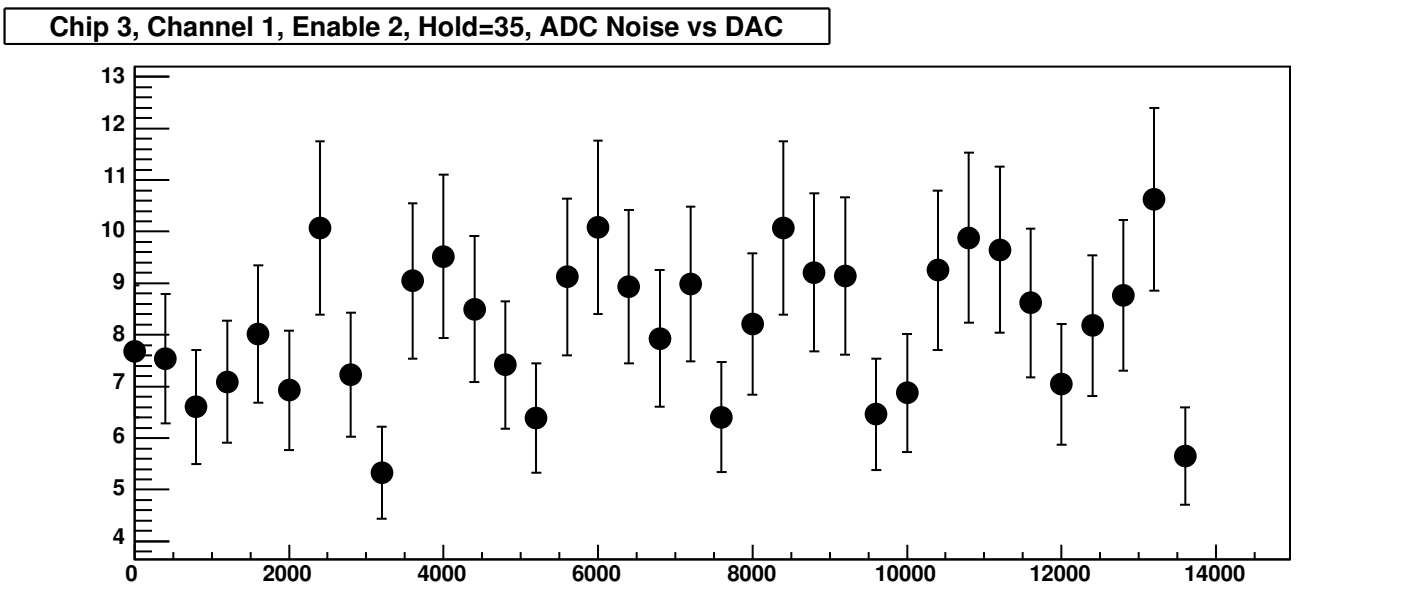

Chip 3, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

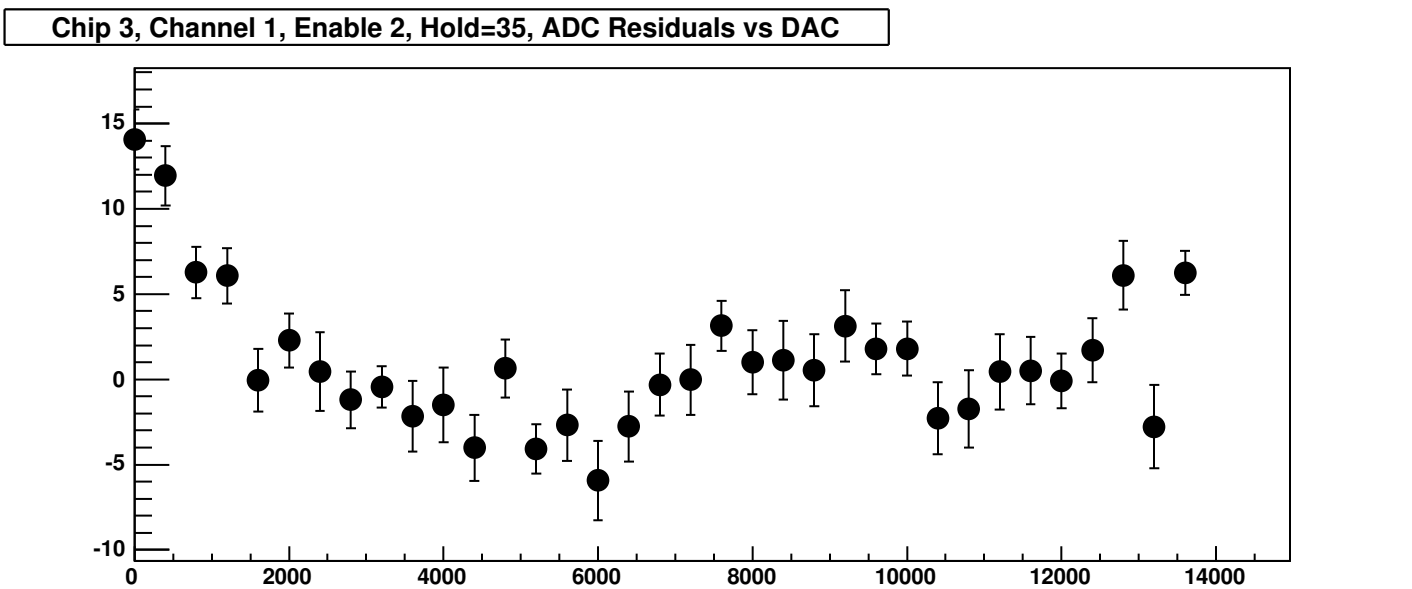

Chip 3, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 60.18 / 23
p0 -432.1 ± 0.8131
p1 0.01372 ± 0.0001238
-220
-240
-260
-280
-300
-320
-340
-360
-380
-400
-420
0
2000
4000
6000
8000
10000
12000
14000

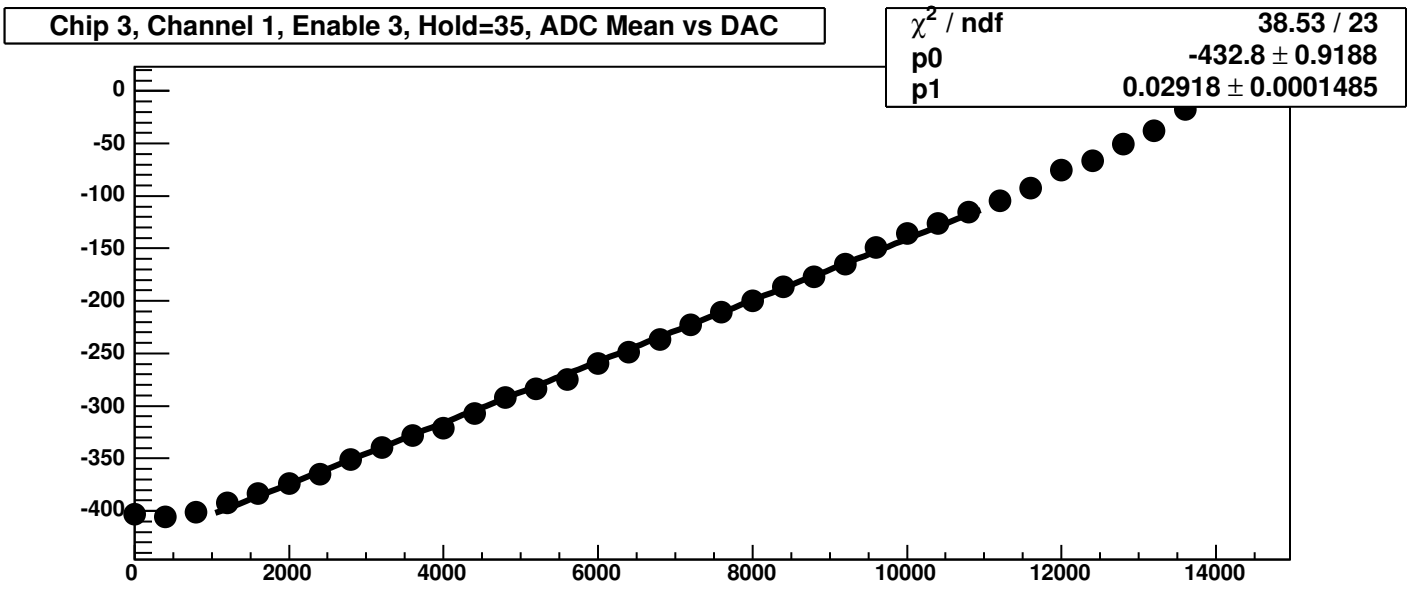

Chip 3, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

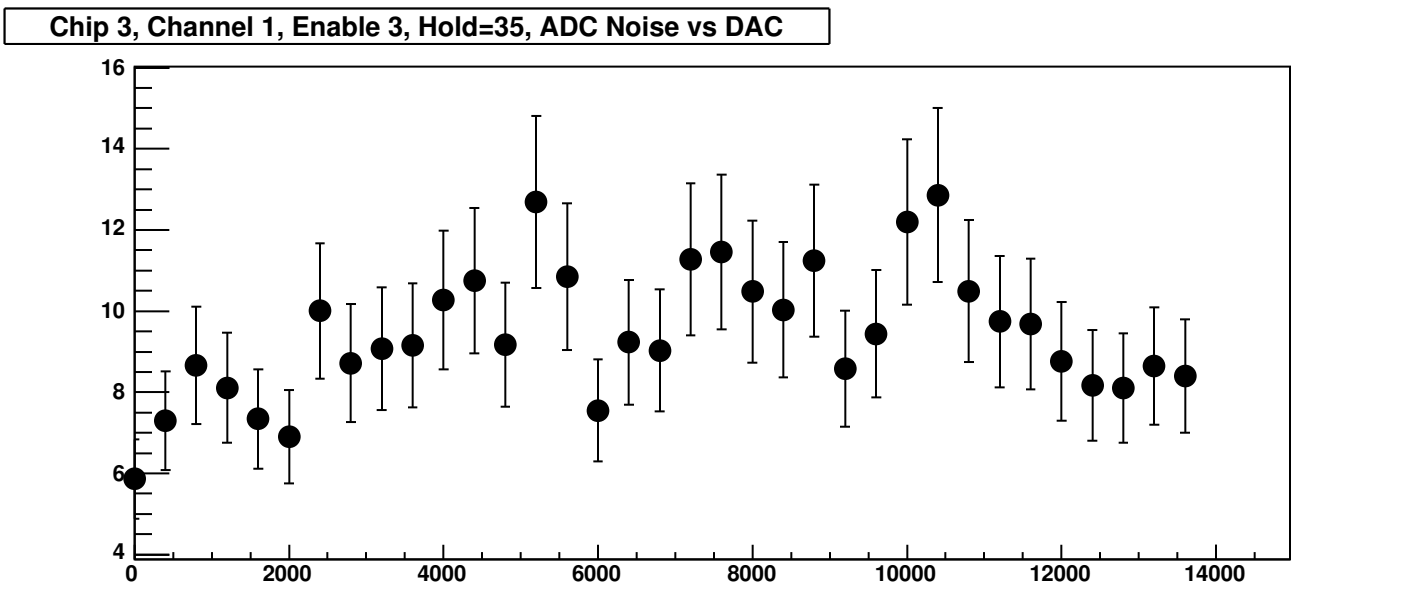

Chip 3, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

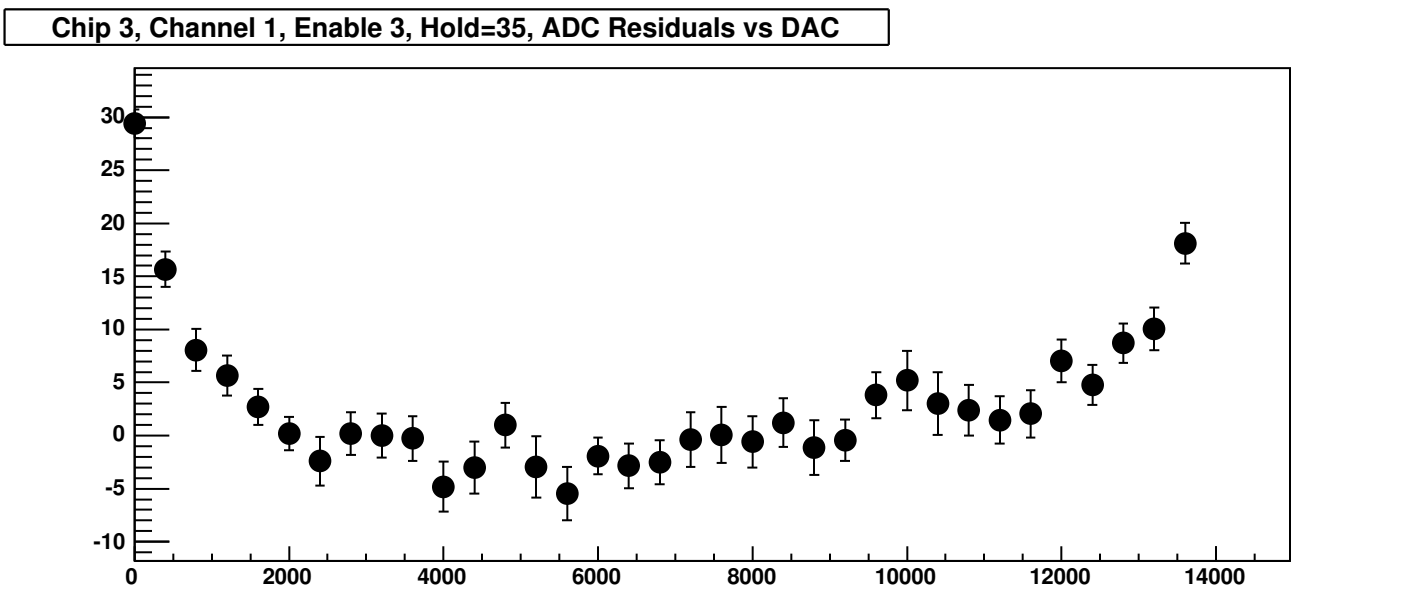

Chip 3, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 52.41 / 23
p0 -431.8 ± 0.8032
p1 0.01179 ± 0.0001244
-260
-280
-300
-320
-340
-360
-380
-400
-420
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

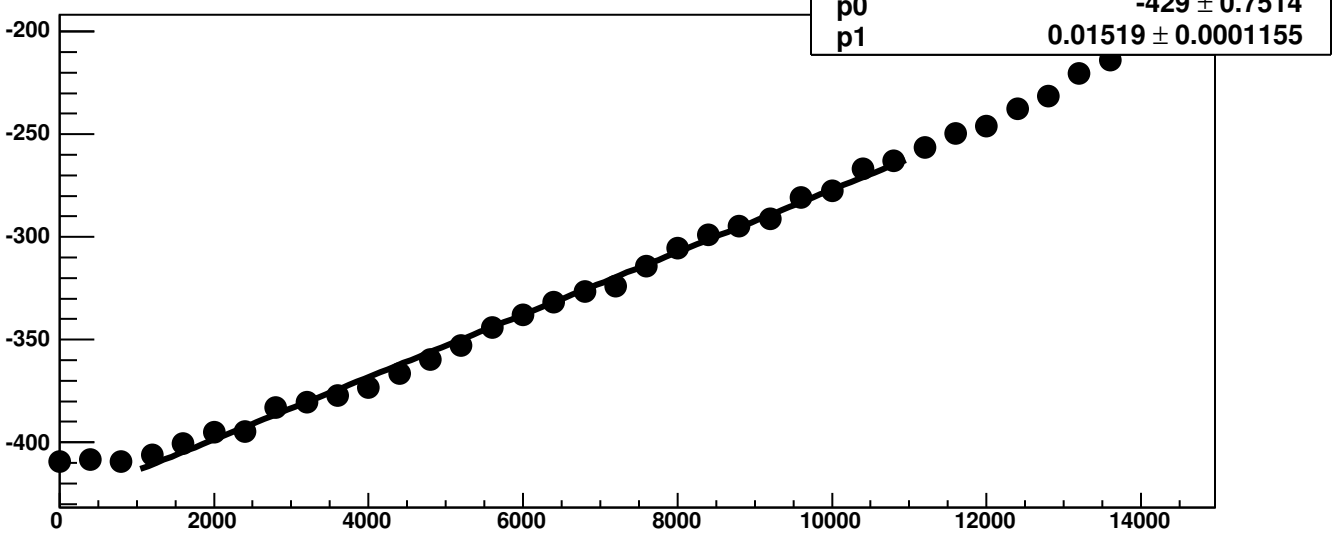

Chip 3, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.53 / 23
p0 -432.8 ± 0.9188
p1 0.02918 ± 0.0001485
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

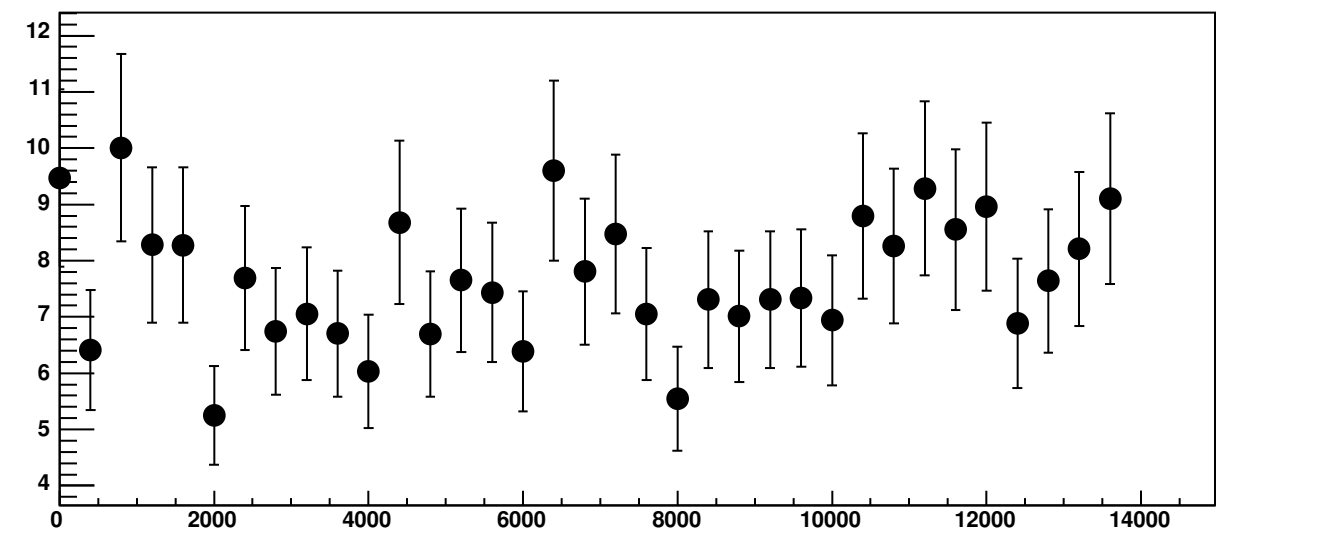

Chip 3, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

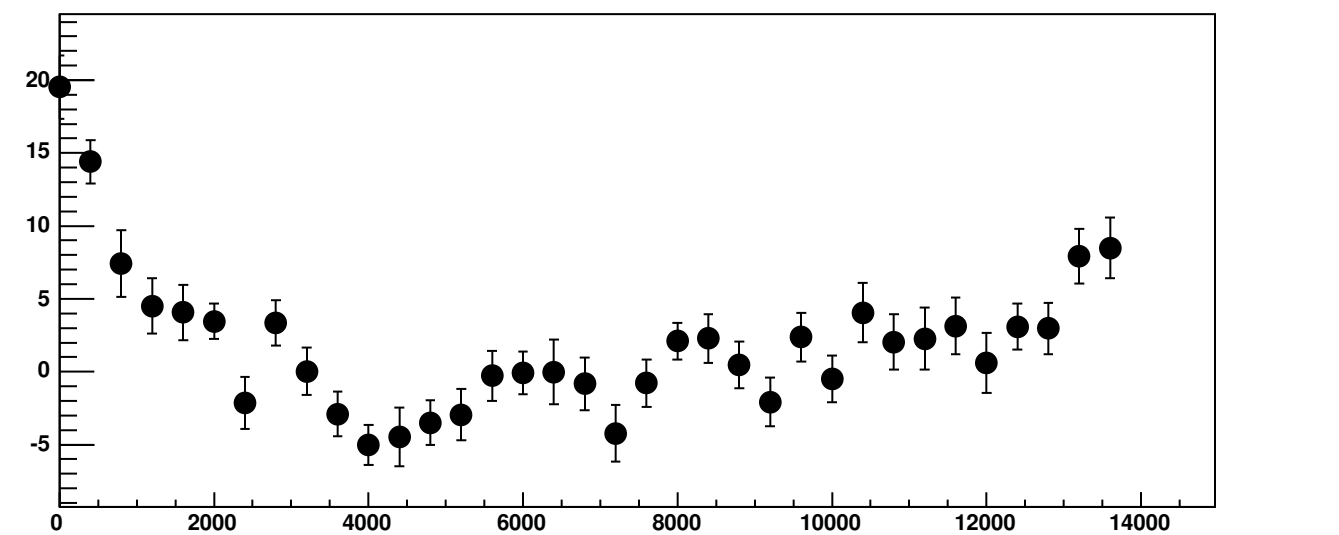
Chip 3, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



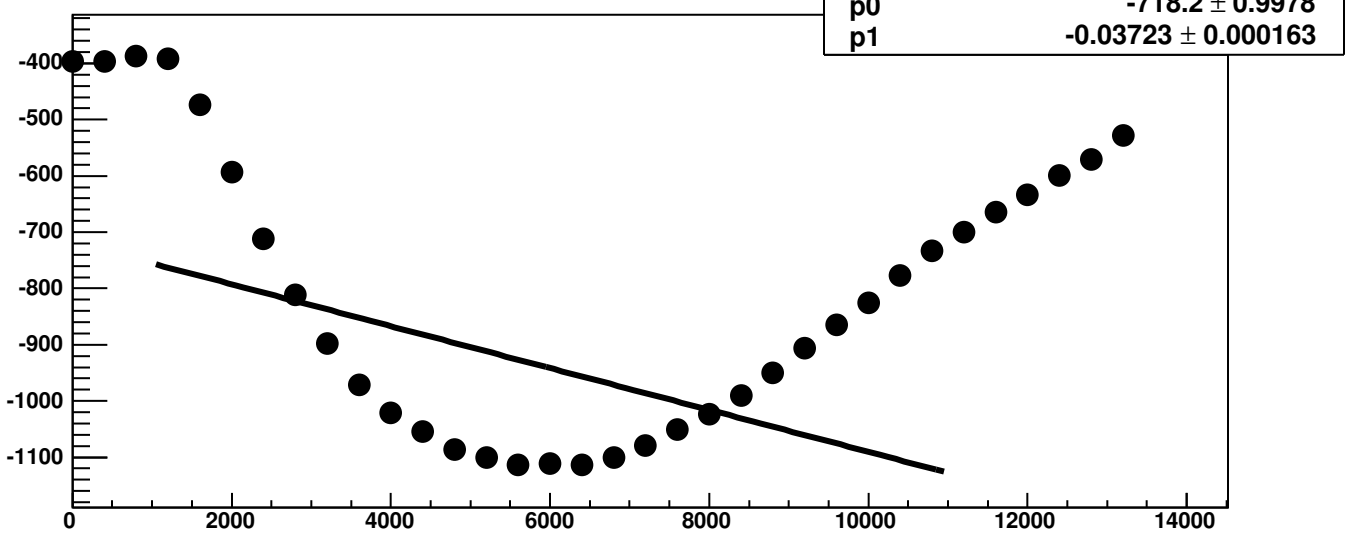
Chip 3, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



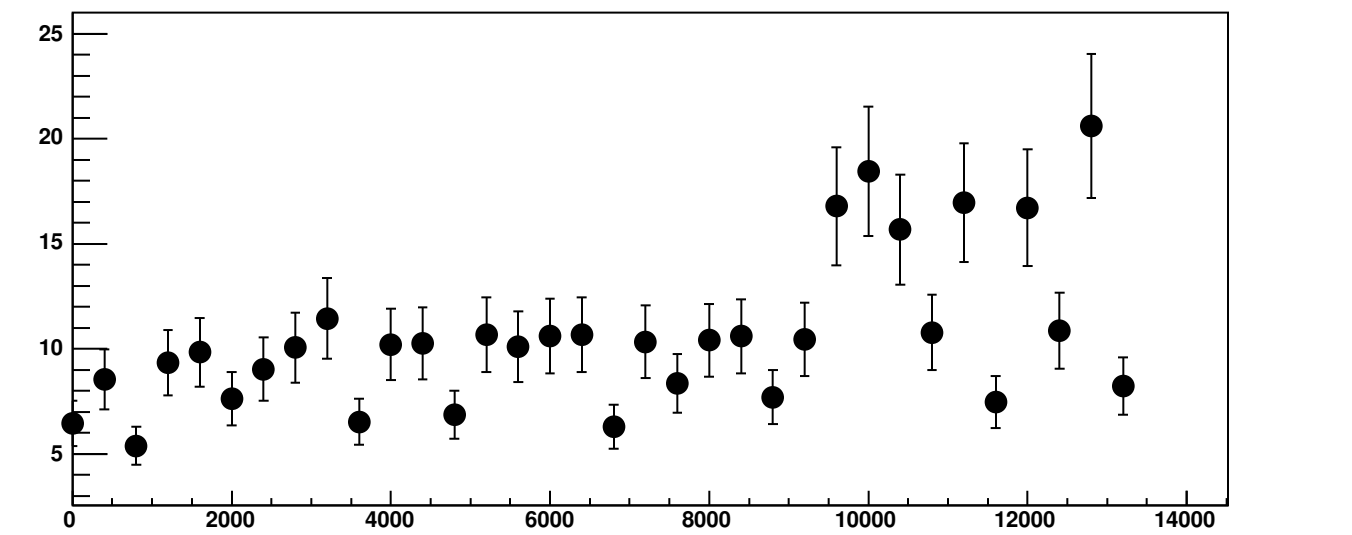
Chip 3, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



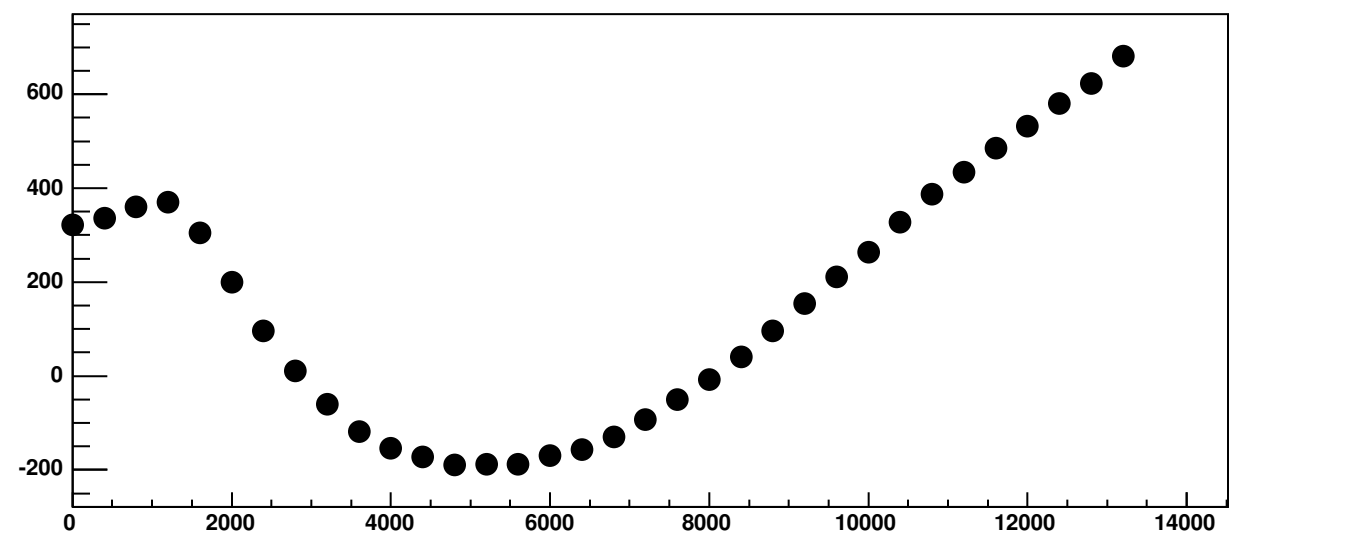
Chip 3, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

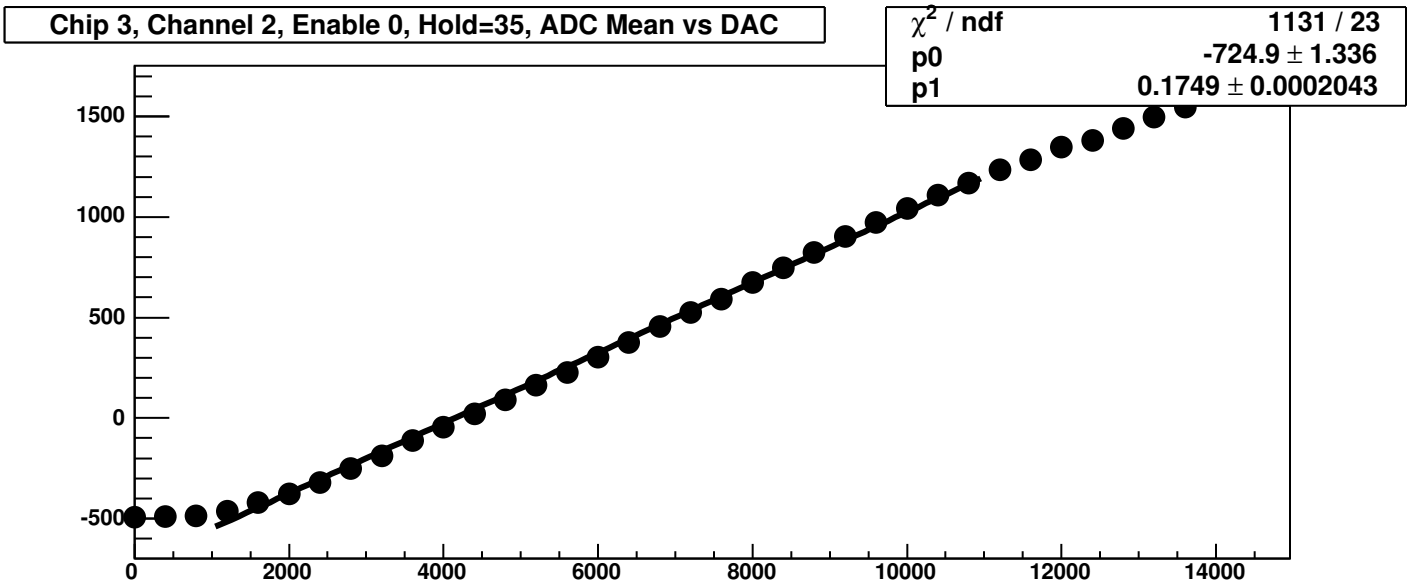


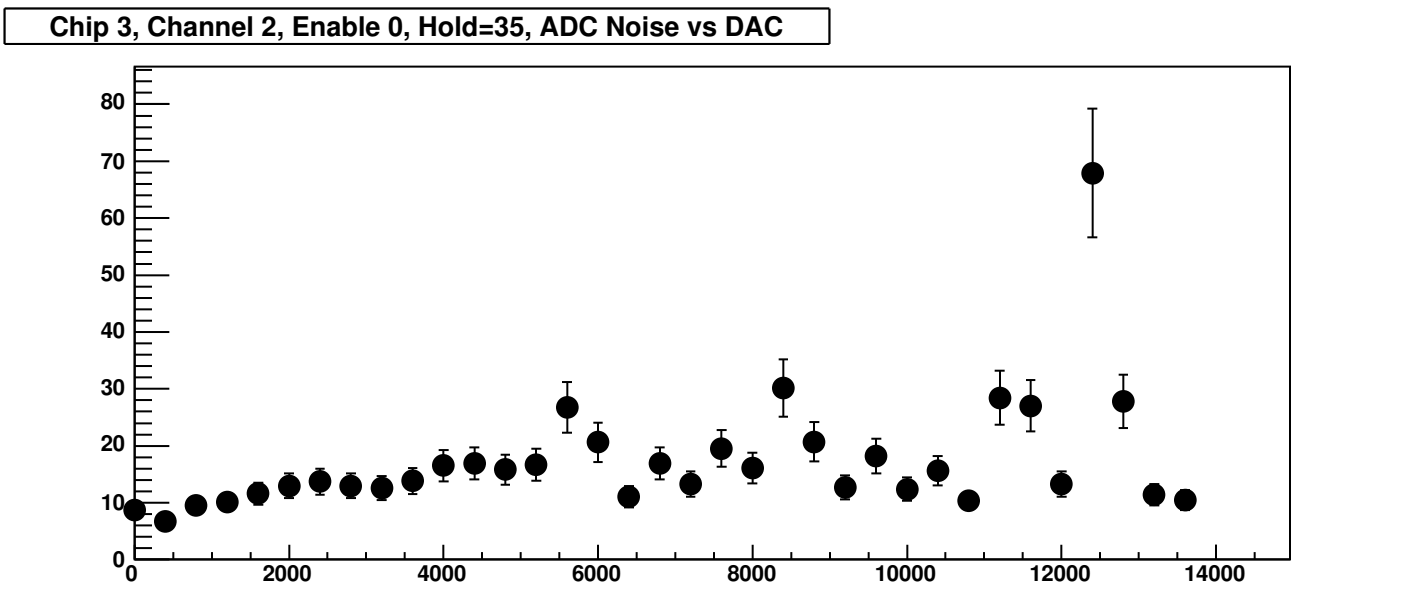
Chip 3, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC

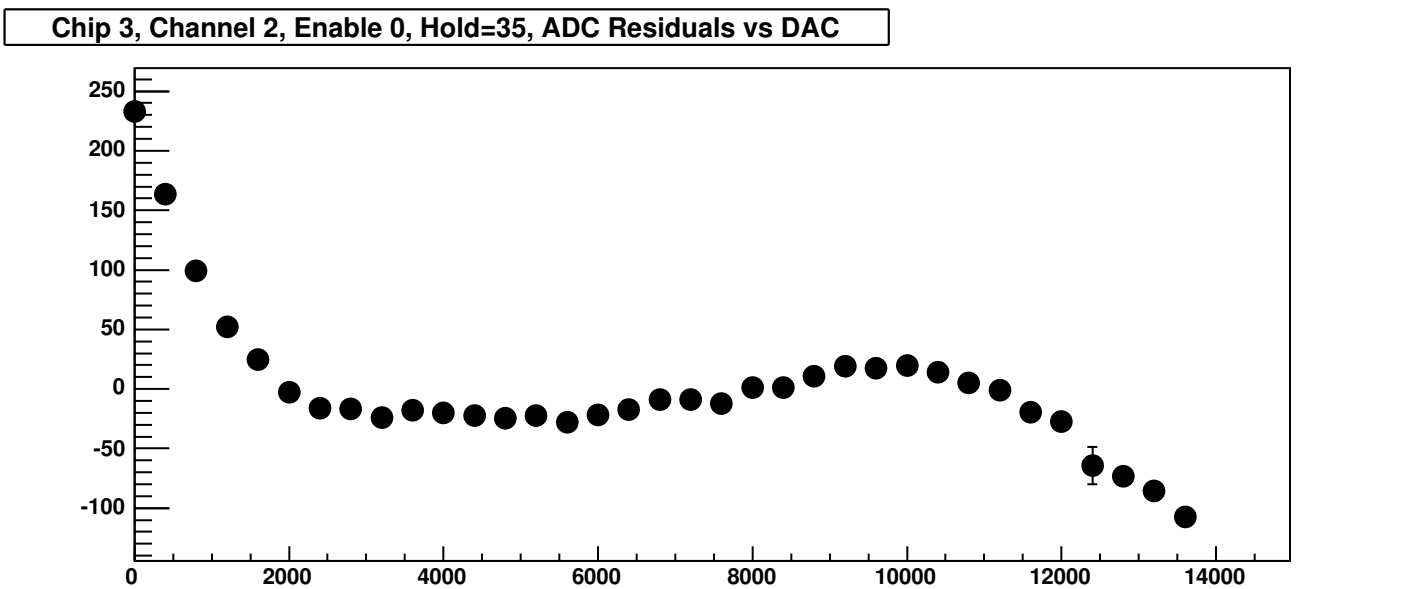


Chip 3, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

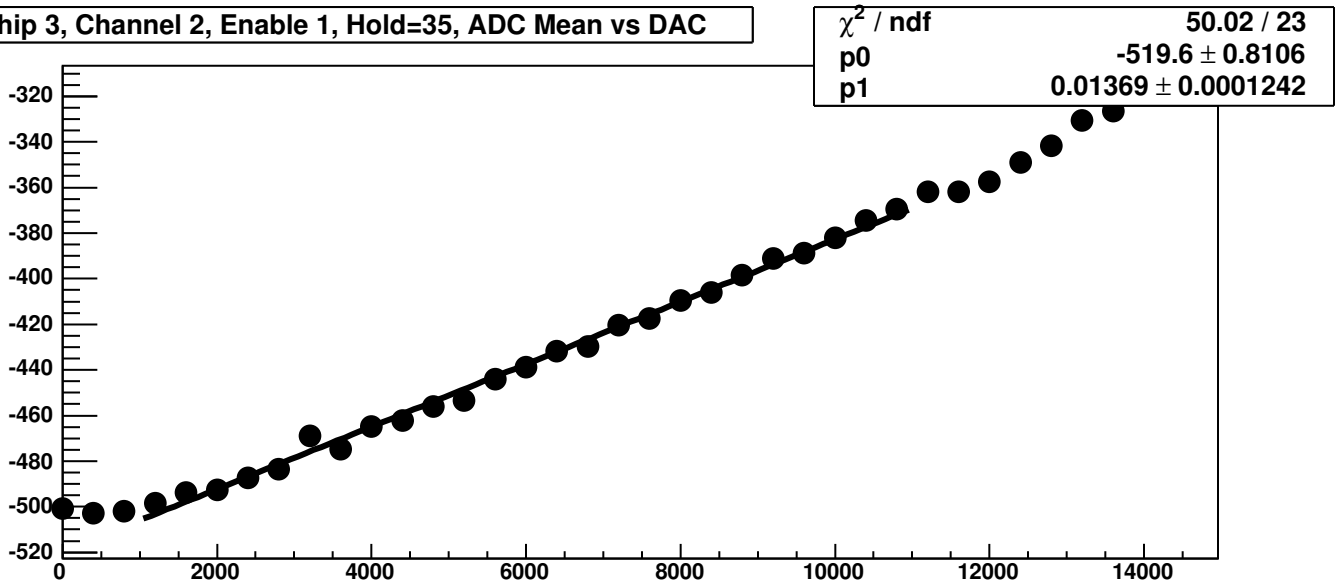



Chip 3, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1131 / 23
p0 -724.9 ± 1.336
p1 0.1749 ± 0.0002043
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

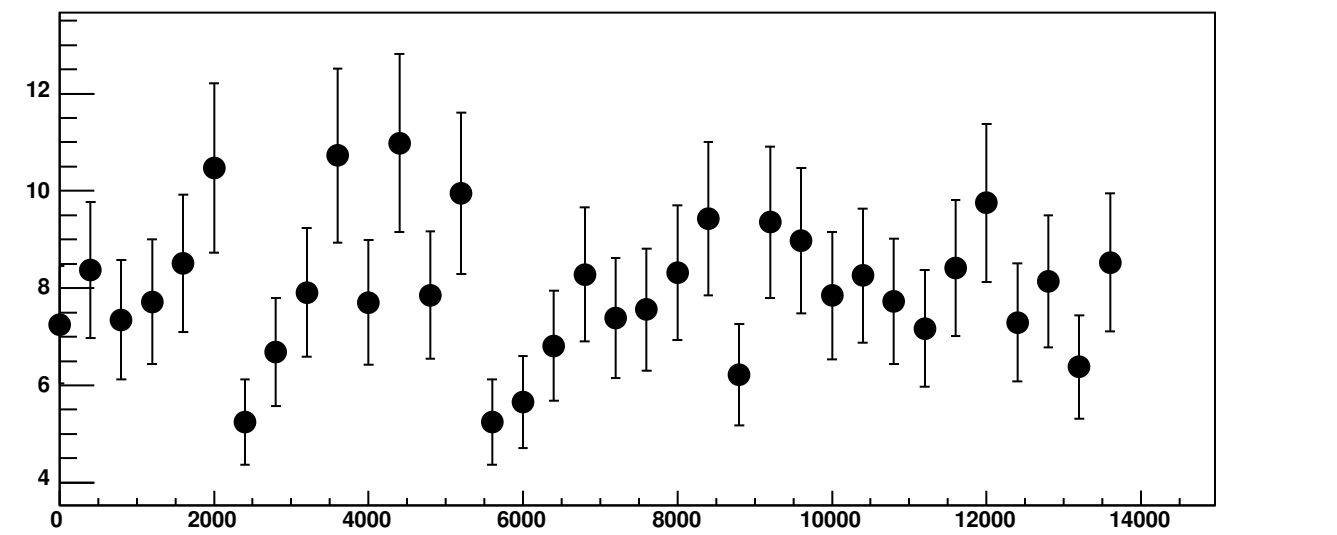

Chip 3, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

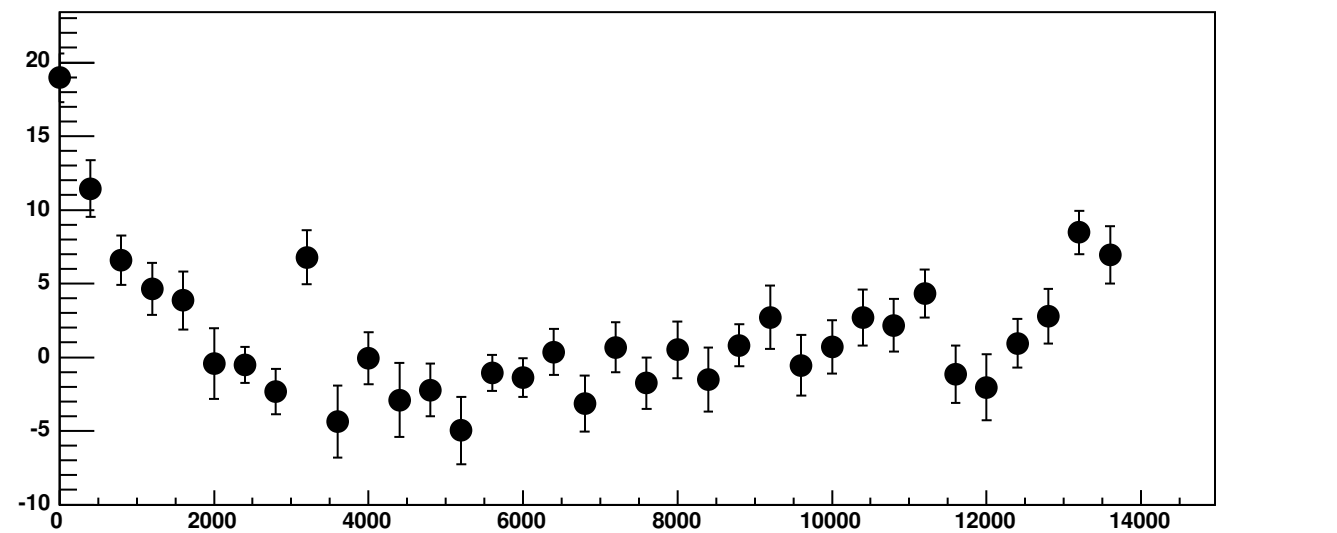
Chip 3, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

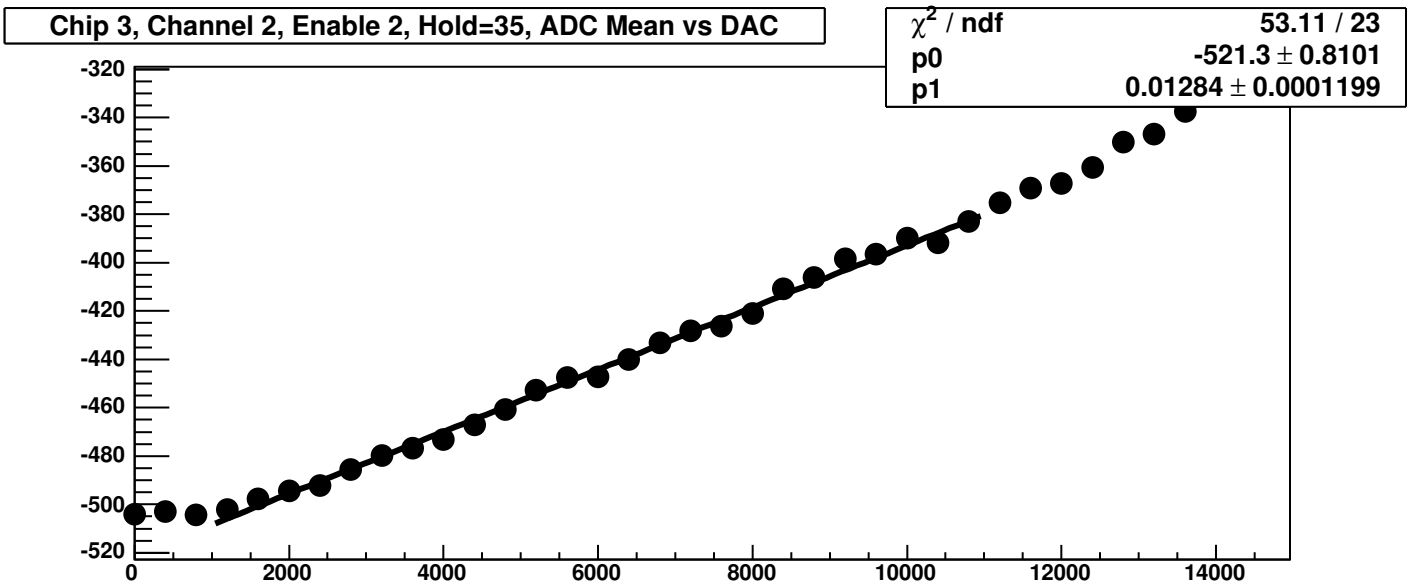


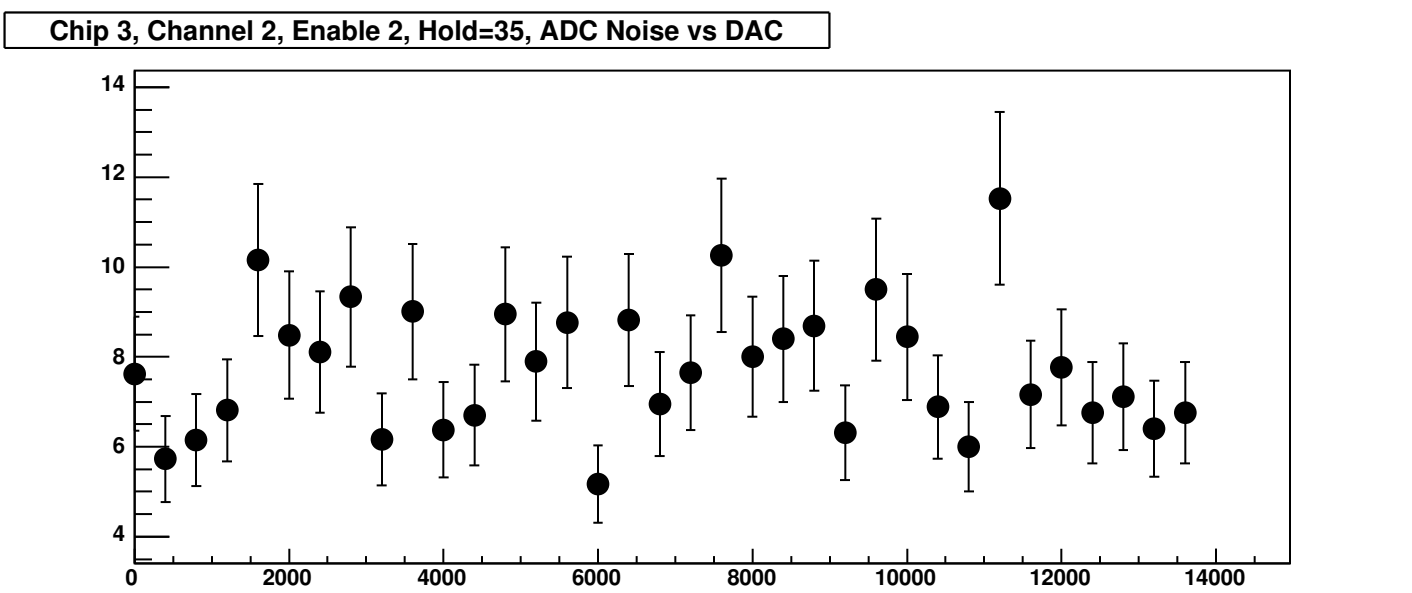
Chip 3, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC

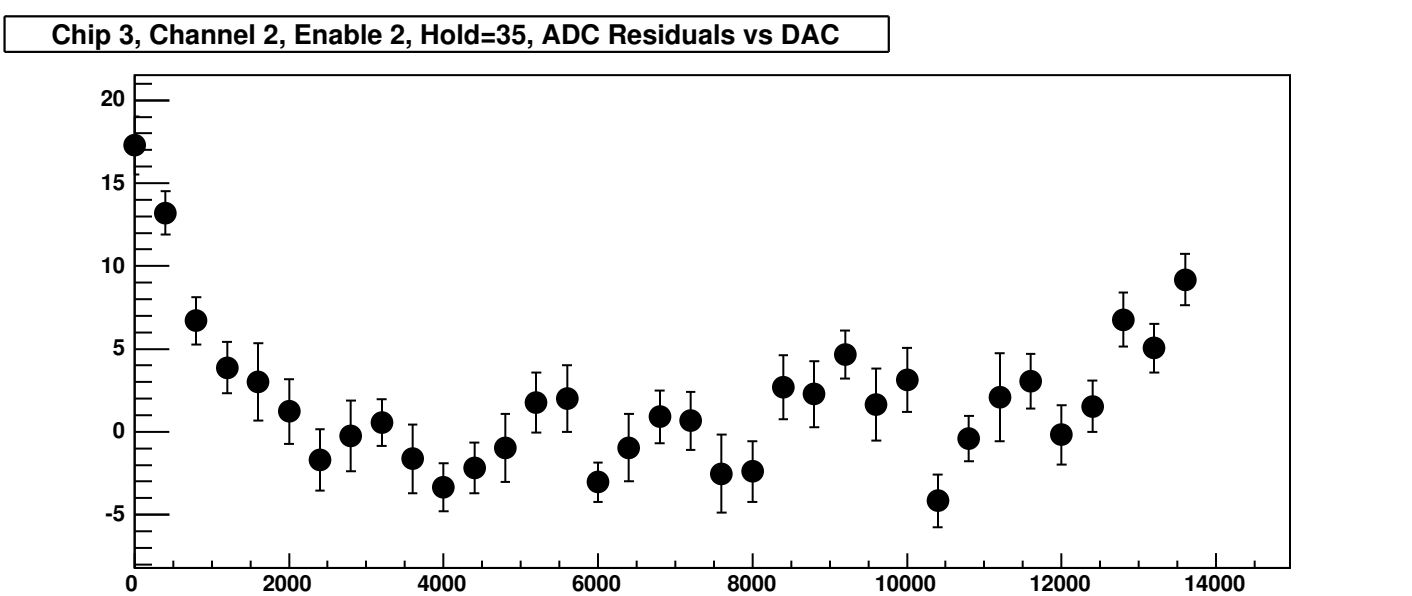


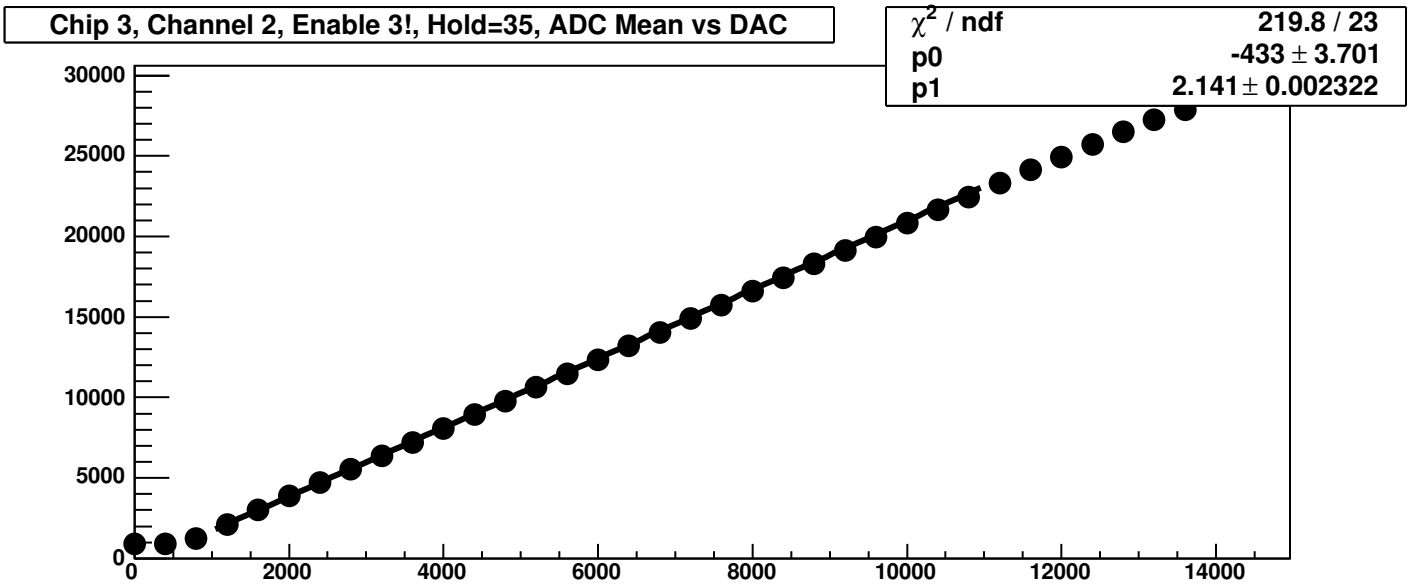
Chip 3, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

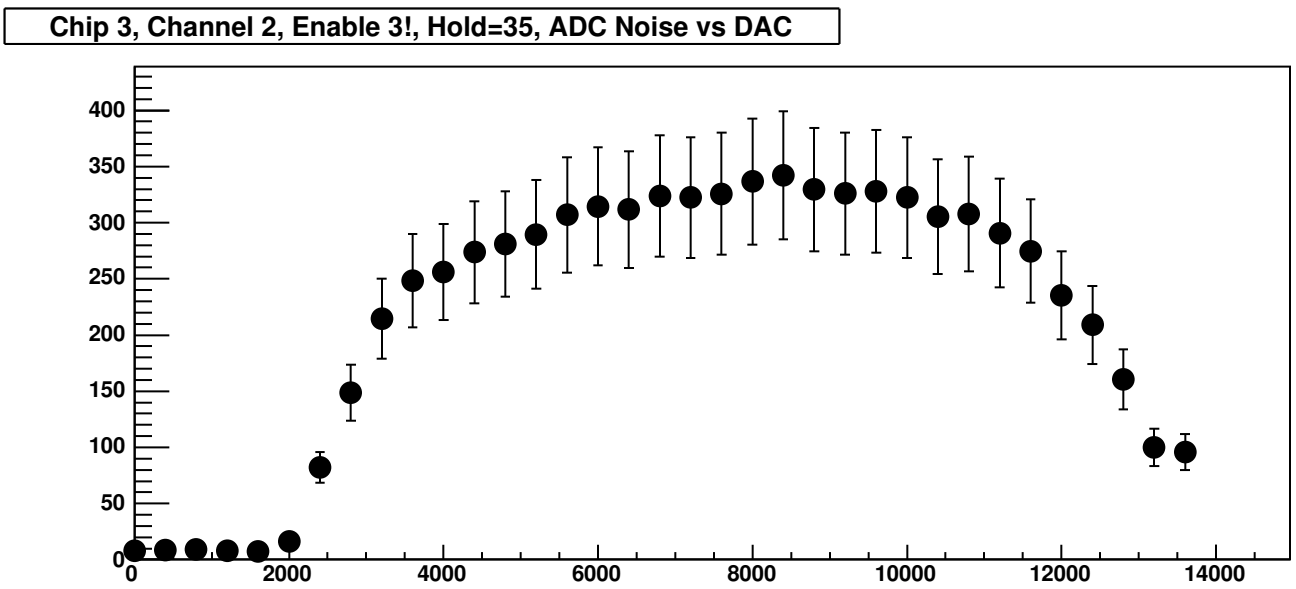


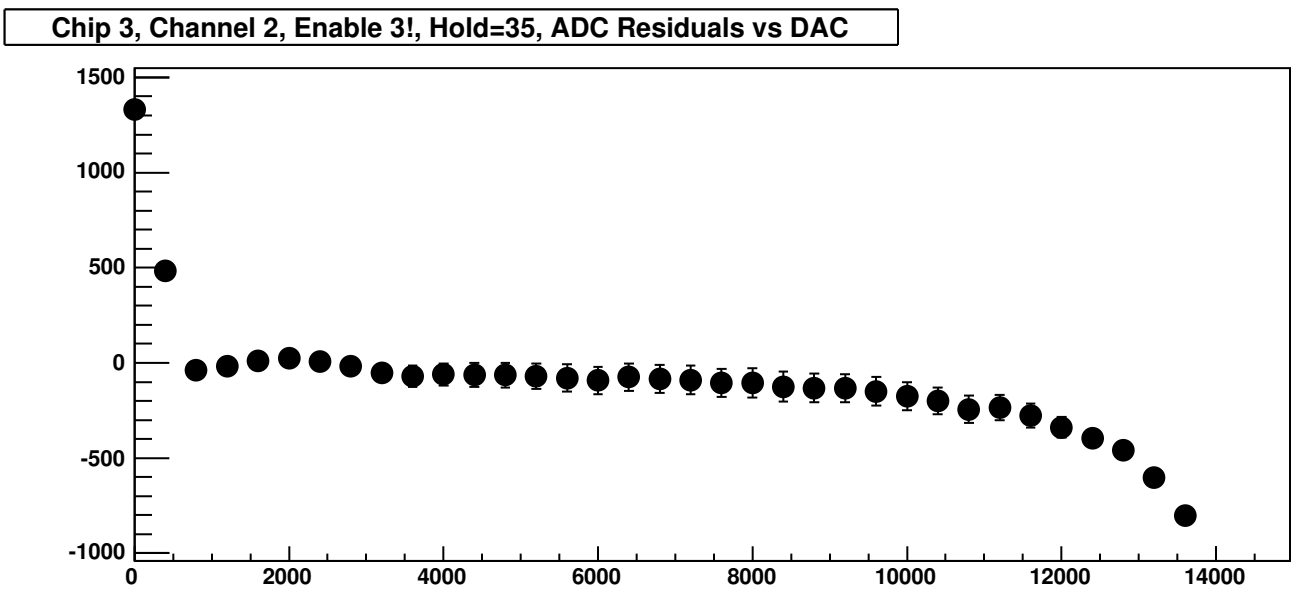

Chip 3, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 53.11 / 23
p0 -521.3 ± 0.8101
p1 0.01284 ± 0.0001199
-320
-340
-360
-380
-400
-420
-440
-460
-480
-500
-520
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

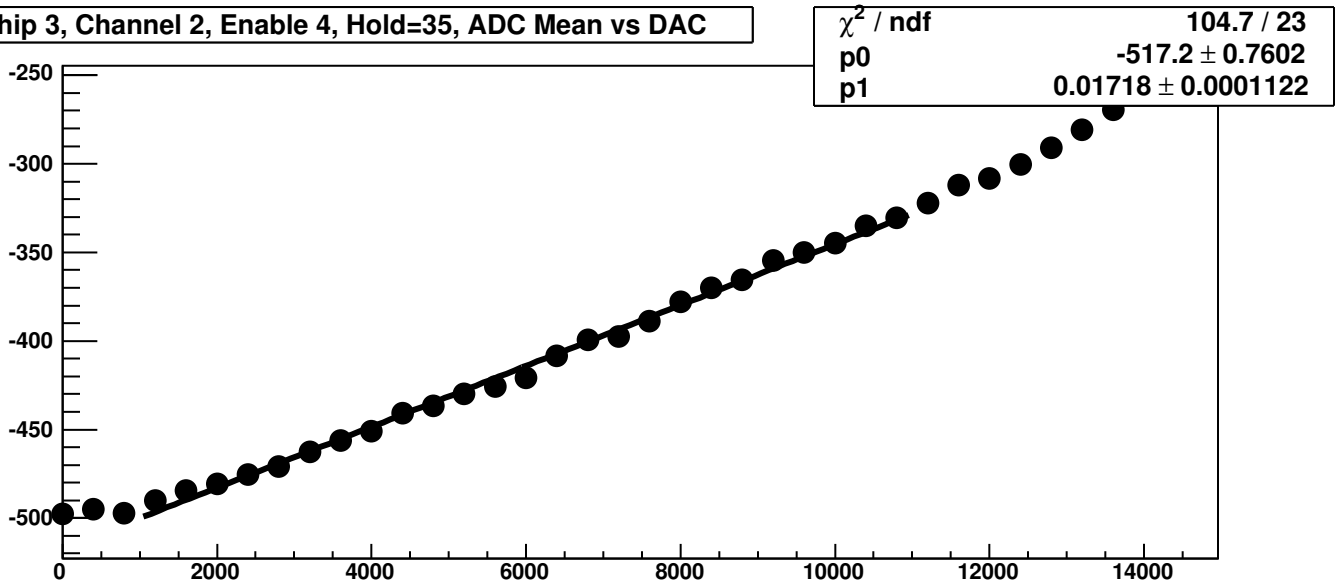

Chip 3, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 219.8 / 23
p0 -433 ± 3.701
p1 2.141 ± 0.002322
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

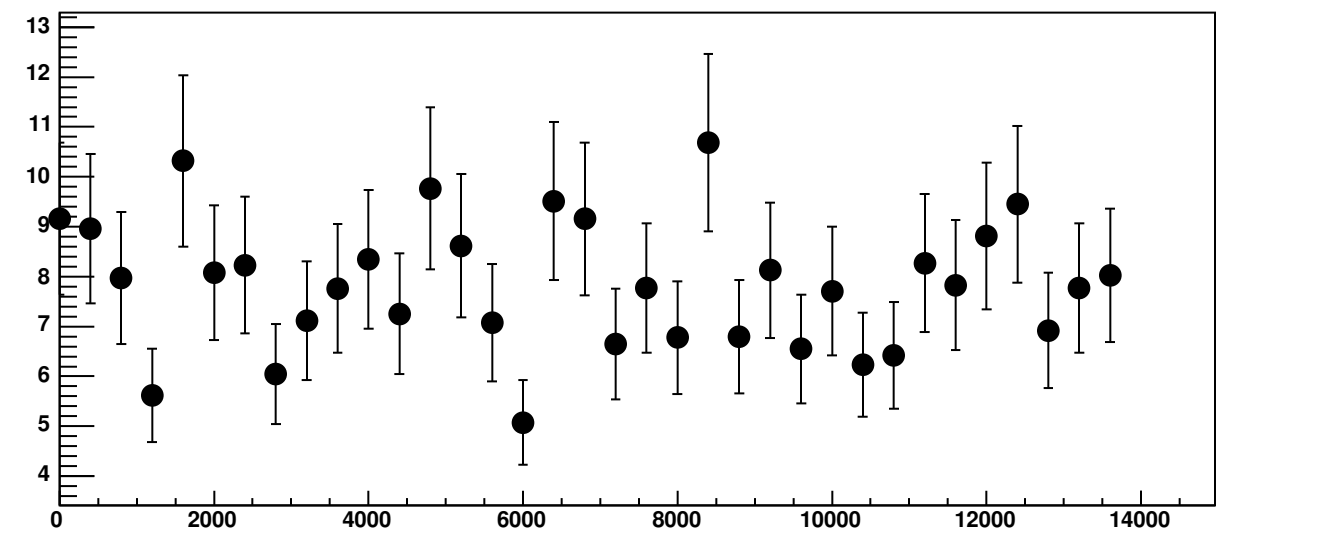

Chip 3, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

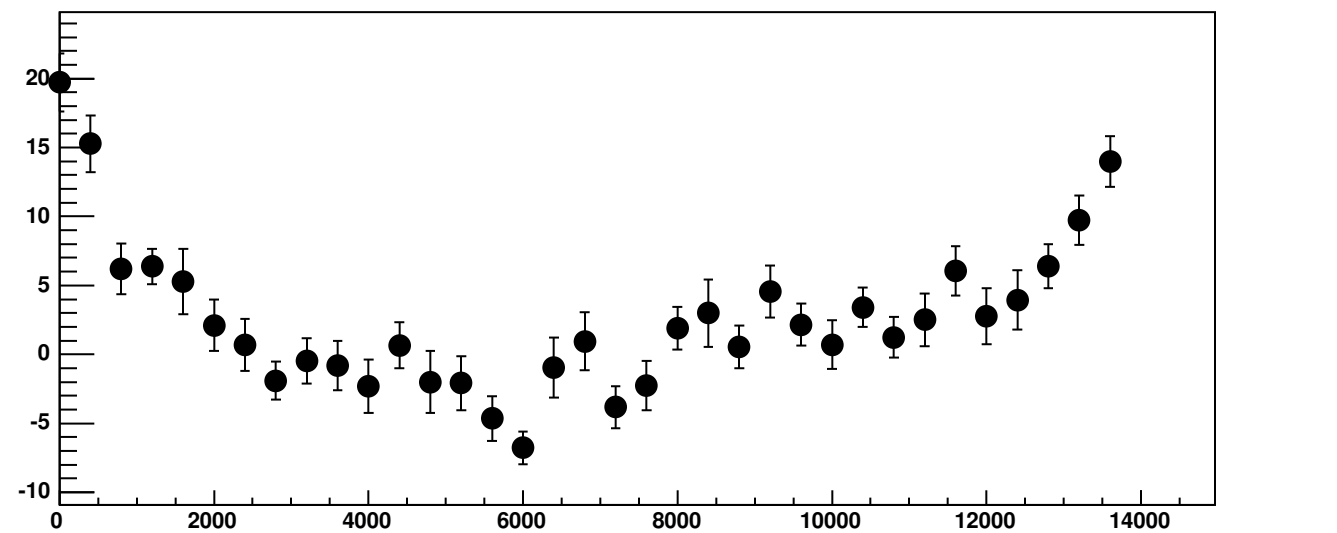
Chip 3, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

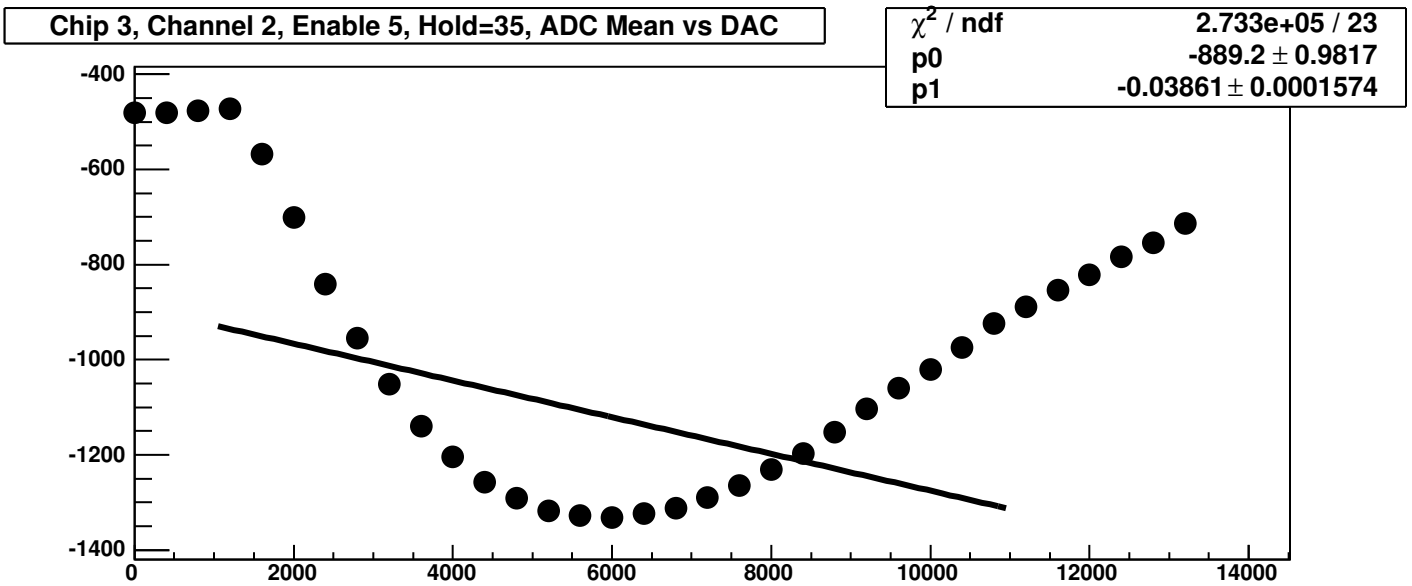


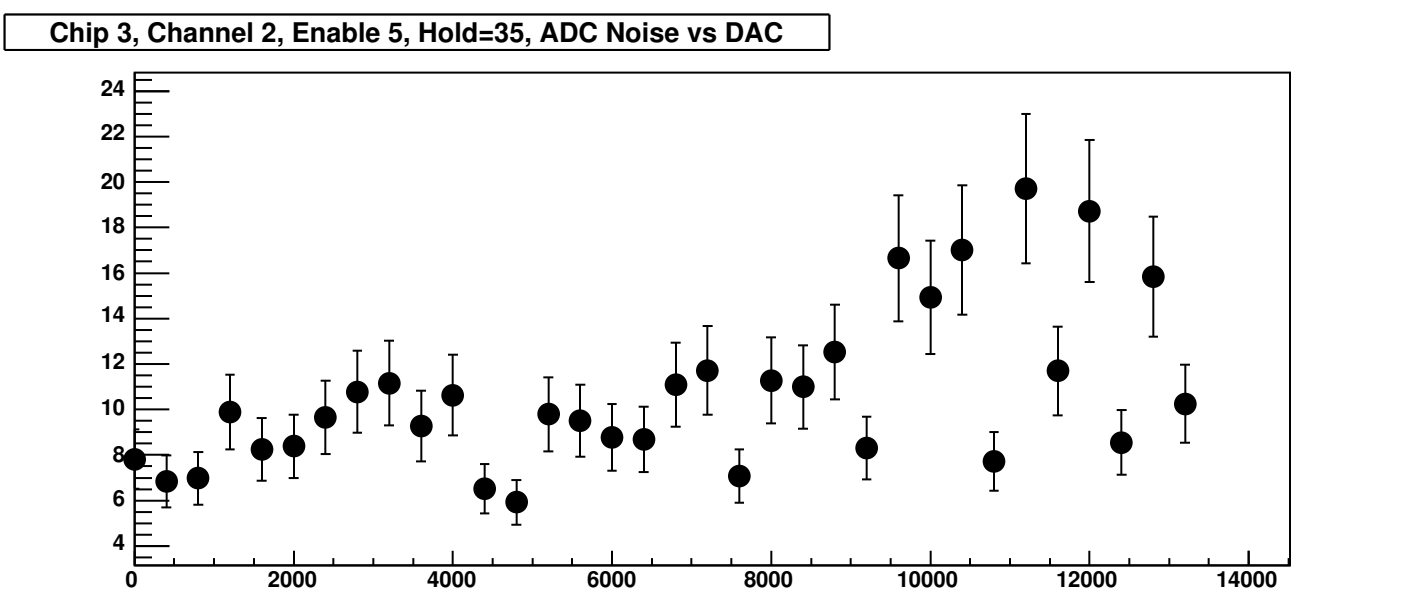
Chip 3, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

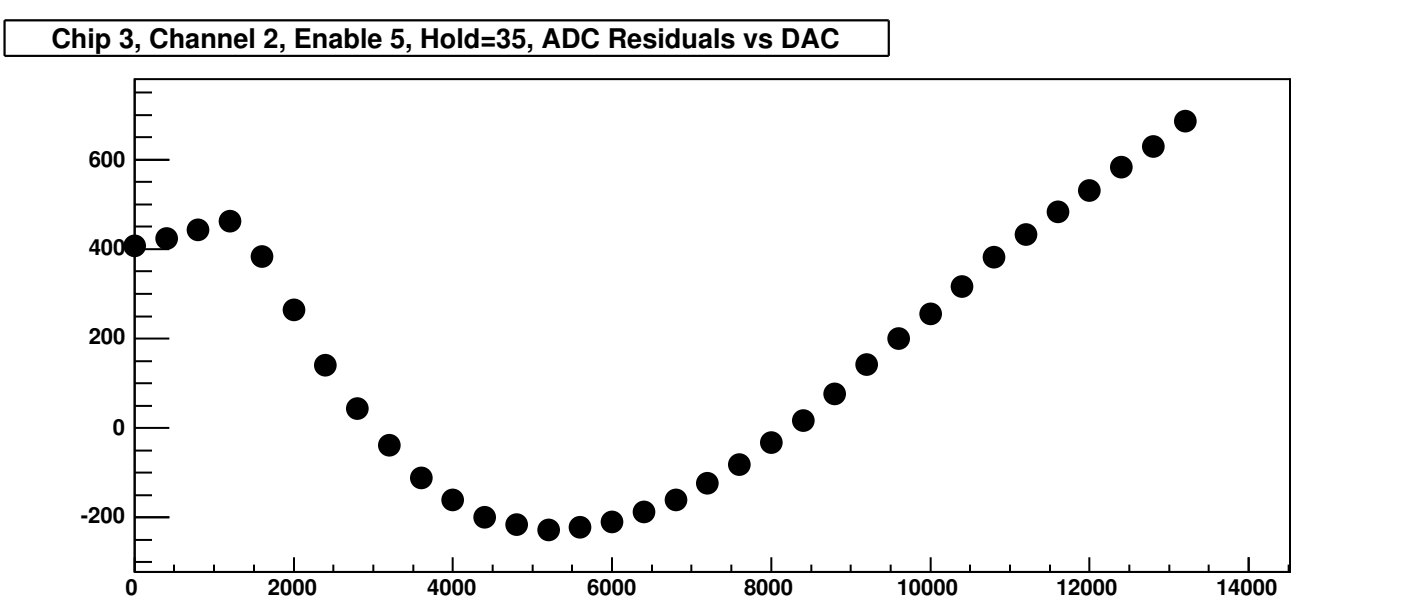


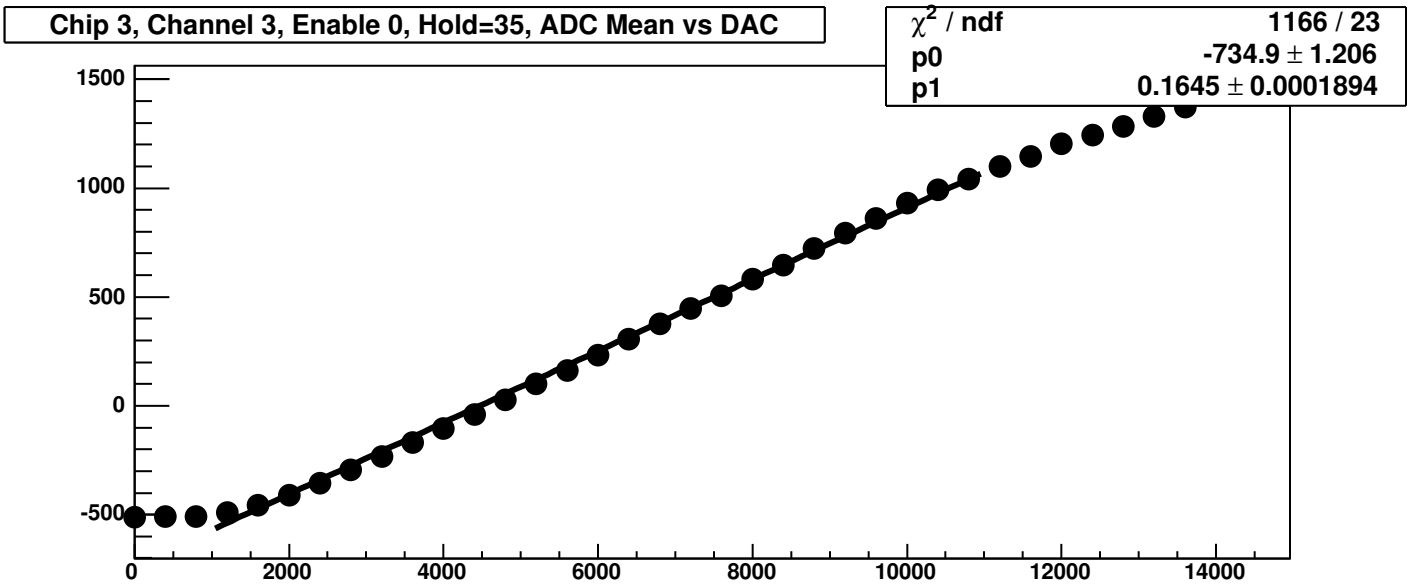
Chip 3, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

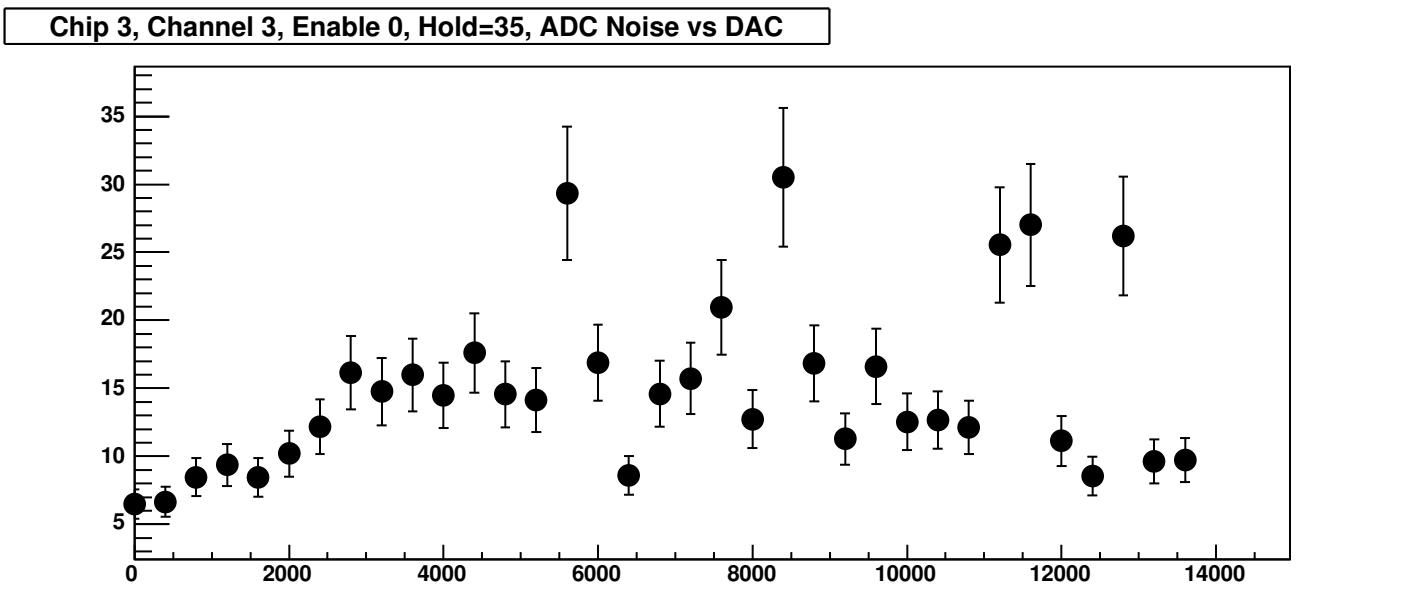


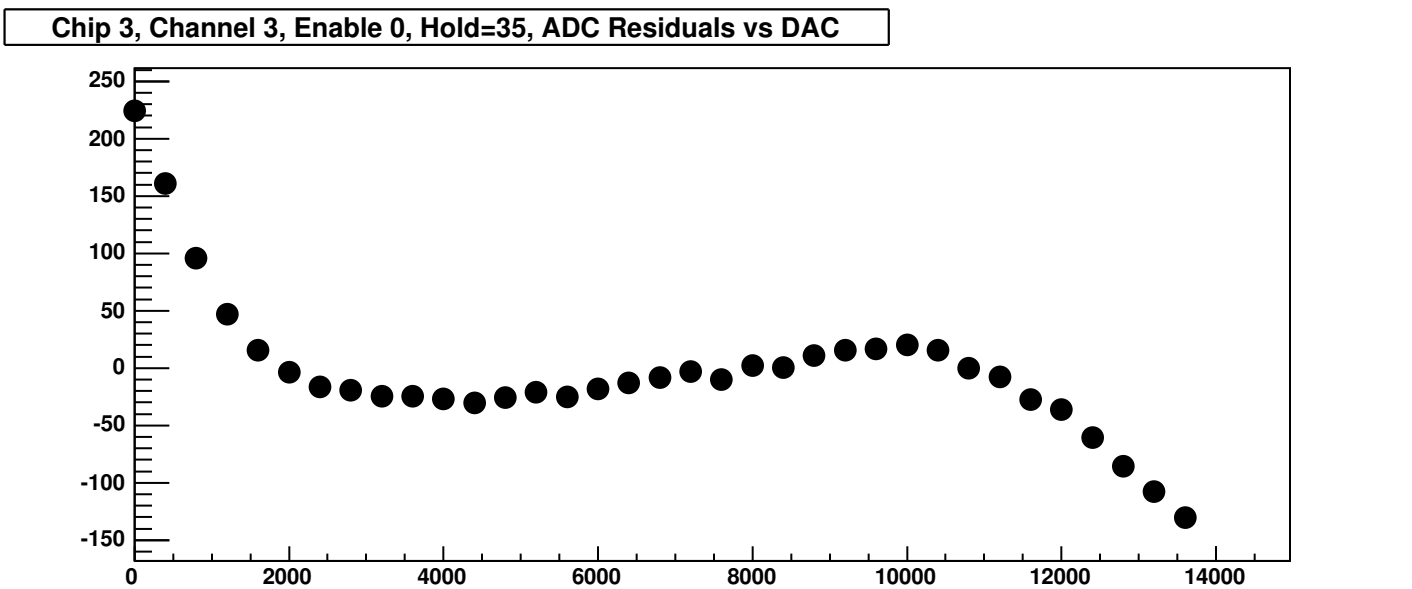

Chip 3, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2.733e+05 / 23
p0 -889.2 ± 0.9817
p1 -0.03861 ± 0.0001574
-400
-600
-800
-1000
-1200
-1400
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
24
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

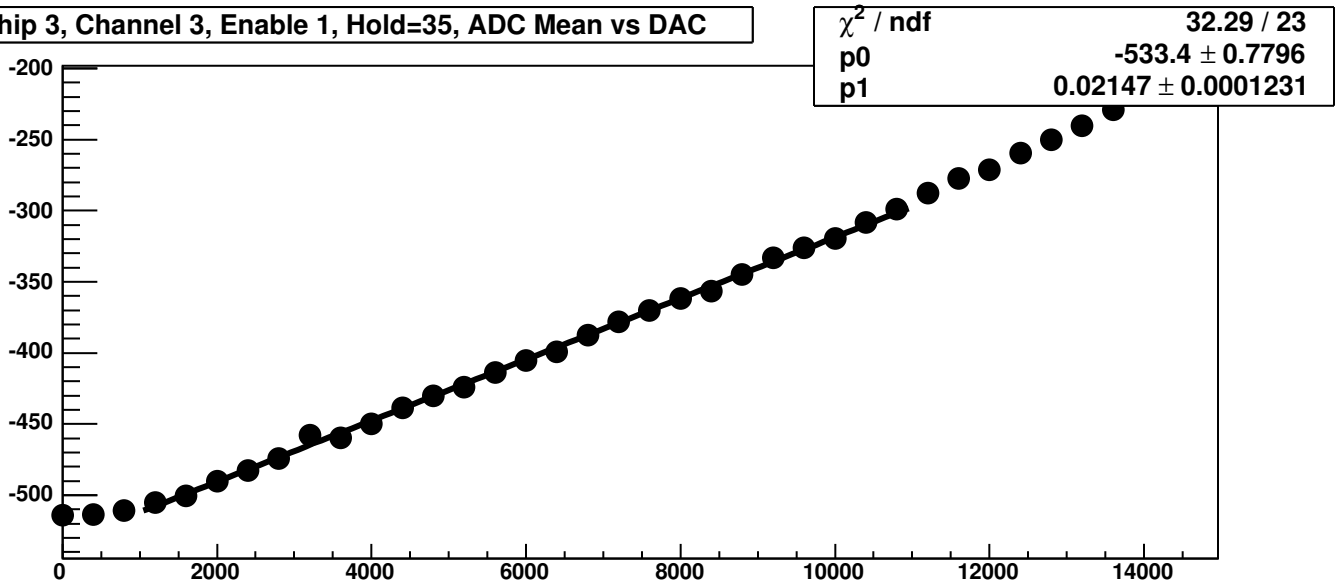

Chip 3, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1166 / 23
p0 -734.9 ± 1.206
p1 0.1645 ± 0.0001894
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

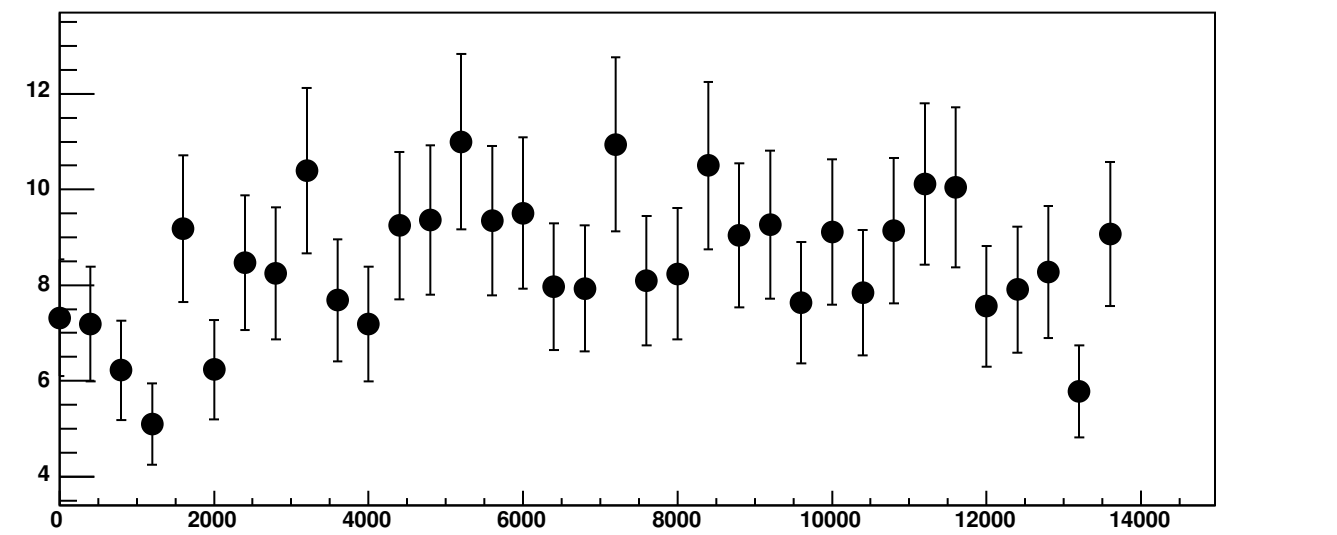

Chip 3, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

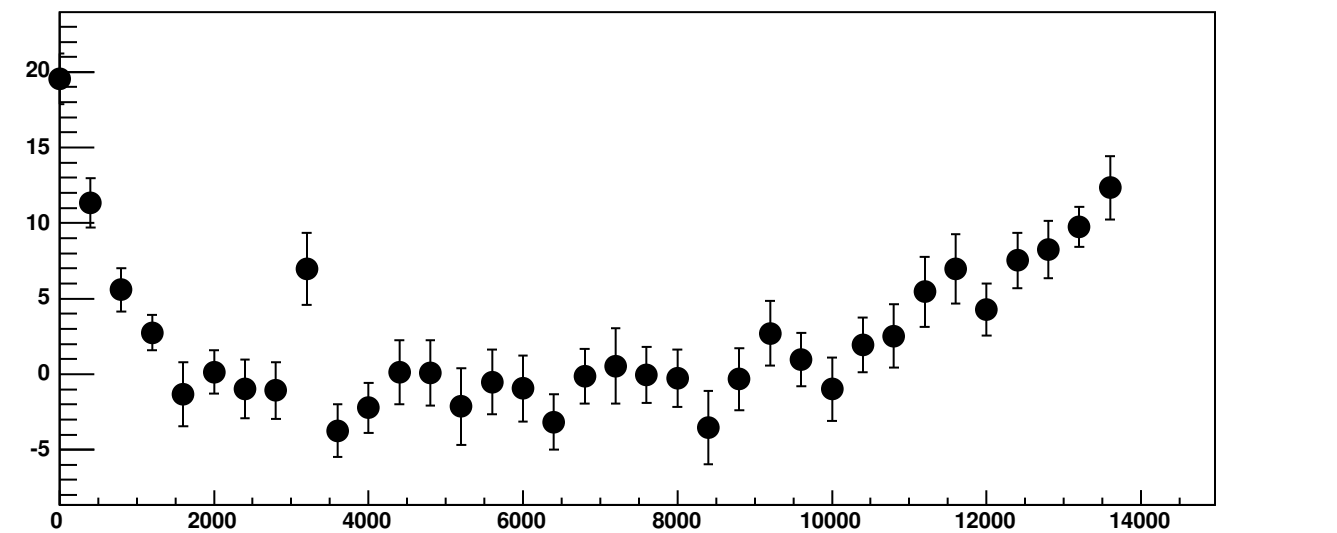
Chip 3, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC



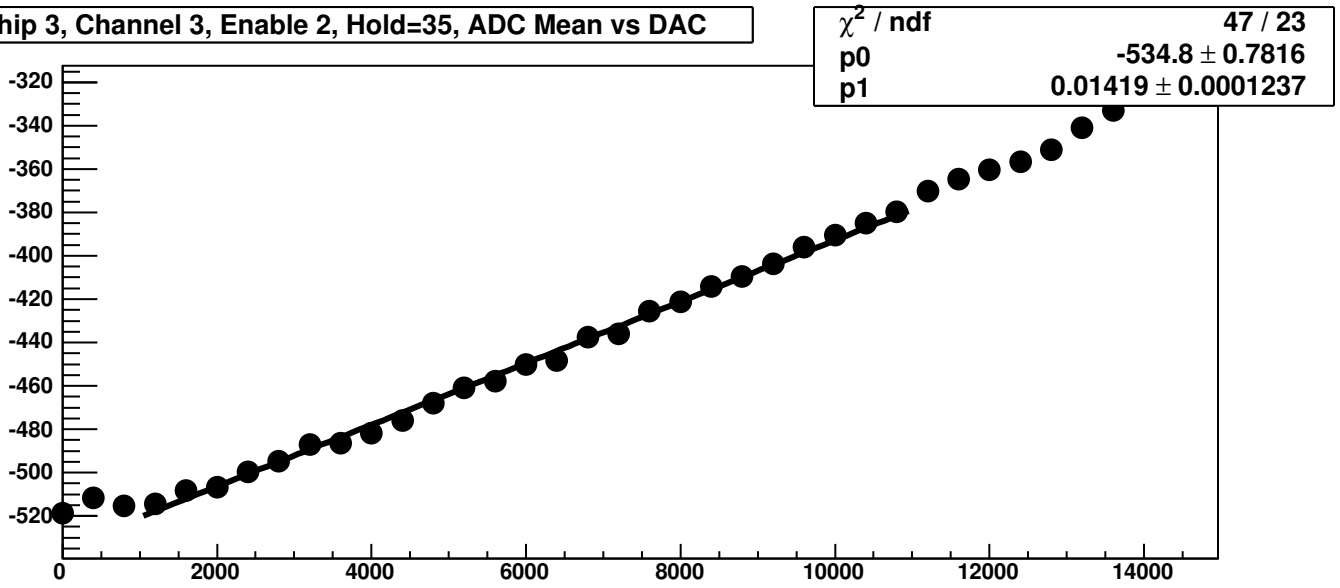
Chip 3, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC



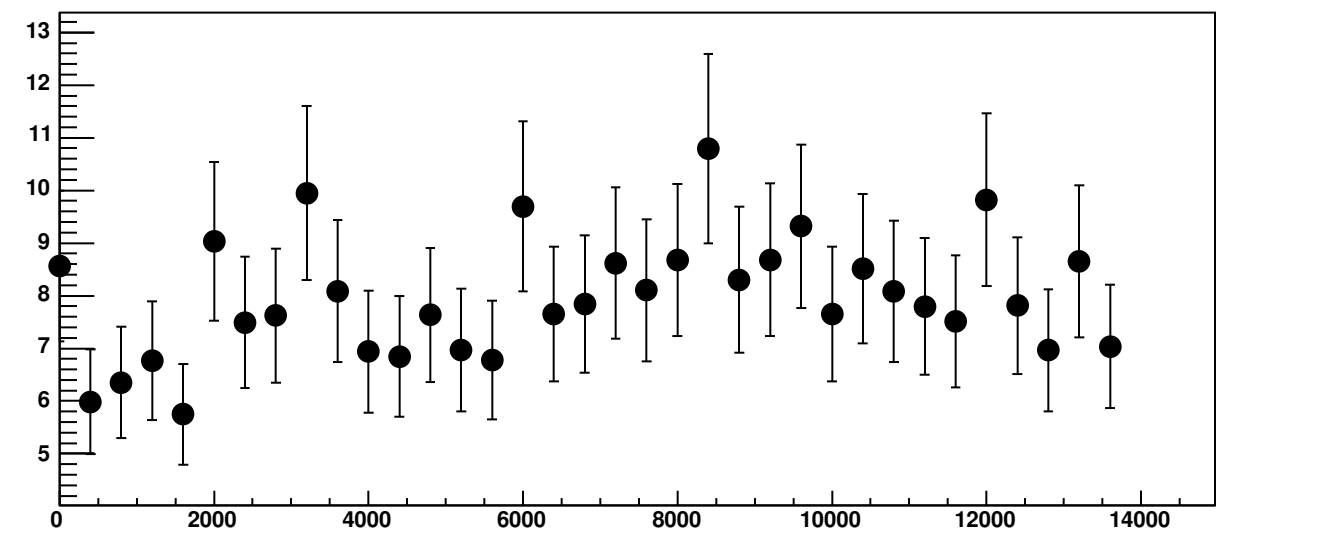
Chip 3, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC



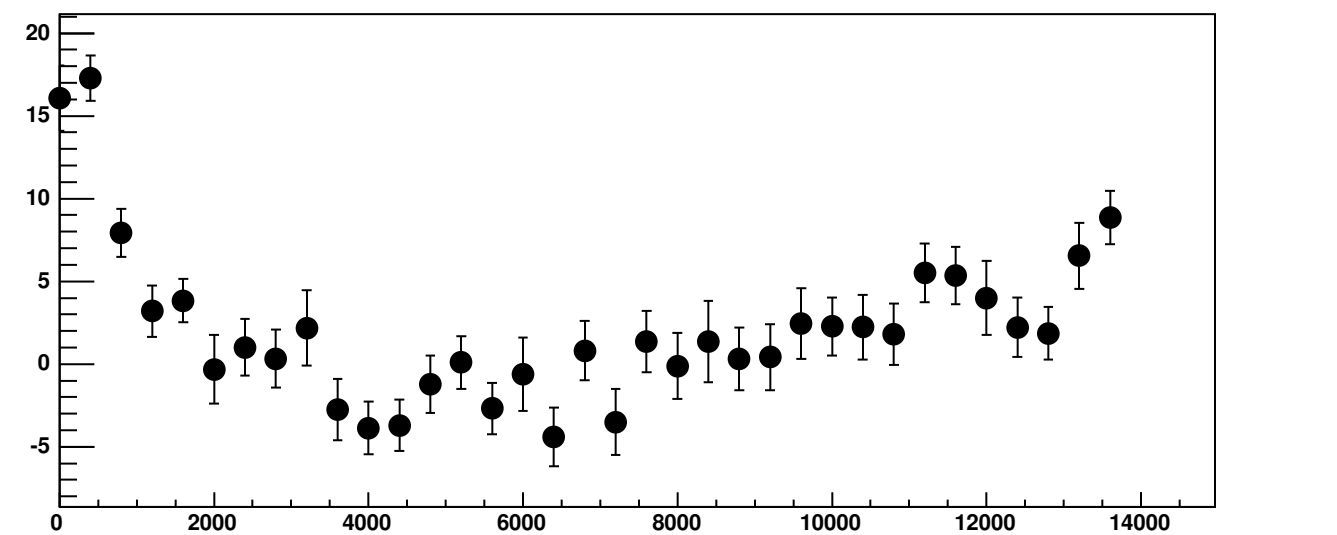
Chip 3, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC



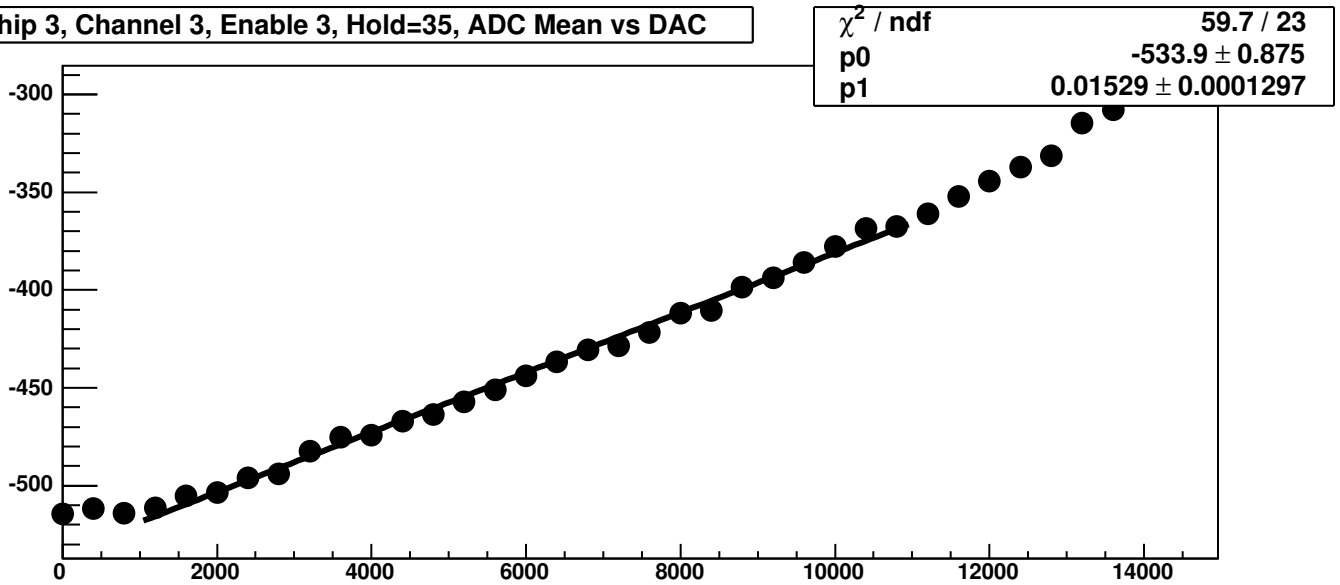
Chip 3, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC



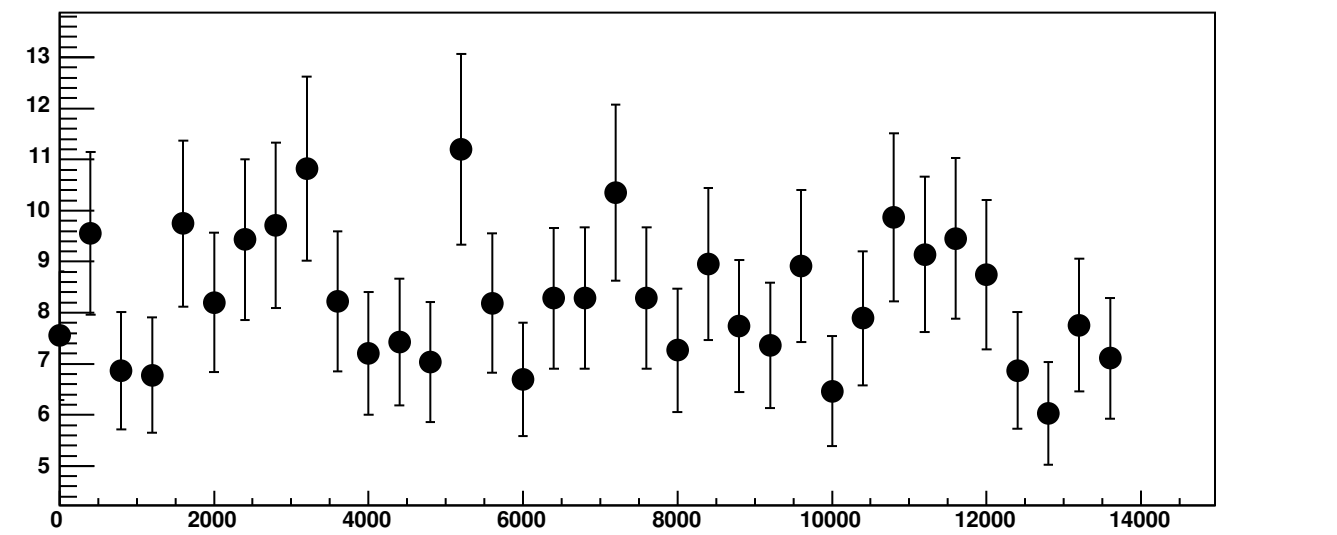
Chip 3, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC



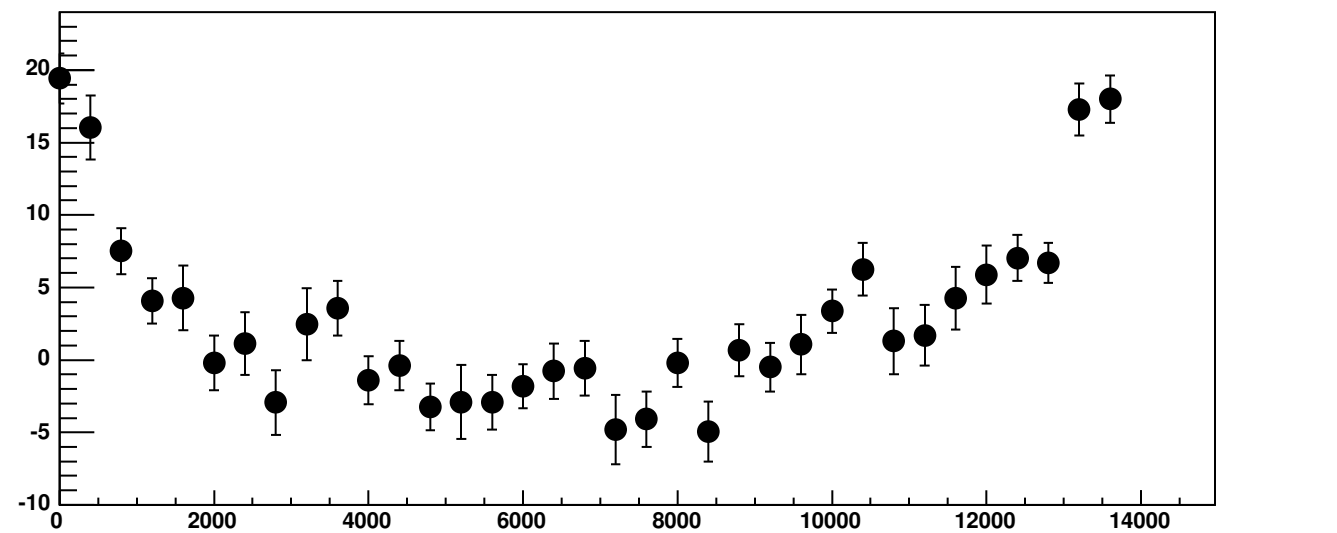
Chip 3, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

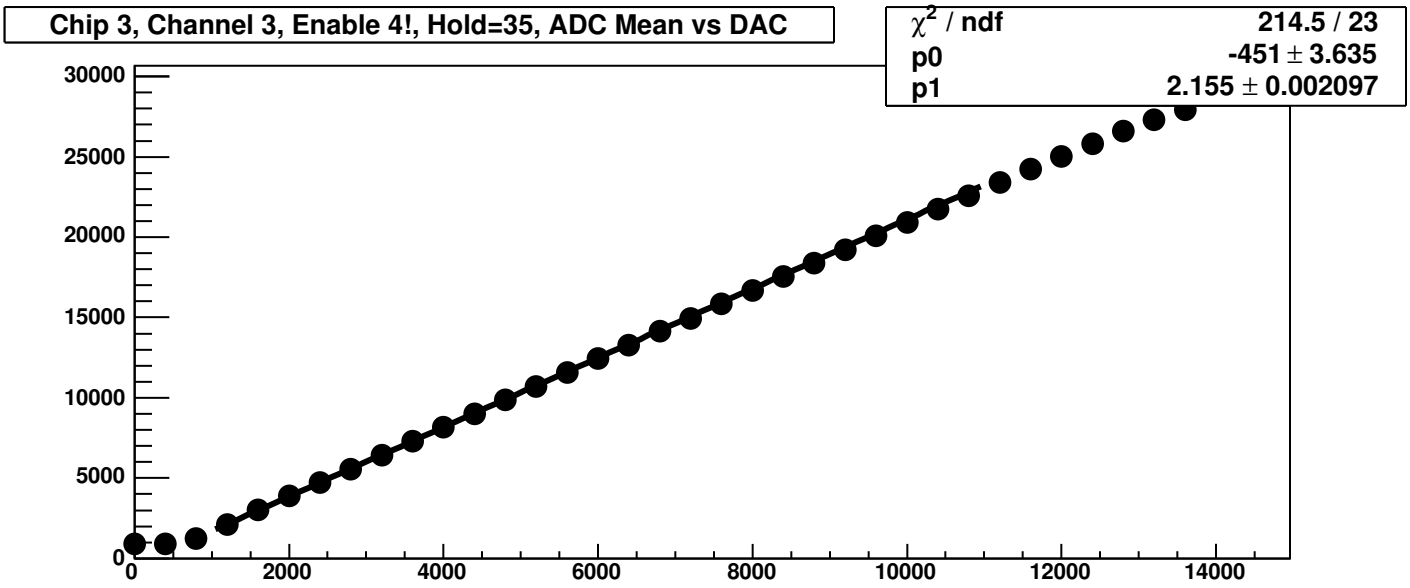


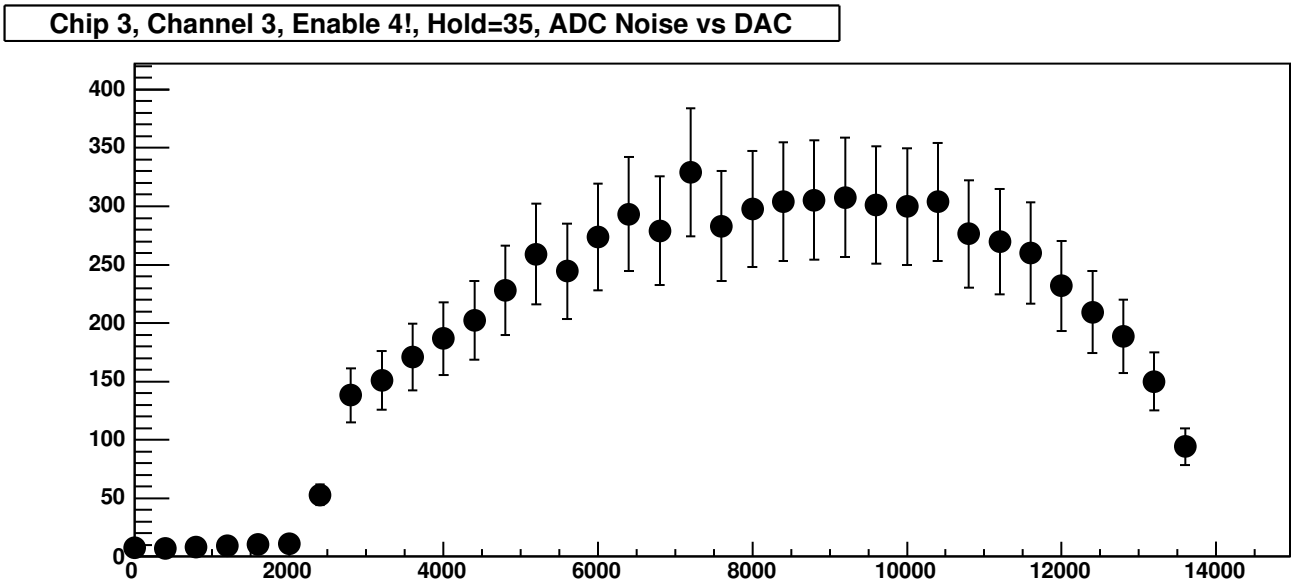
Chip 3, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC

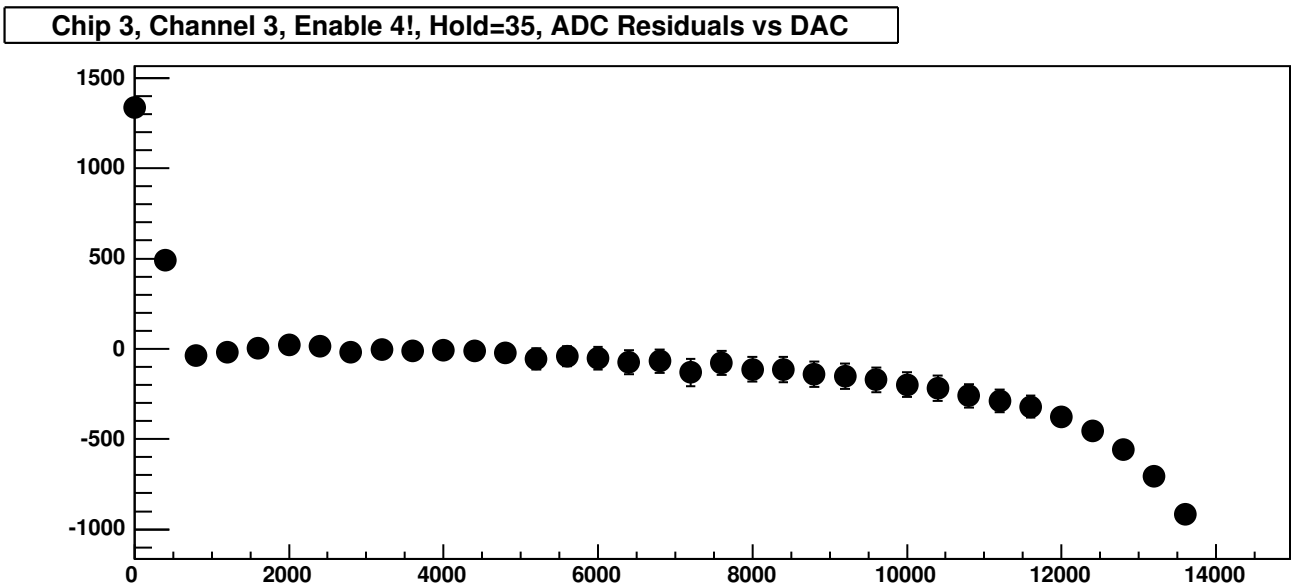


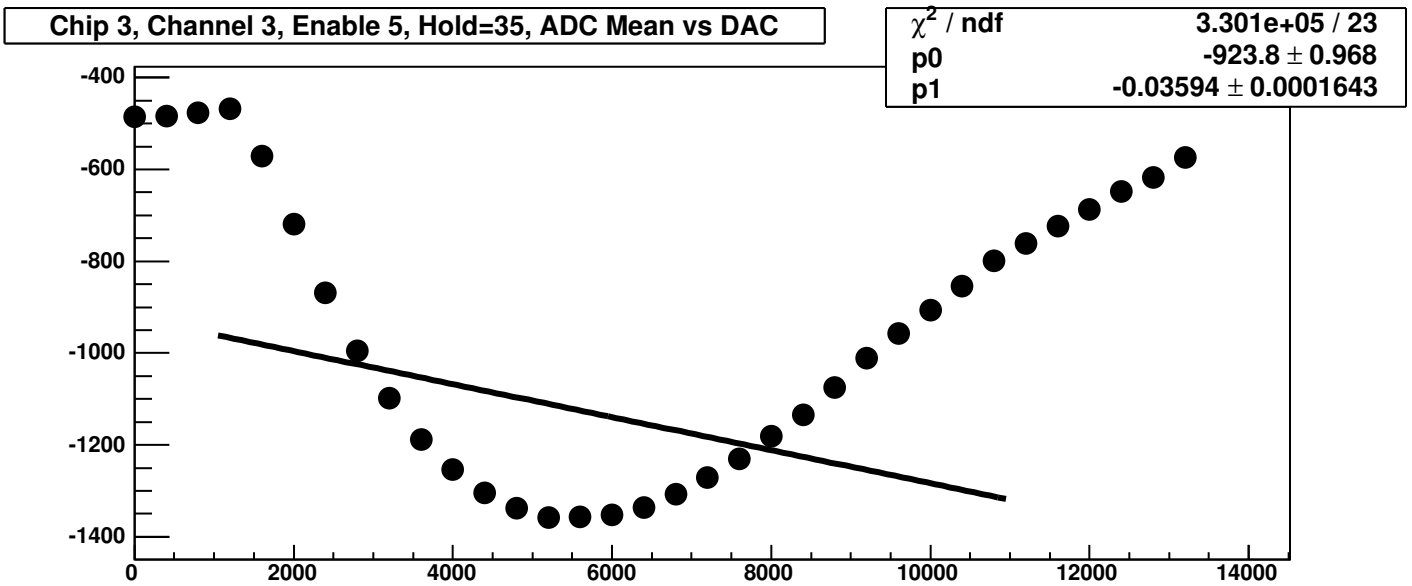
Chip 3, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

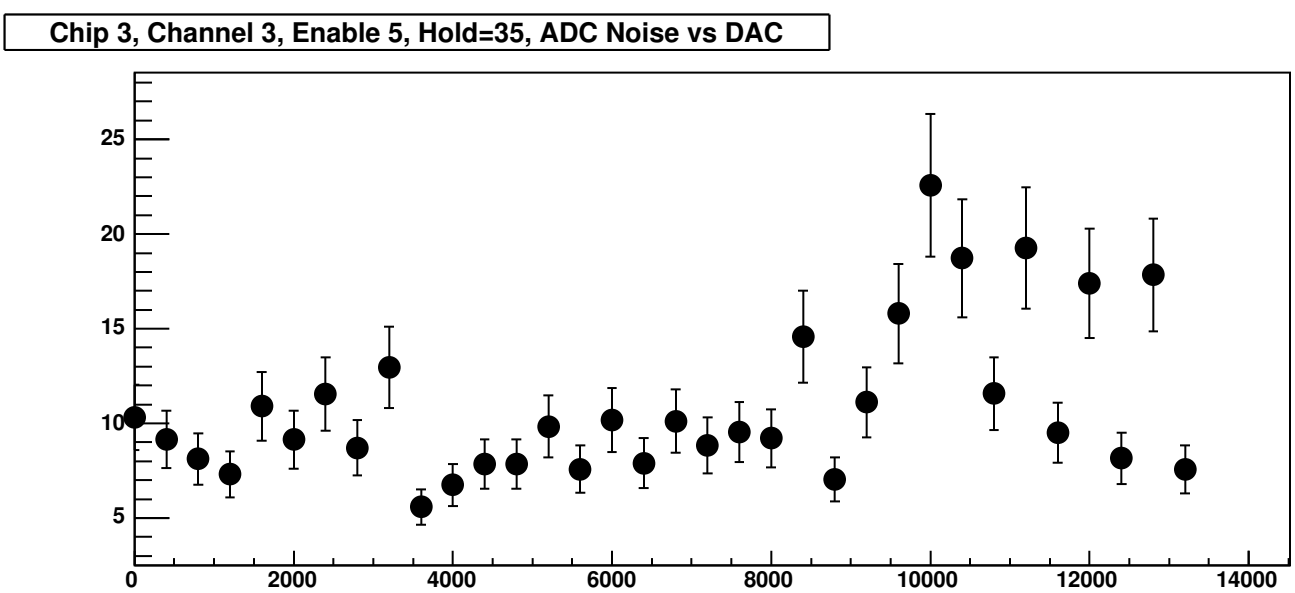


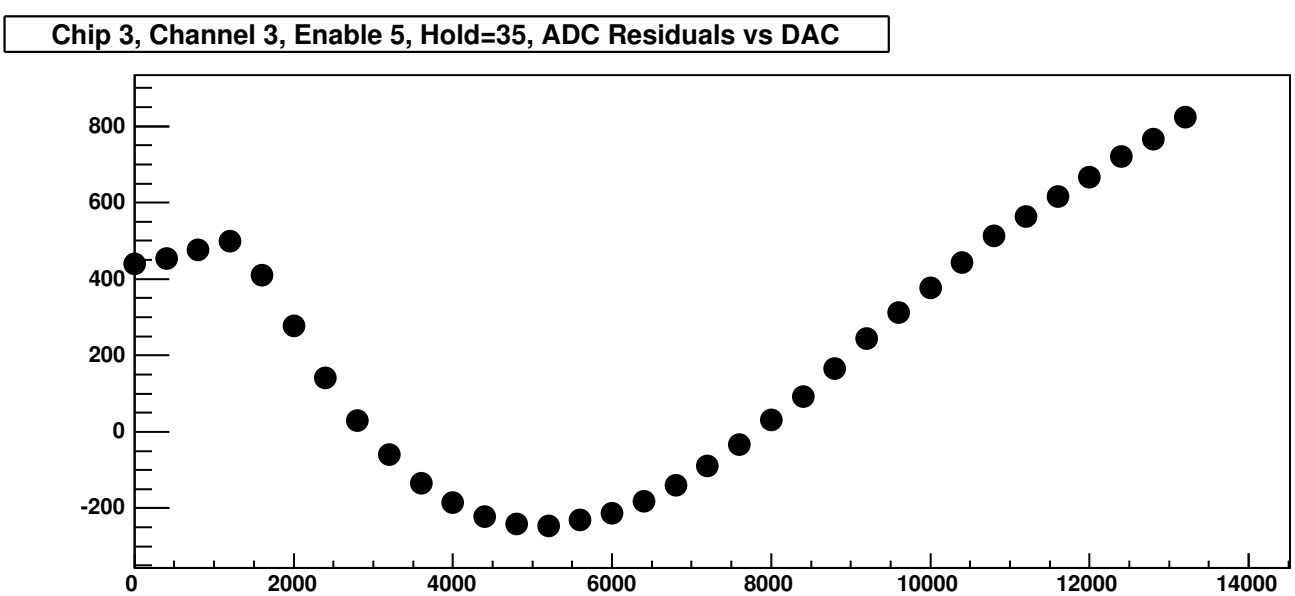

Chip 3, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 214.5 / 23
p0 -451 ± 3.635
p1 2.155 ± 0.002097
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

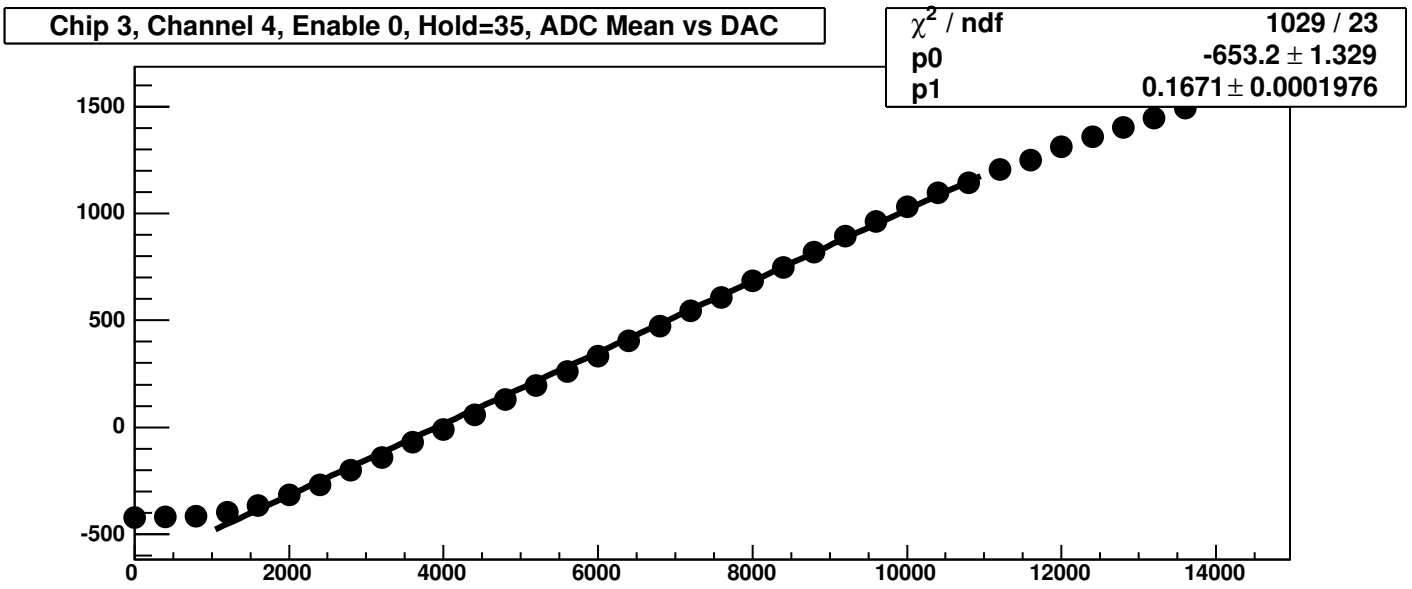

Chip 3, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

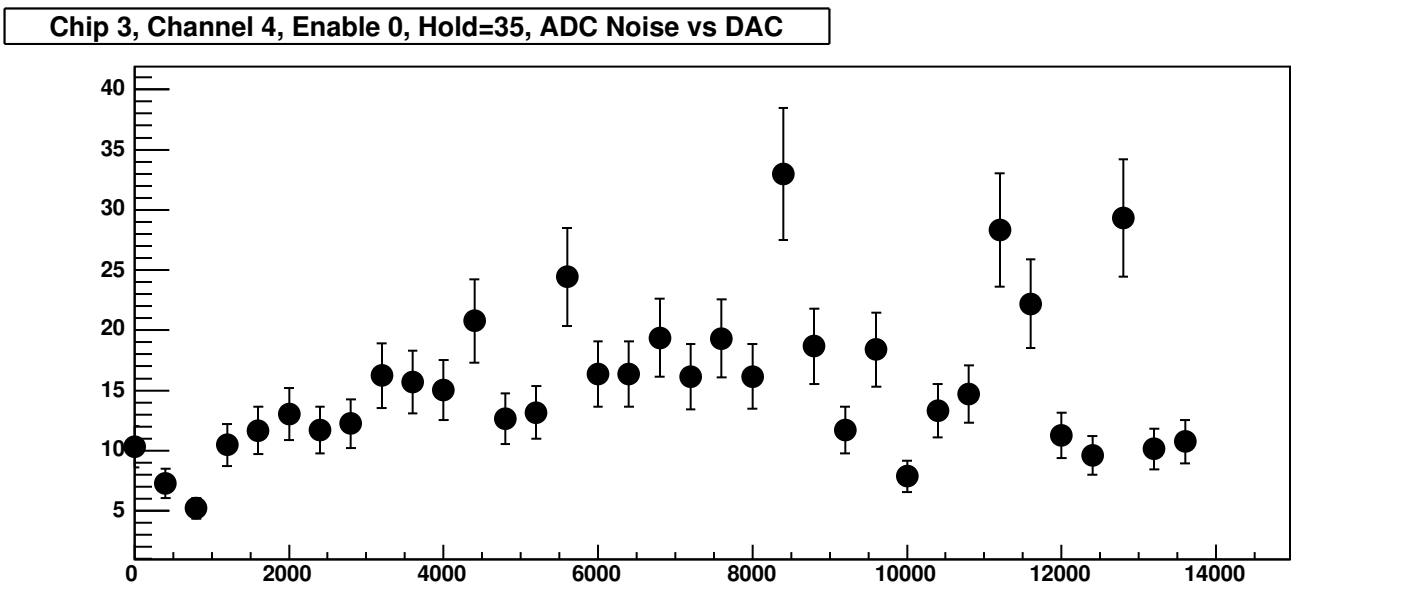

Chip 3, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

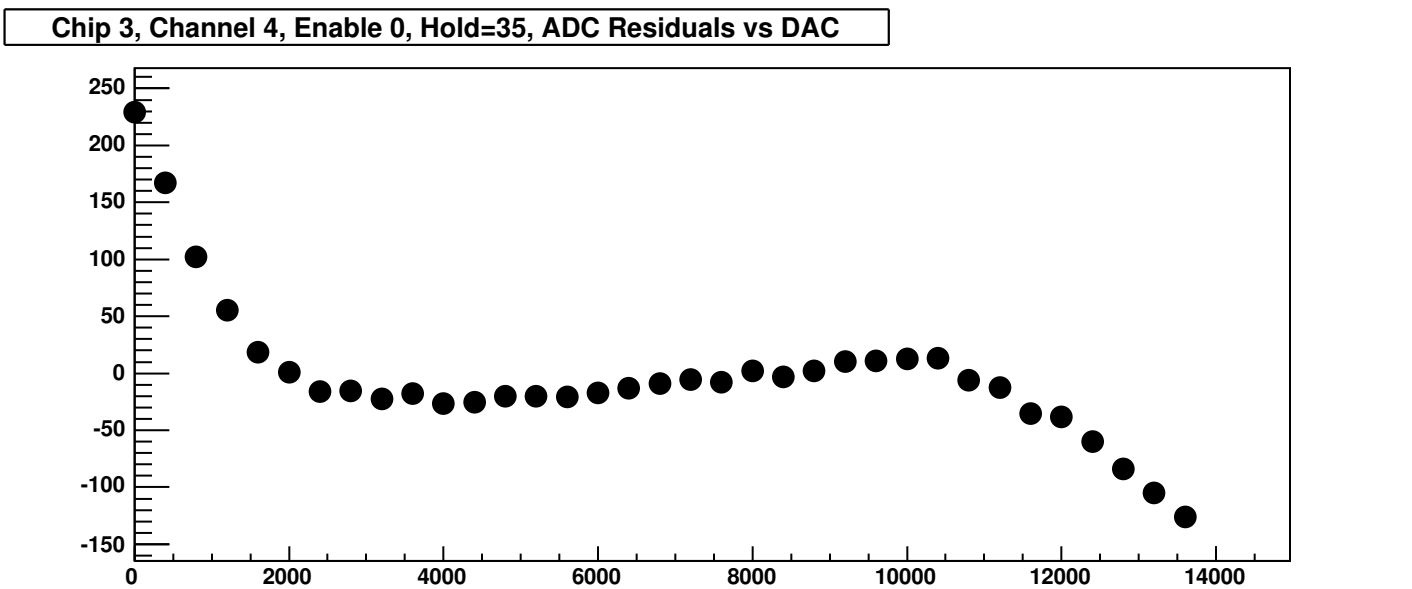

Chip 3, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.301e+05 / 23
p0 -923.8 ± 0.968
p1 -0.03594 ± 0.0001643
-400
-600
-800
-1000
-1200
-1400
0
2000
4000
6000
8000
10000
12000
14000

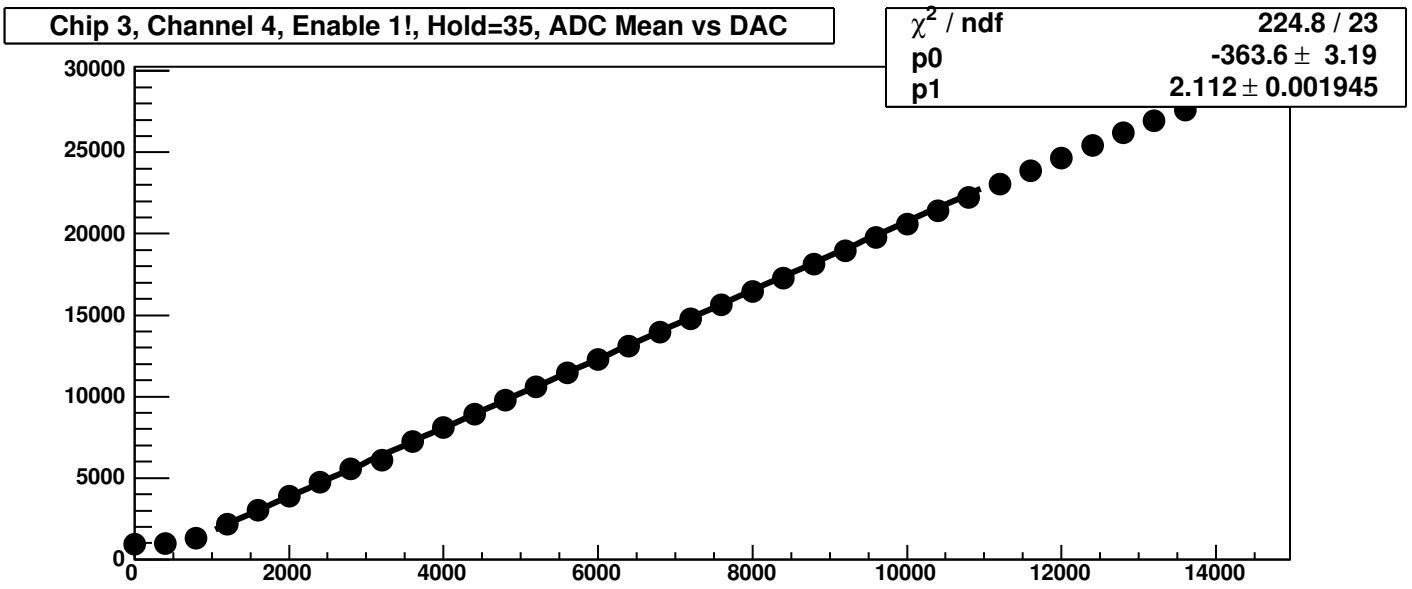

Chip 3, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

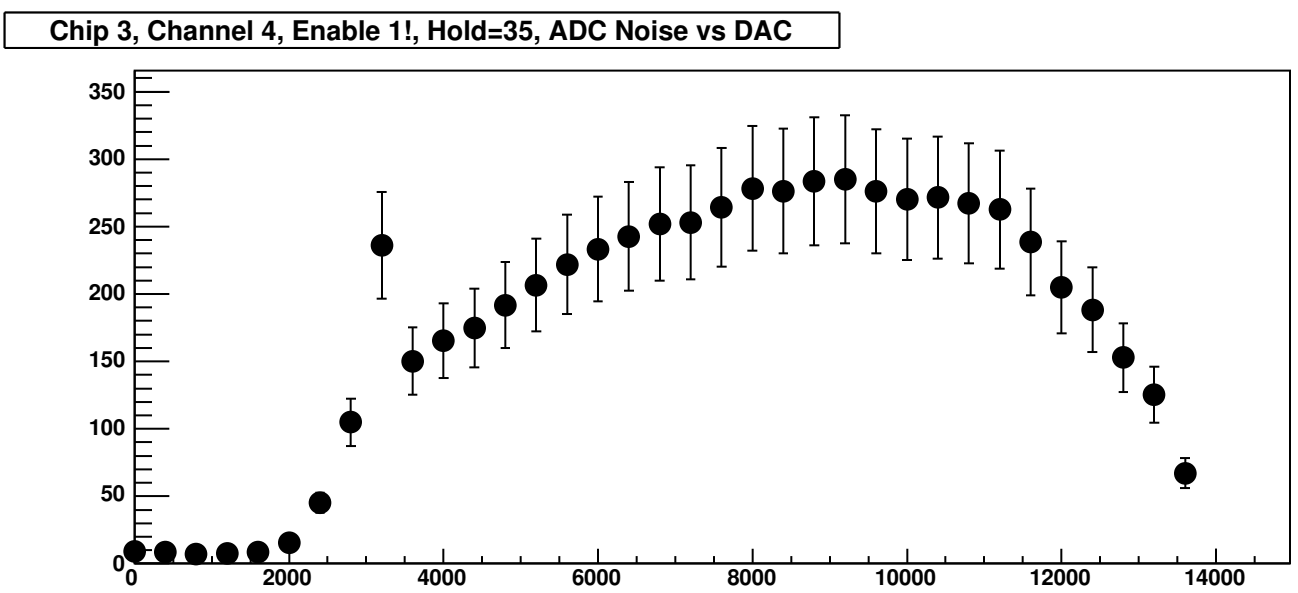

Chip 3, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

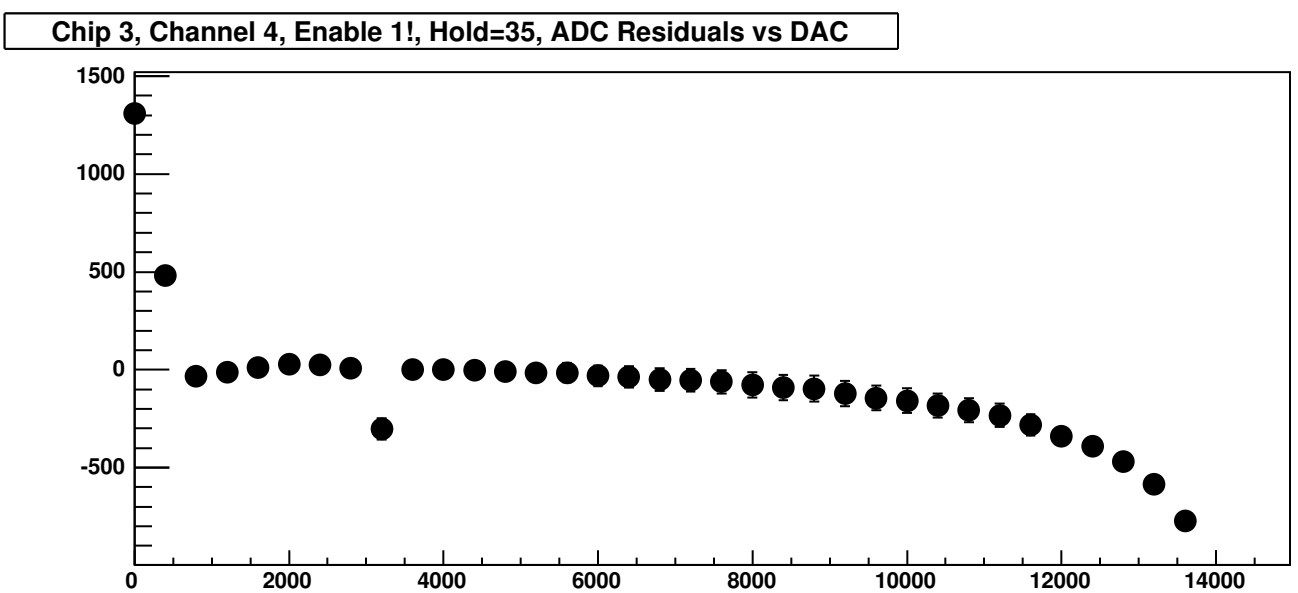

Chip 3, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1029 / 23
p0 -653.2 ± 1.329
p1 0.1671 ± 0.0001976
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

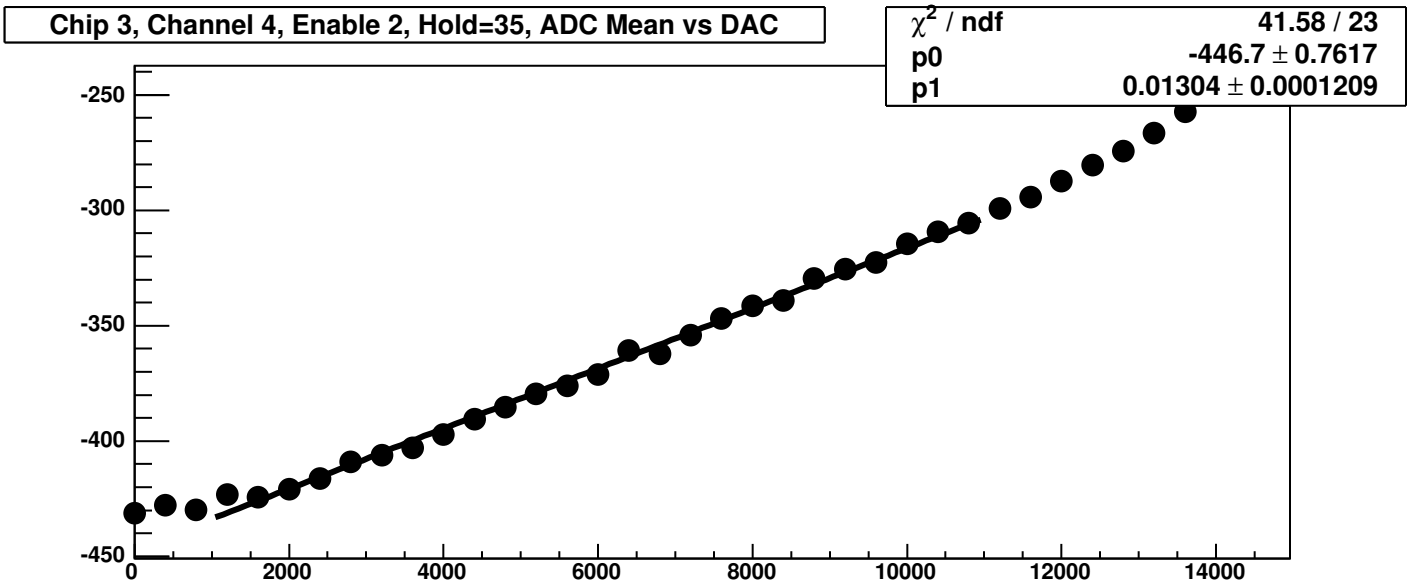

Chip 3, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

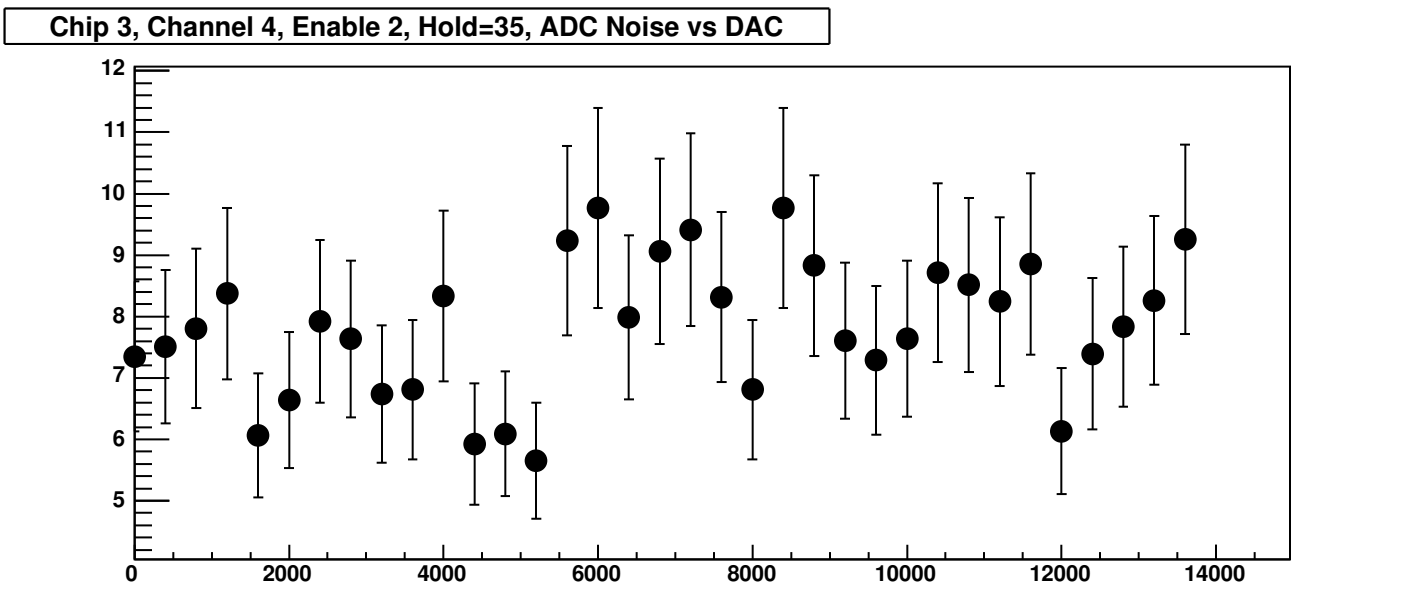

Chip 3, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

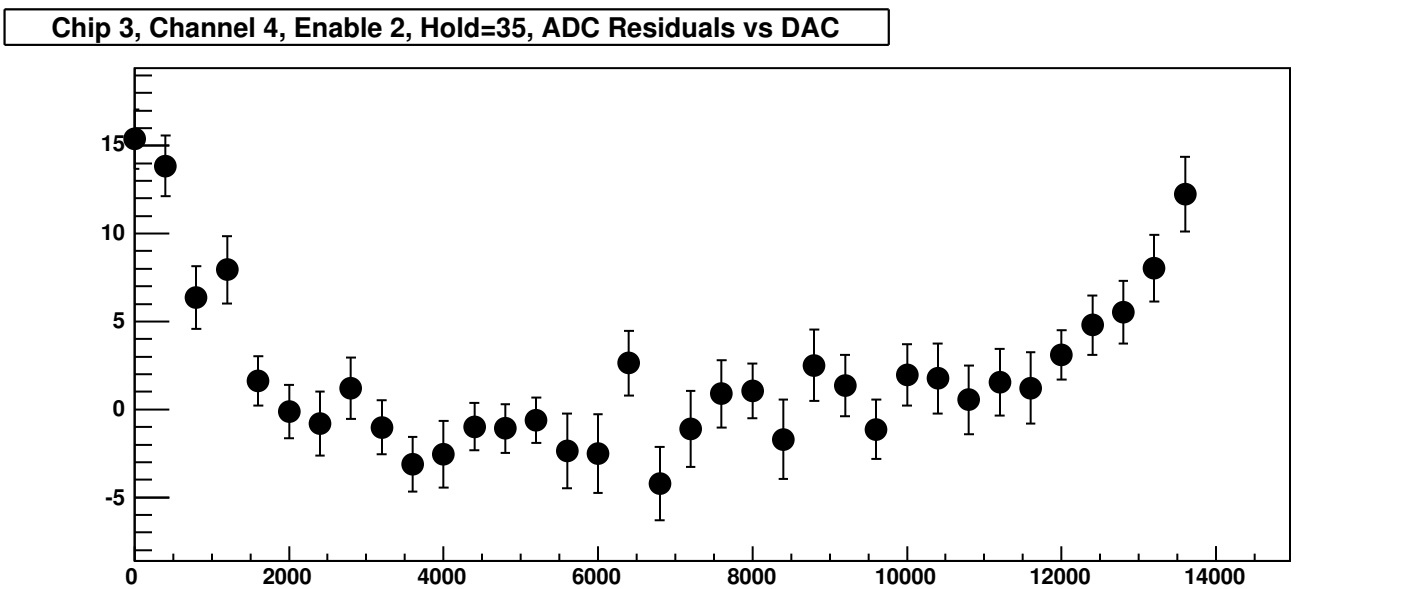

Chip 3, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 224.8 / 23
p0 -363.6 ± 3.19
p1 2.112 ± 0.001945
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

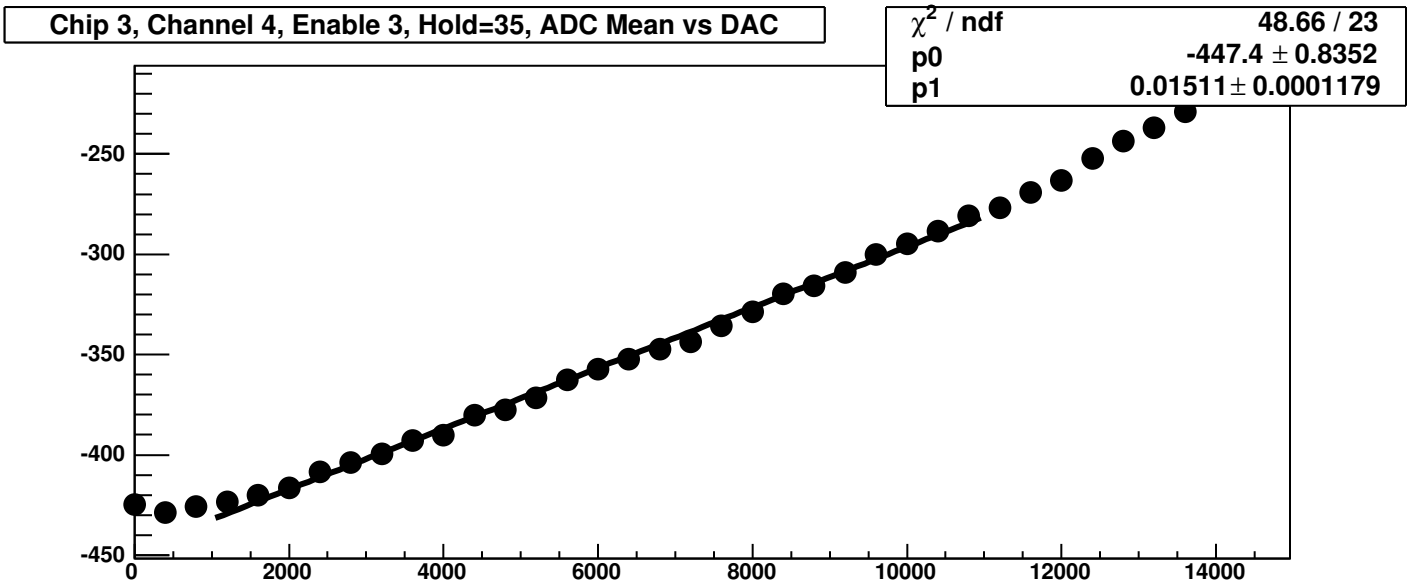

Chip 3, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

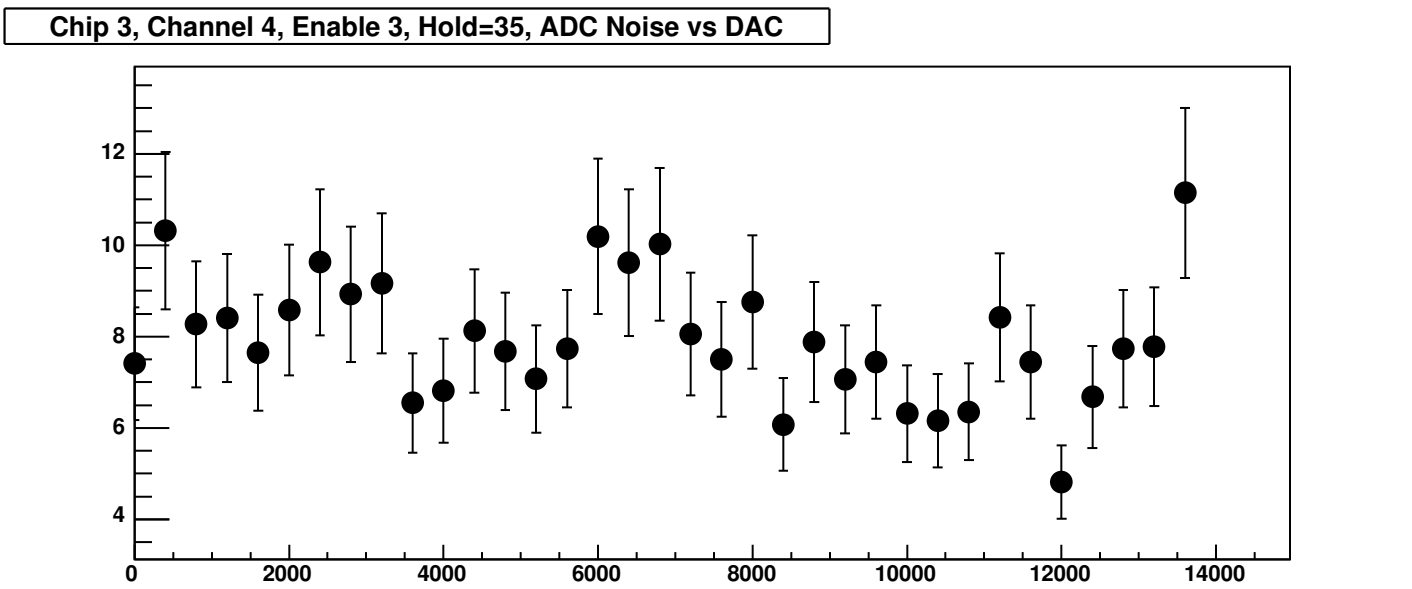

Chip 3, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

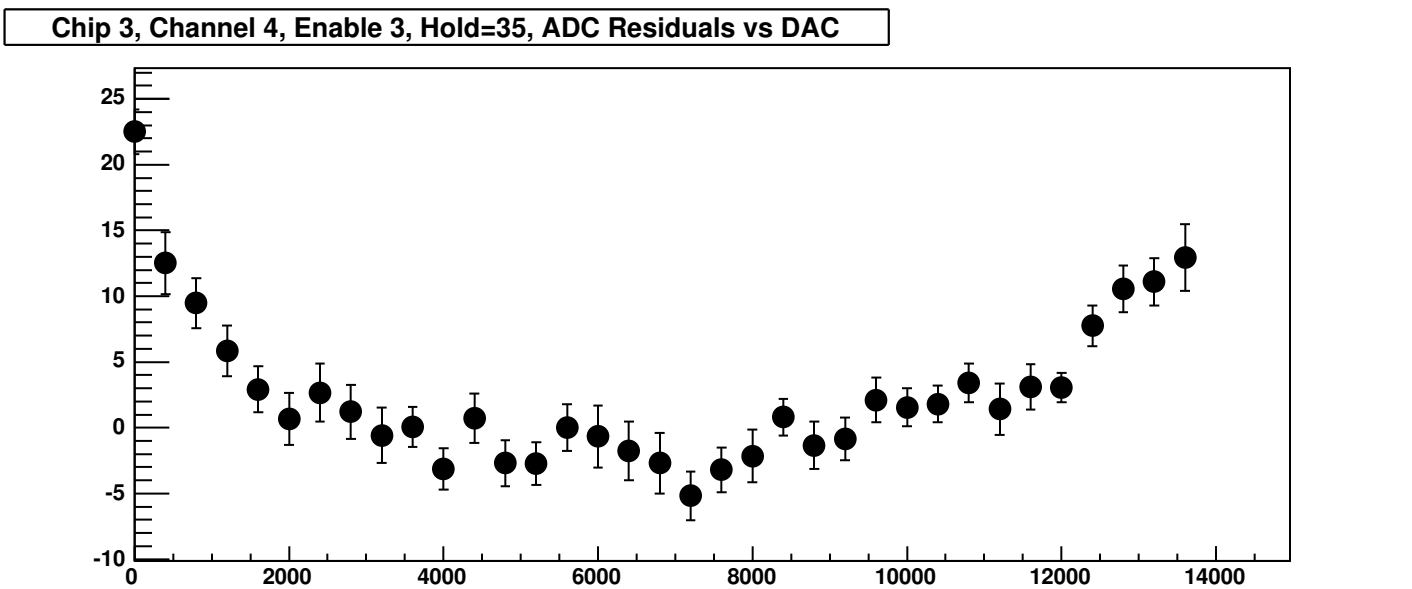

Chip 3, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 41.58 / 23
p0 -446.7 ± 0.7617
p1 0.01304 ± 0.0001209
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

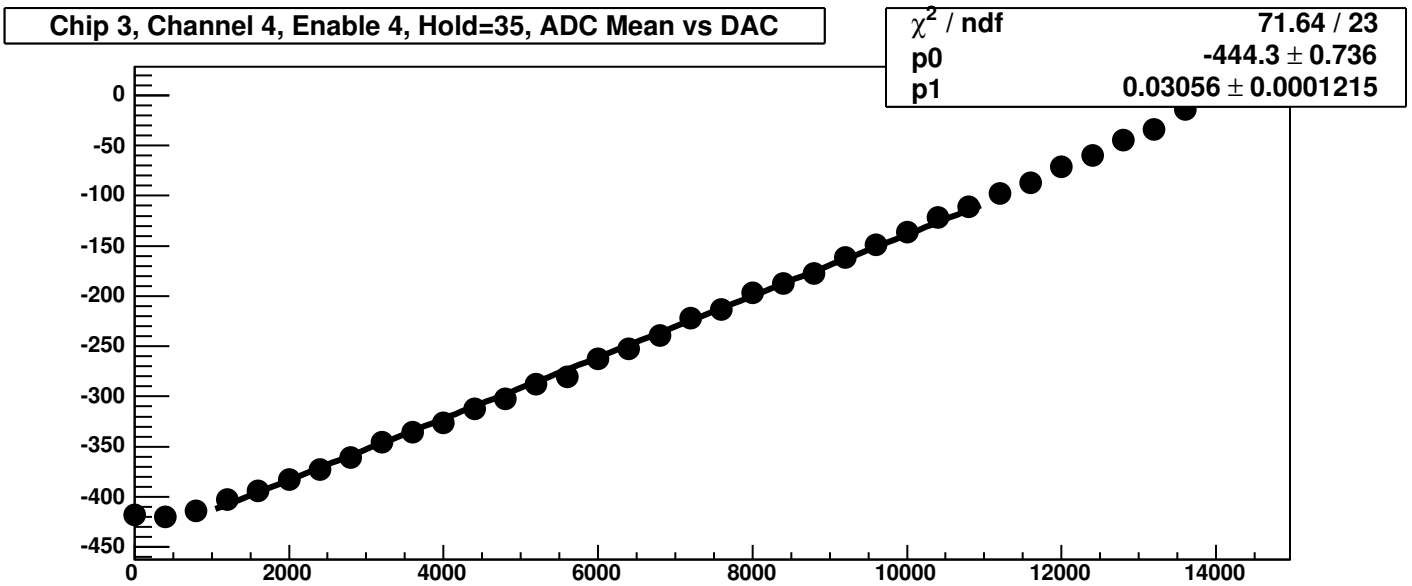

Chip 3, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

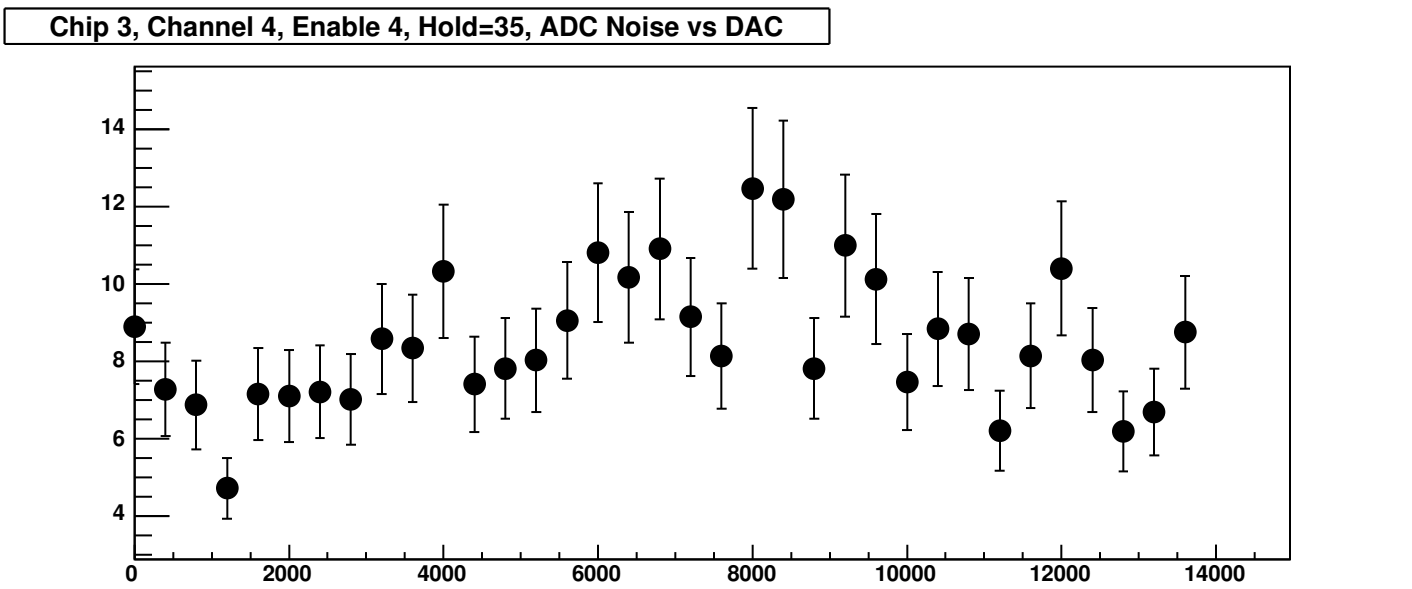

Chip 3, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

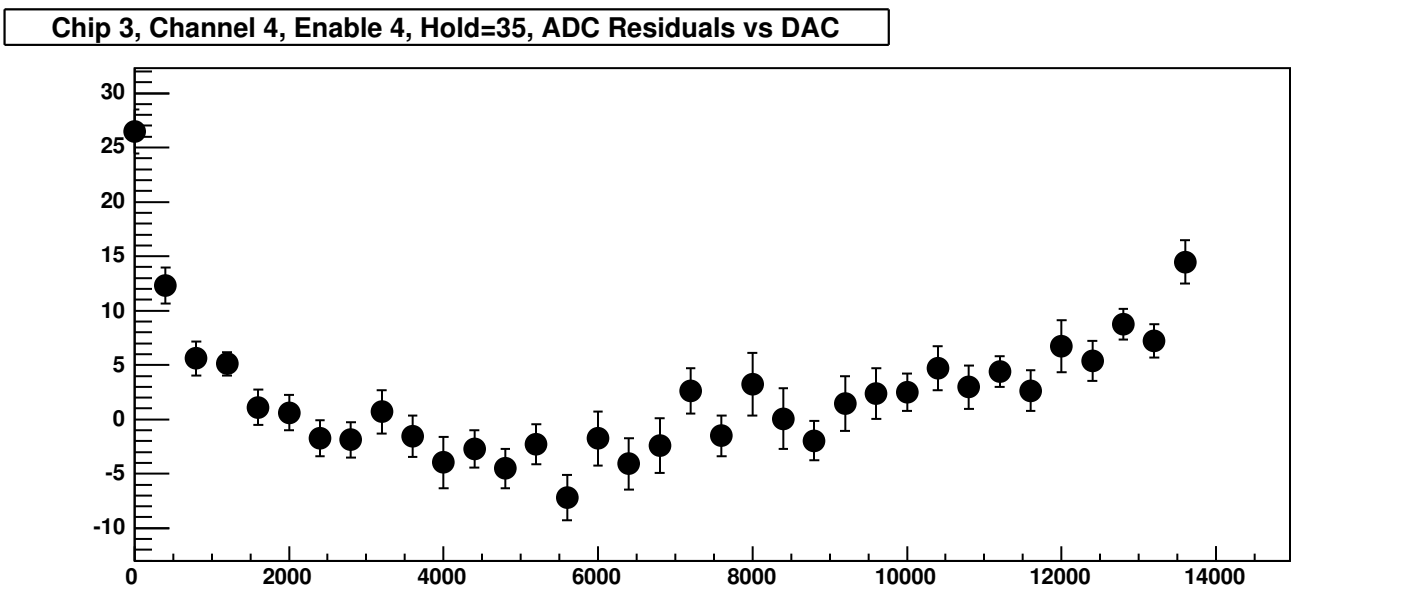

Chip 3, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 48.66 / 23
p0 -447.4 ± 0.8352
p1 0.01511 ± 0.0001179
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

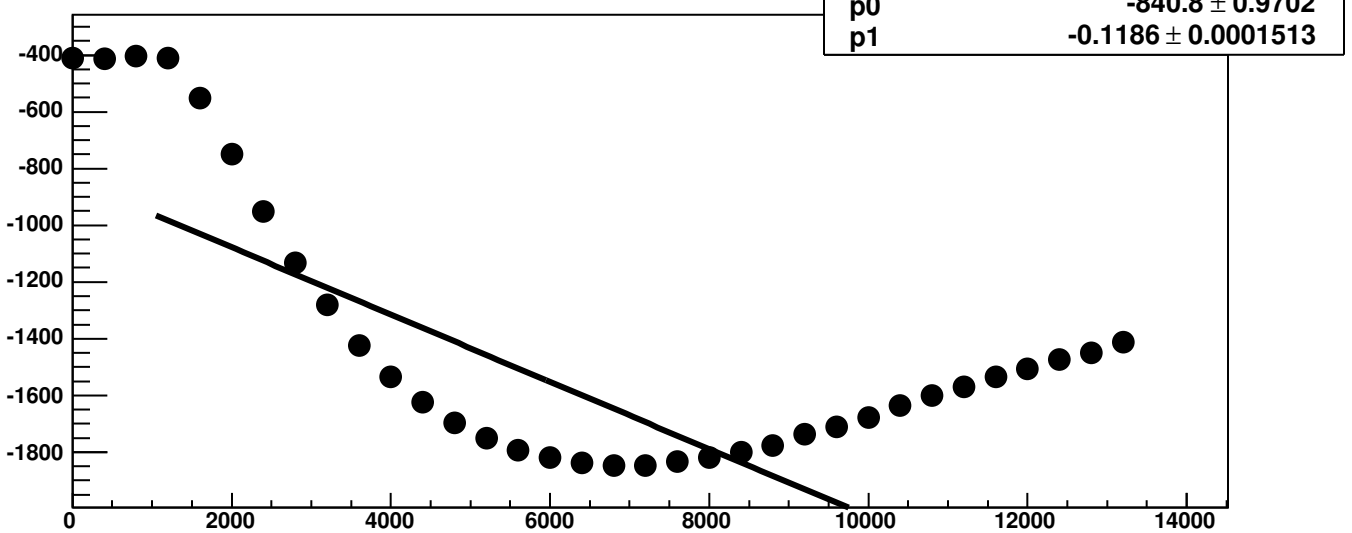

Chip 3, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 71.64 / 23
p0 -444.3 ± 0.736
p1 0.03056 ± 0.0001215
0
-50
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

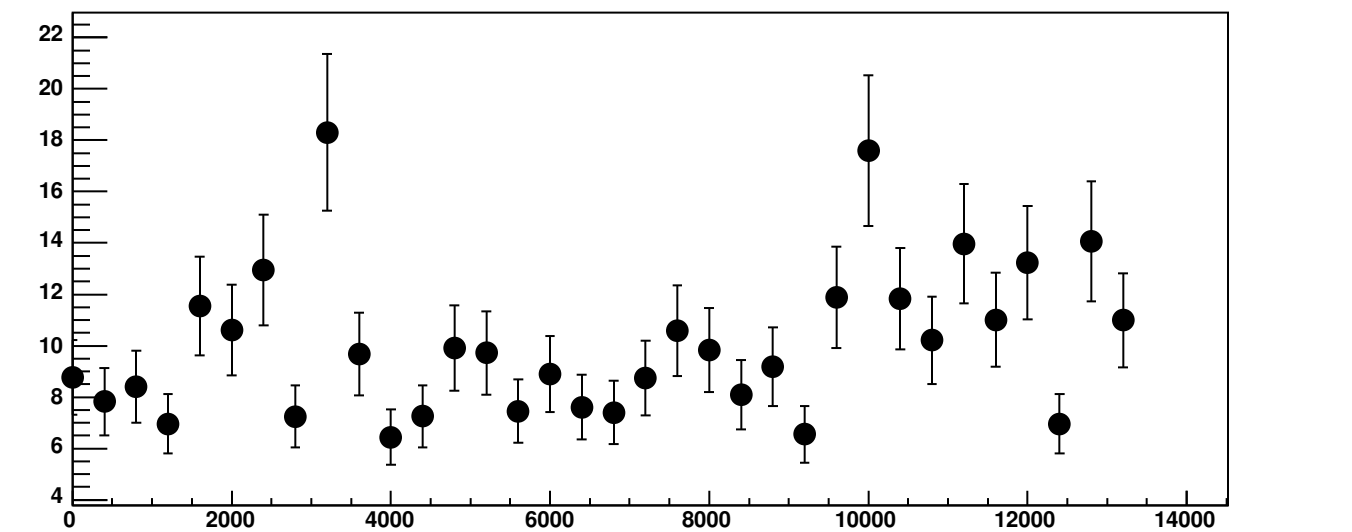

Chip 3, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

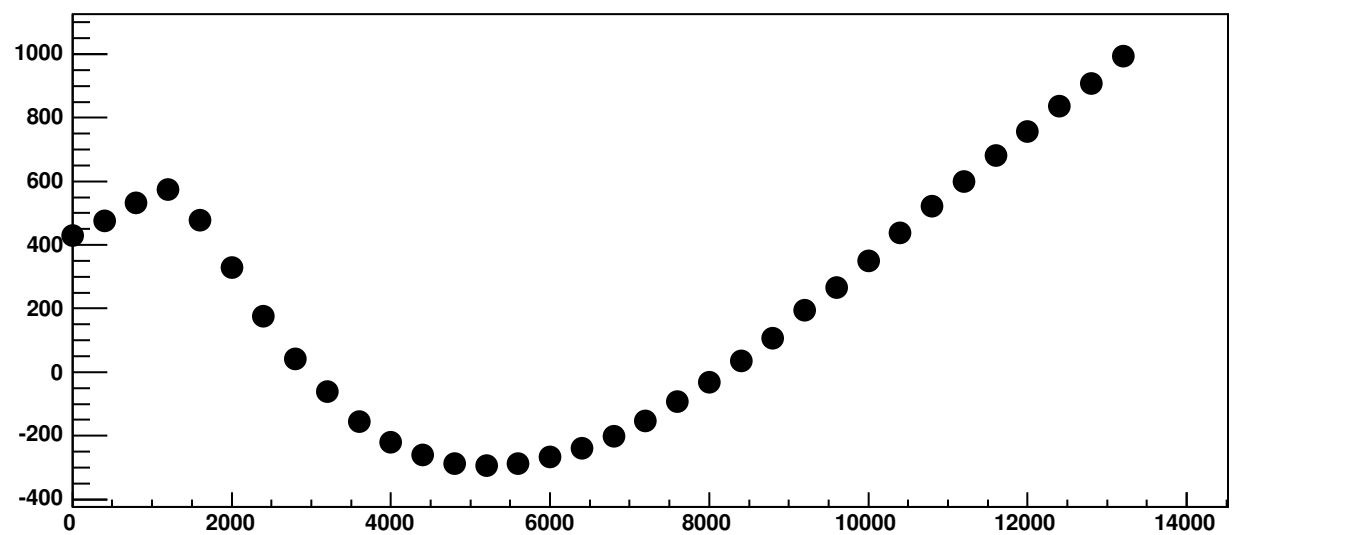
Chip 3, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

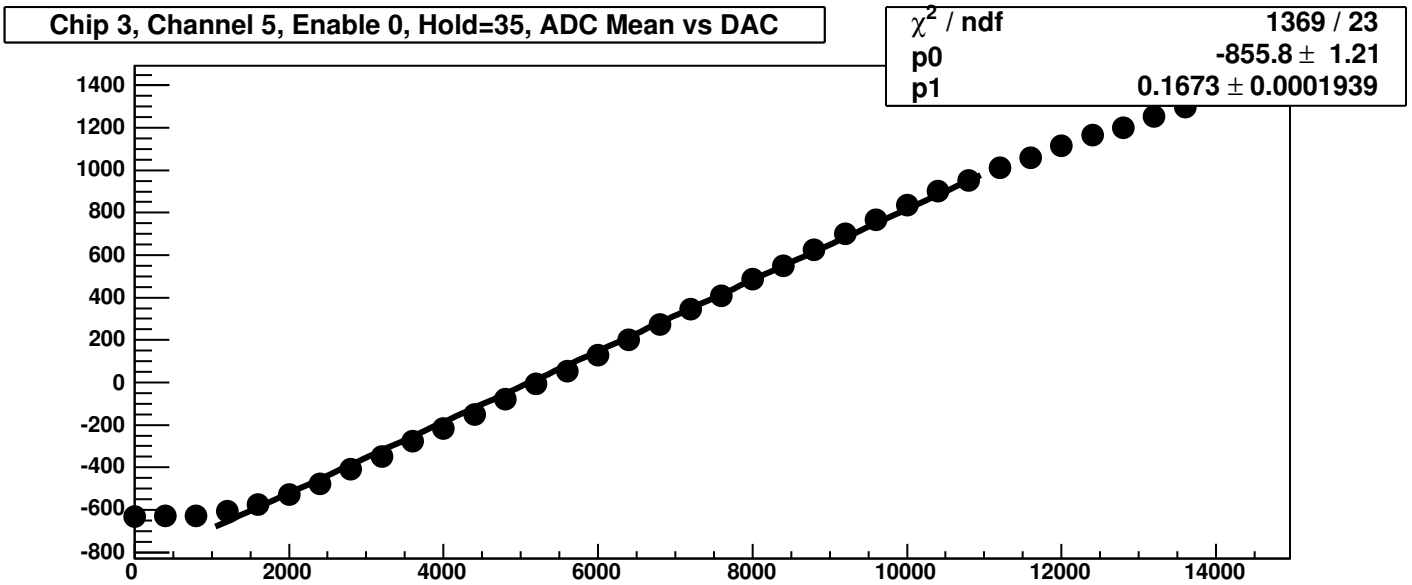


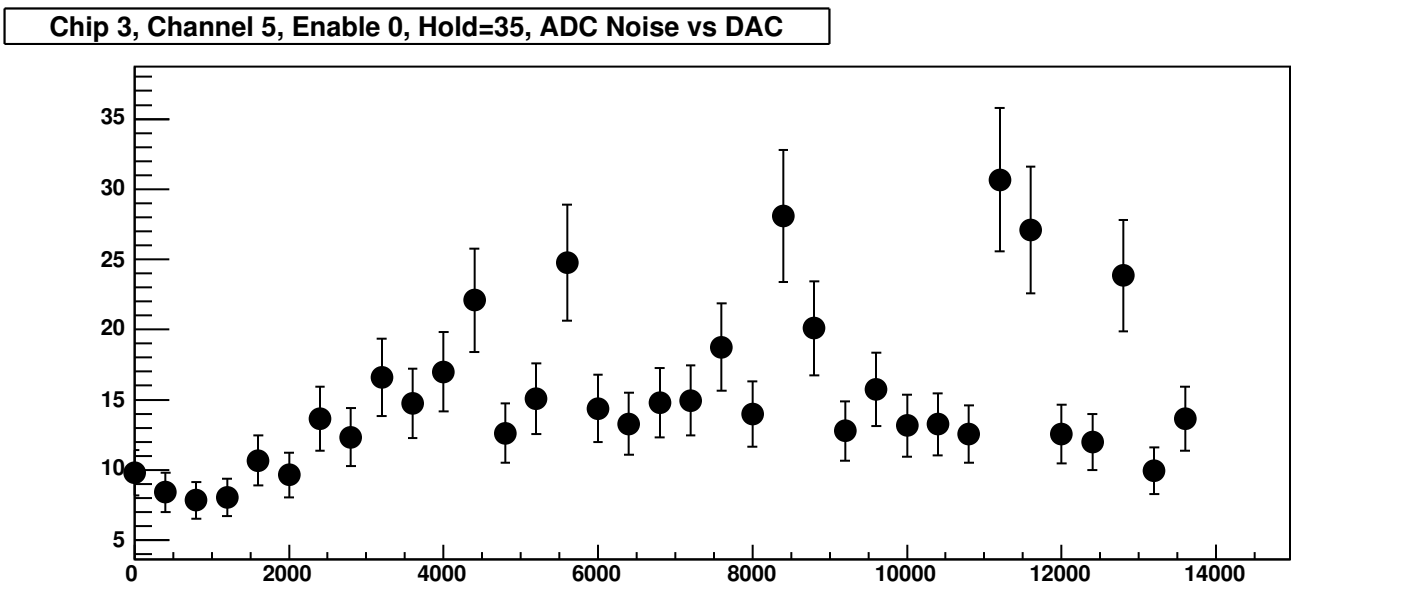
Chip 3, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC

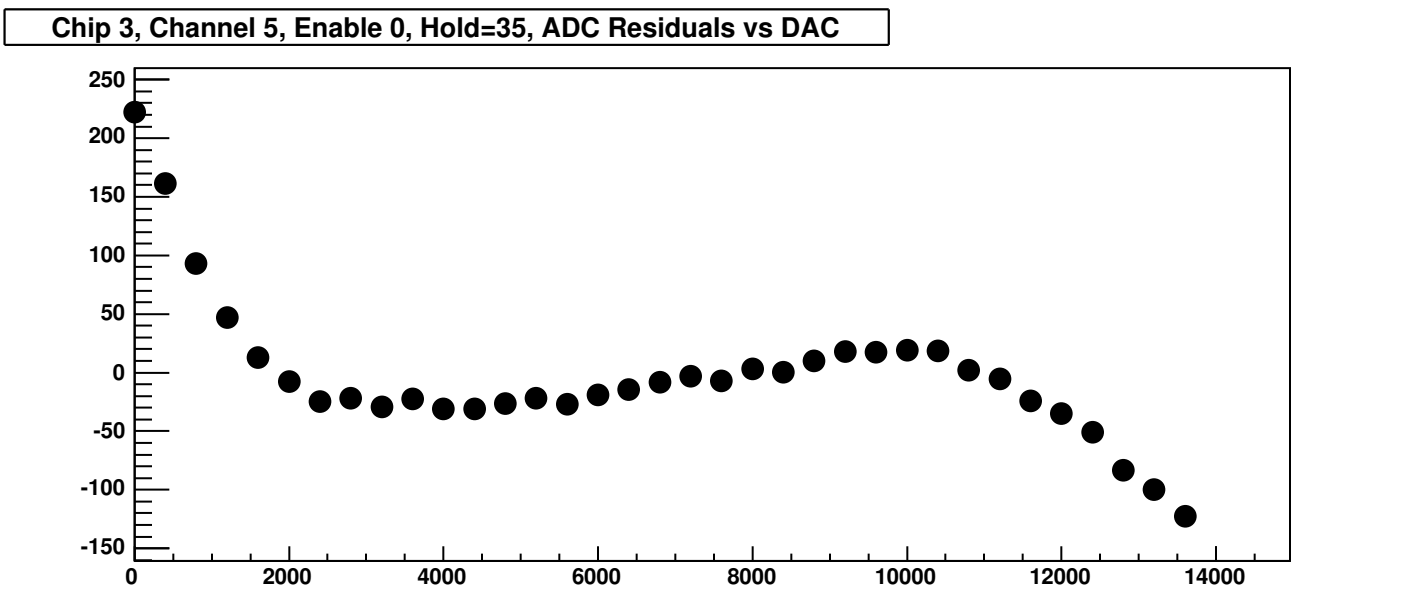


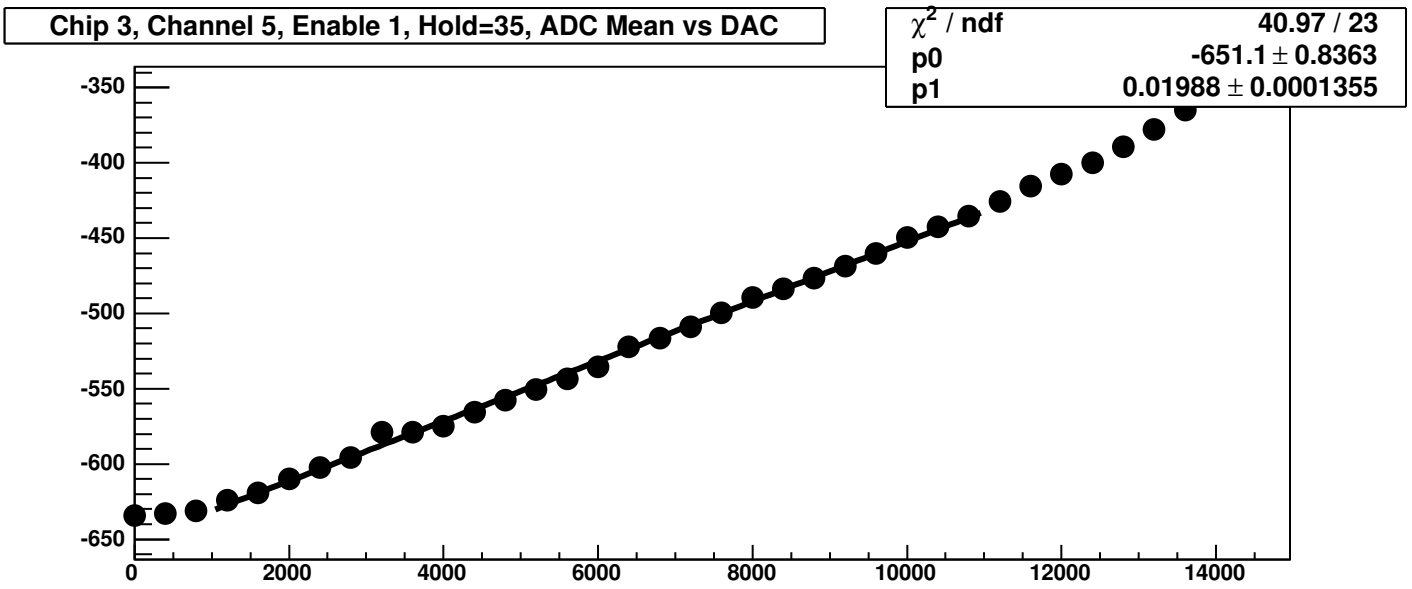
Chip 3, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

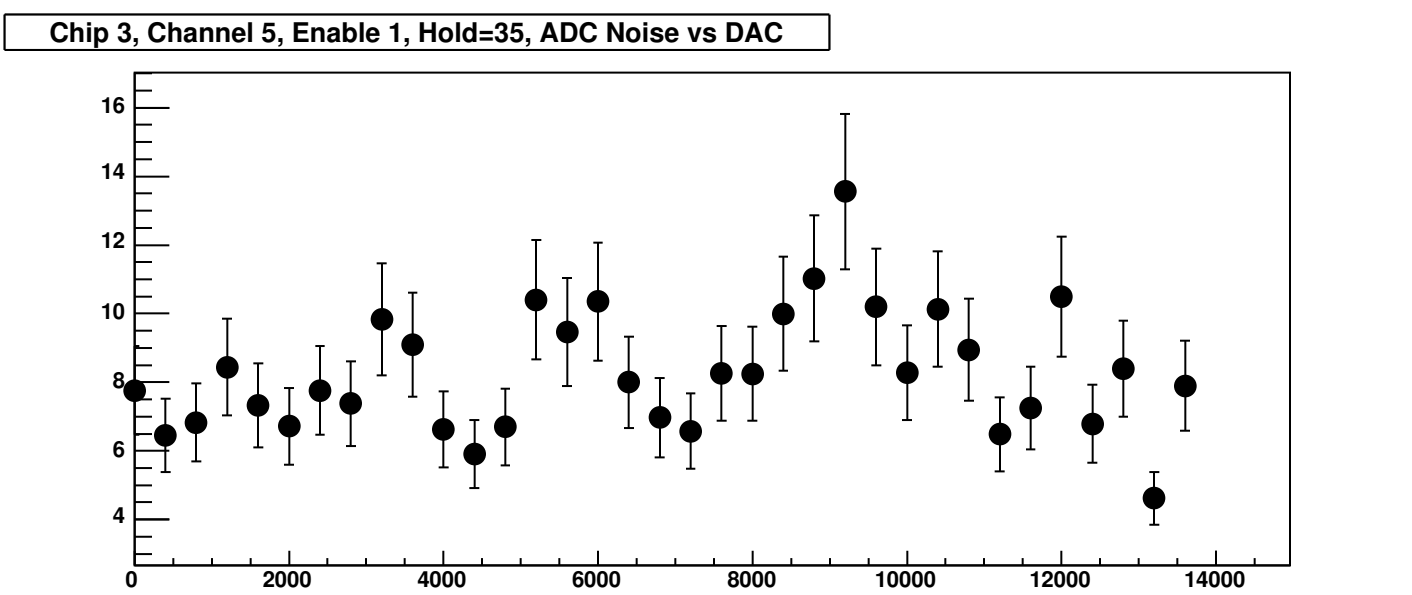


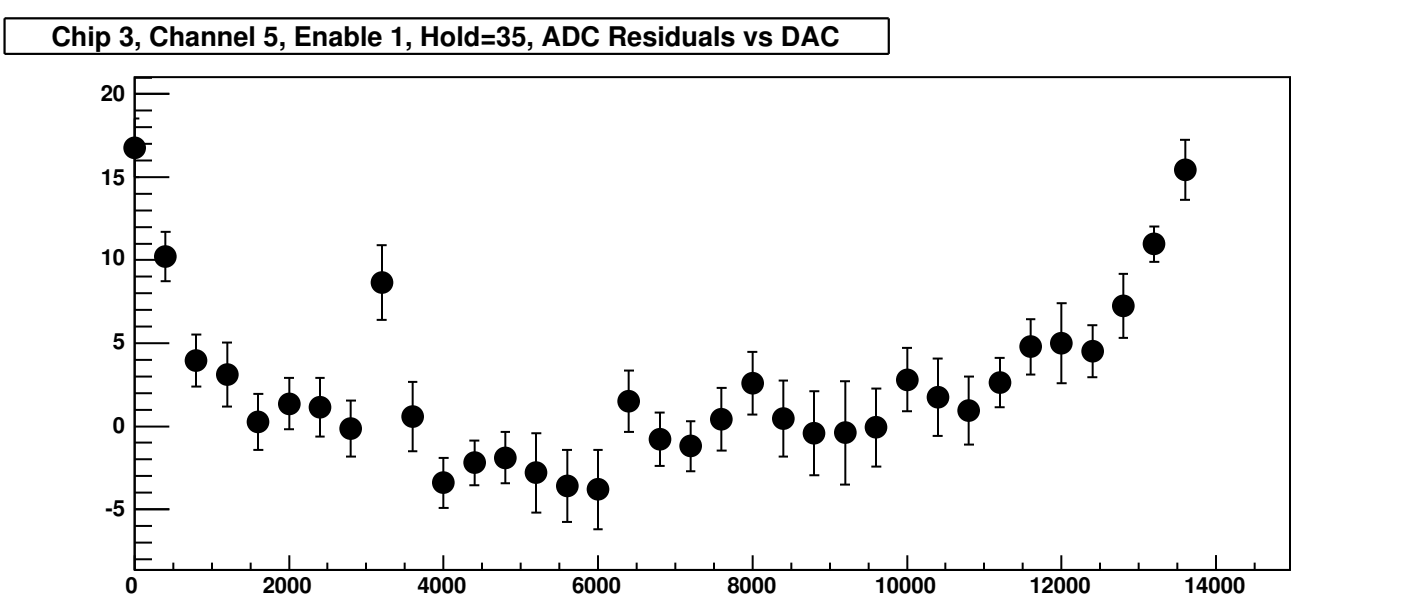

Chip 3, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1369 / 23
p0 -855.8 ± 1.21
p1 0.1673 ± 0.0001939
1400
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

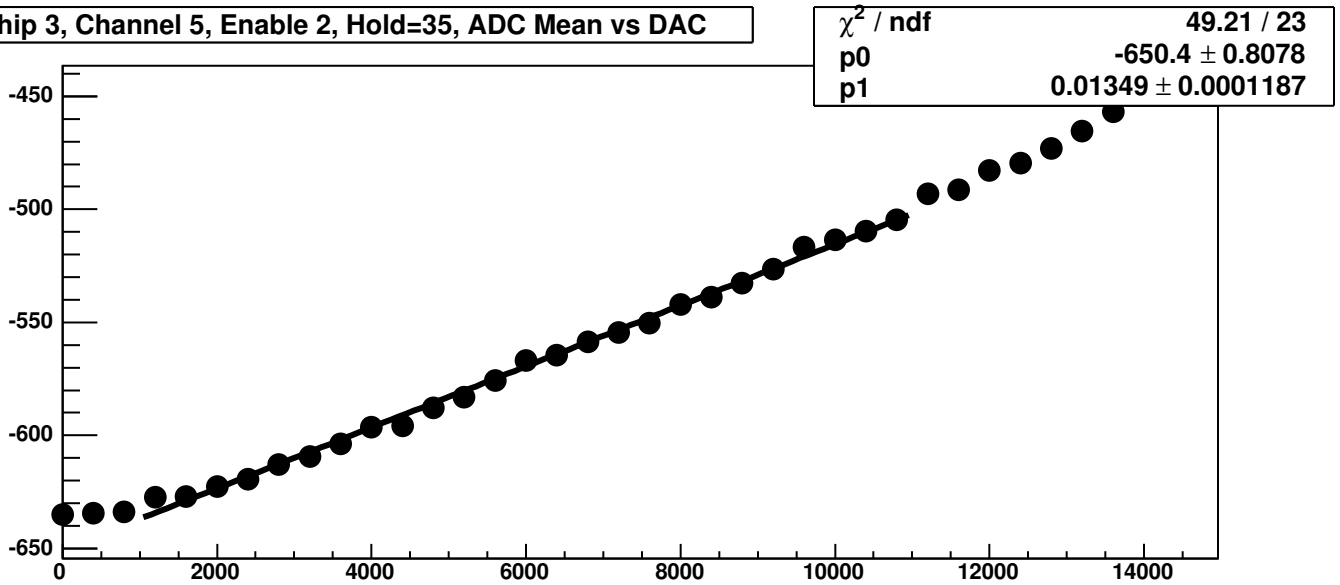

Chip 3, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 40.97 / 23
p0 -651.1 ± 0.8363
p1 0.01988 ± 0.0001355
-350
-400
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

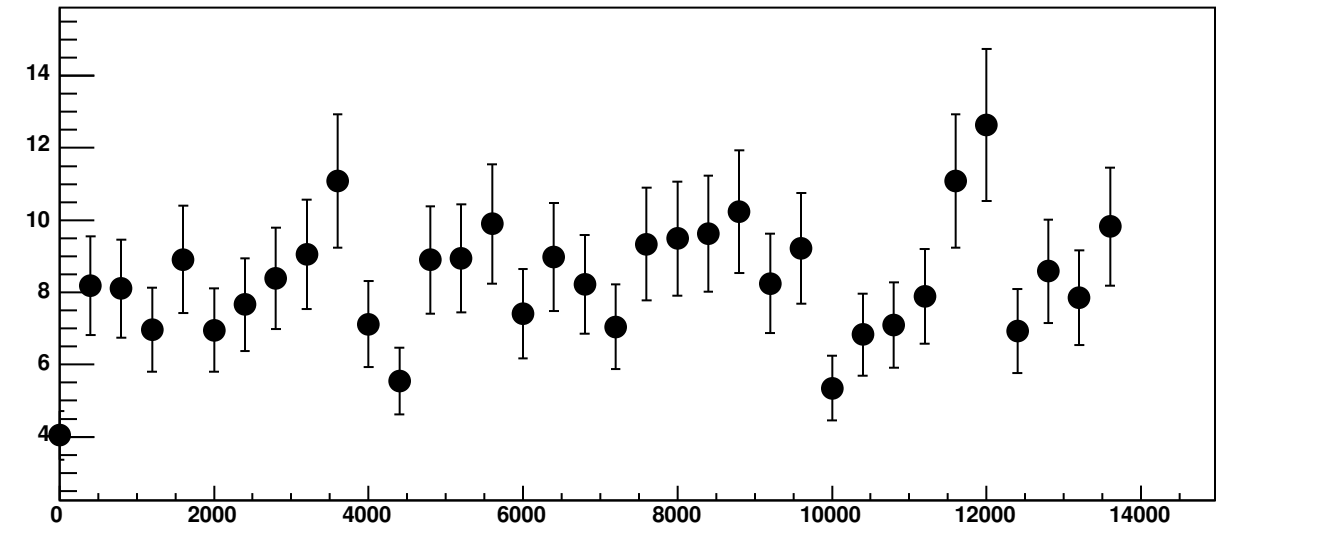

Chip 3, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

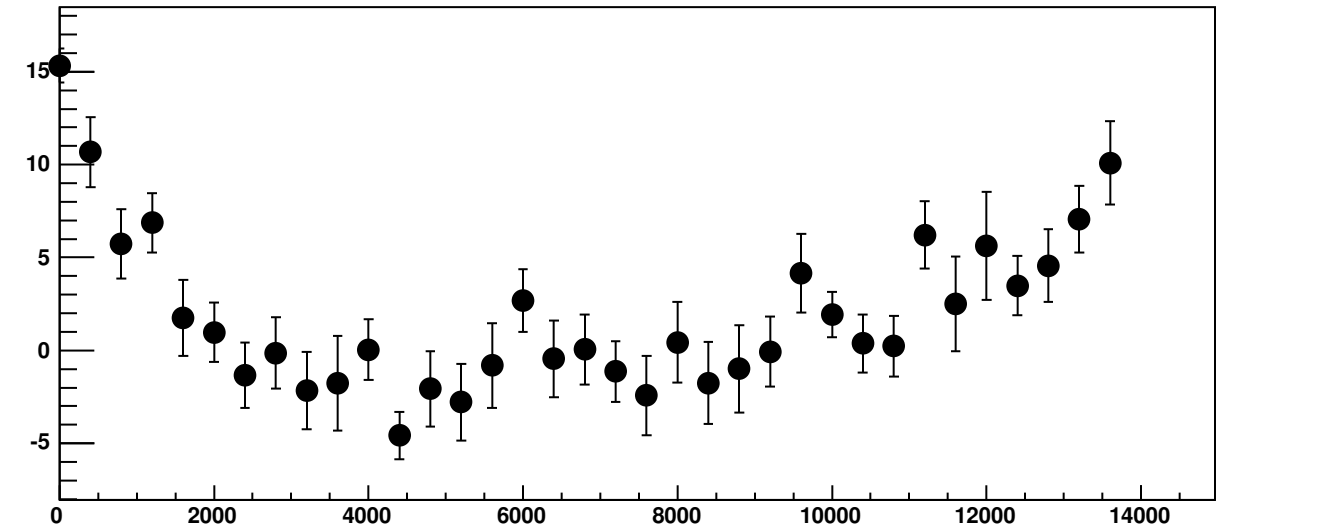
Chip 3, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

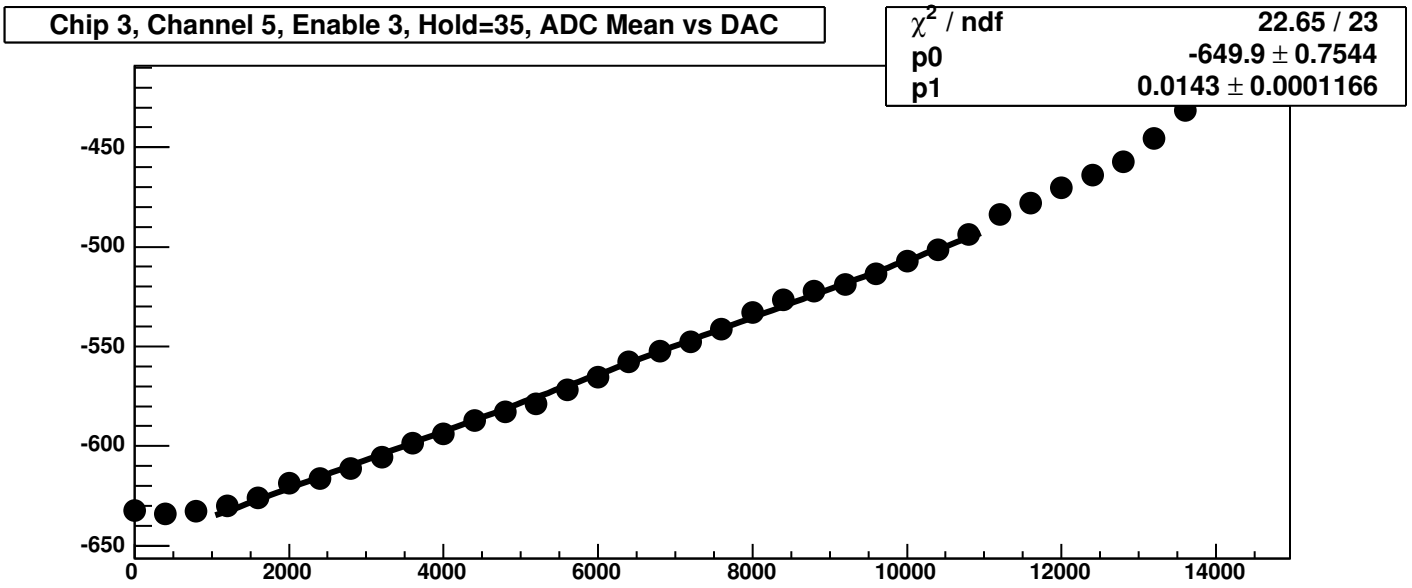


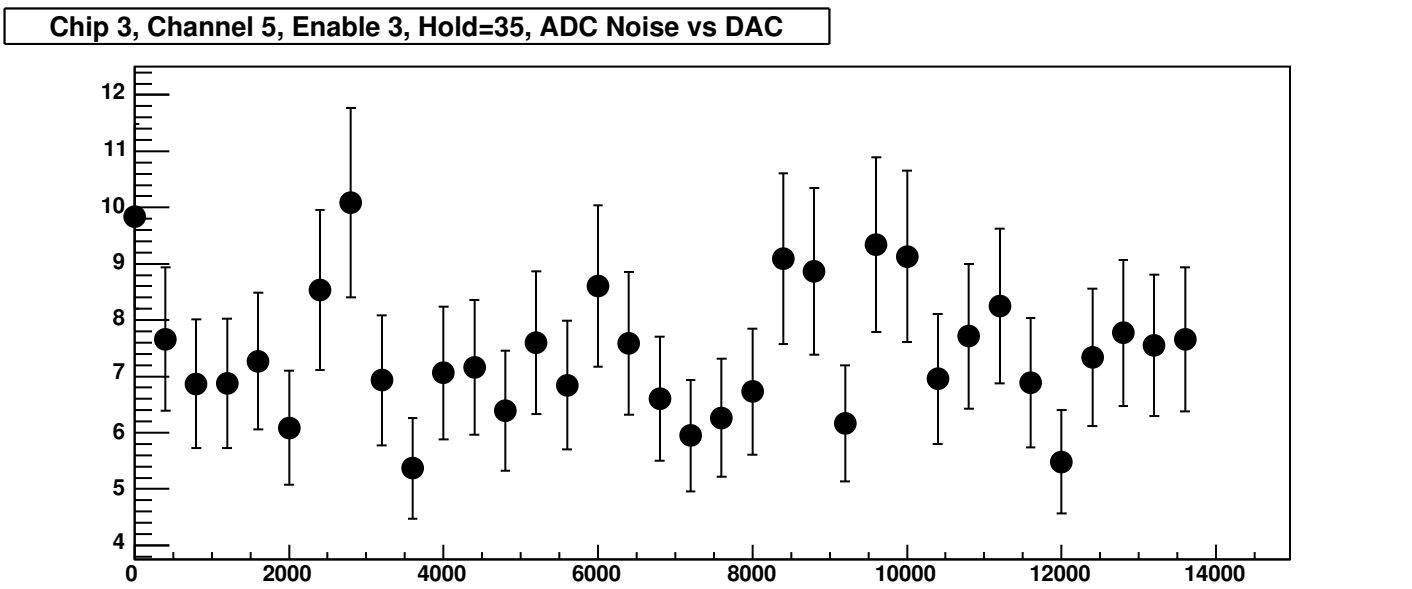
Chip 3, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC

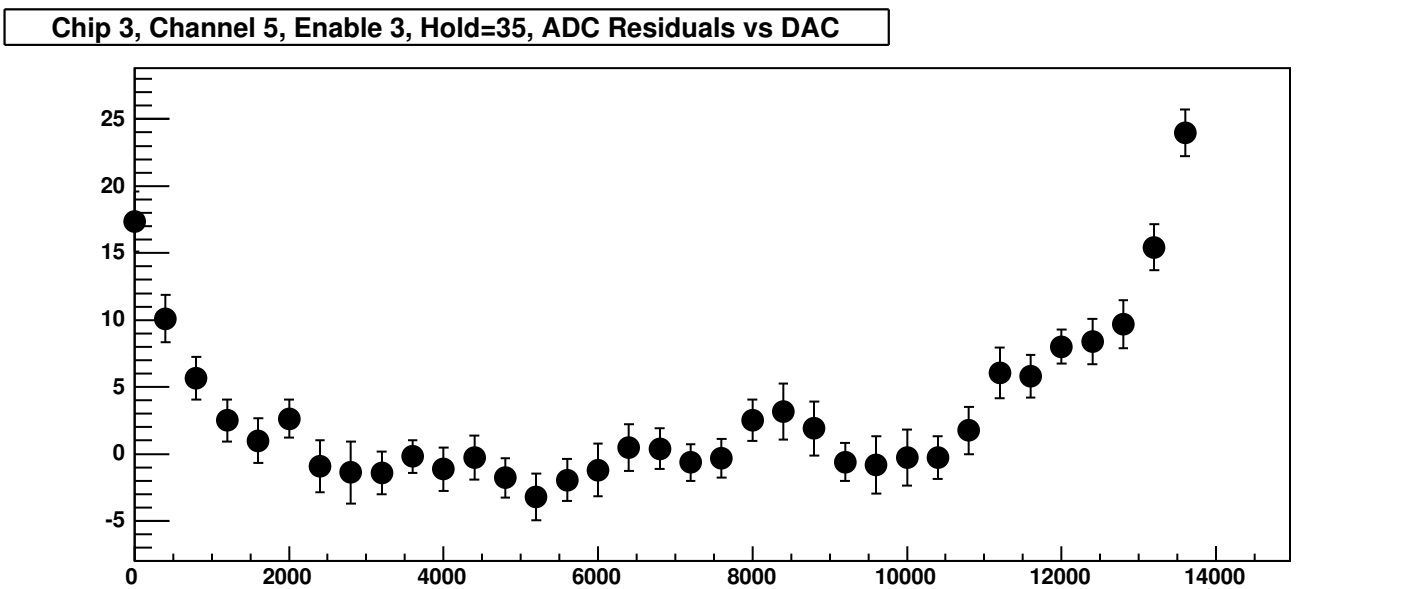


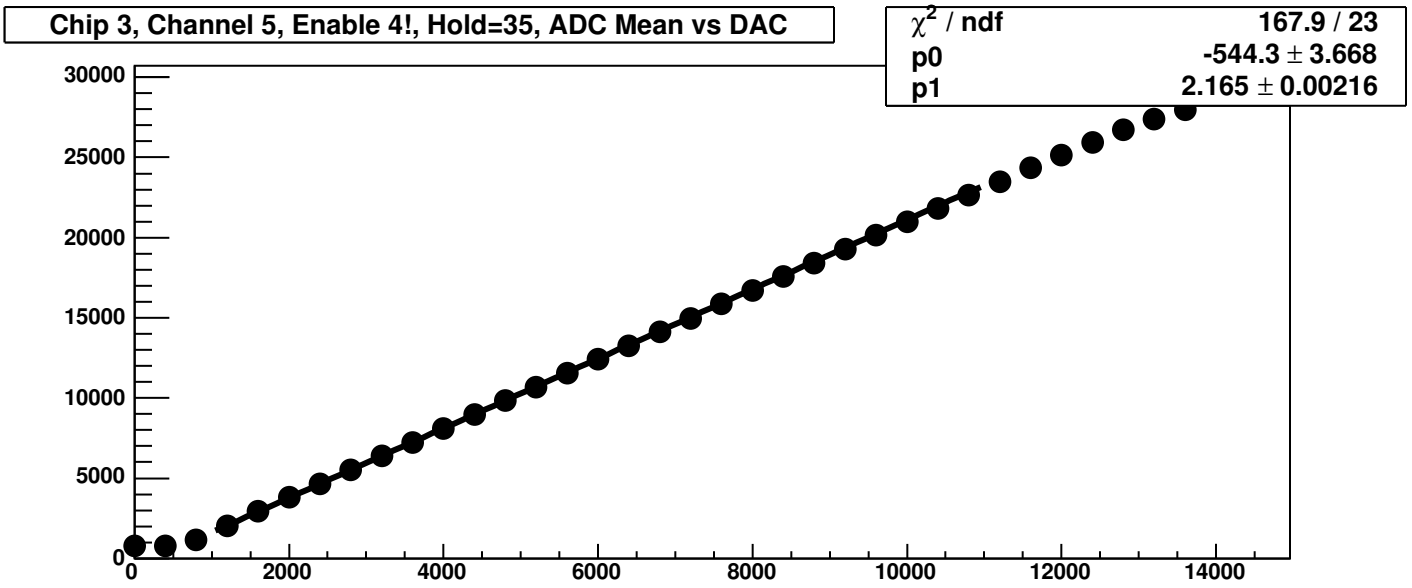
Chip 3, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

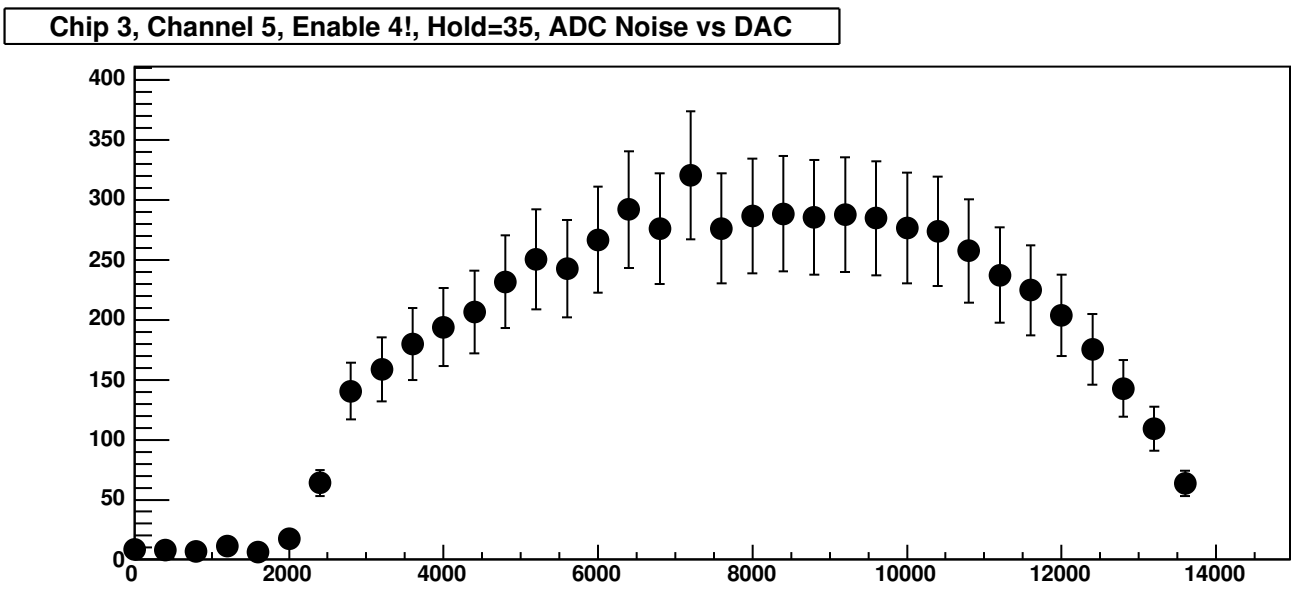


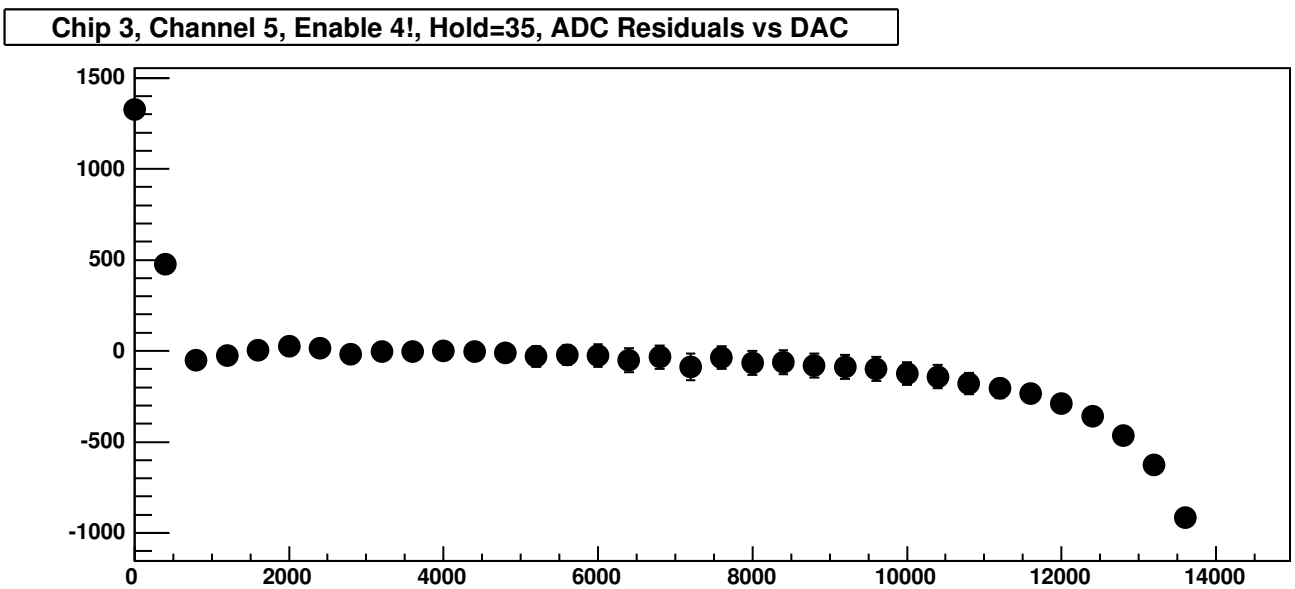

Chip 3, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 22.65 / 23
p0 -649.9 ± 0.7544
p1 0.0143 ± 0.0001166
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

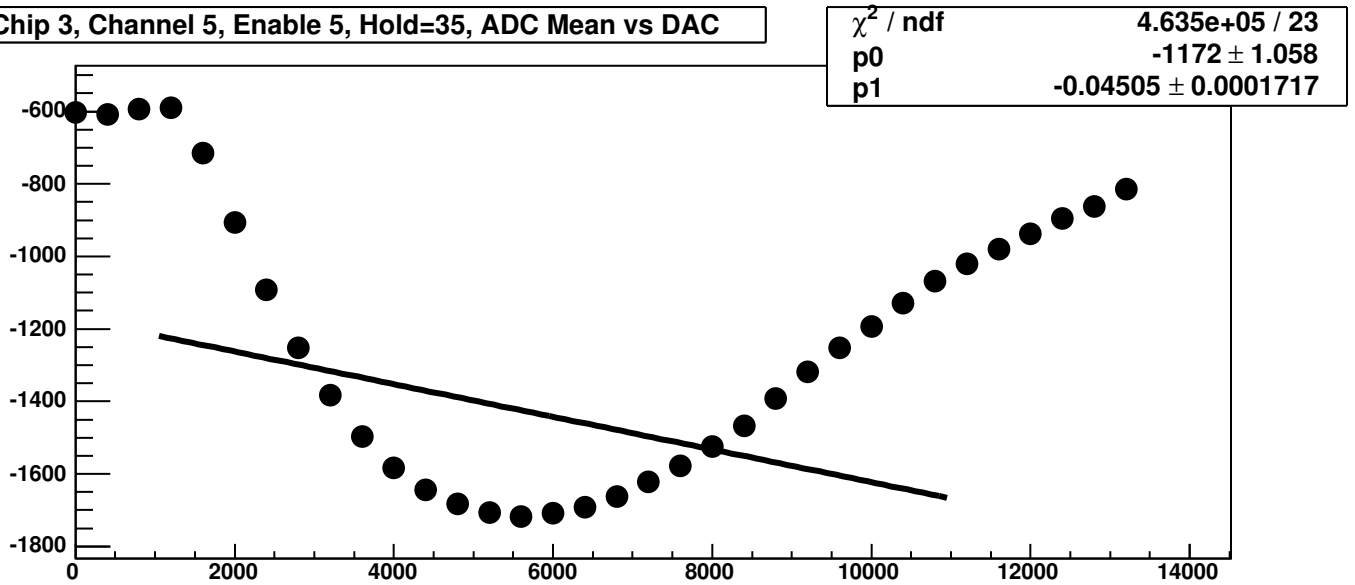

Chip 3, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 167.9 / 23
p0 -544.3 ± 3.668
p1 2.165 ± 0.00216
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

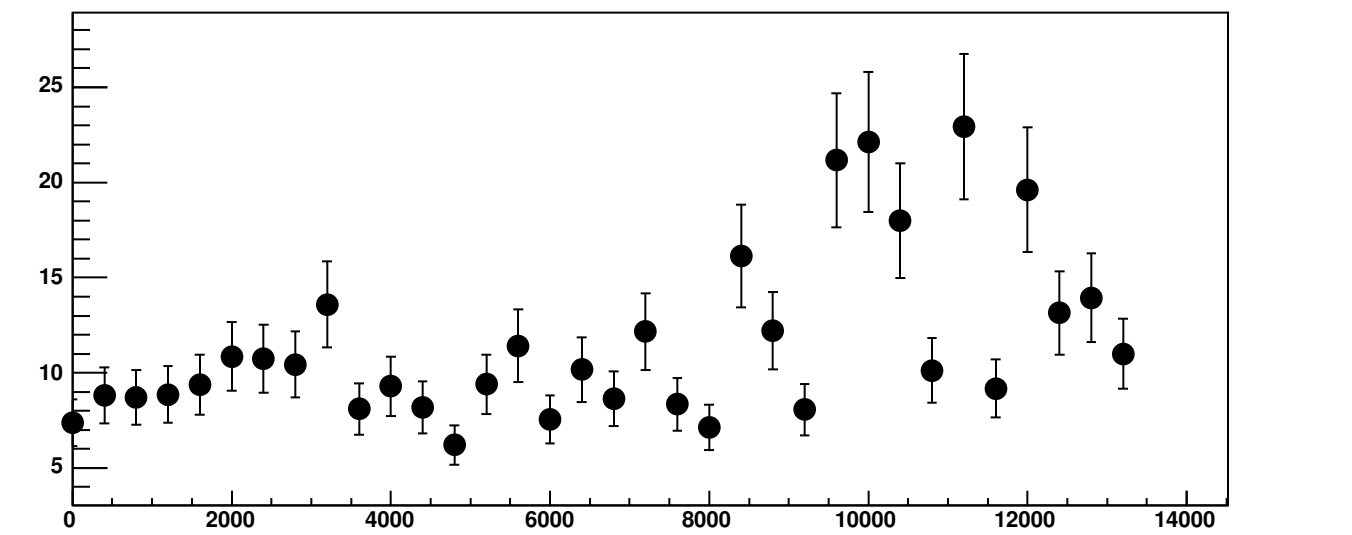

Chip 3, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

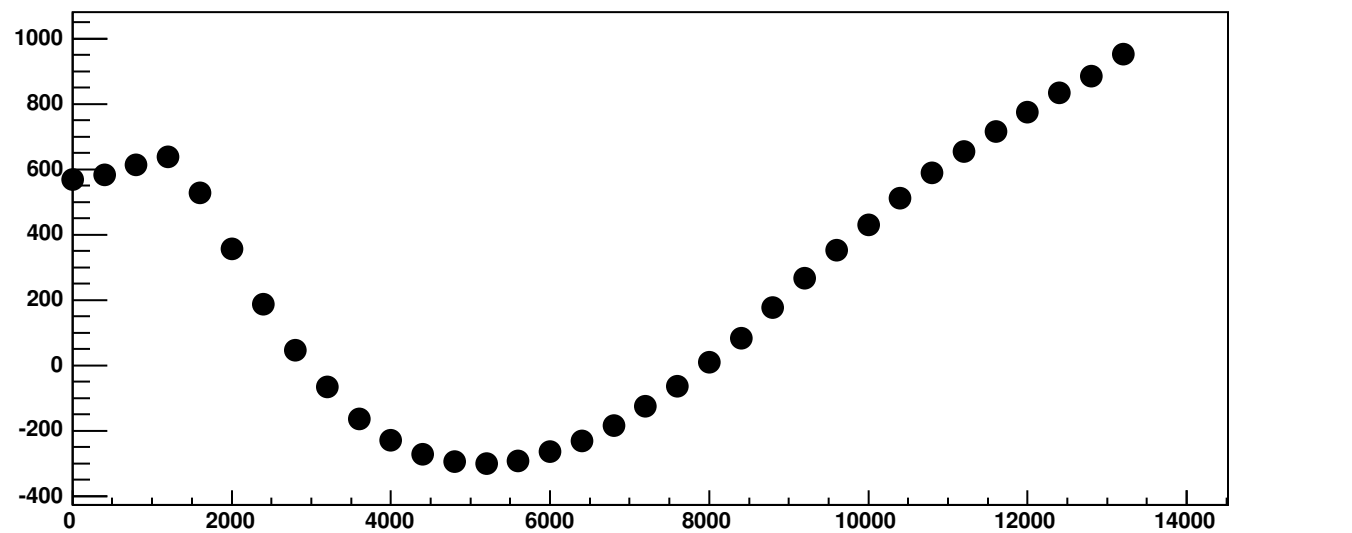
Chip 3, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

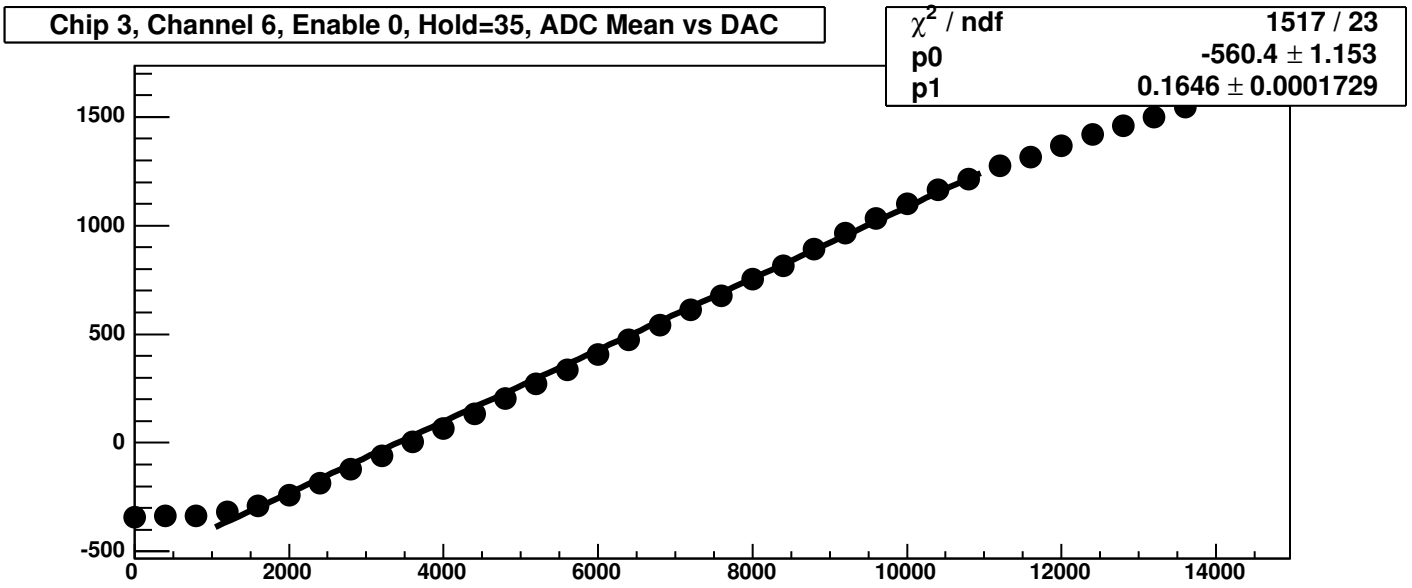


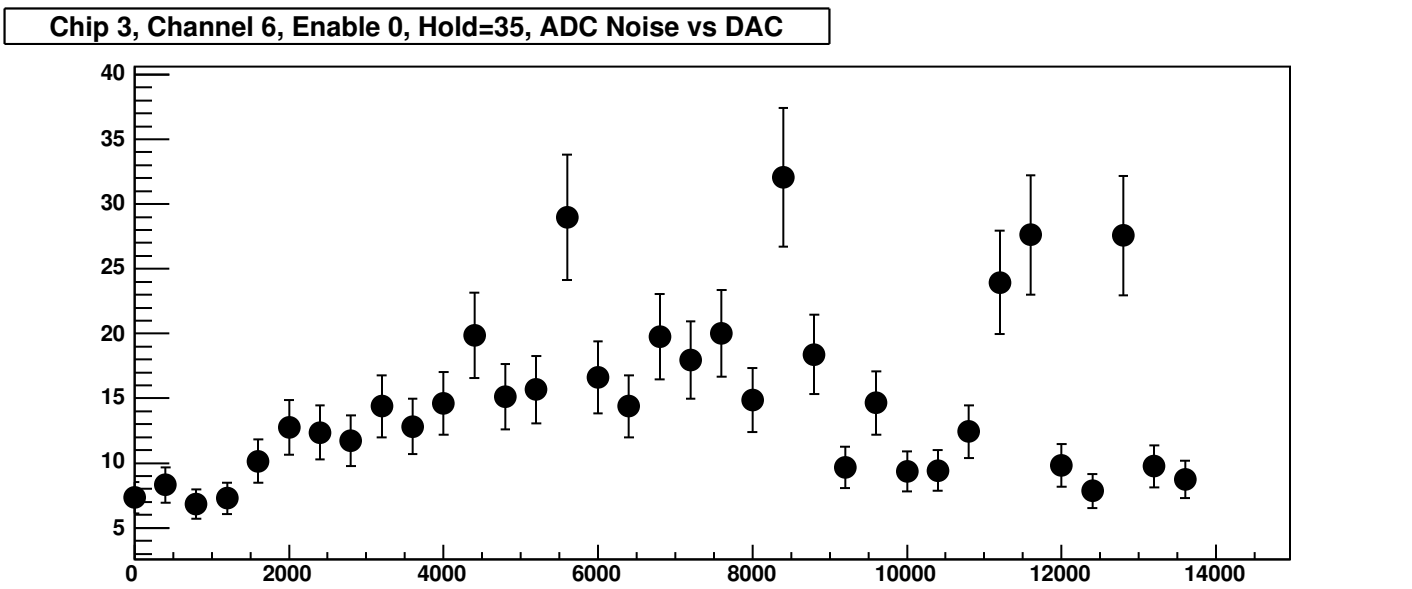
Chip 3, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC

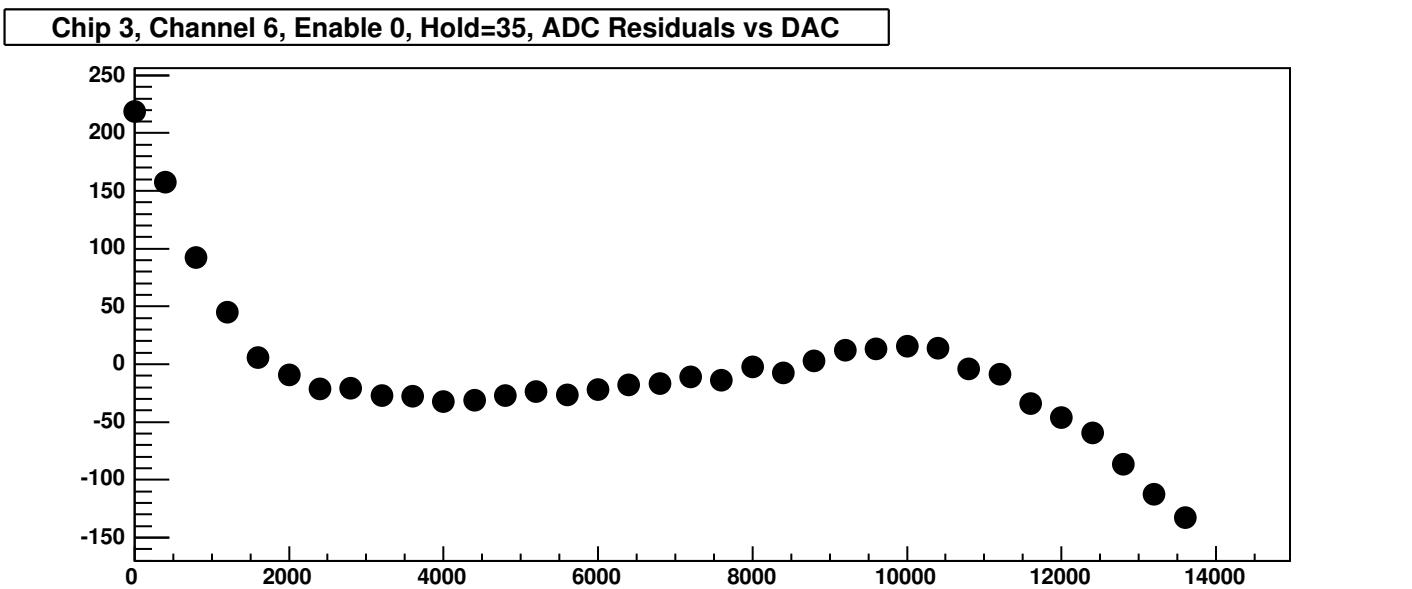


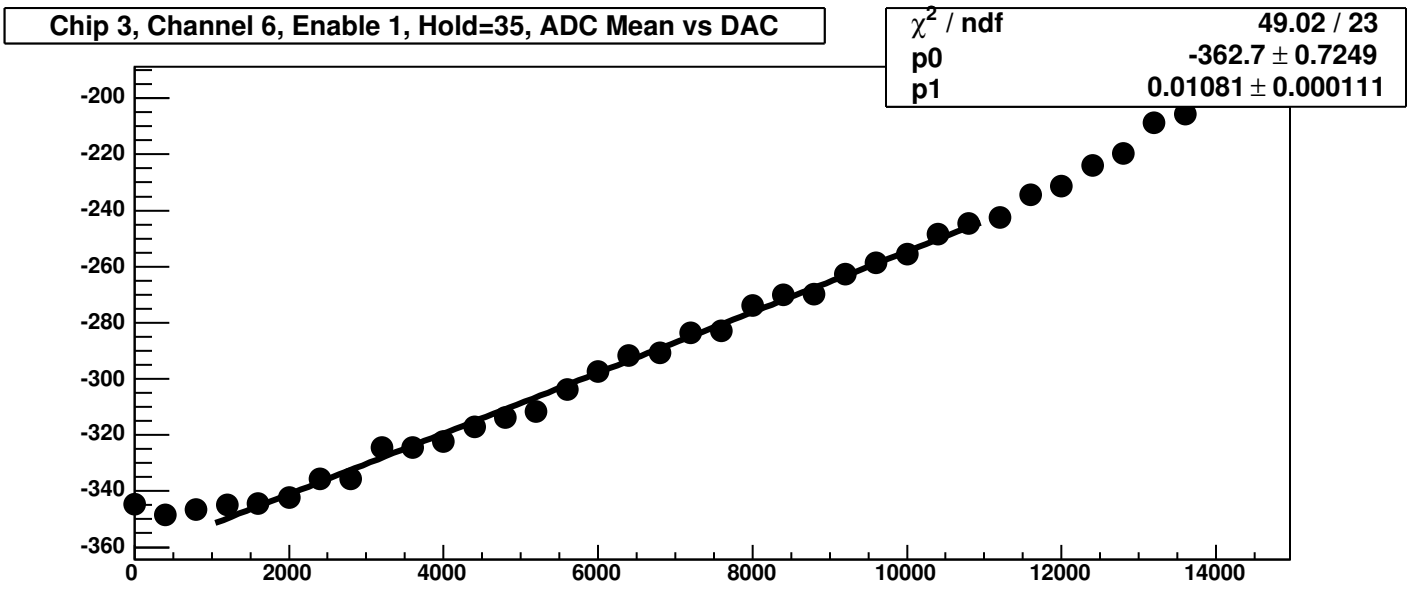
Chip 3, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

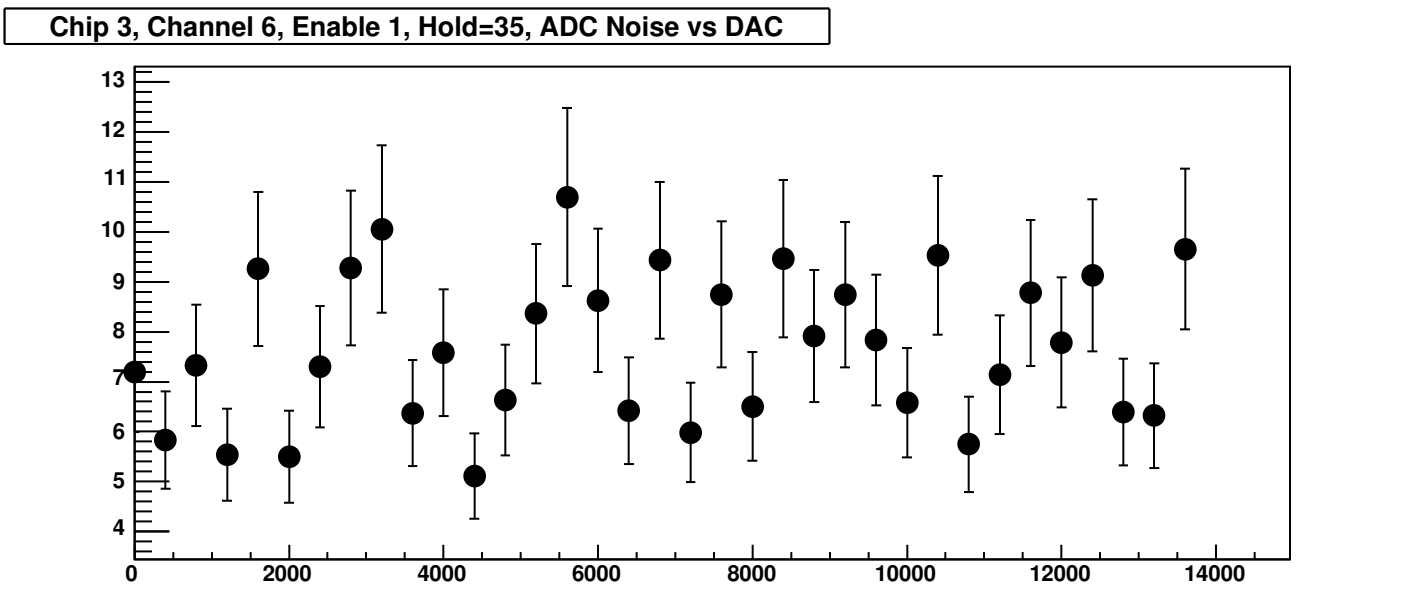


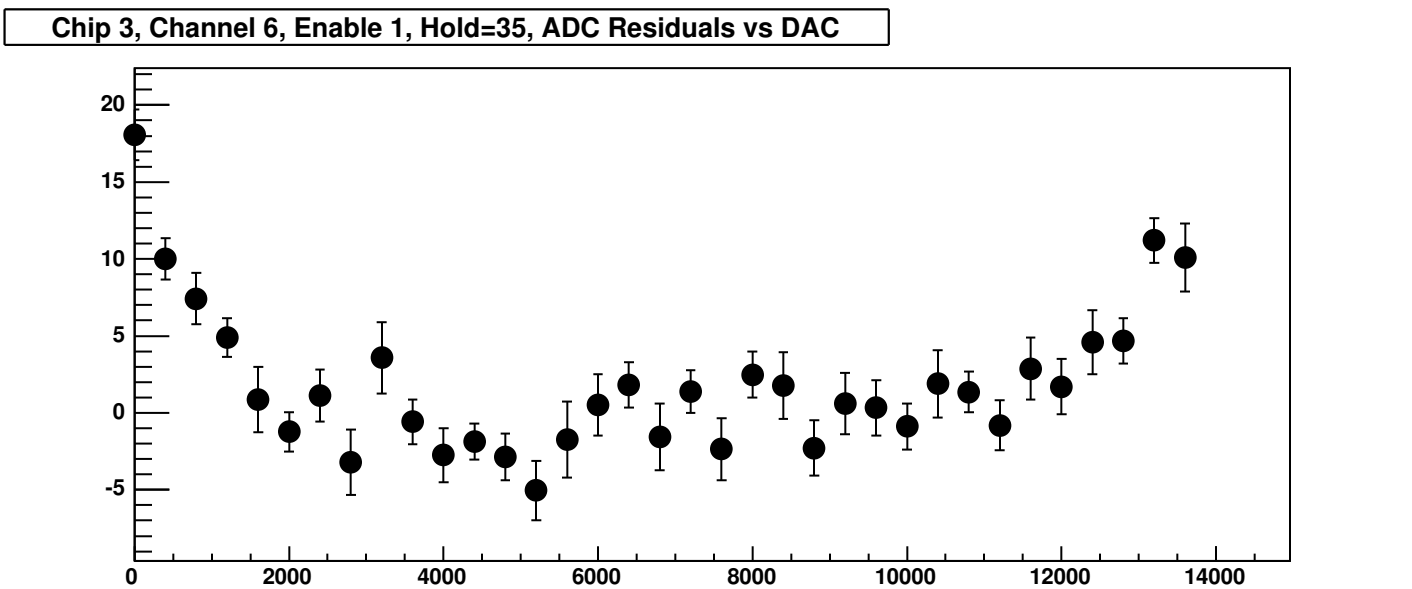

Chip 3, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1517 / 23
p0 -560.4 ± 1.153
p1 0.1646 ± 0.0001729
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

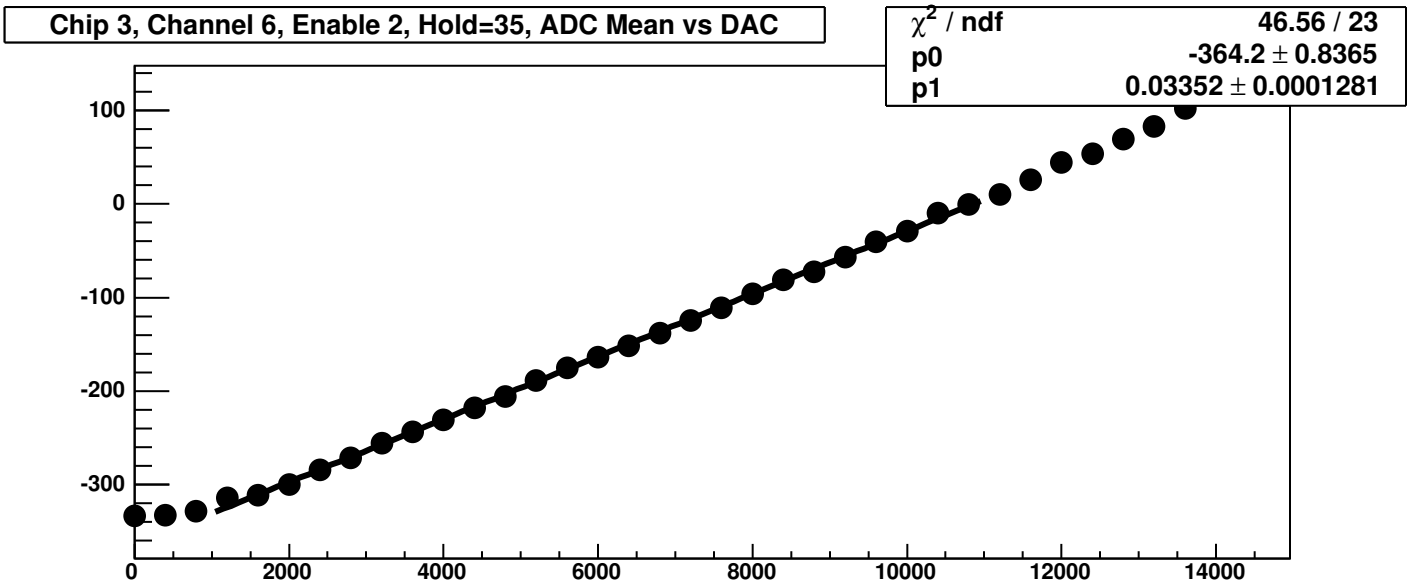

Chip 3, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

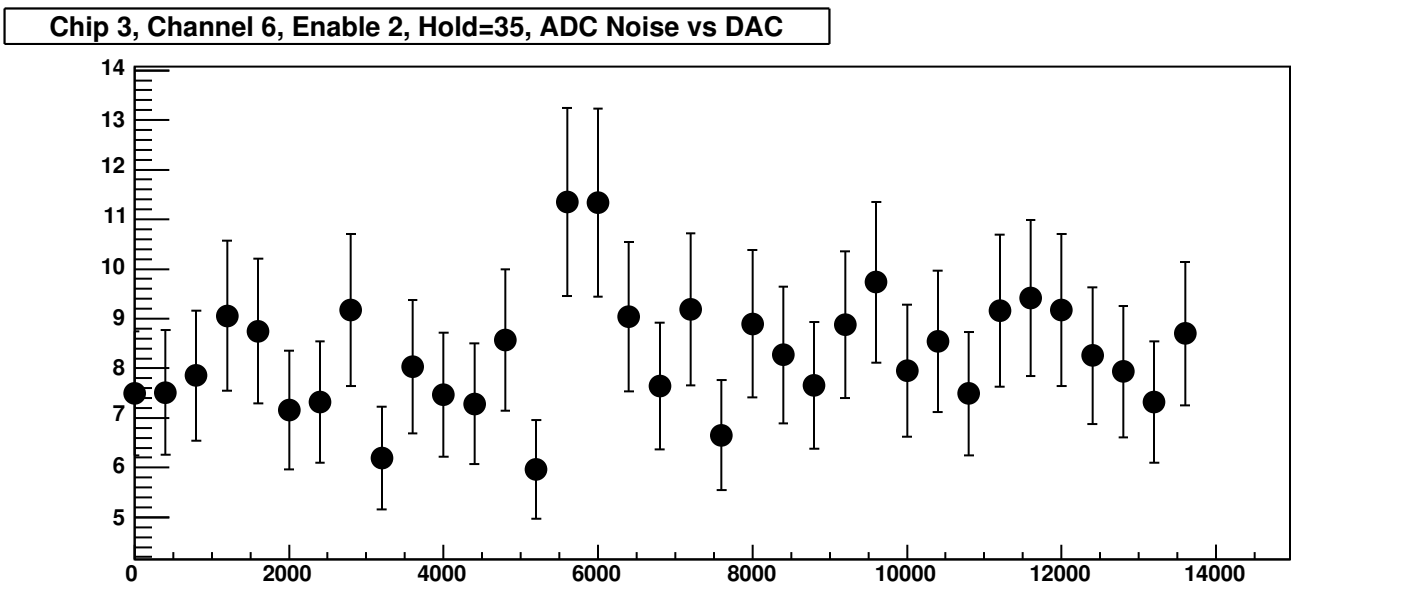

Chip 3, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

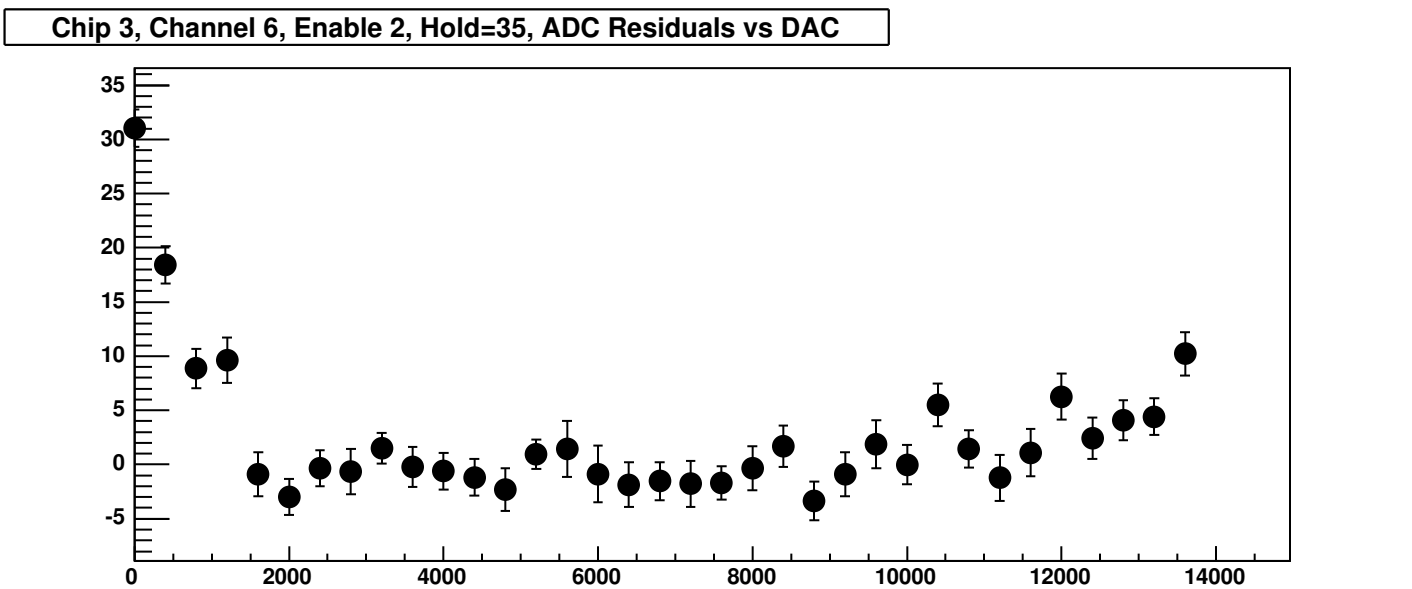

Chip 3, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 49.02 / 23
p0 -362.7 ± 0.7249
p1 0.01081 ± 0.000111
-200
-220
-240
-260
-280
-300
-320
-340
-360
0
2000
4000
6000
8000
10000
12000
14000

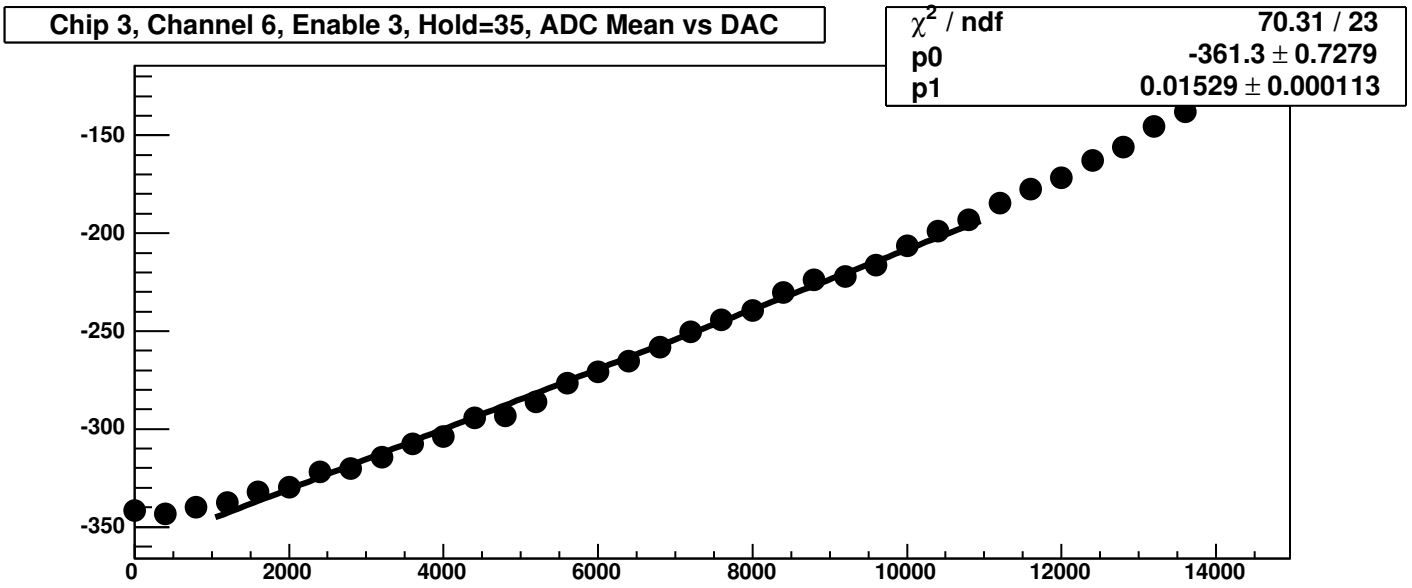

Chip 3, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

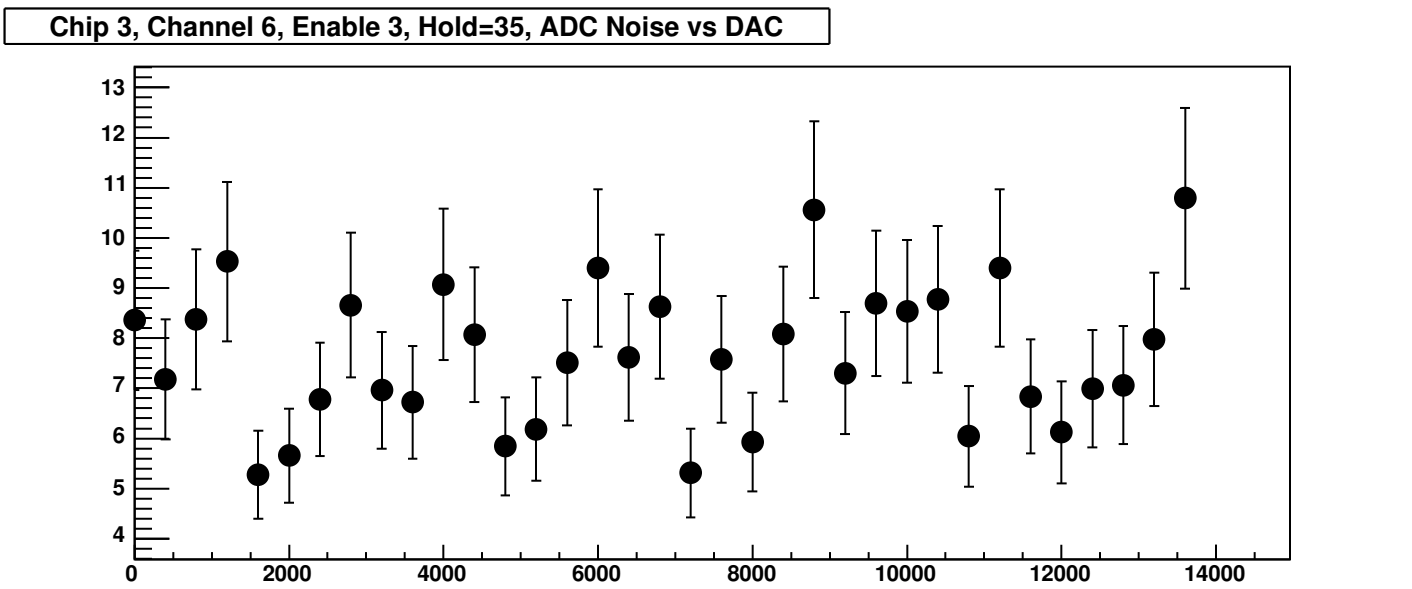

Chip 3, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

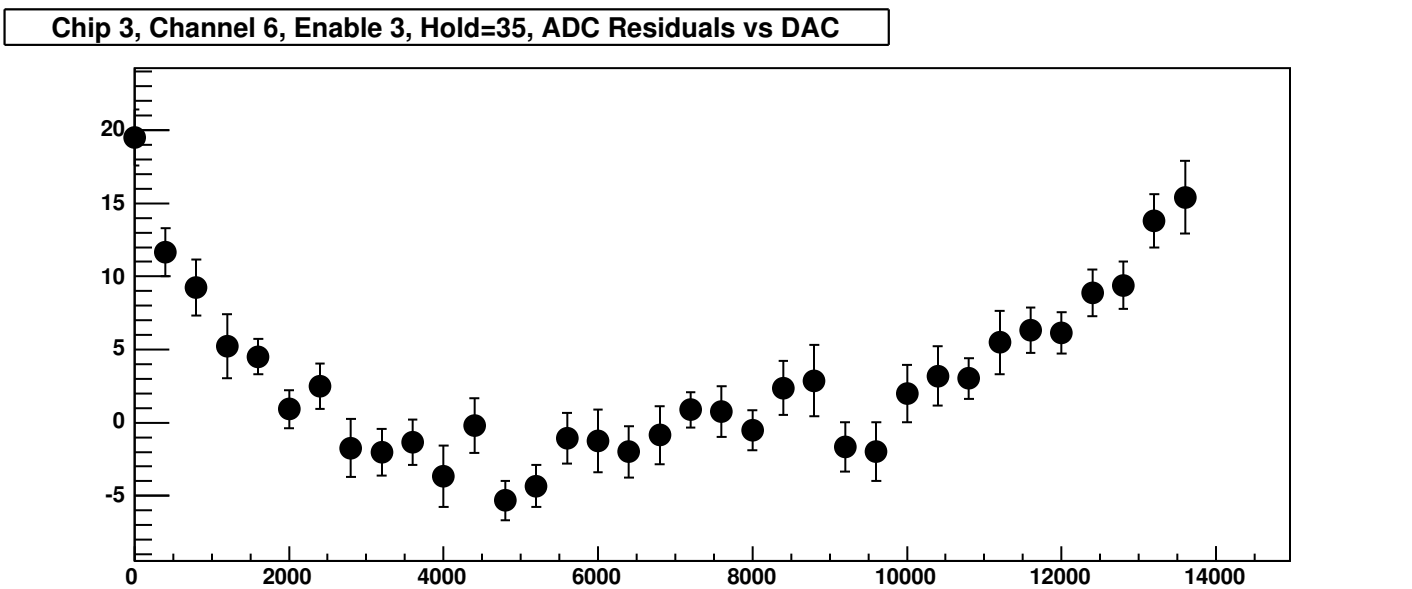

Chip 3, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 46.56 / 23
p0 -364.2 ± 0.8365
p1 0.03352 ± 0.0001281
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

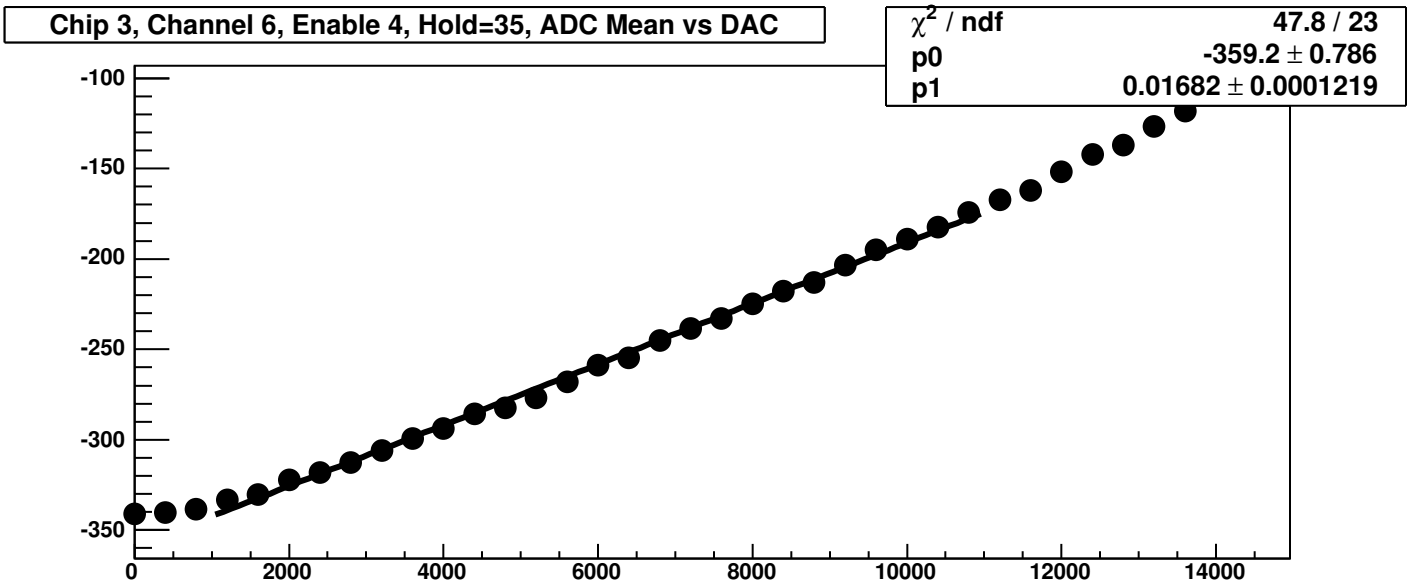

Chip 3, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC
14
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

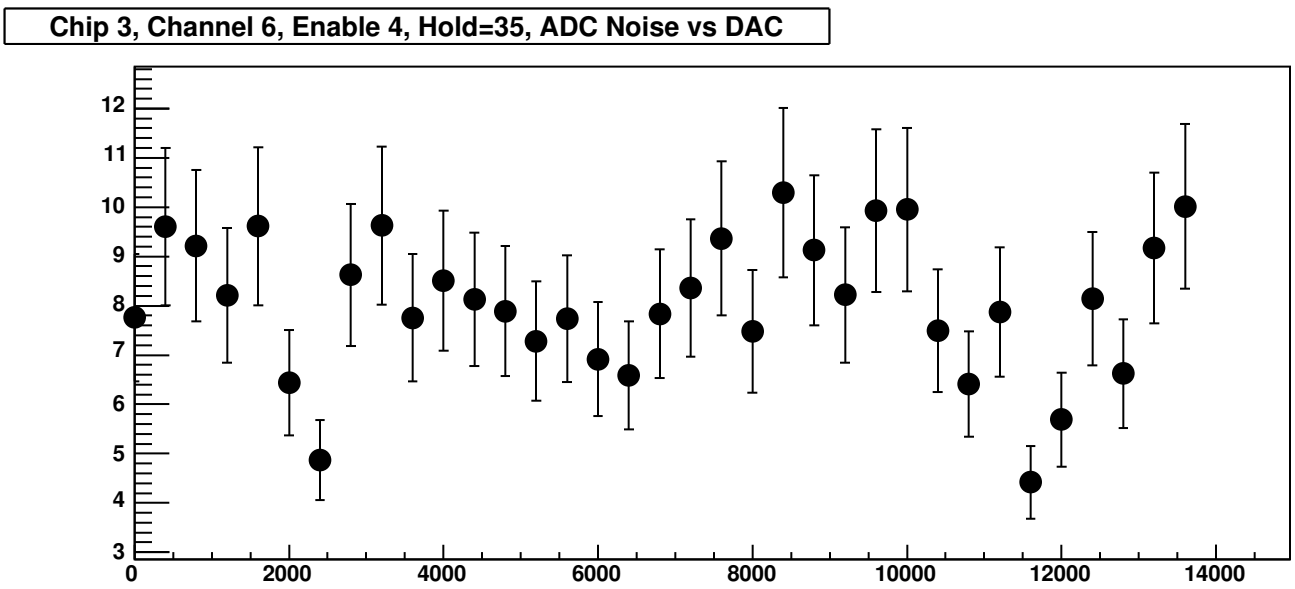

Chip 3, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

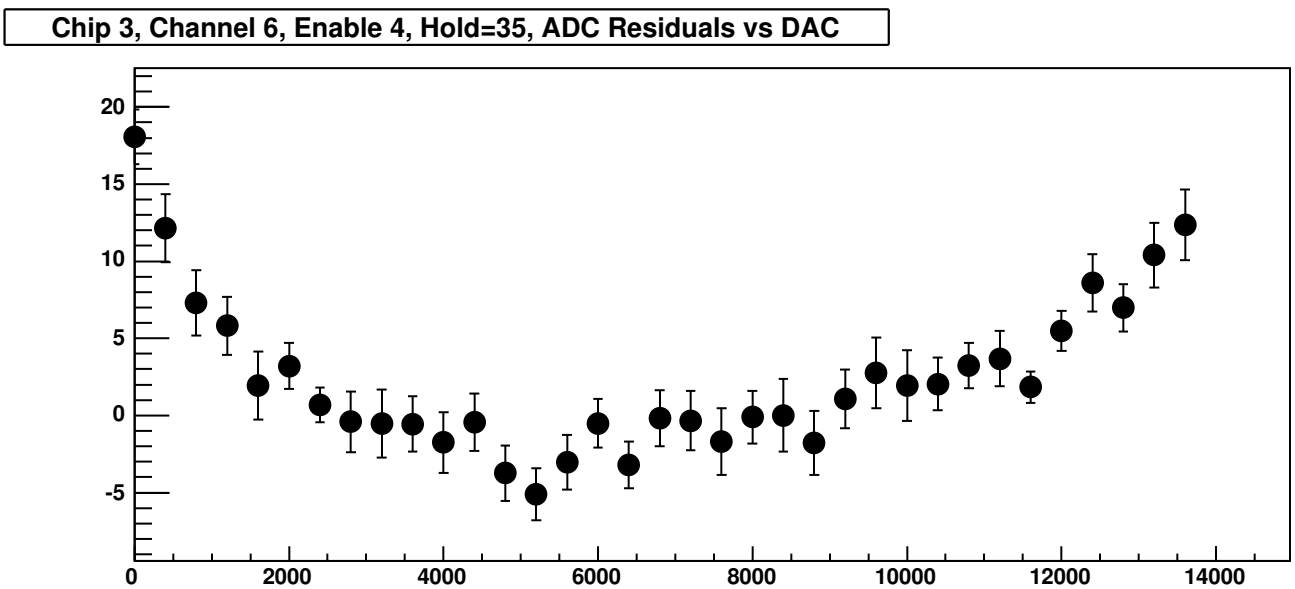

Chip 3, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 70.31 / 23
p0 -361.3 ± 0.7279
p1 0.01529 ± 0.000113
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

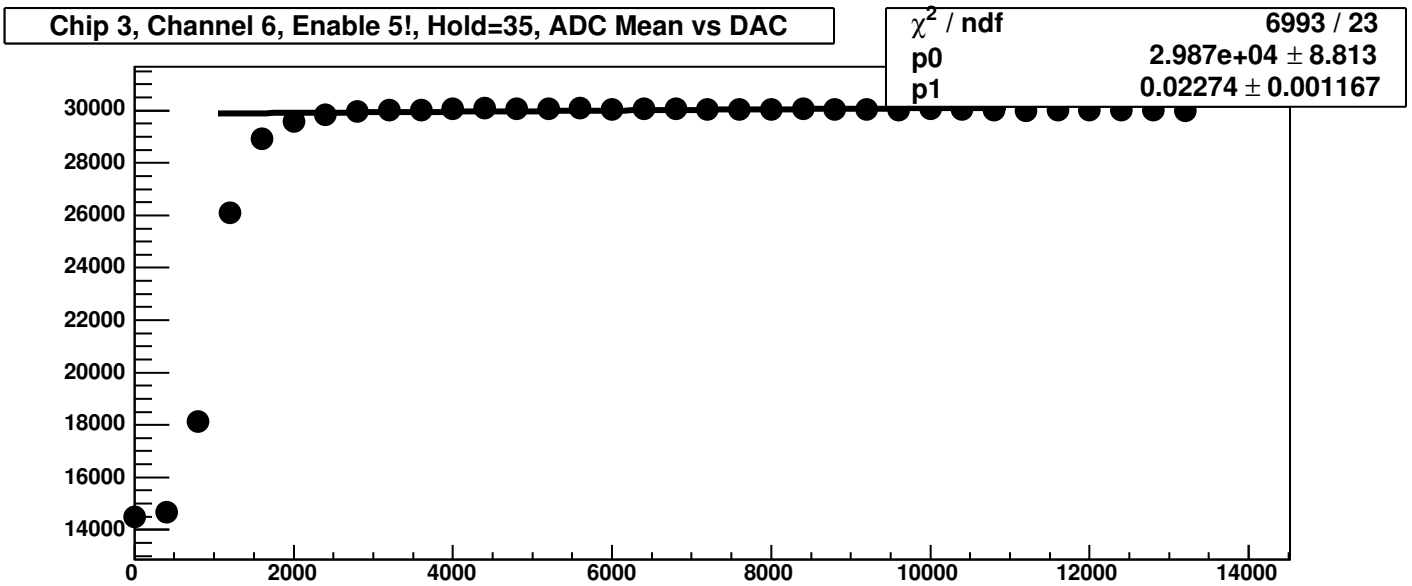

Chip 3, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

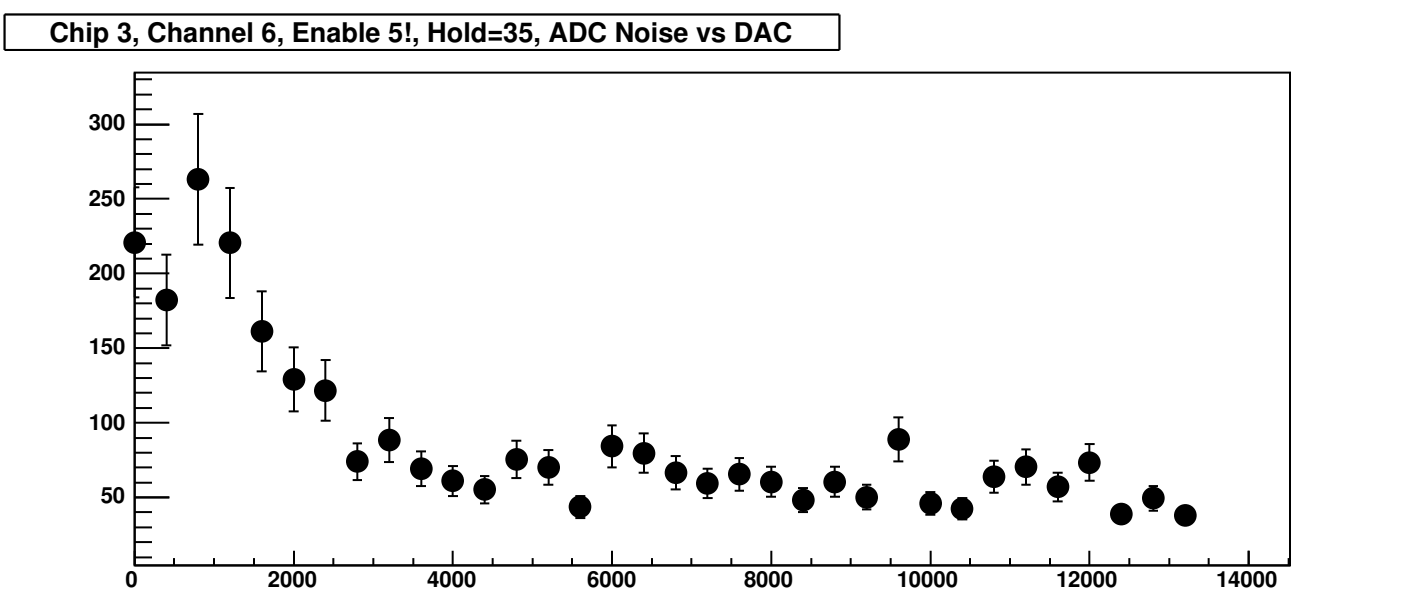

Chip 3, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

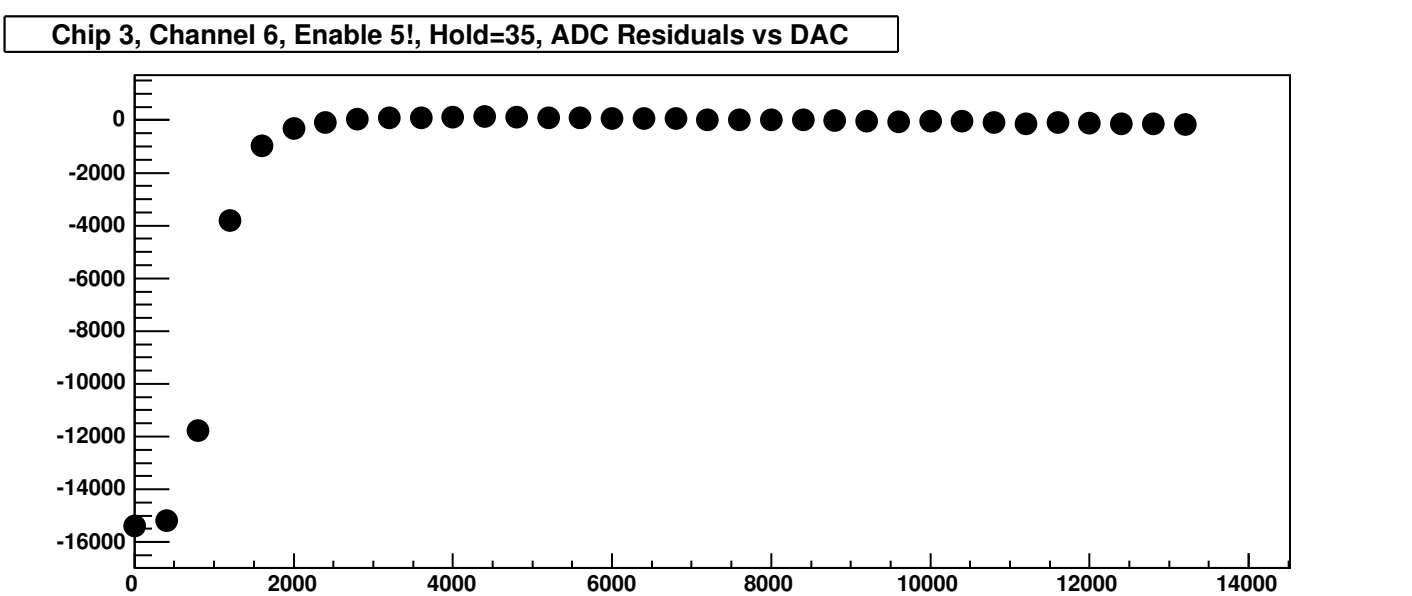

Chip 3, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 47.8 / 23
p0 -359.2 ± 0.786
p1 0.01682 ± 0.0001219
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

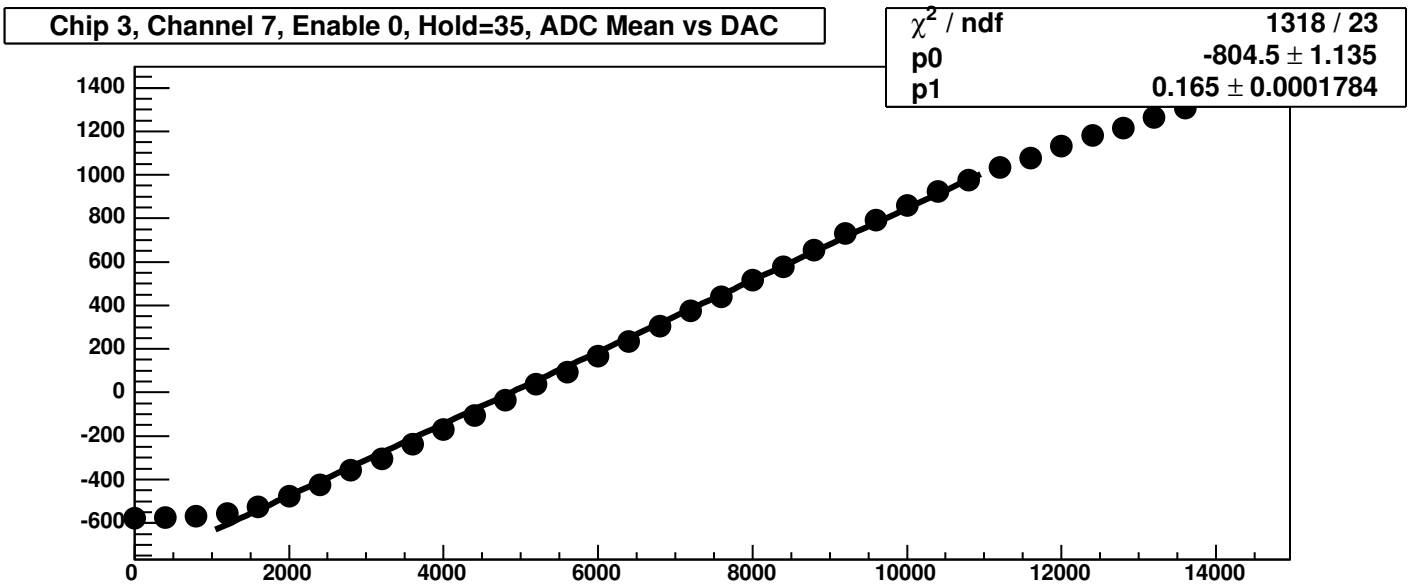

Chip 3, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

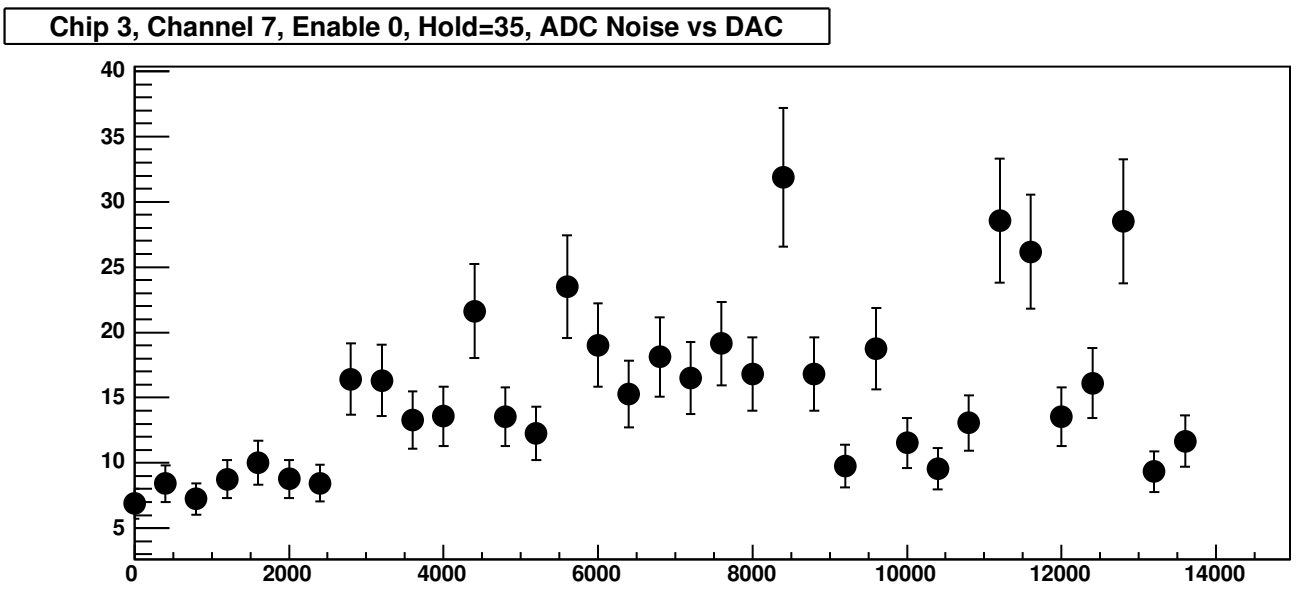

Chip 3, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

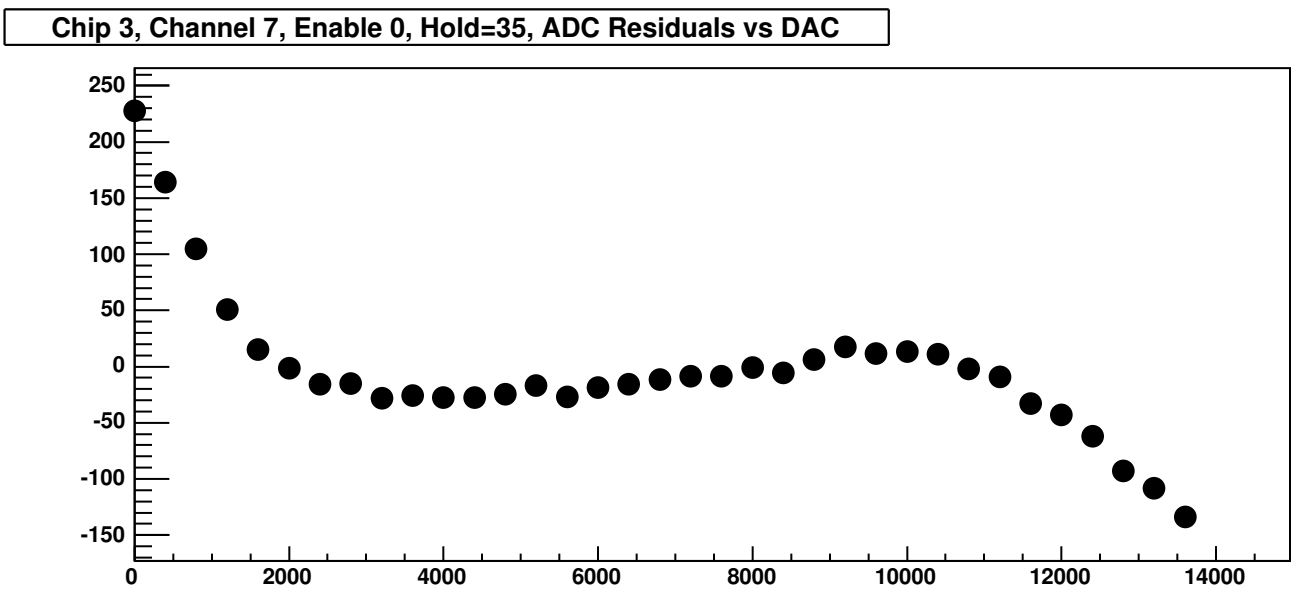

Chip 3, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 6993 / 23
p0 2.987e+04 ± 8.813
p1 0.02274 ± 0.001167
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

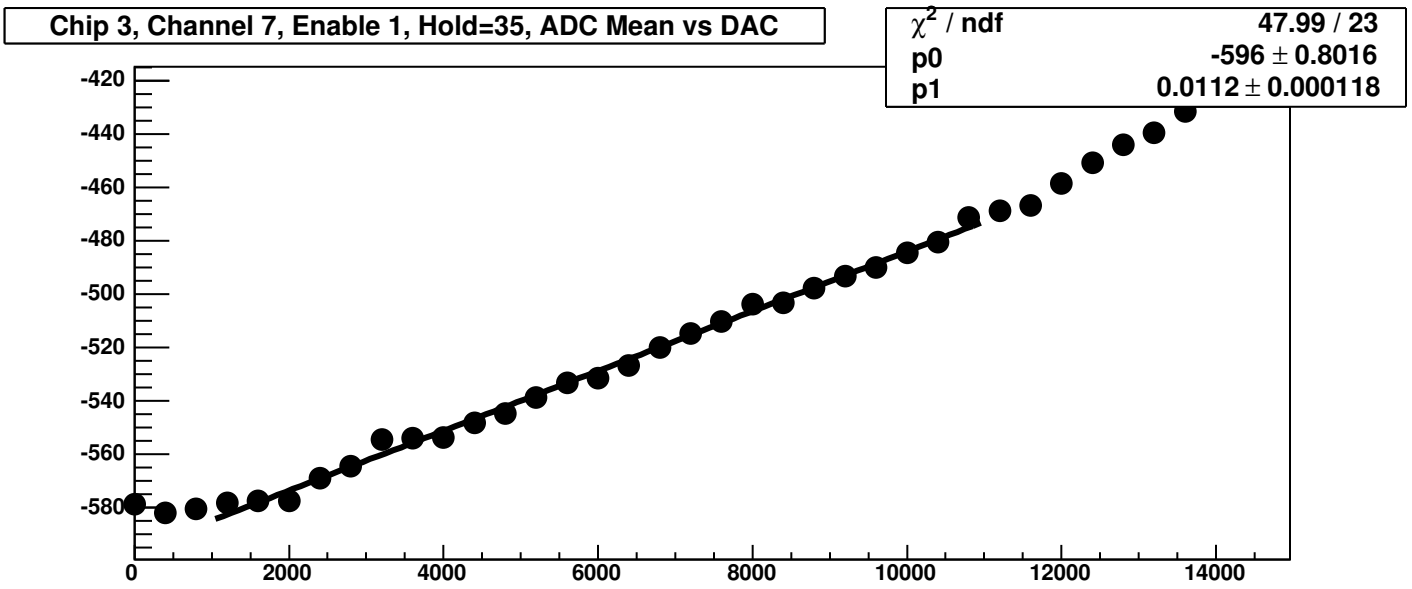

Chip 3, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

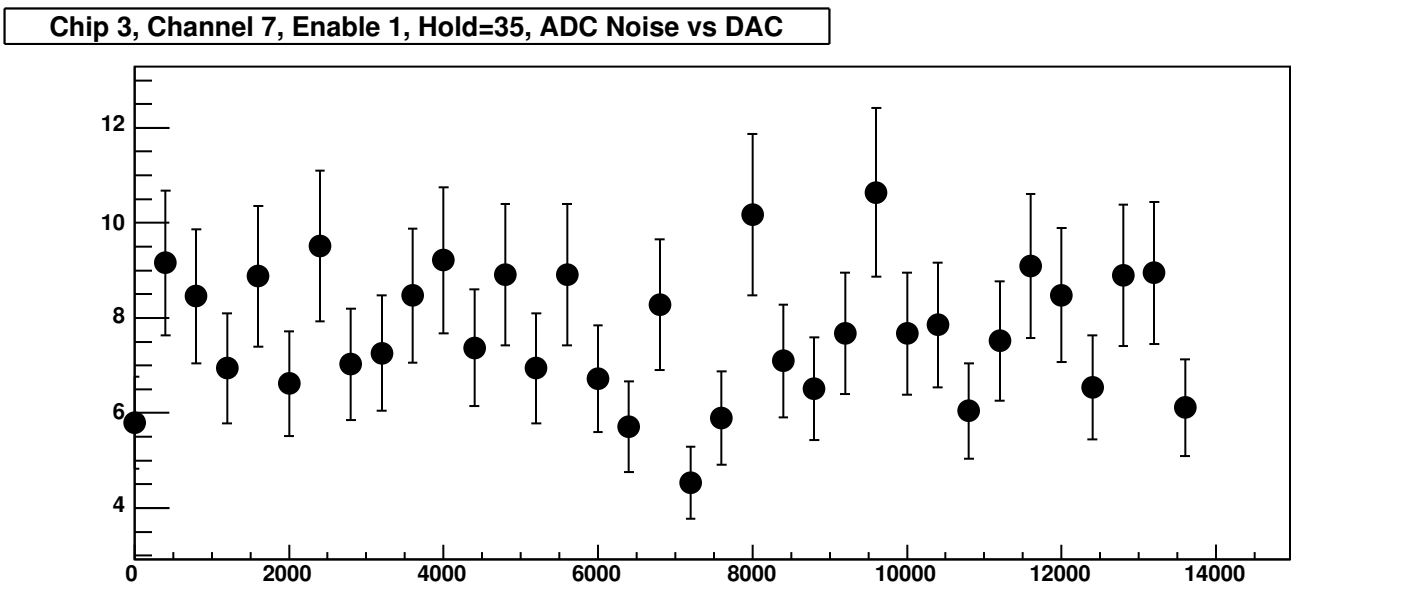

Chip 3, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

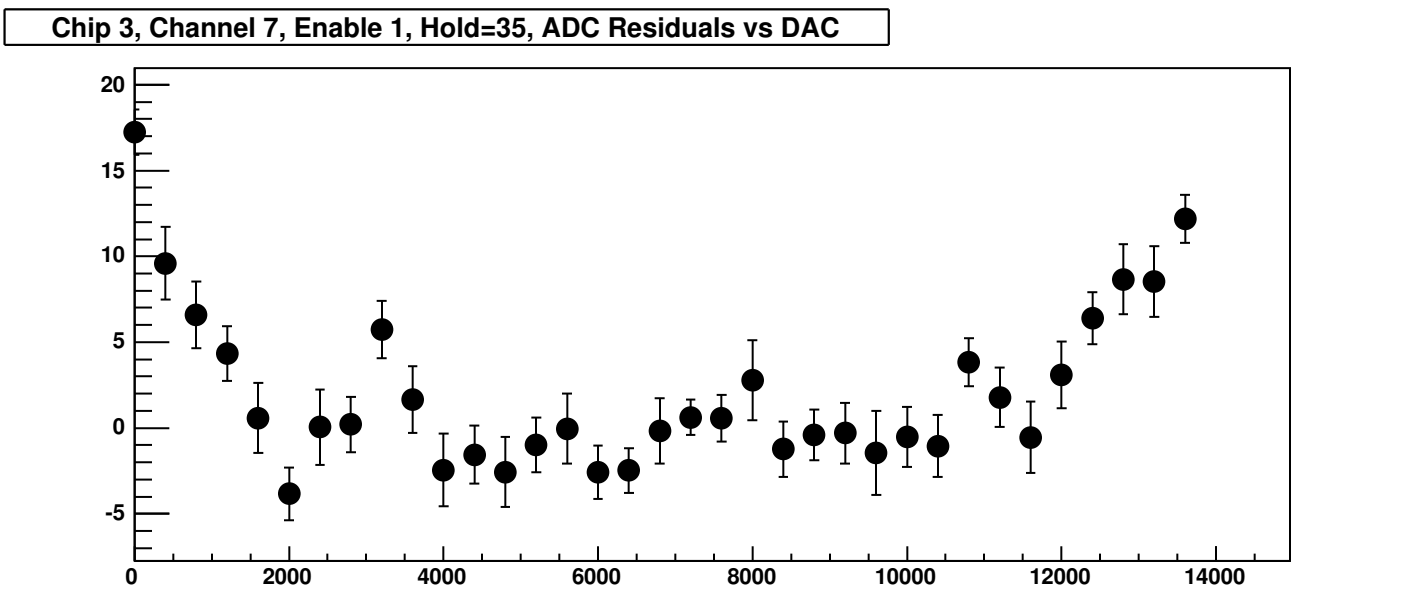

Chip 3, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1318 / 23
p0 -804.5 ± 1.135
p1 0.165 ± 0.0001784
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

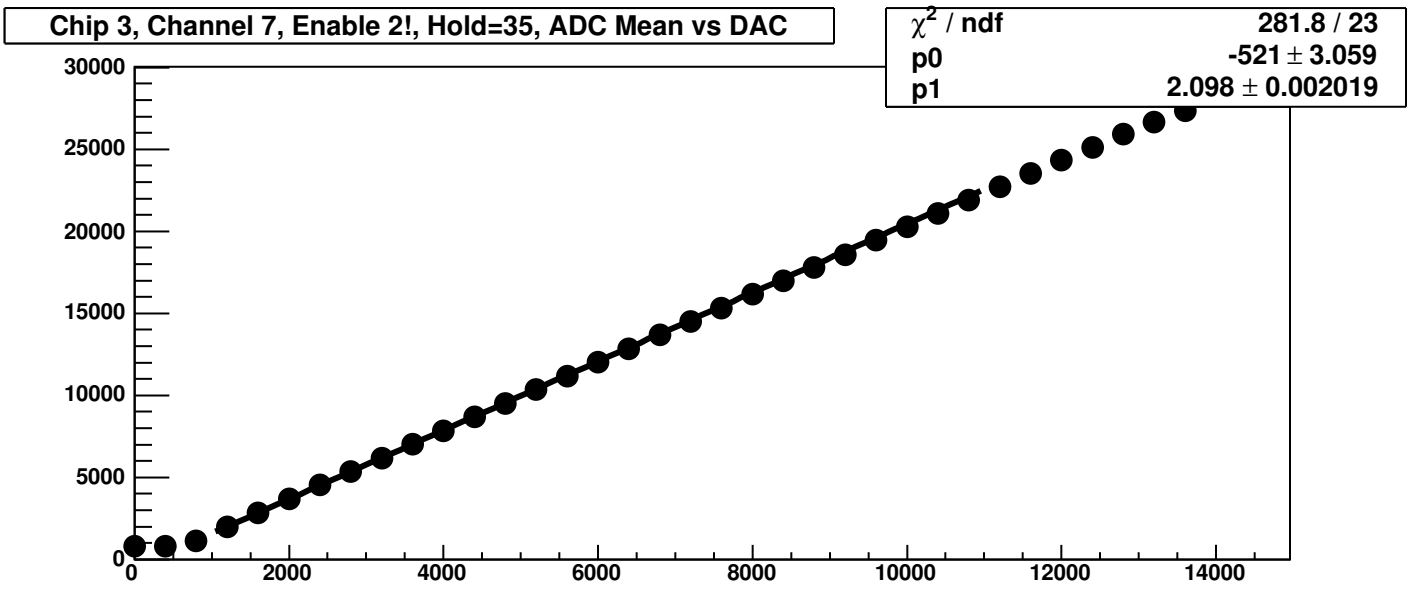

Chip 3, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

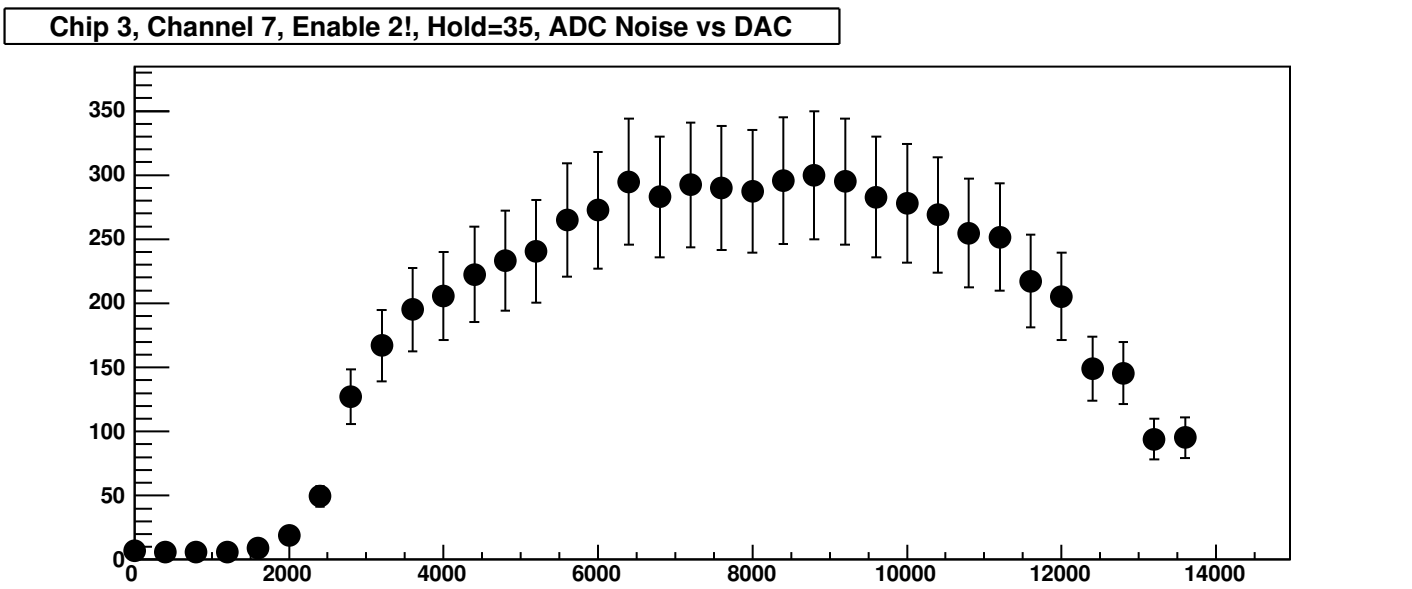

Chip 3, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

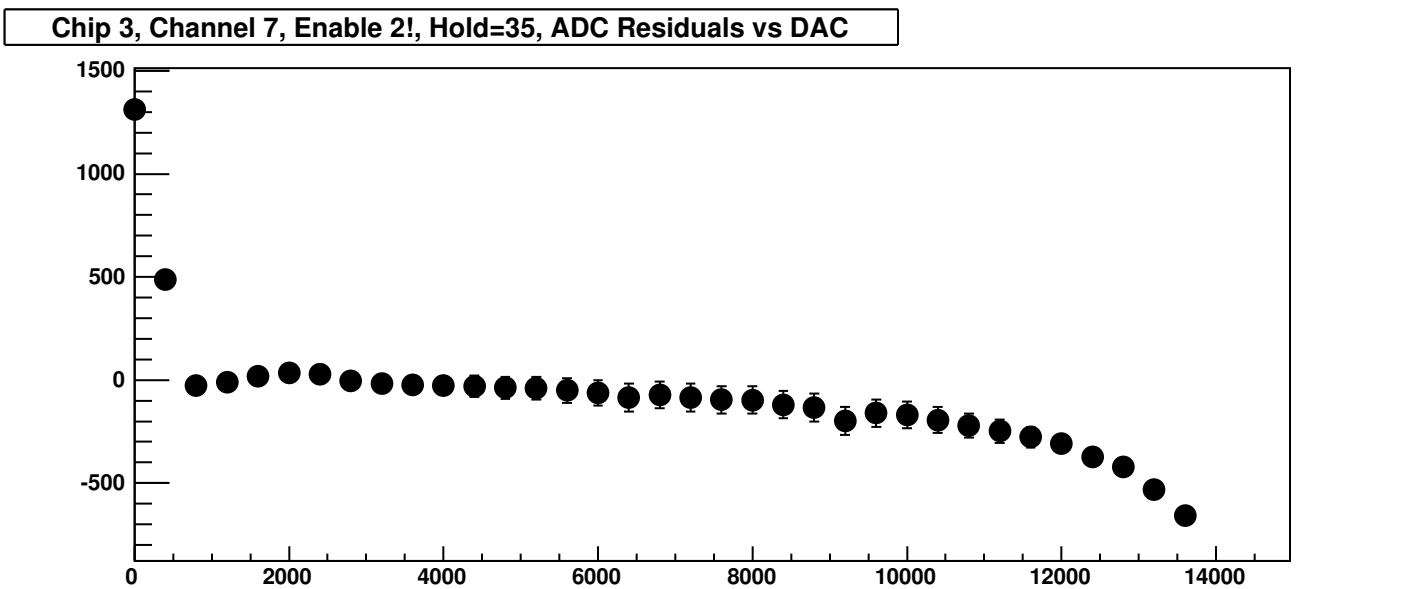

Chip 3, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 47.99 / 23
p0 -596 ± 0.8016
p1 0.0112 ± 0.000118
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000

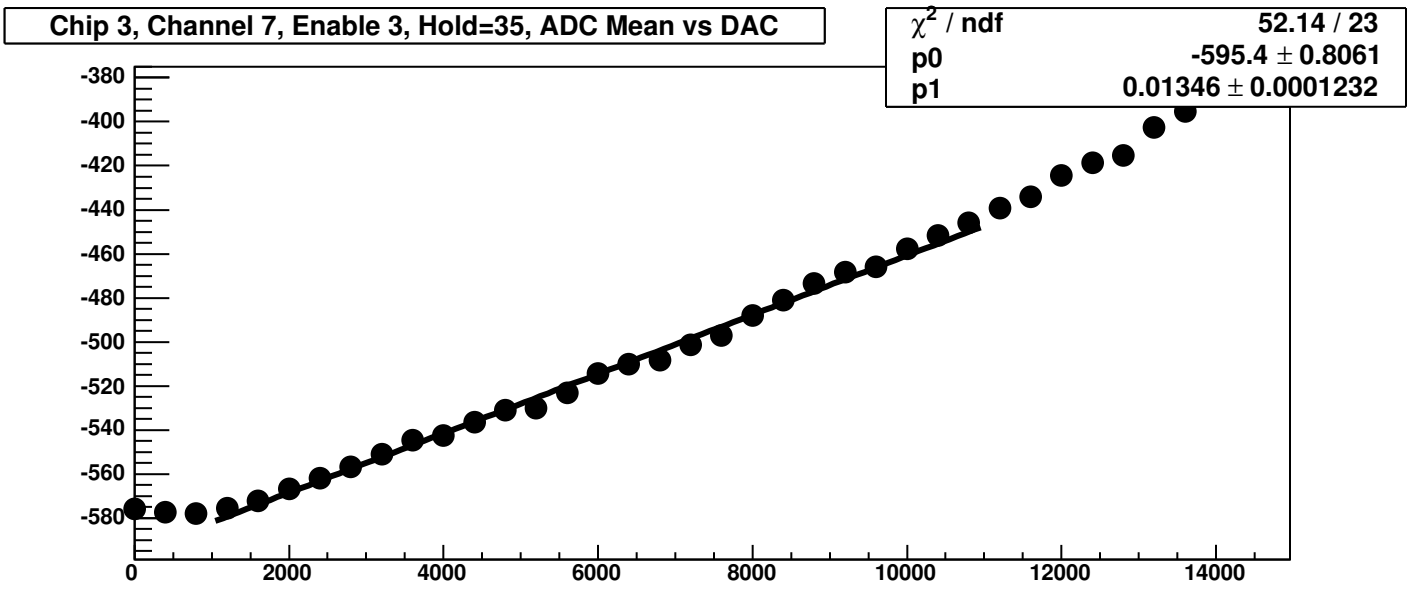

Chip 3, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

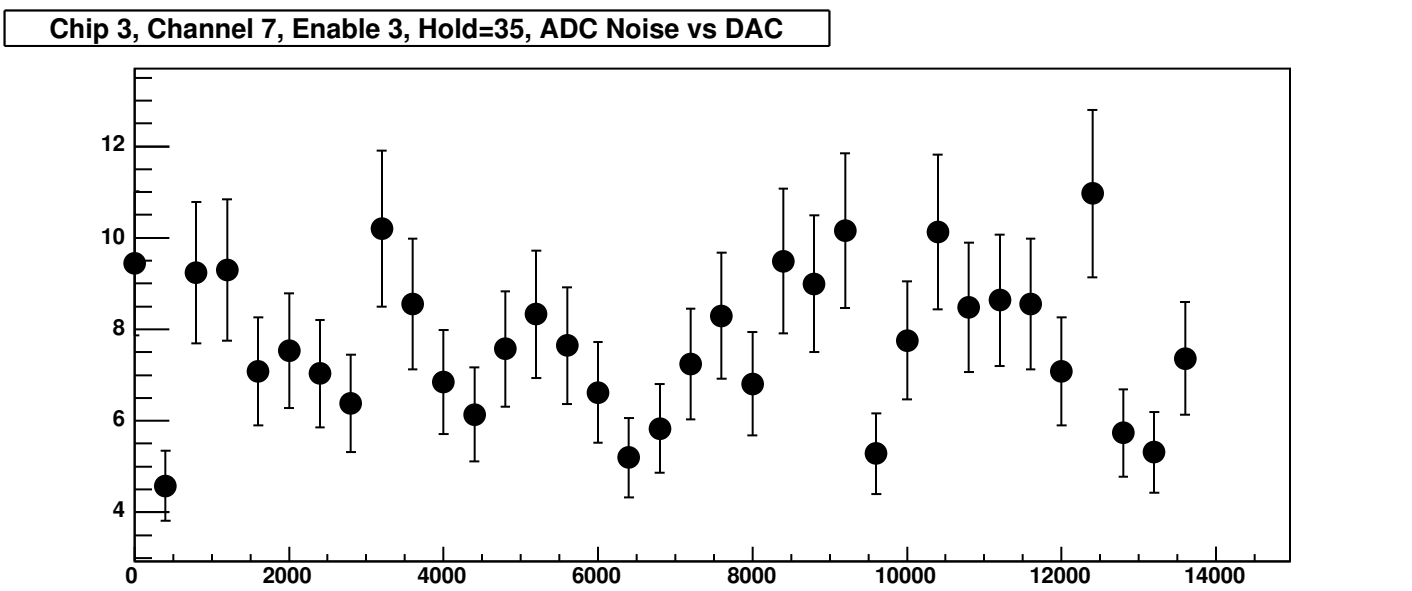

Chip 3, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

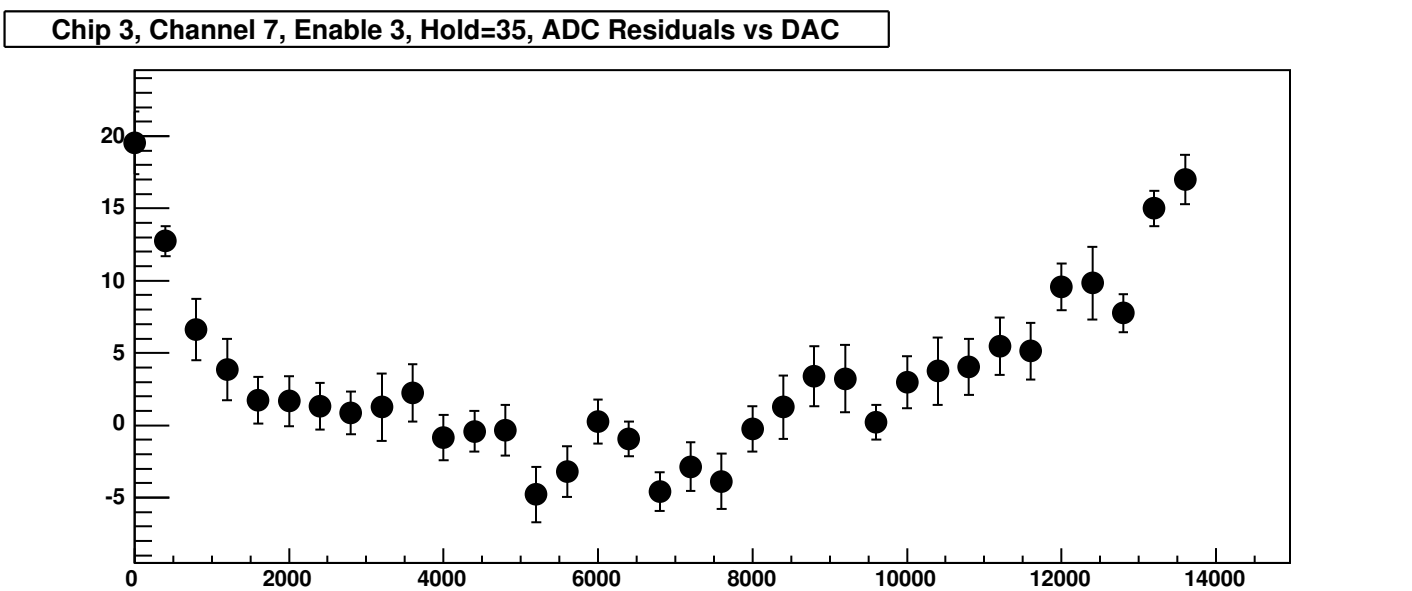

Chip 3, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 281.8 / 23
p0 -521 ± 3.059
p1 2.098 ± 0.002019
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

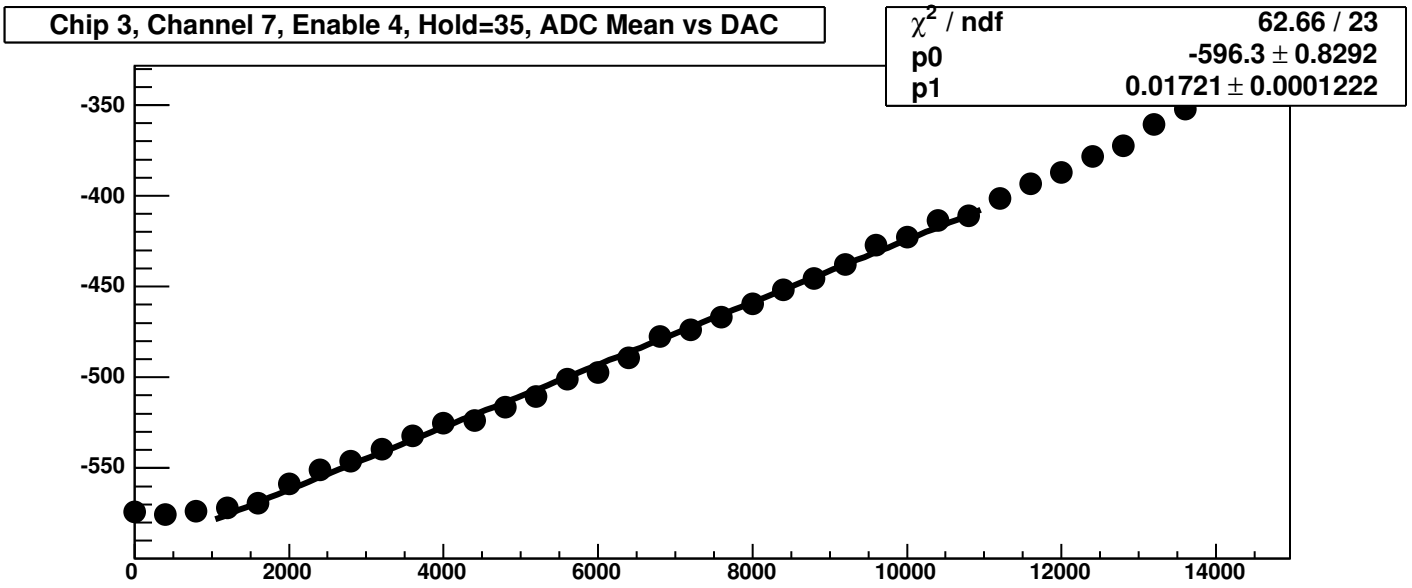

Chip 3, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

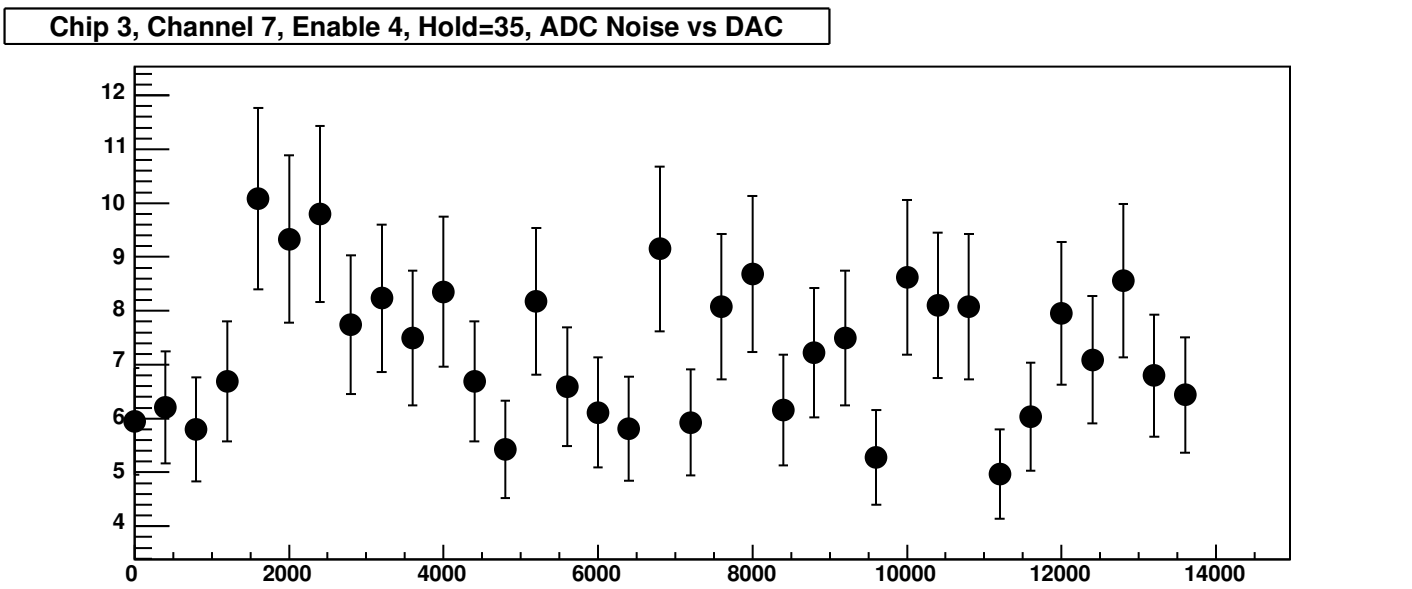

Chip 3, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

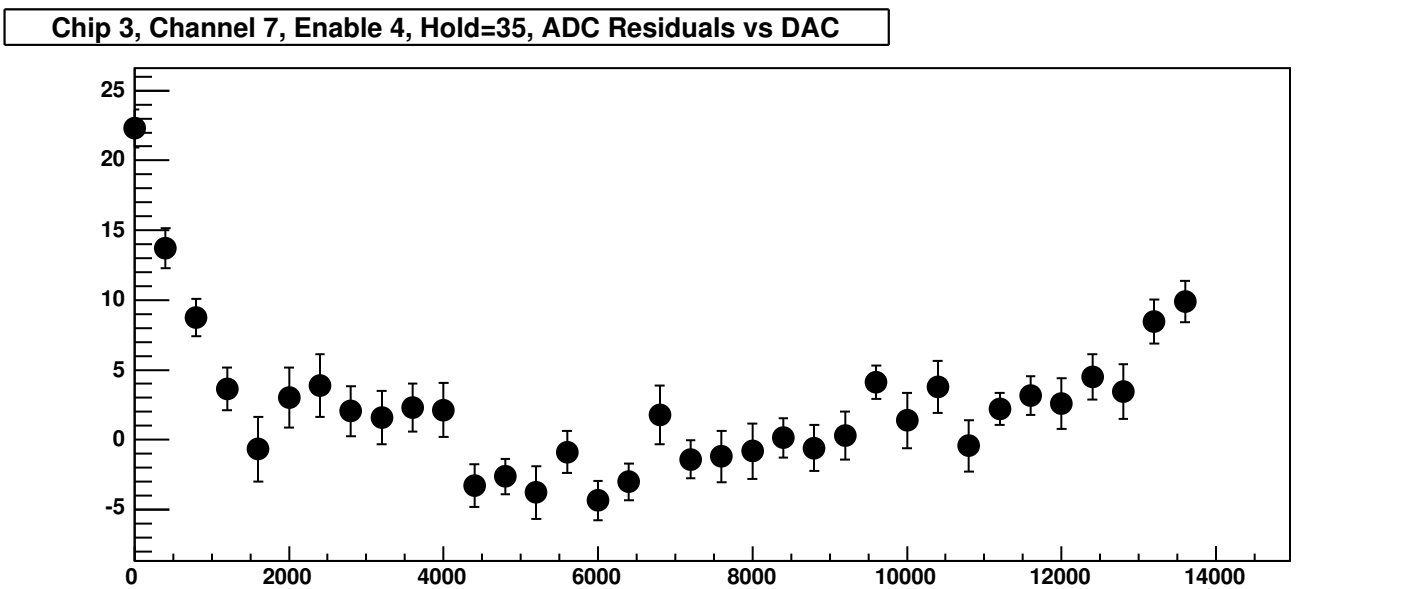

Chip 3, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 52.14 / 23
p0 -595.4 ± 0.8061
p1 0.01346 ± 0.0001232
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000

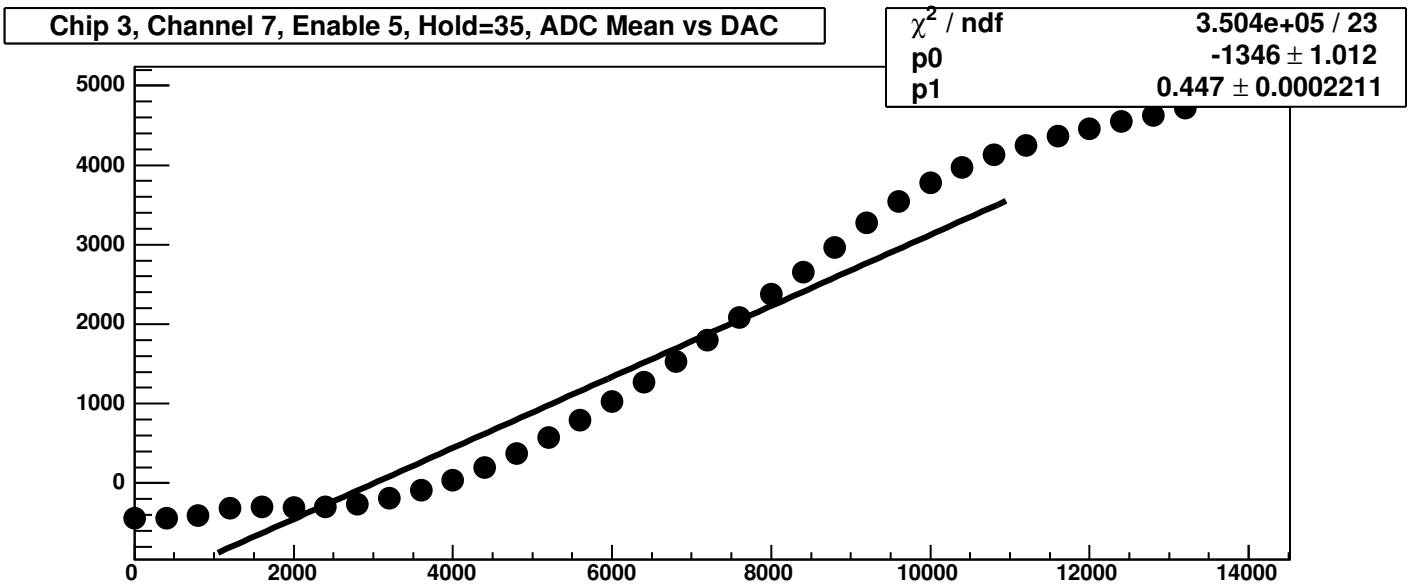

Chip 3, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

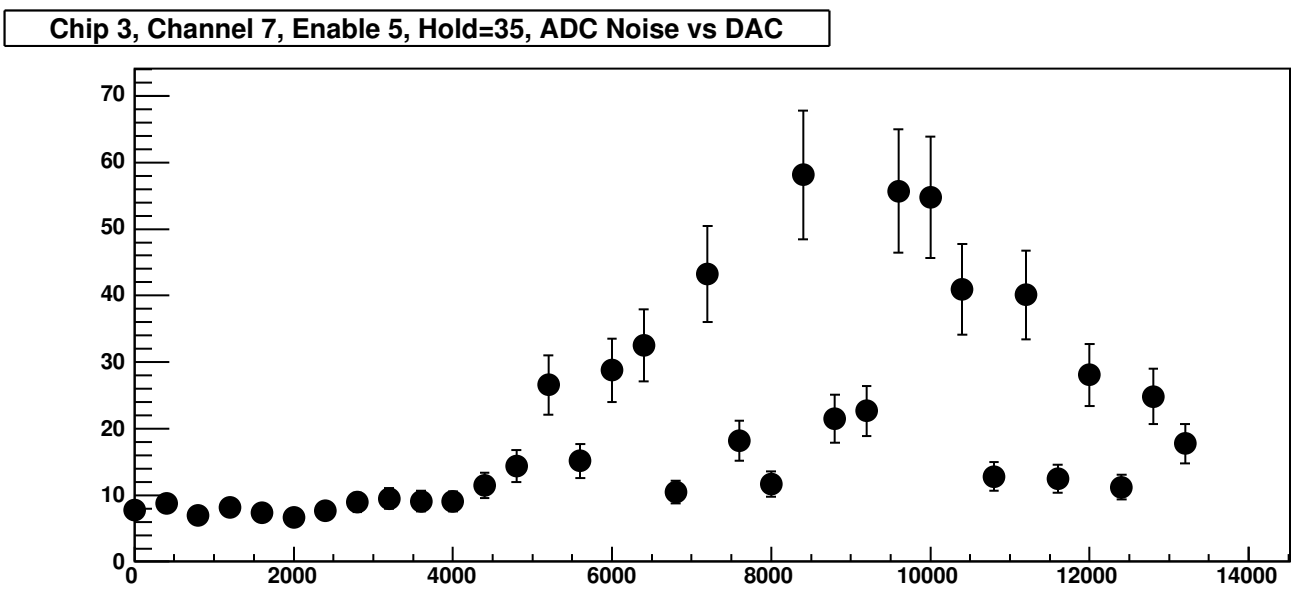

Chip 3, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

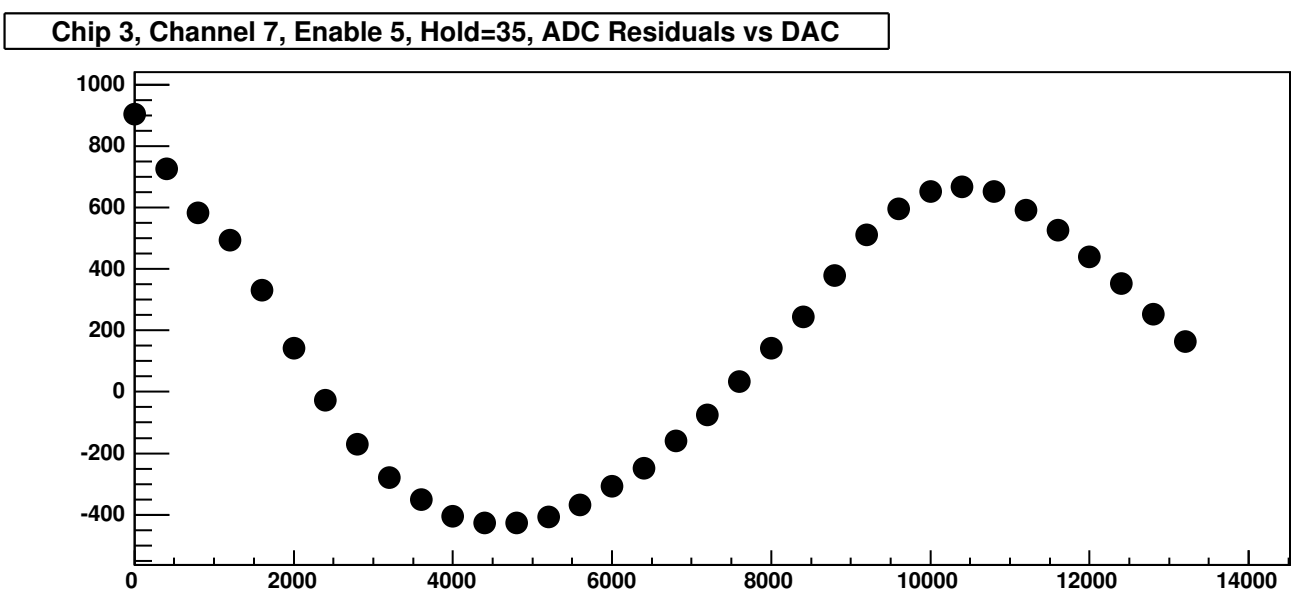

Chip 3, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 62.66 / 23
p0 -596.3 ± 0.8292
p1 0.01721 ± 0.0001222
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

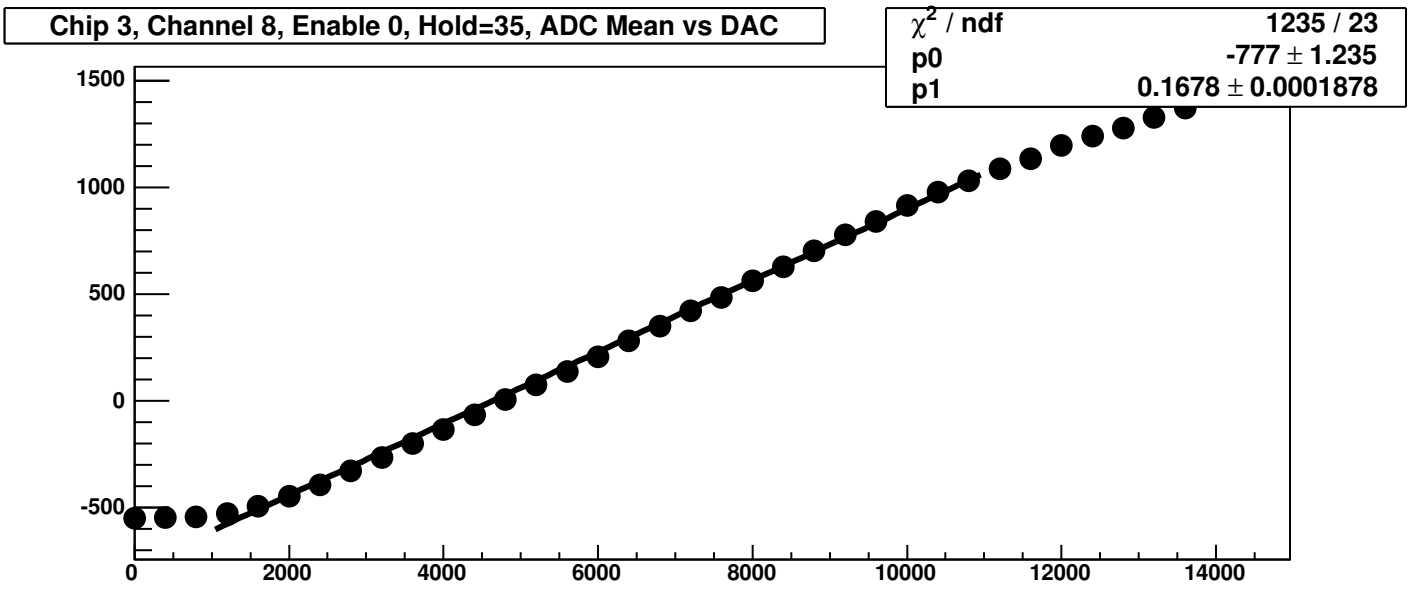

Chip 3, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

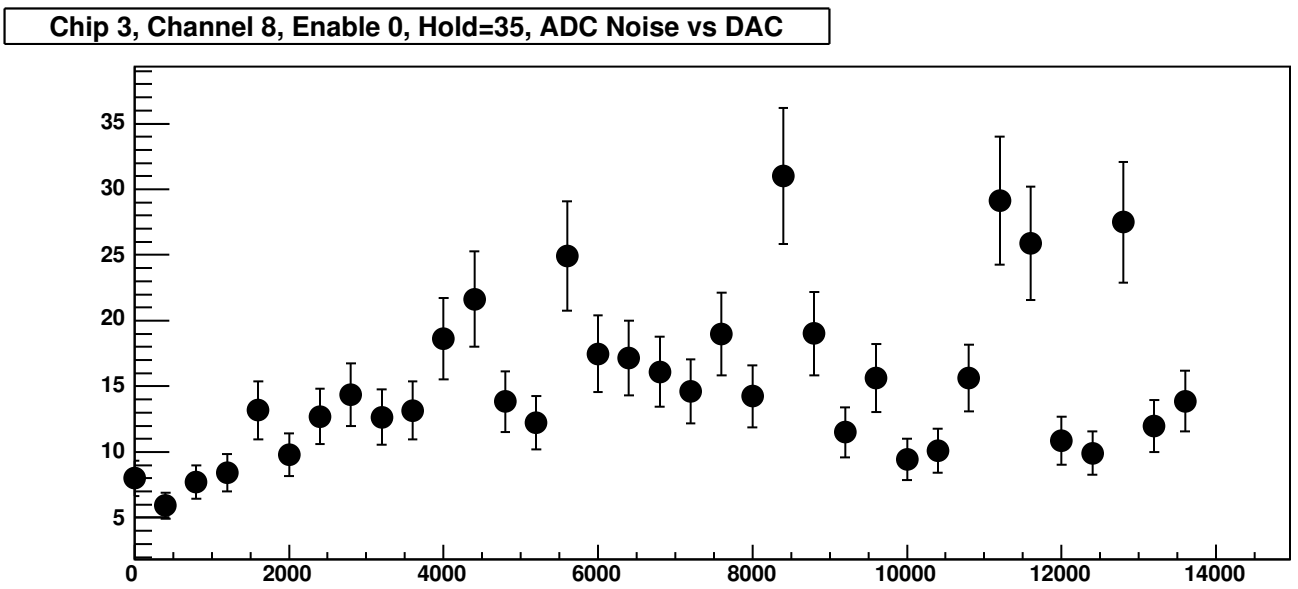

Chip 3, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

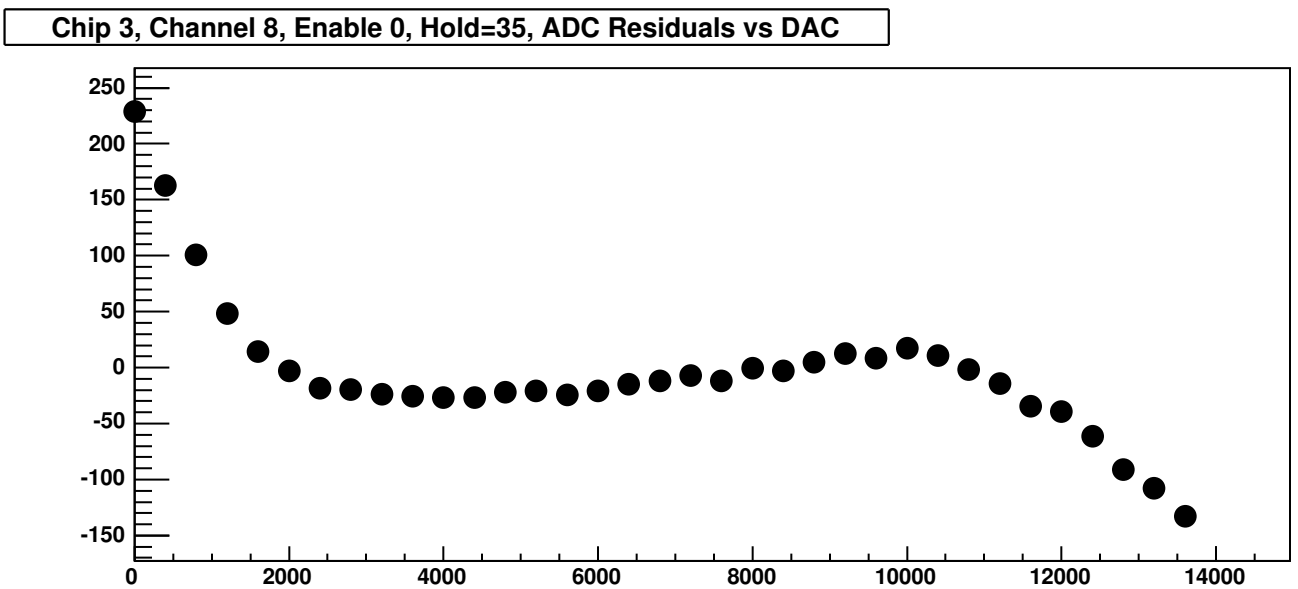

Chip 3, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.504e+05 / 23
p0 -1346 ± 1.012
p1 0.447 ± 0.0002211
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

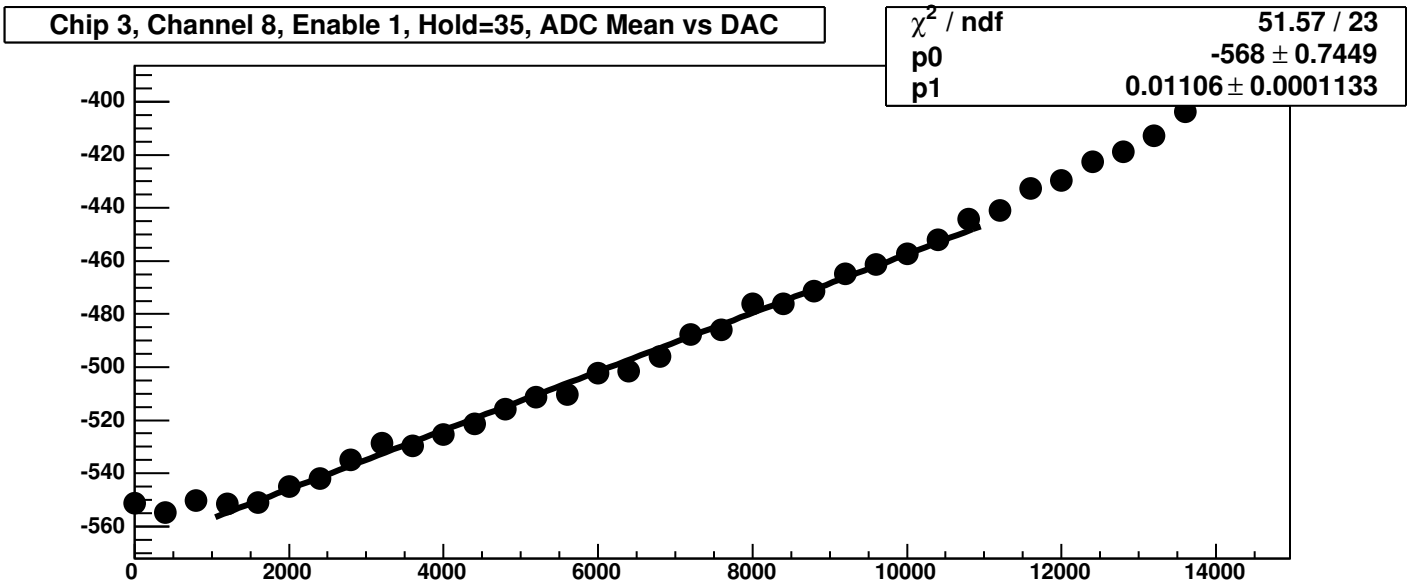

Chip 3, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

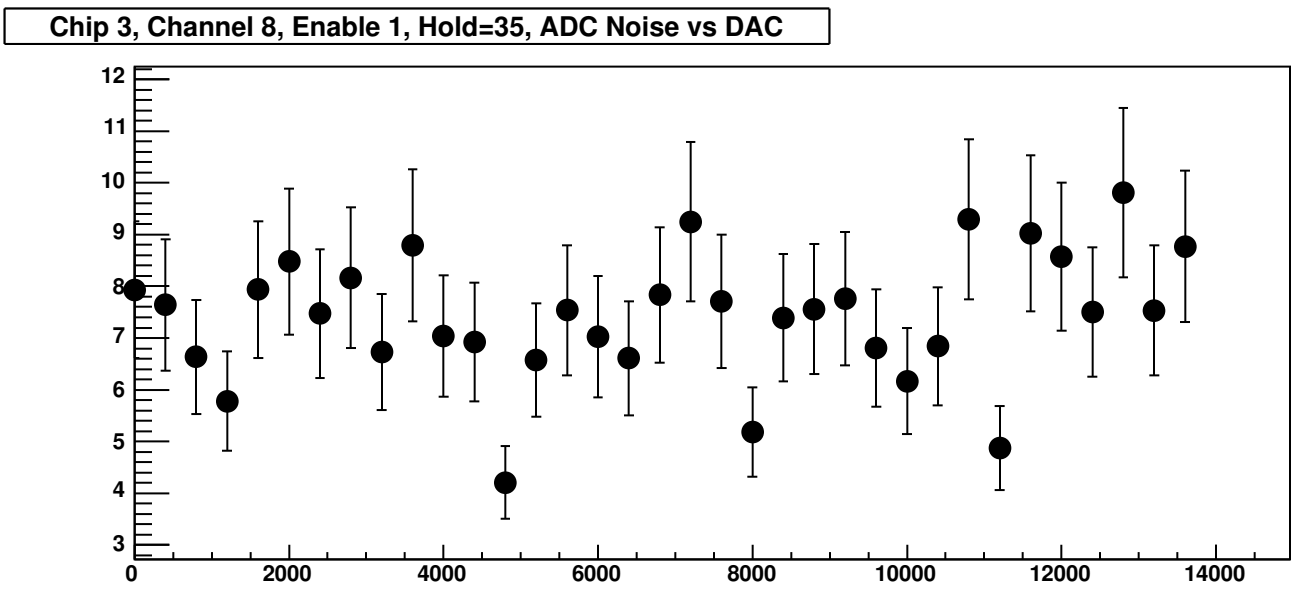

Chip 3, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

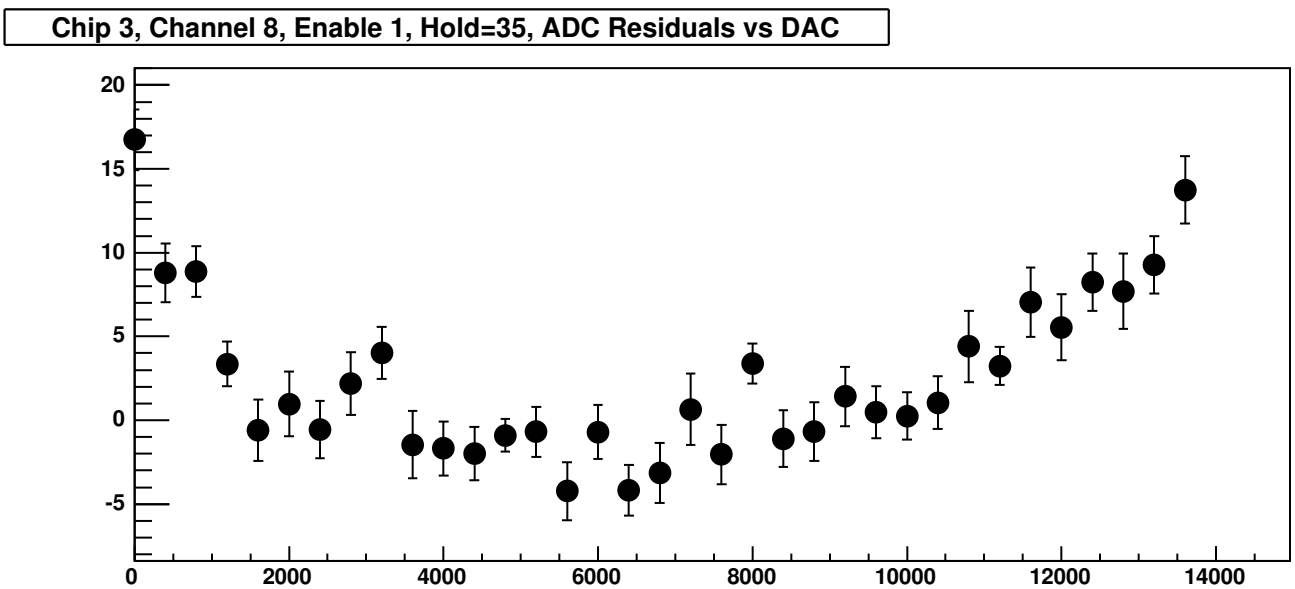

Chip 3, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1235 / 23
p0 -777 ± 1.235
p1 0.1678 ± 0.0001878
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

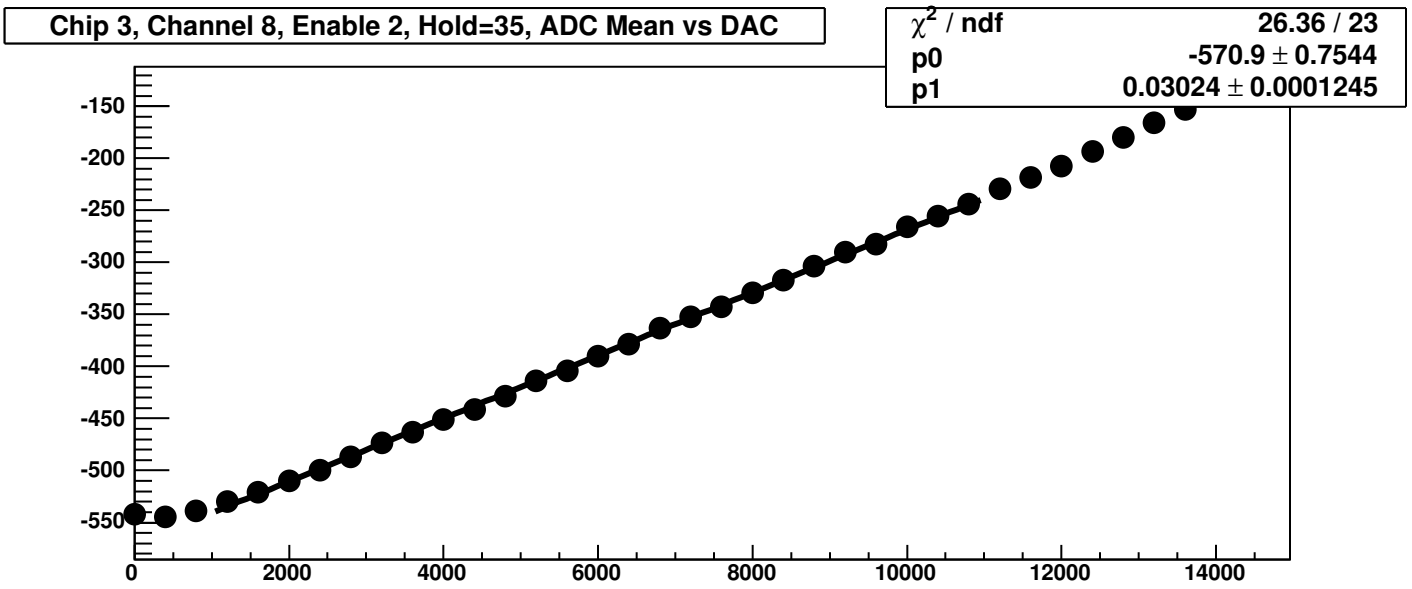

Chip 3, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

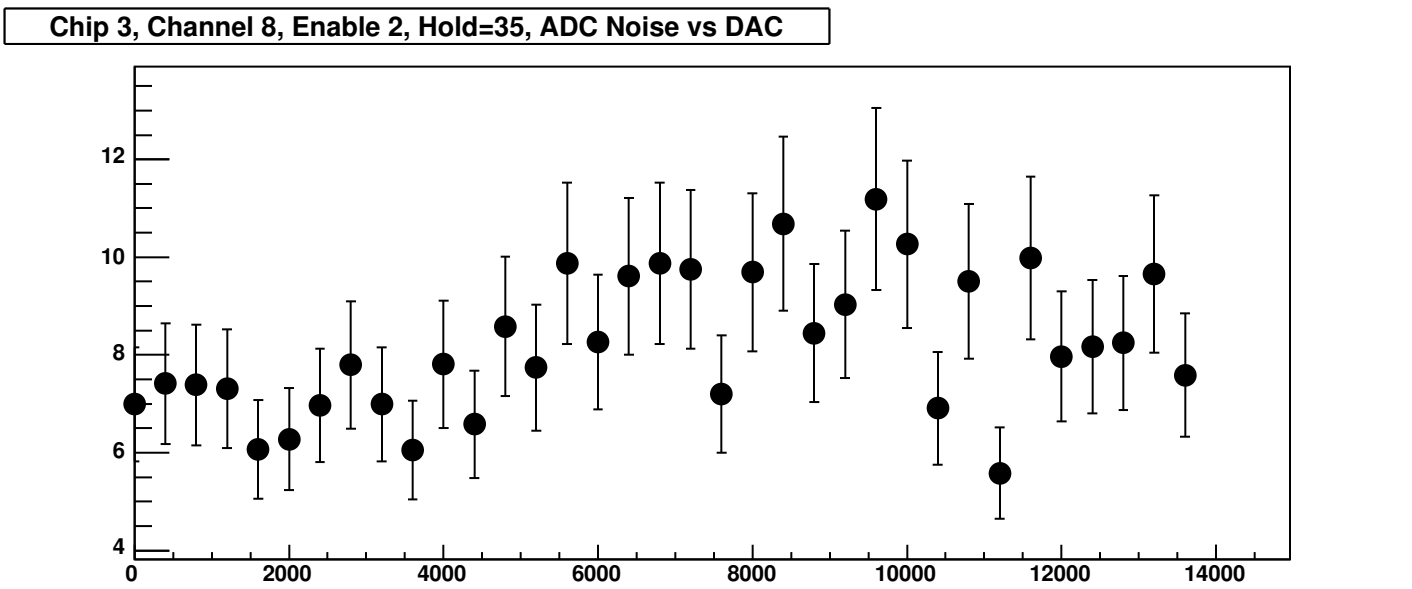

Chip 3, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

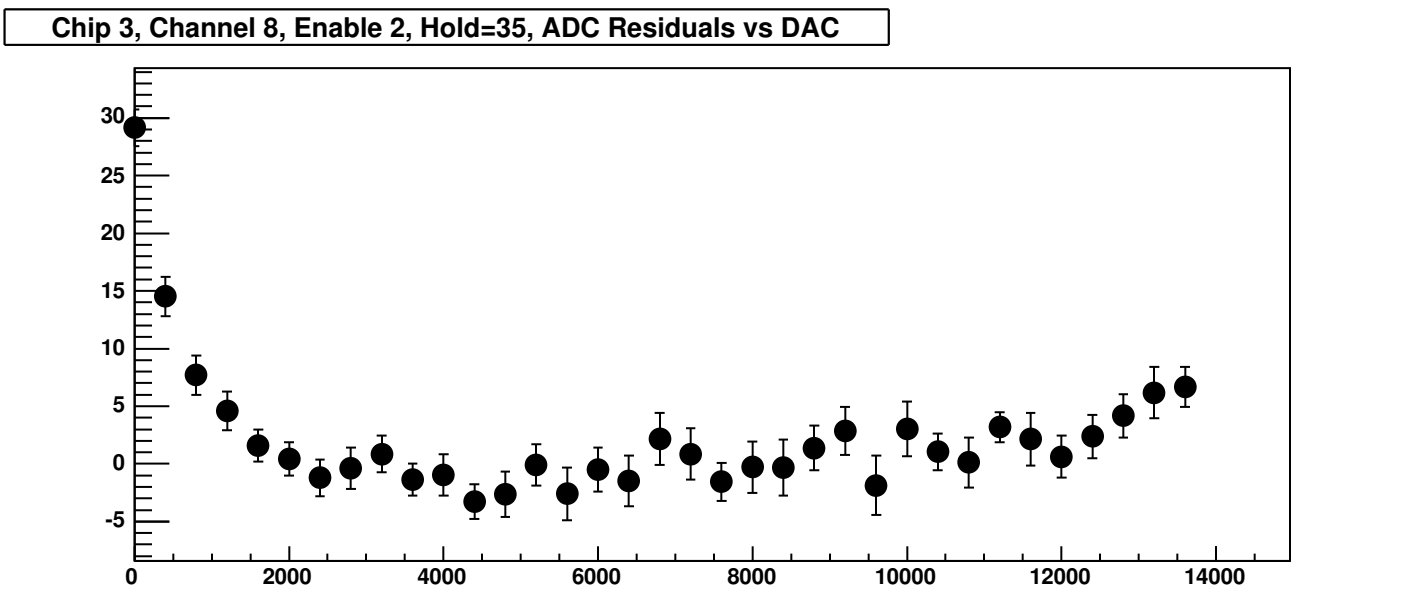

Chip 3, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 51.57 / 23
p0 -568 ± 0.7449
p1 0.01106 ± 0.0001133
-400
-420
-440
-460
-480
-500
-520
-540
-560
0
2000
4000
6000
8000
10000
12000
14000

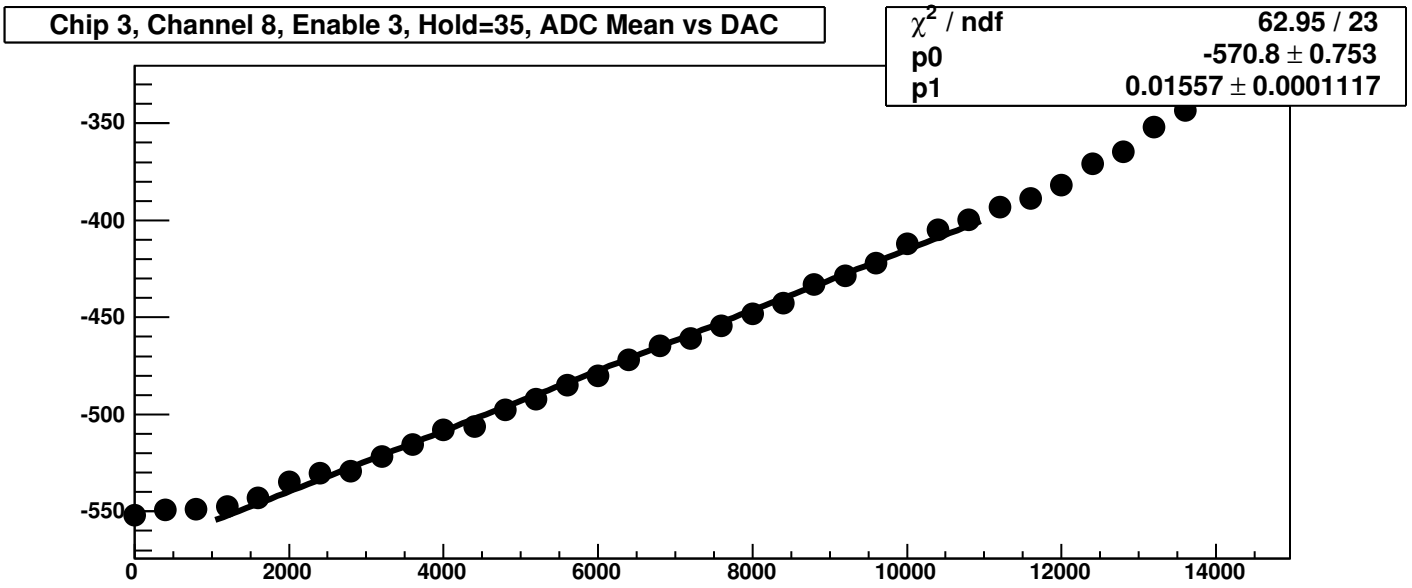

Chip 3, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

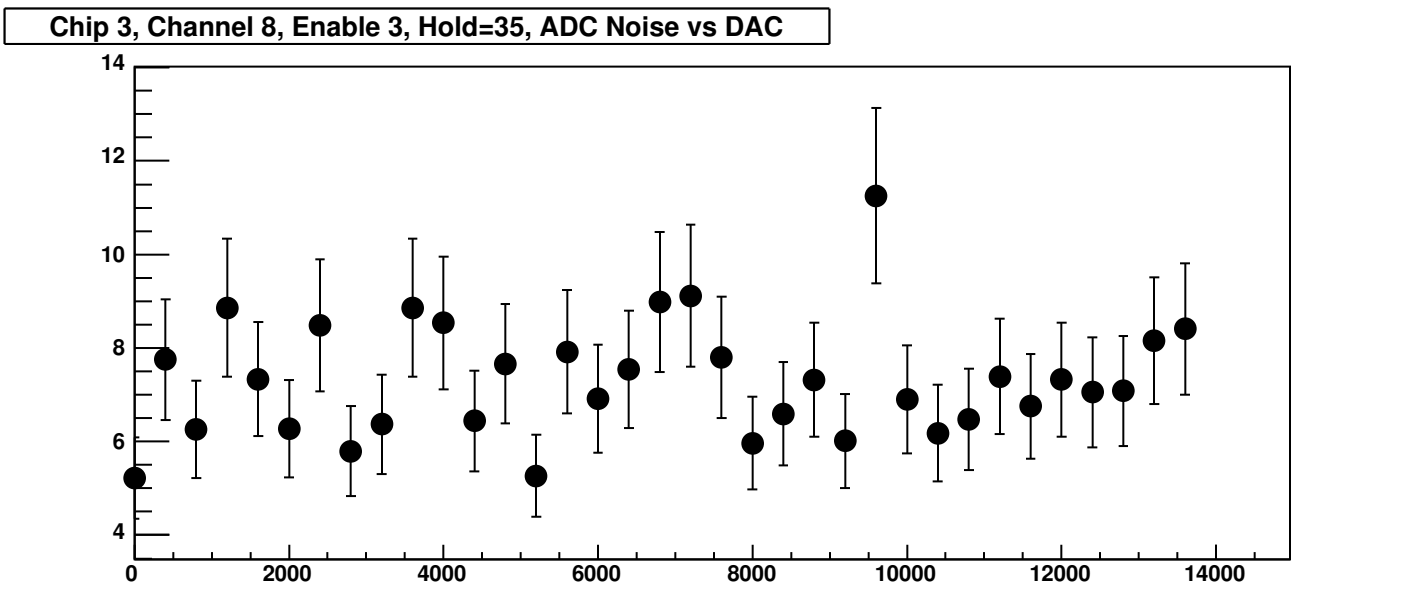

Chip 3, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

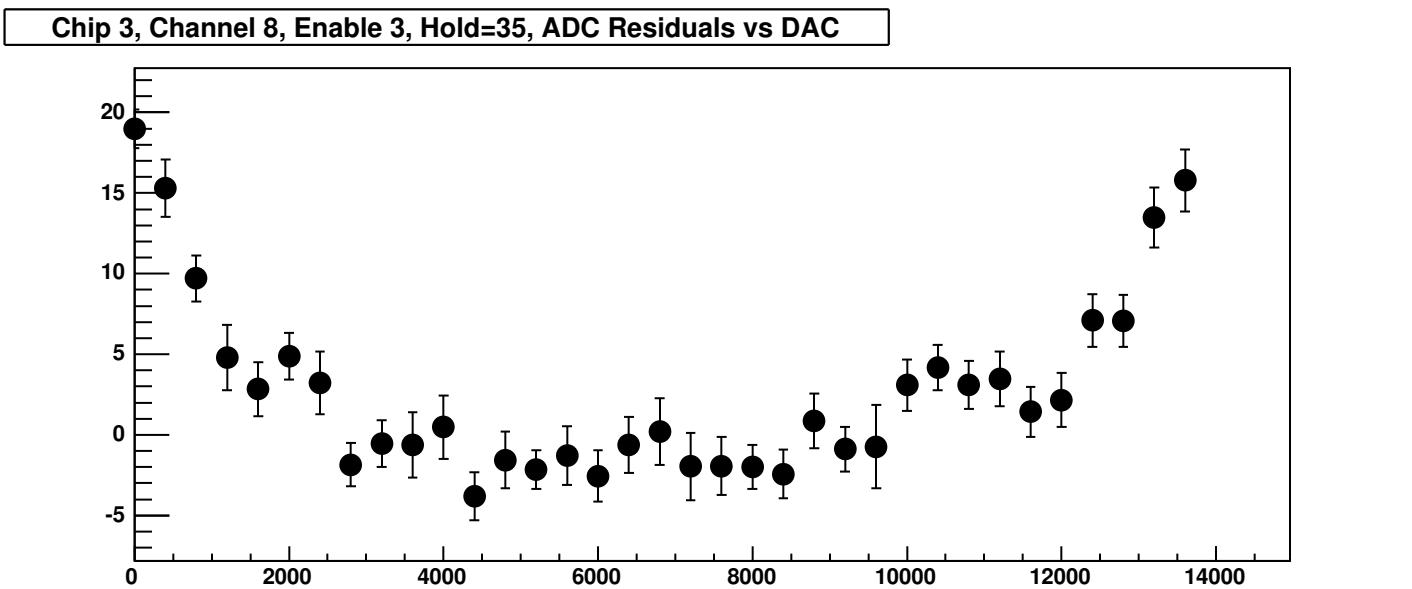

Chip 3, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 26.36 / 23
p0 -570.9 ± 0.7544
p1 0.03024 ± 0.0001245
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

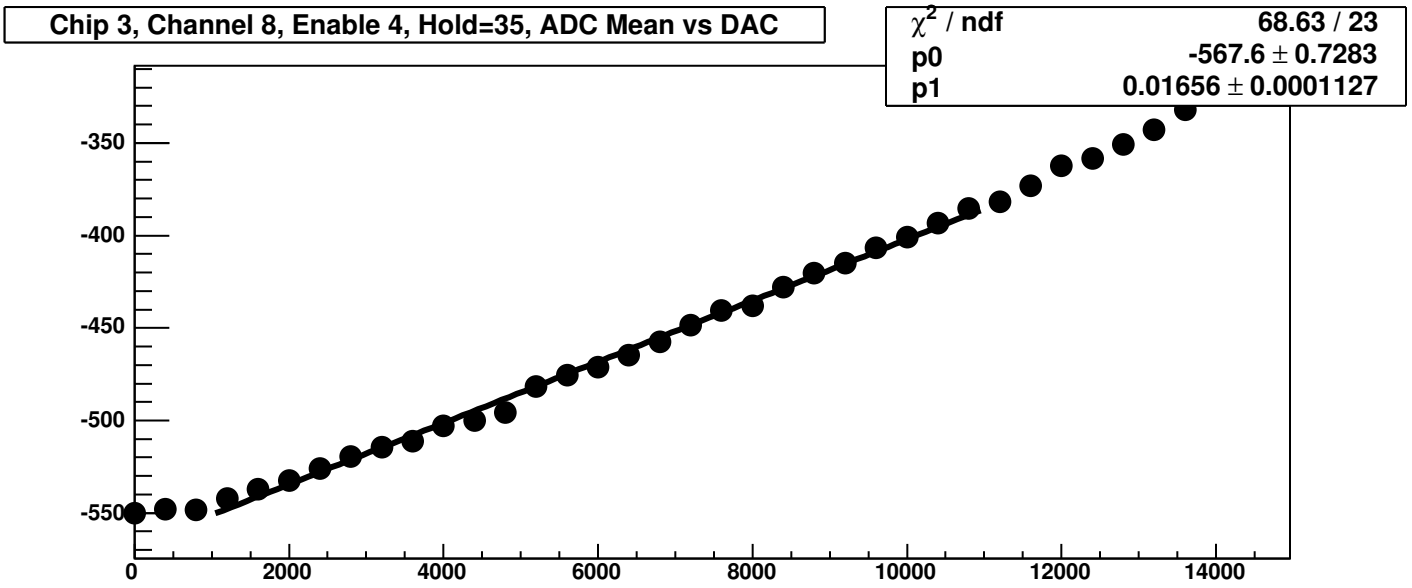

Chip 3, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

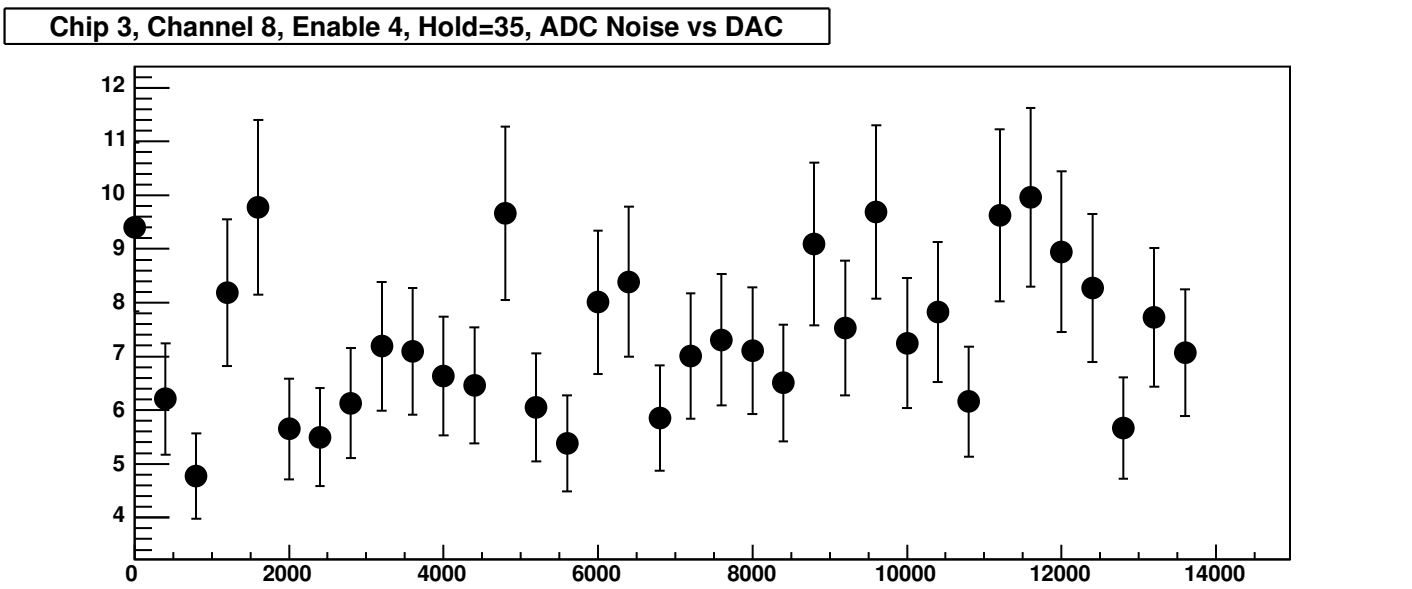

Chip 3, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

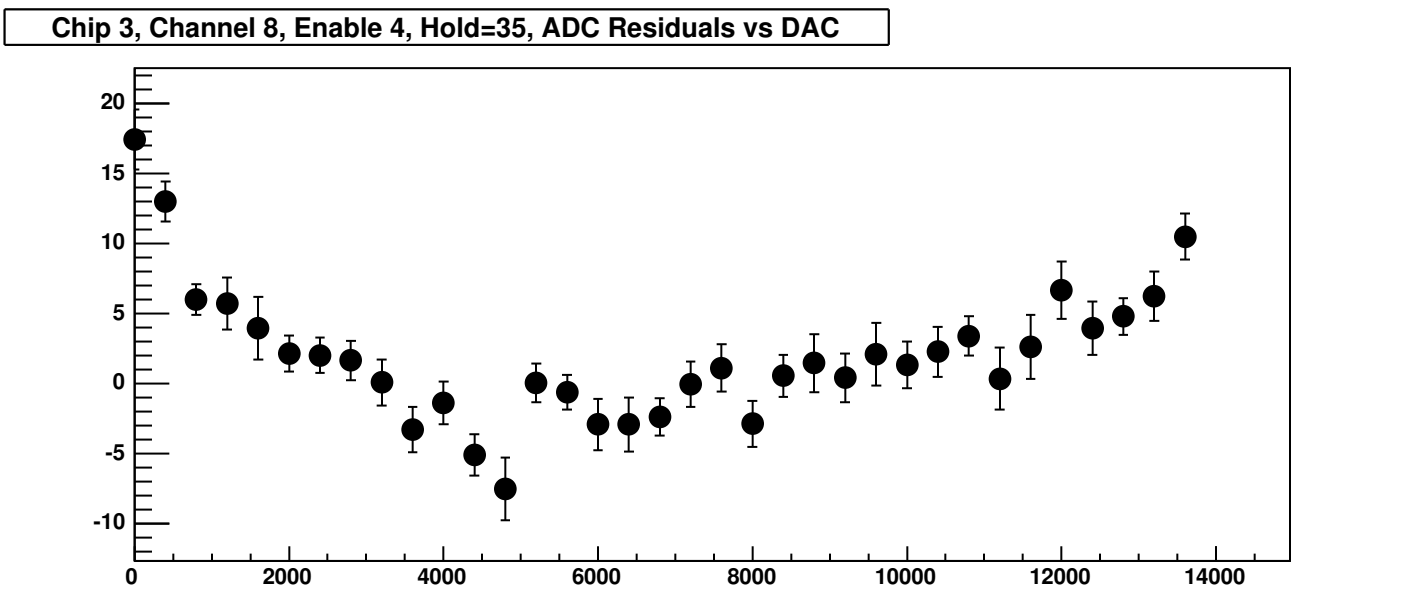

Chip 3, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 62.95 / 23
p0 -570.8 ± 0.753
p1 0.01557 ± 0.0001117
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

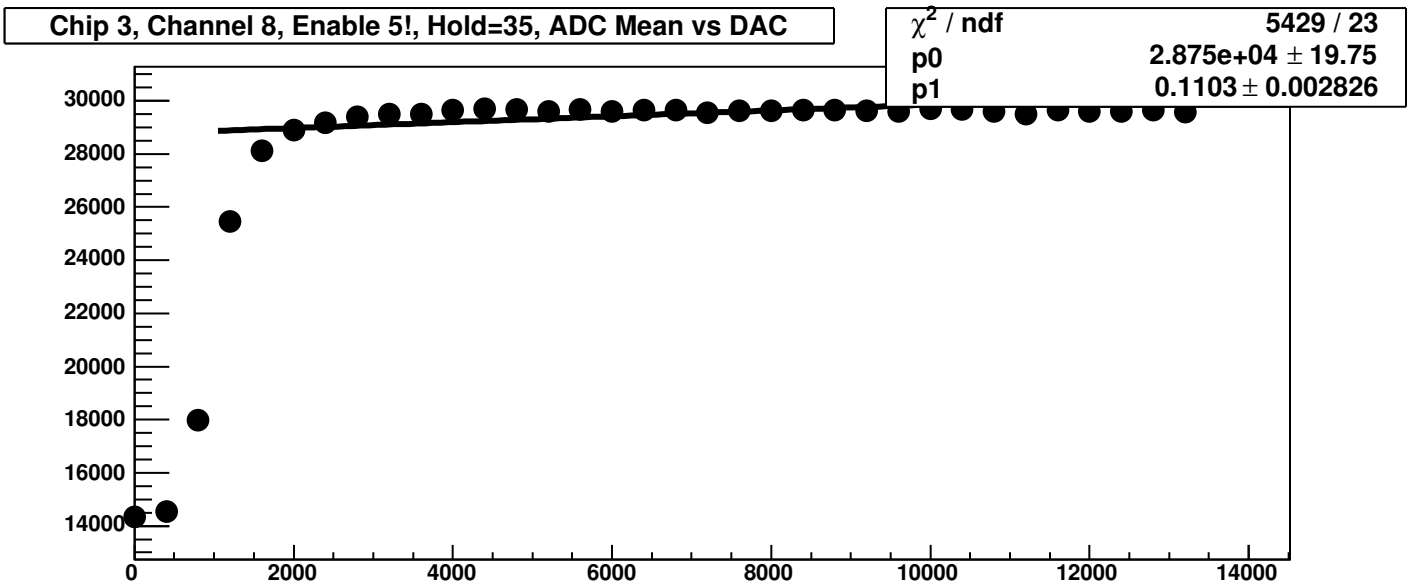

Chip 3, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

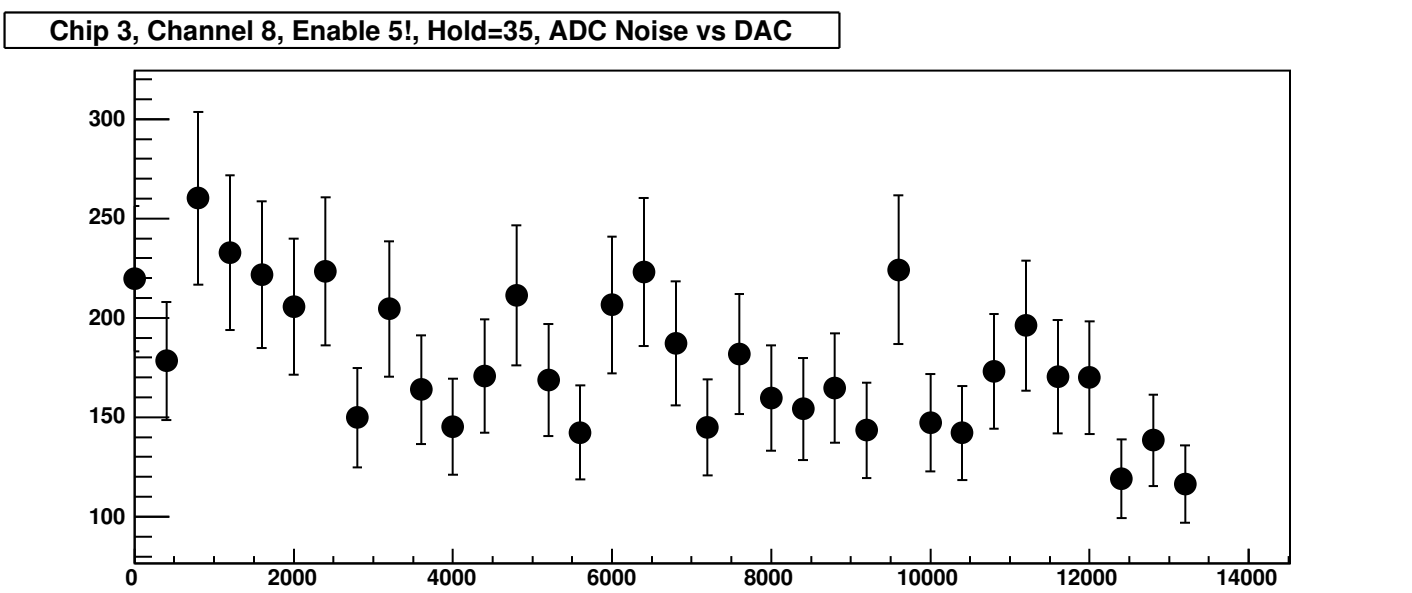

Chip 3, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

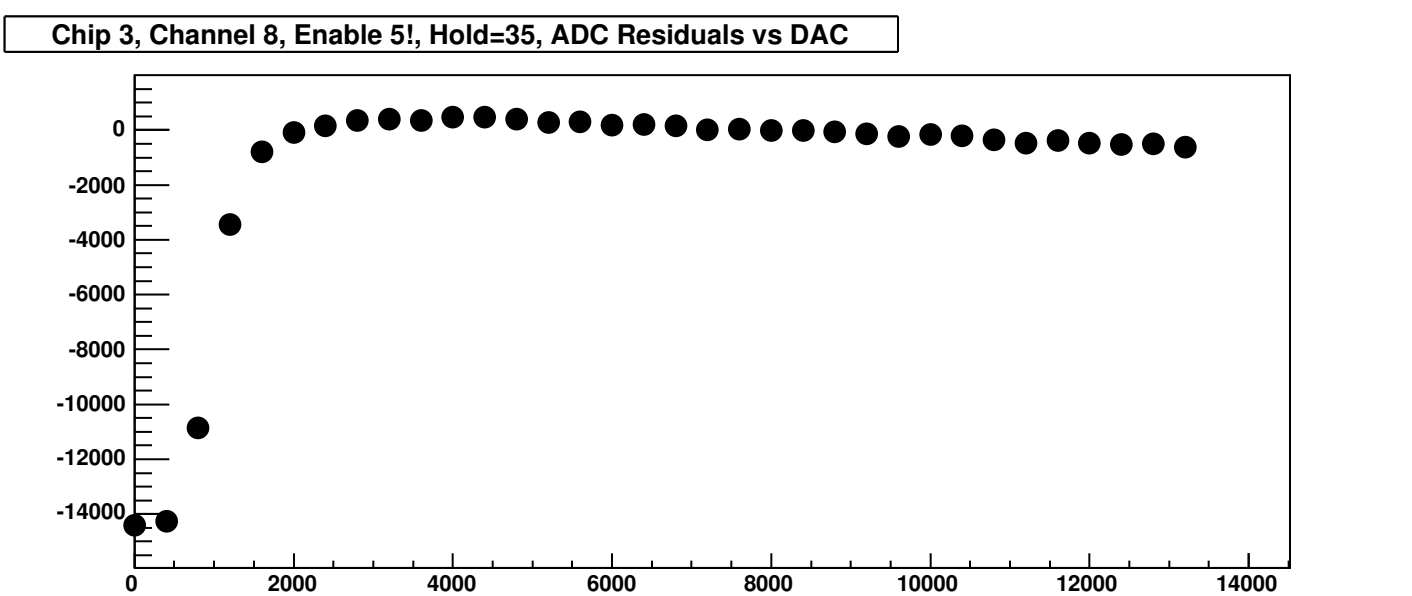

Chip 3, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 68.63 / 23
p0 -567.6 ± 0.7283
p1 0.01656 ± 0.0001127
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

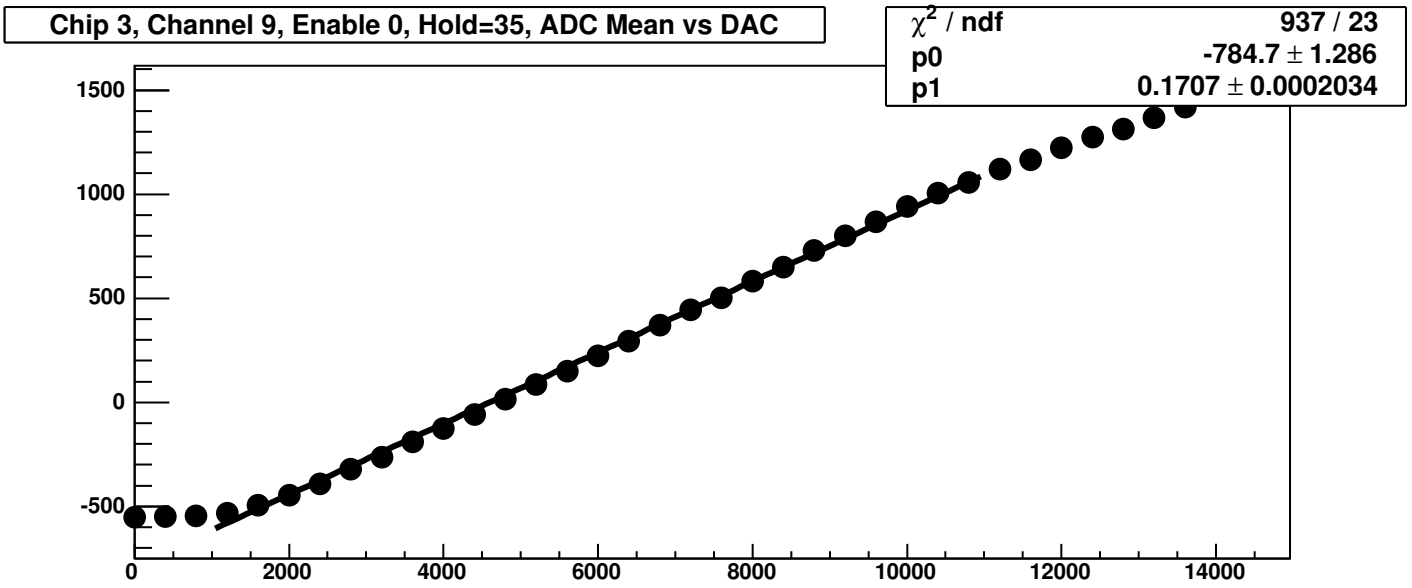

Chip 3, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

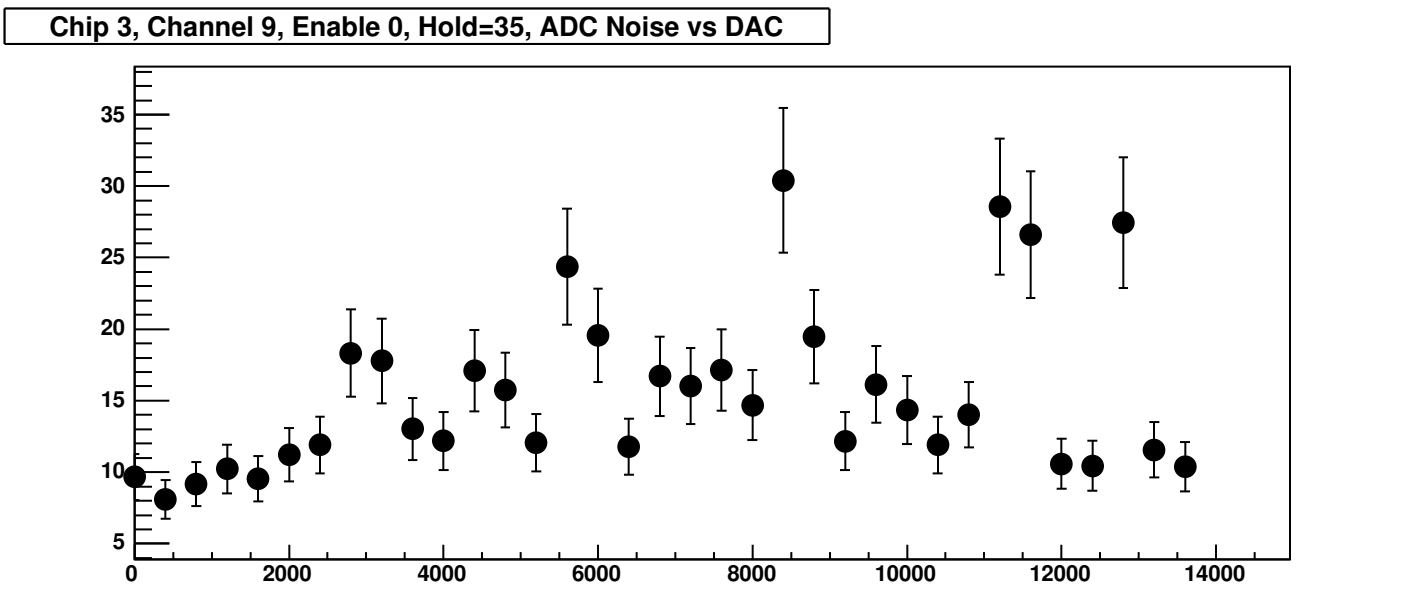

Chip 3, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

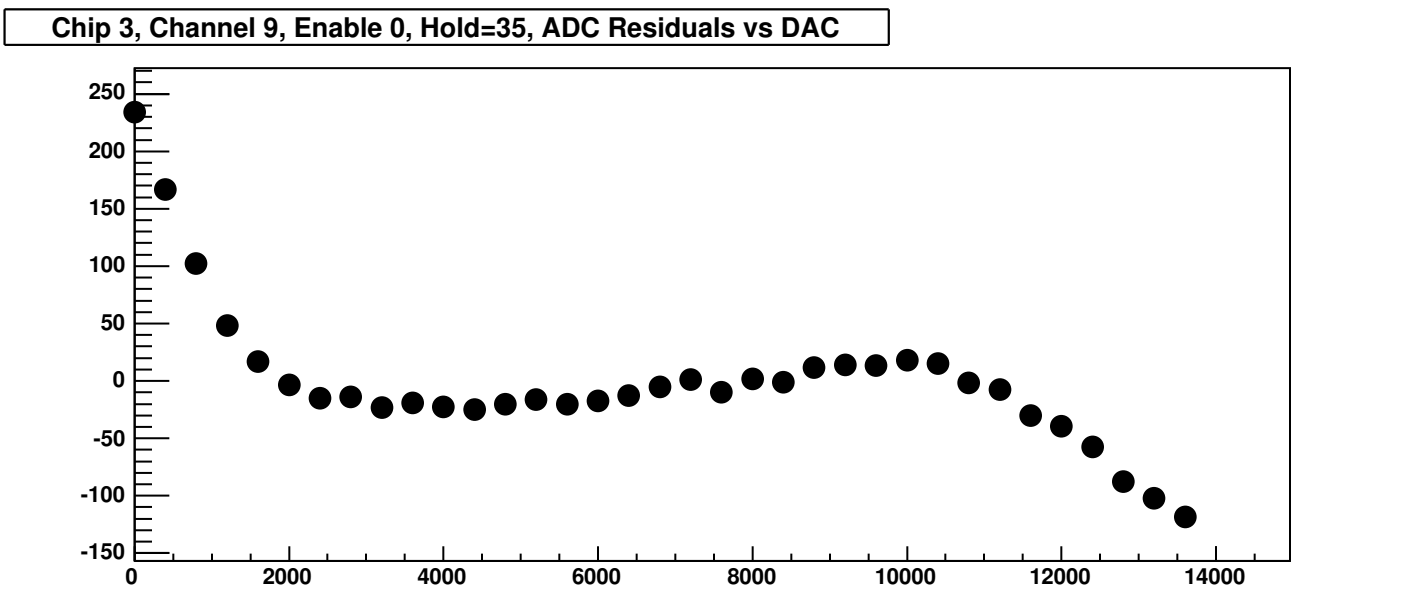

Chip 3, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 5429 / 23
p0 2.875e+04 ± 19.75
p1 0.1103 ± 0.002826
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

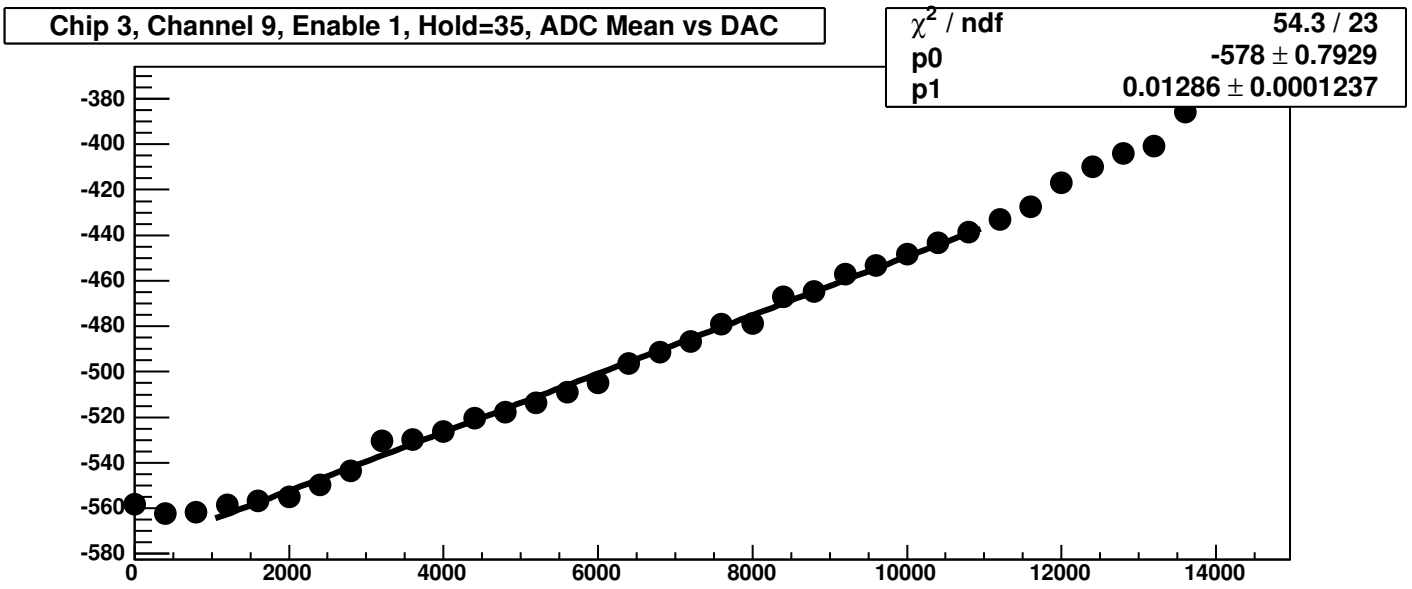

Chip 3, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

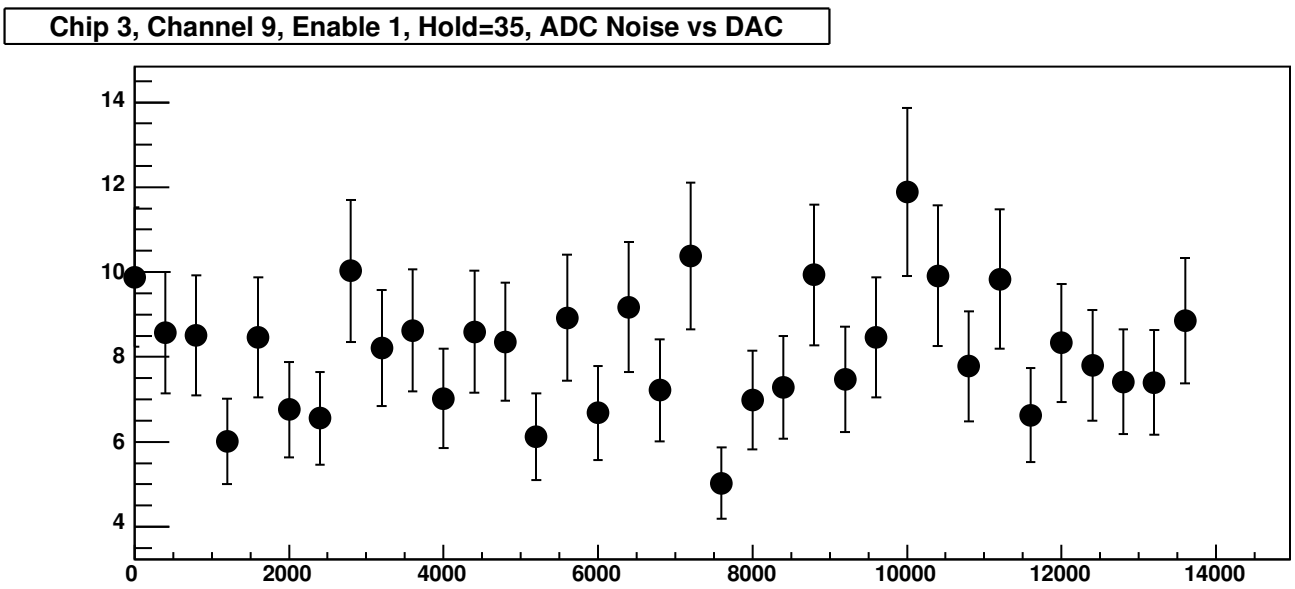

Chip 3, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

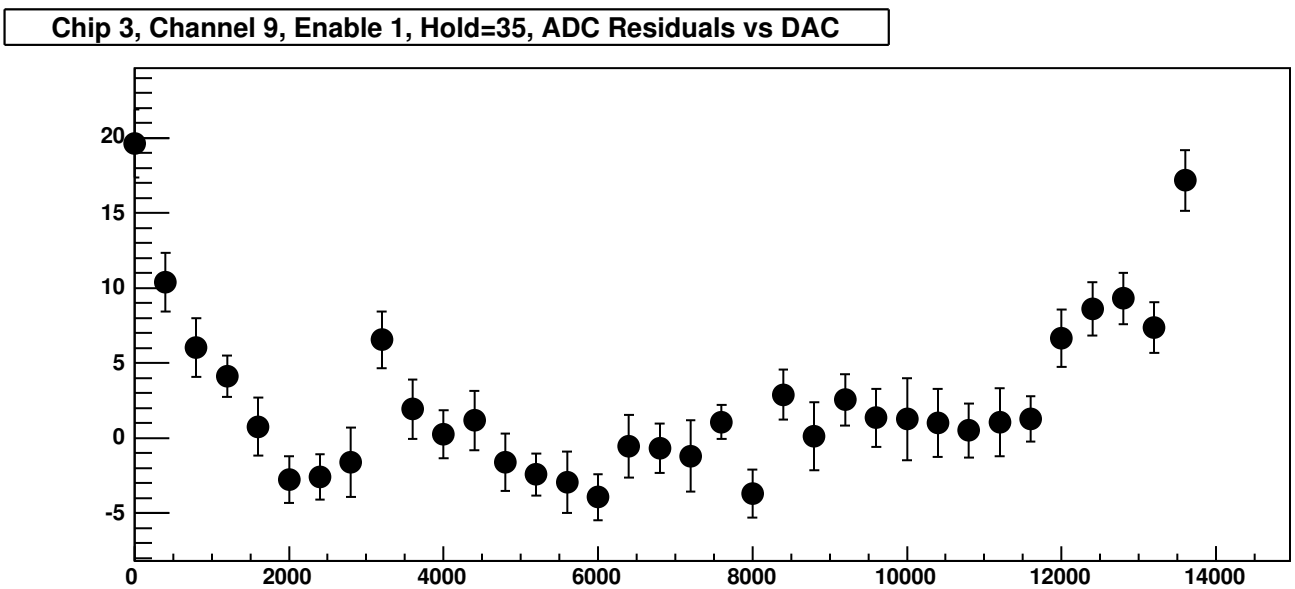

Chip 3, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 937 / 23
p0 -784.7 ± 1.286
p1 0.1707 ± 0.0002034
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

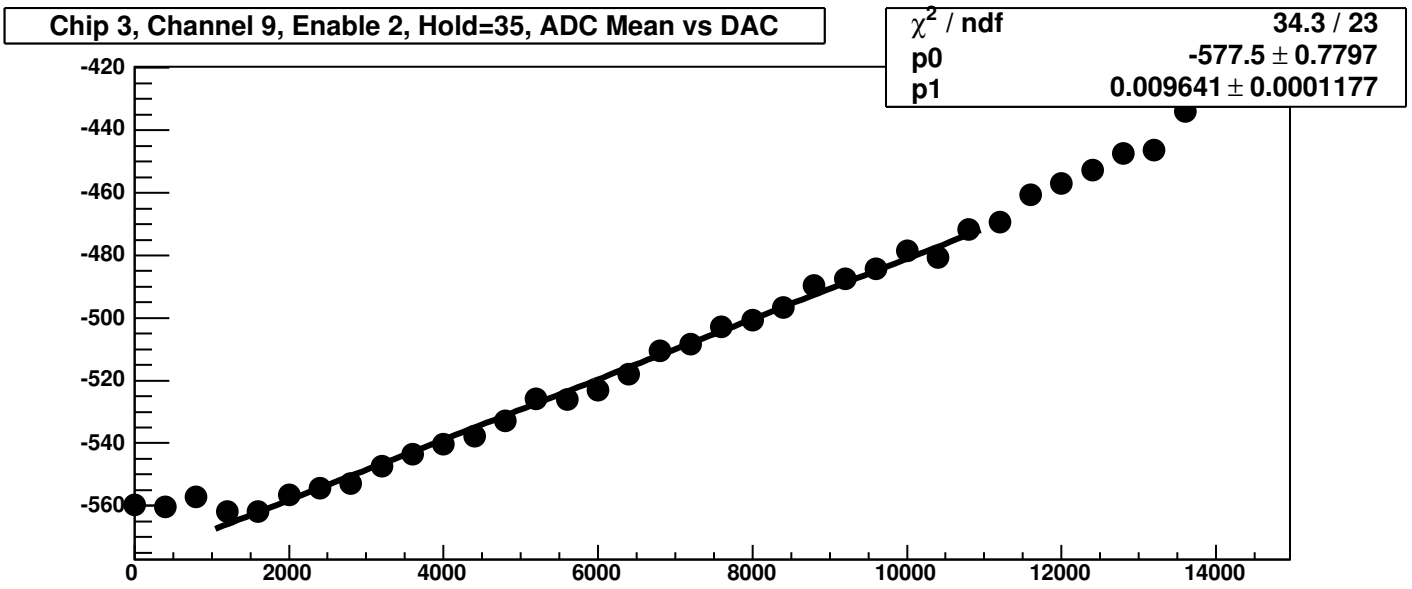

Chip 3, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

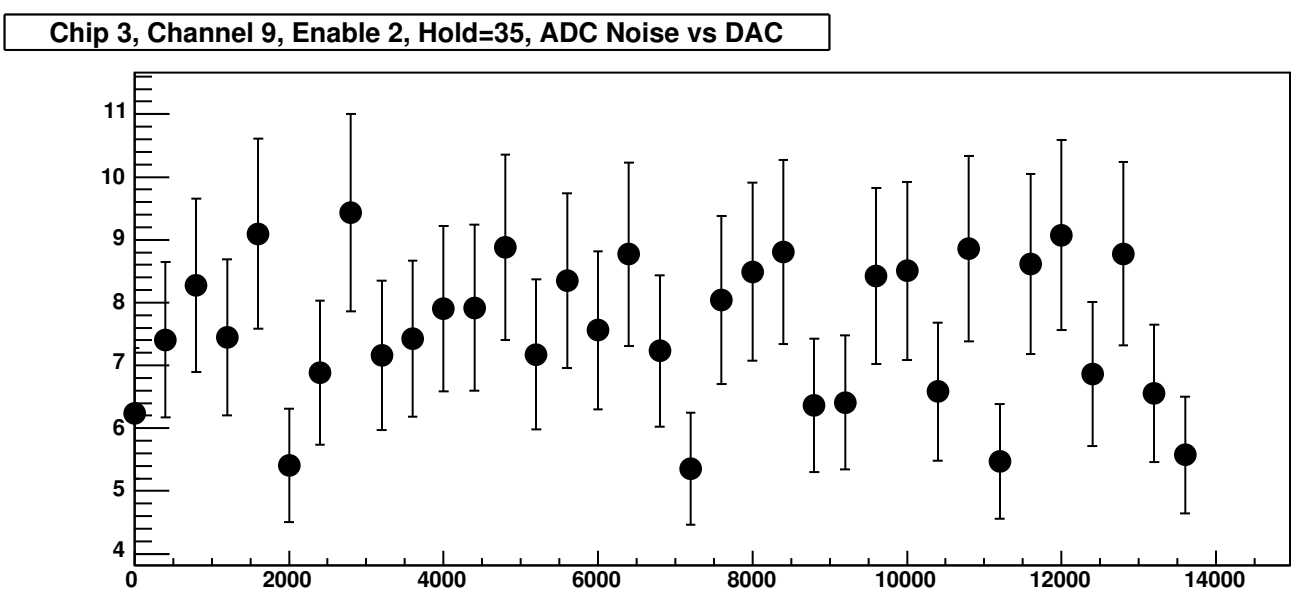

Chip 3, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

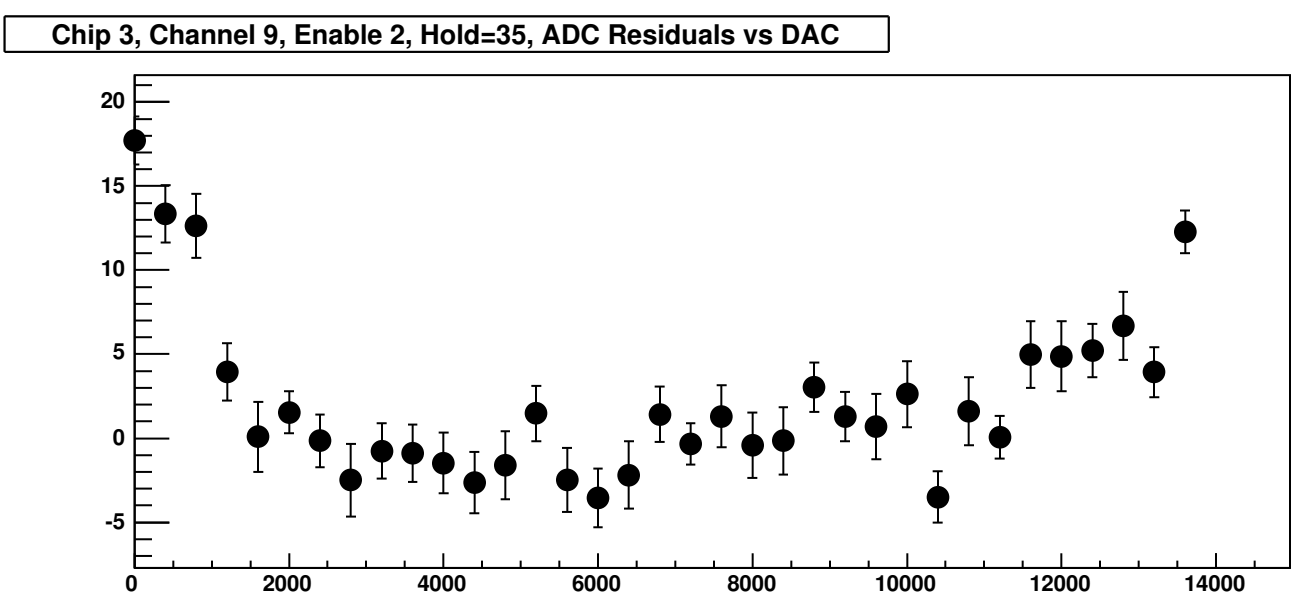

Chip 3, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 54.3 / 23
p0 -578 ± 0.7929
p1 0.01286 ± 0.0001237
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000

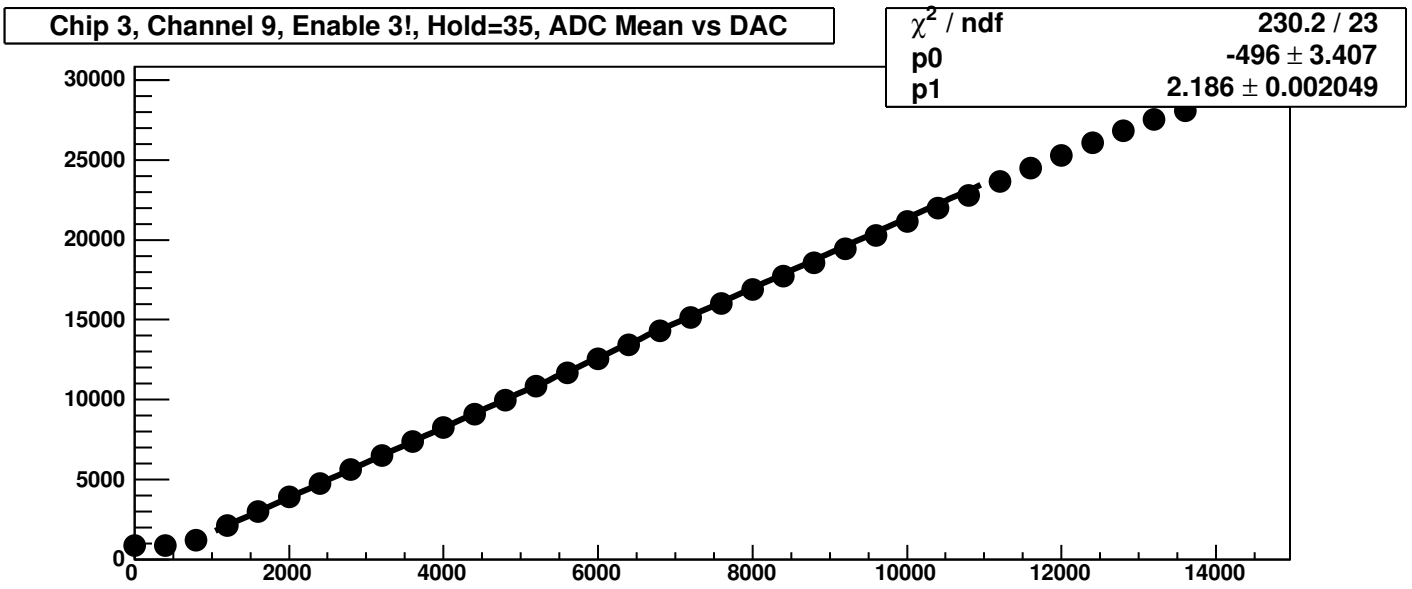

Chip 3, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

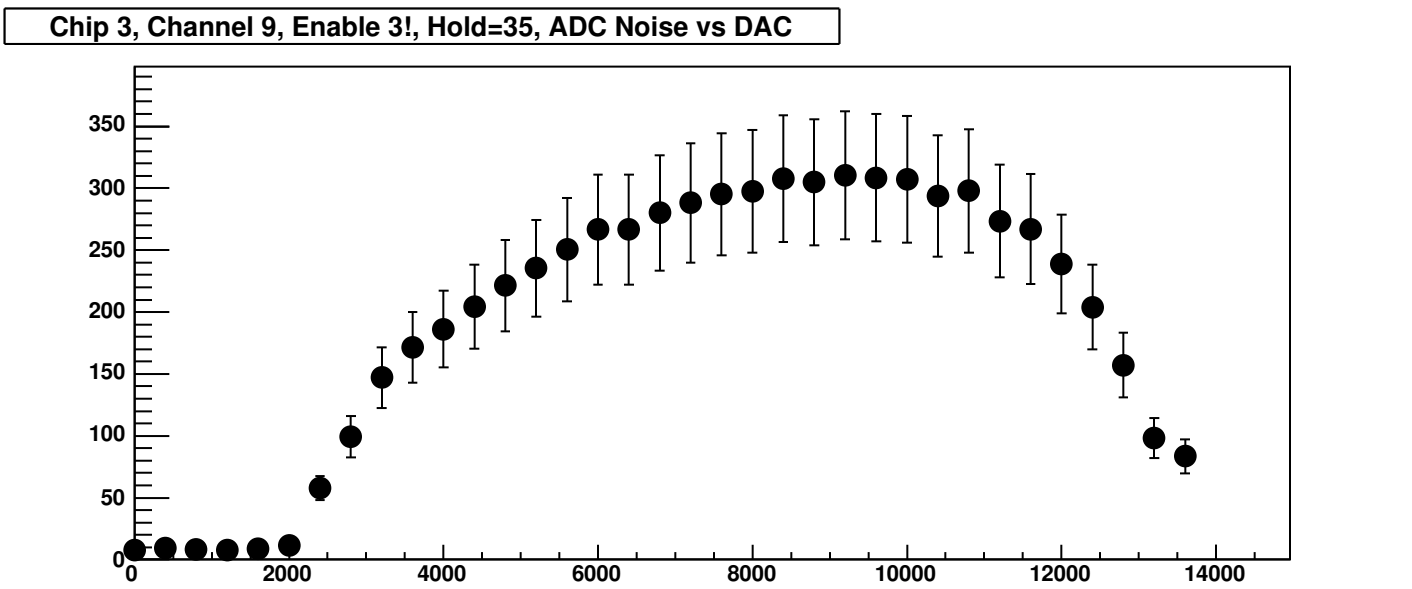

Chip 3, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

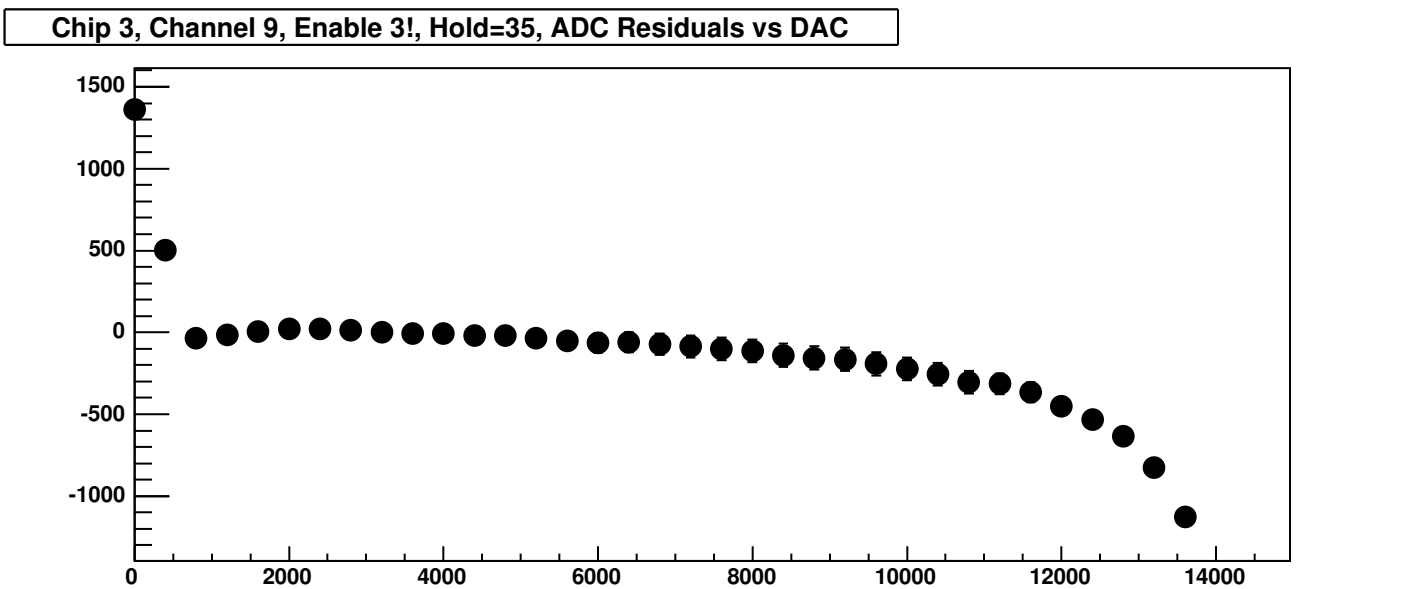

Chip 3, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 34.3 / 23
p0 -577.5 ± 0.7797
p1 0.009641 ± 0.0001177
-420
-440
-460
-480
-500
-520
-540
-560
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

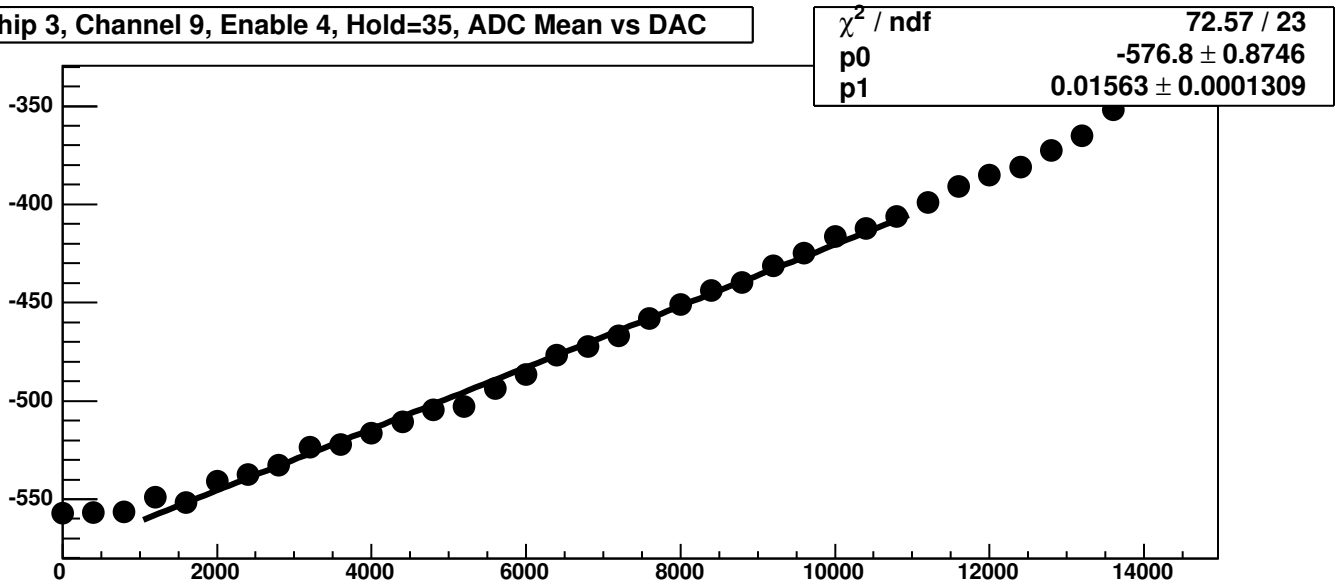

Chip 3, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 230.2 / 23
p0 -496 ± 3.407
p1 2.186 ± 0.002049
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

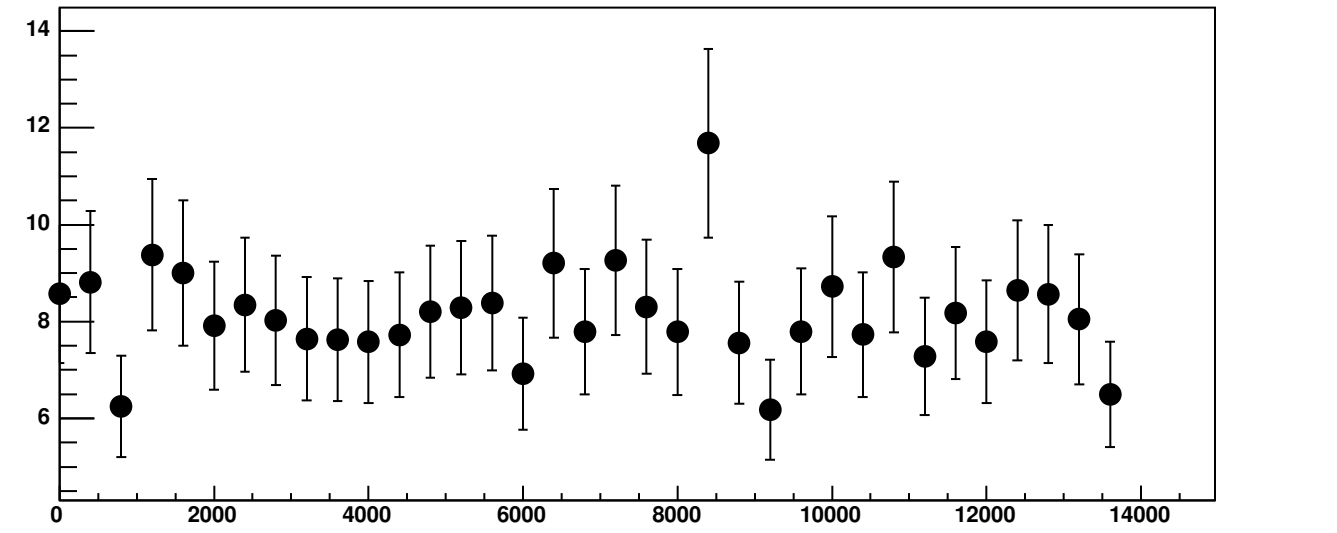

Chip 3, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

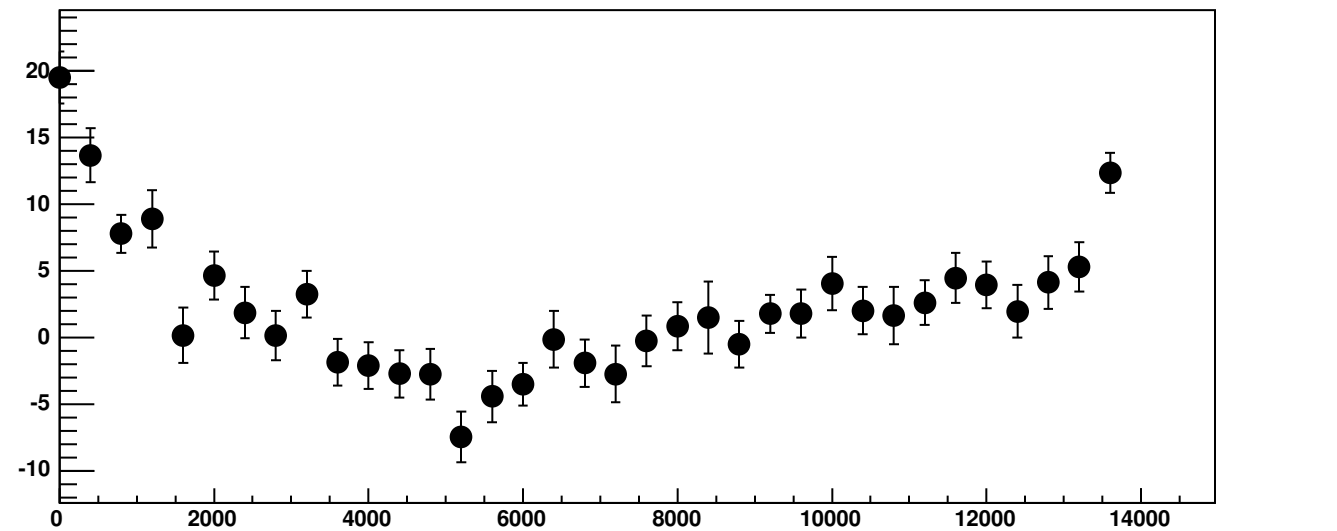
Chip 3, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



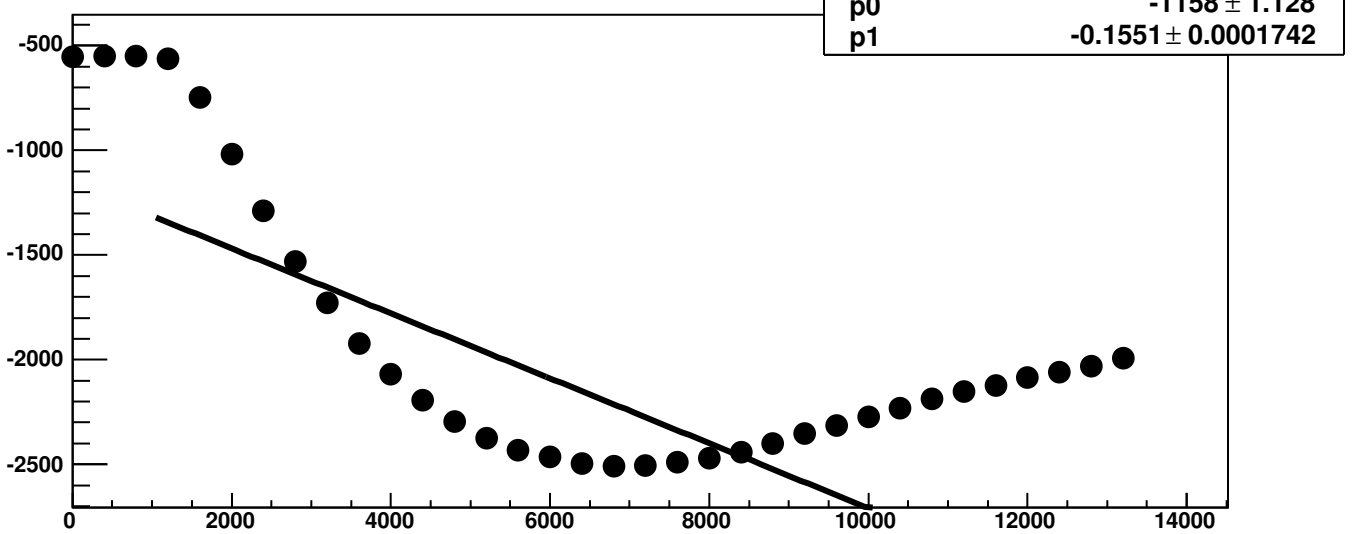
Chip 3, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



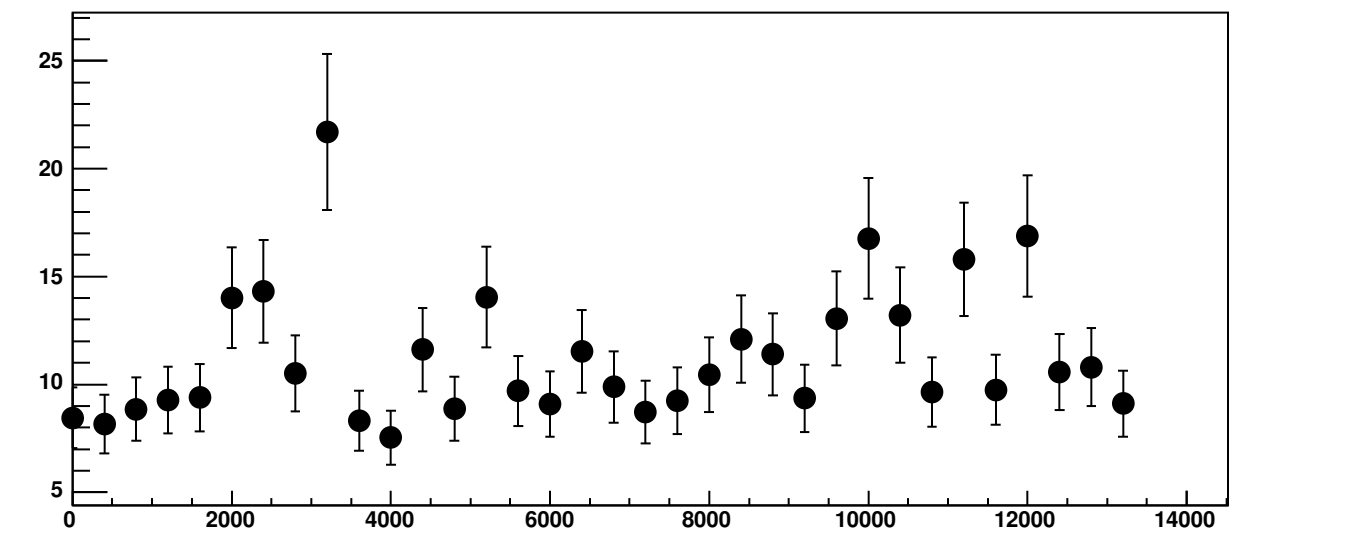
Chip 3, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



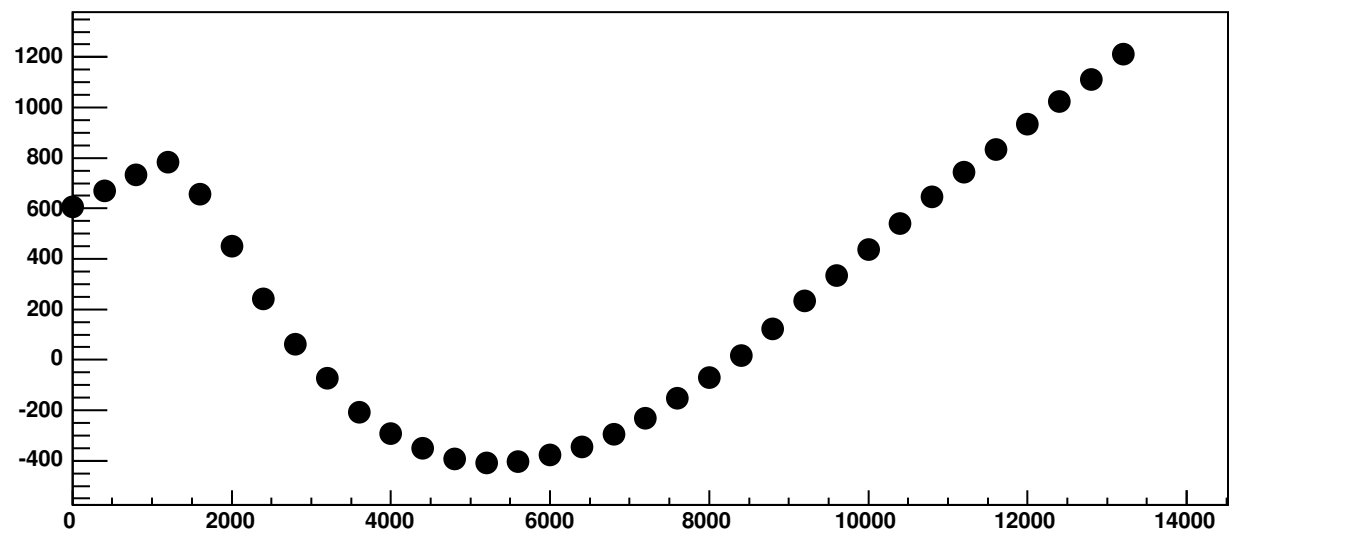
Chip 3, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

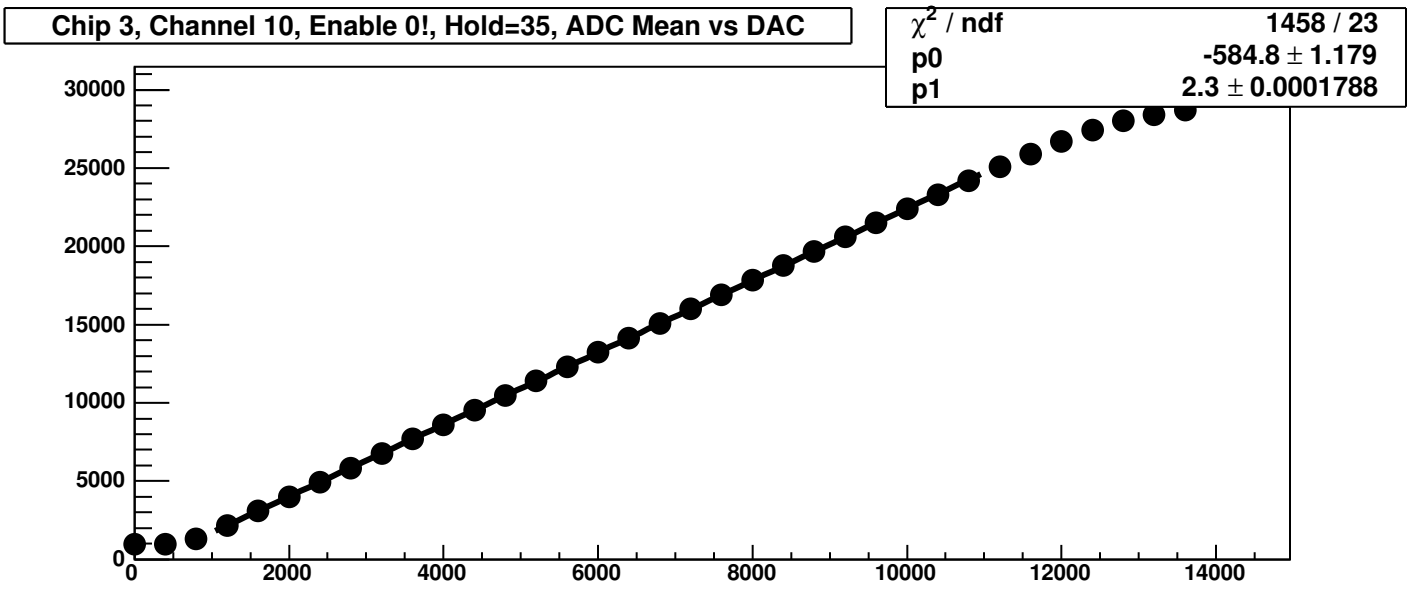


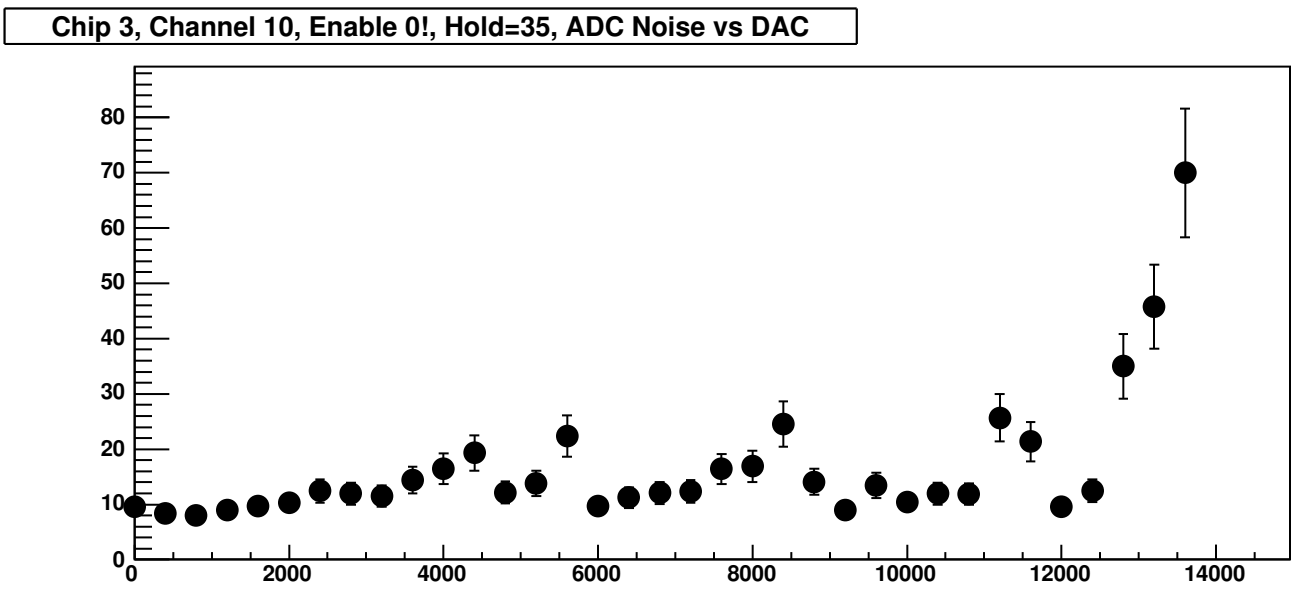
Chip 3, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

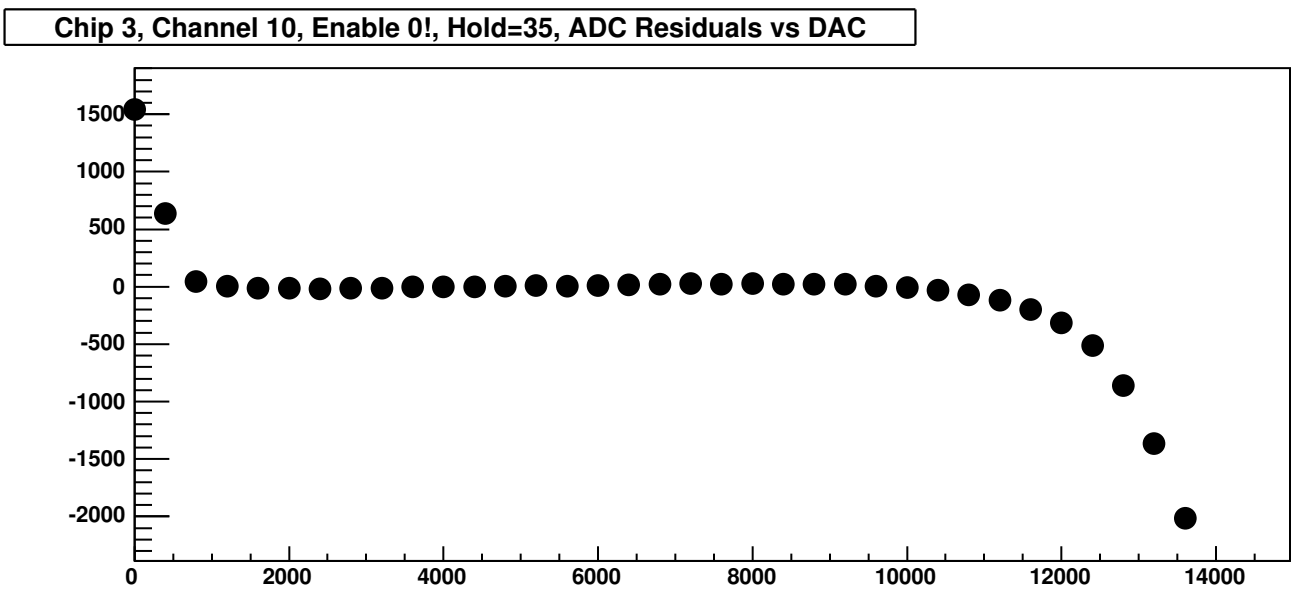


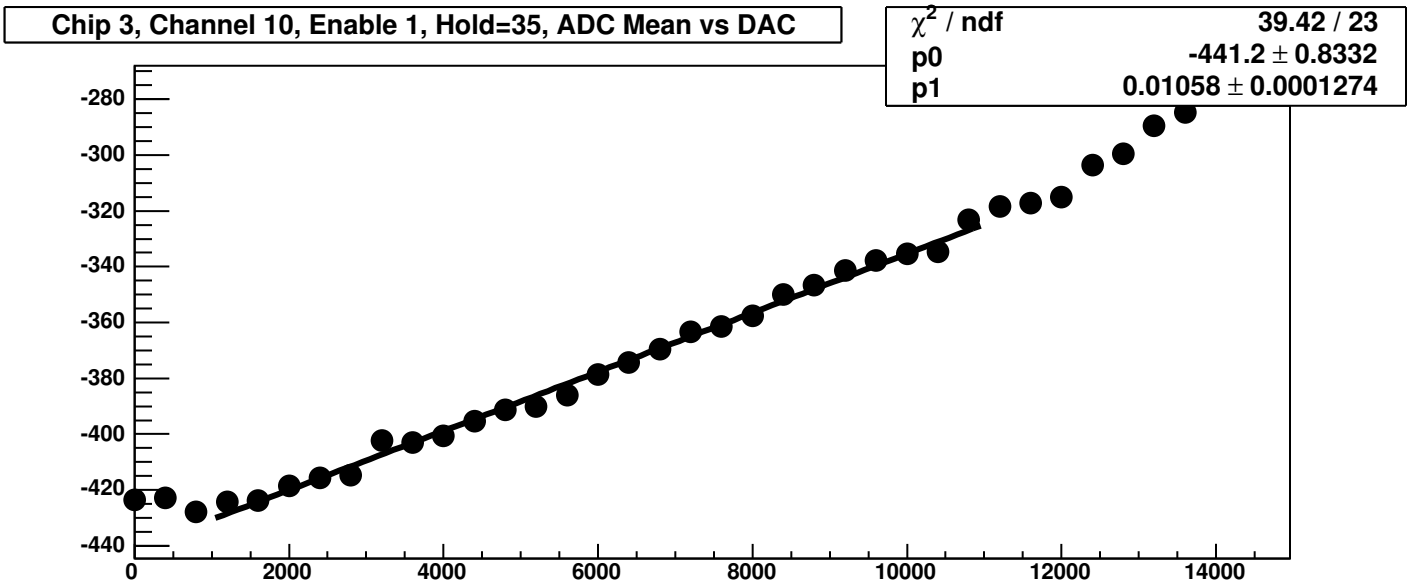
Chip 3, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

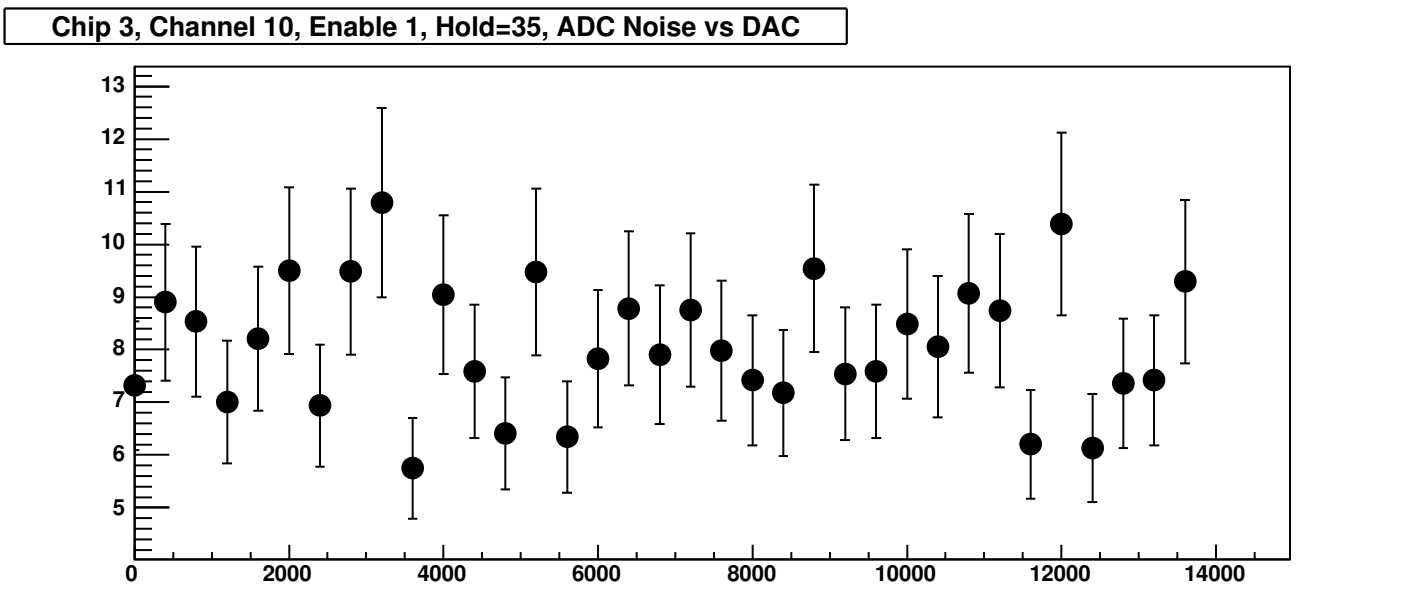


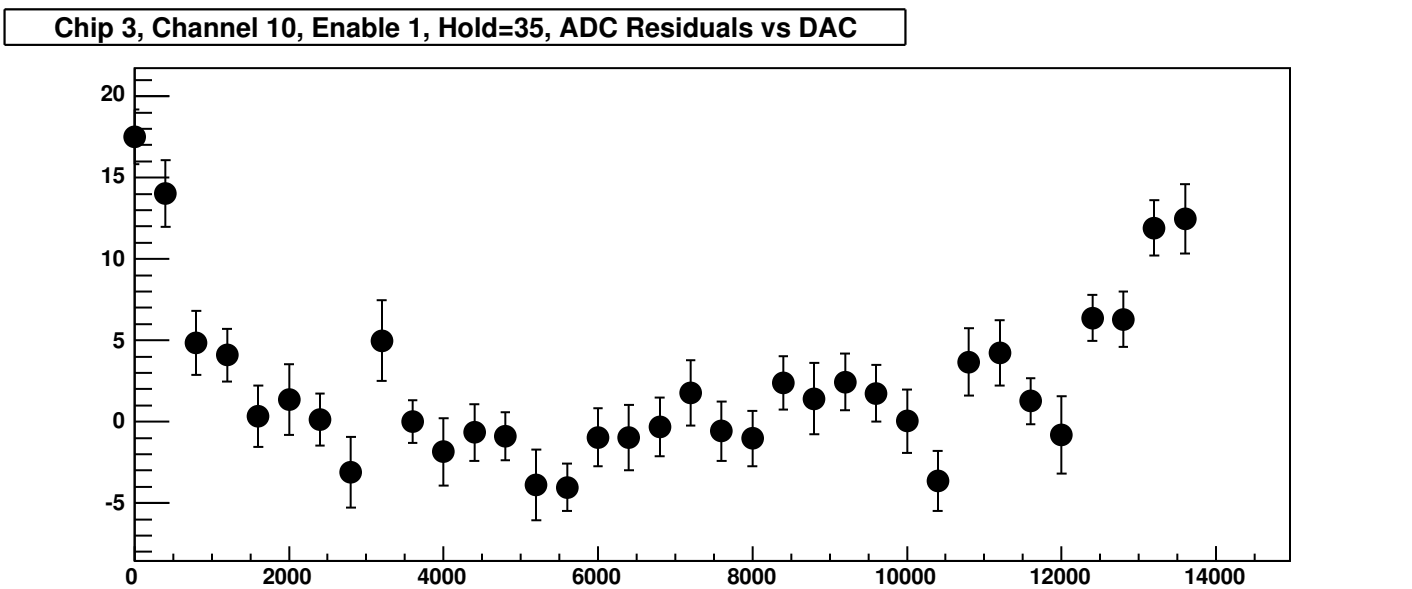

Chip 3, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1458 / 23
p0 -584.8 ± 1.179
p1 2.3 ± 0.0001788
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

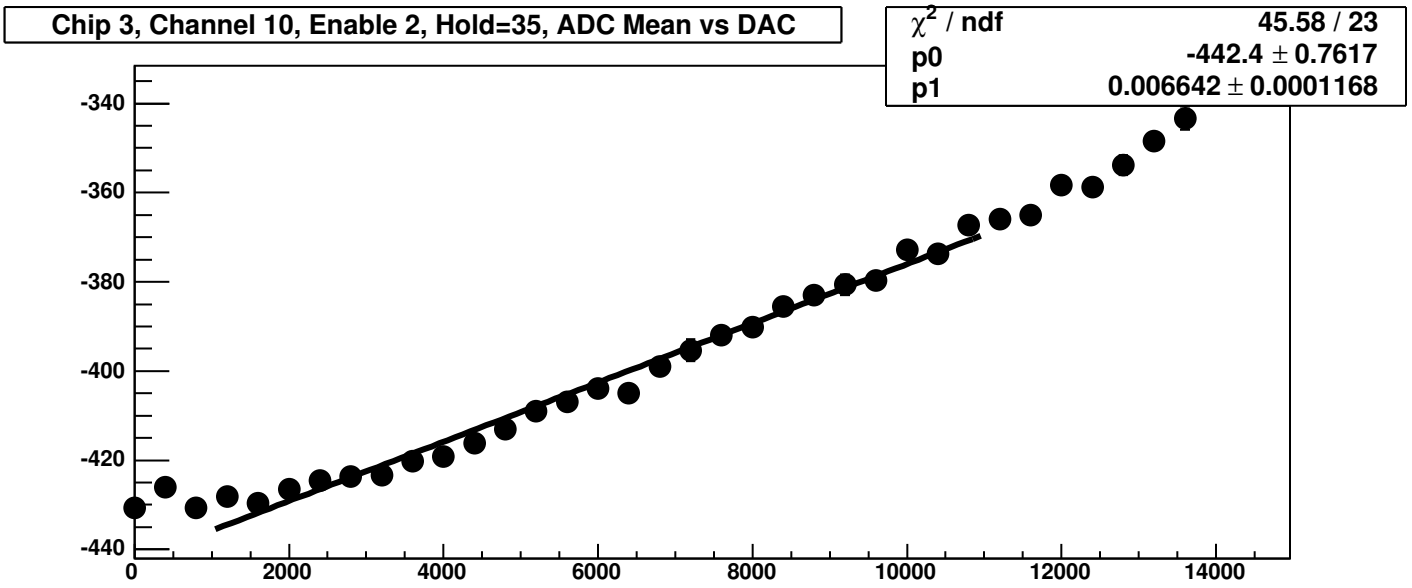

Chip 3, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

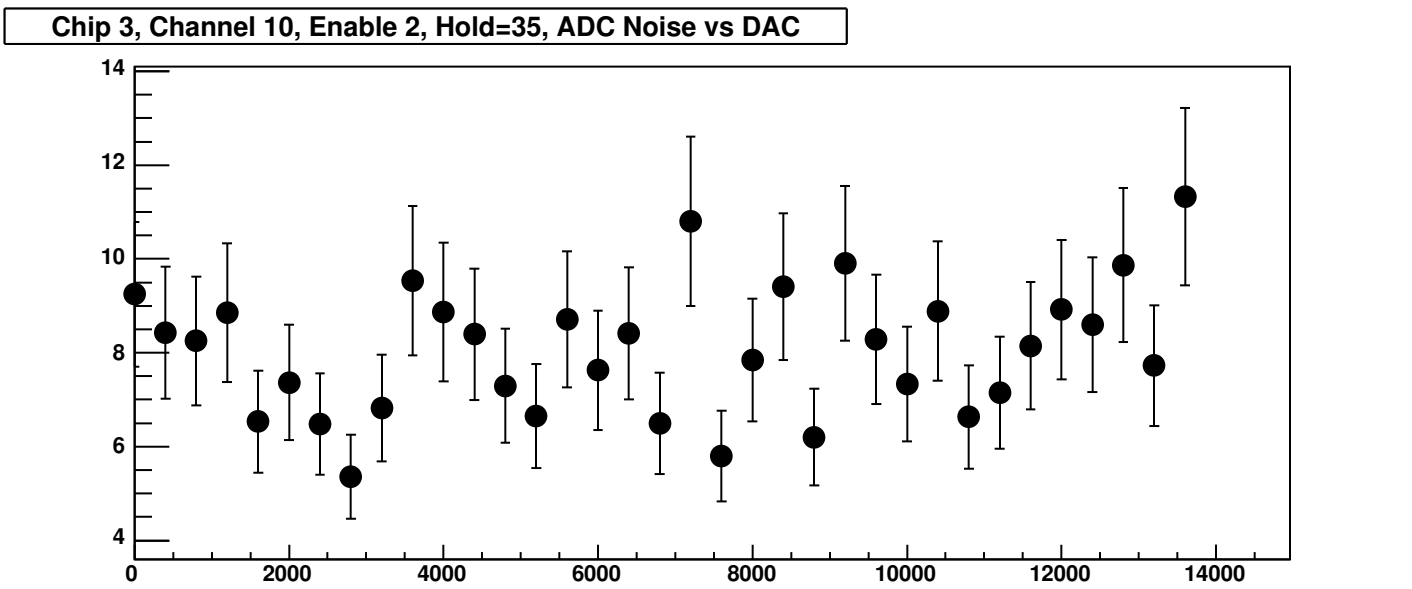

Chip 3, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

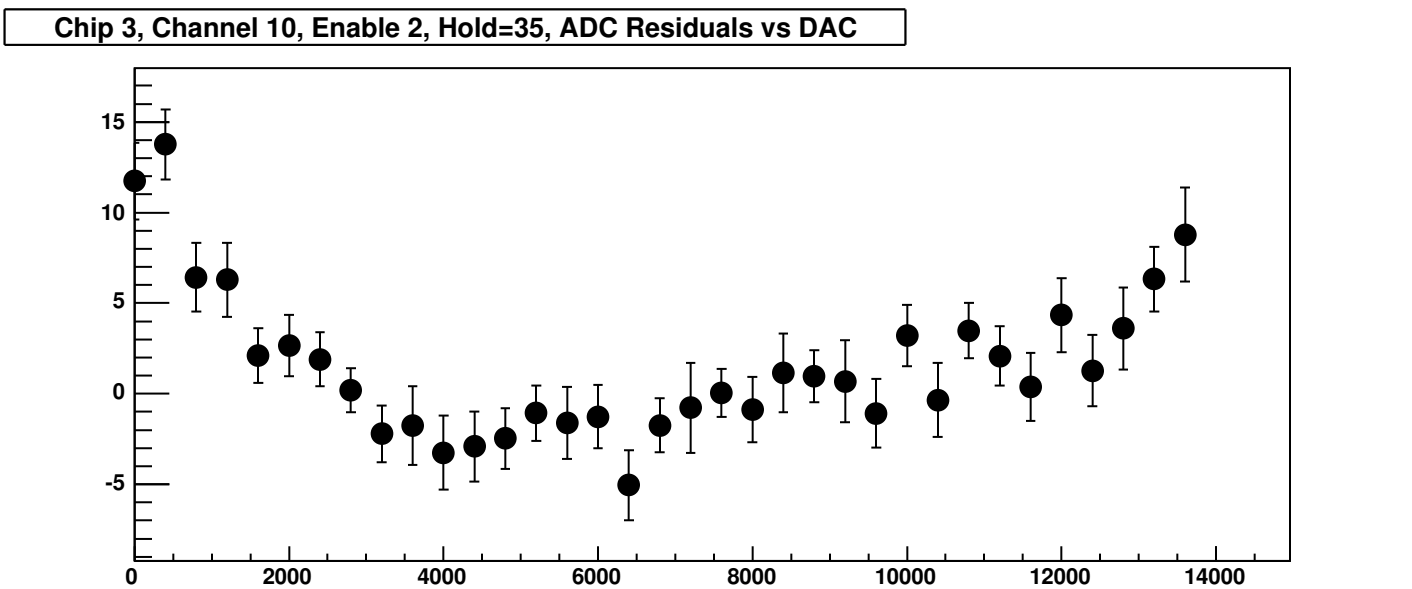

Chip 3, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 39.42 / 23
p0 -441.2 ± 0.8332
p1 0.01058 ± 0.0001274
-280
-300
-320
-340
-360
-380
-400
-420
-440
0
2000
4000
6000
8000
10000
12000
14000

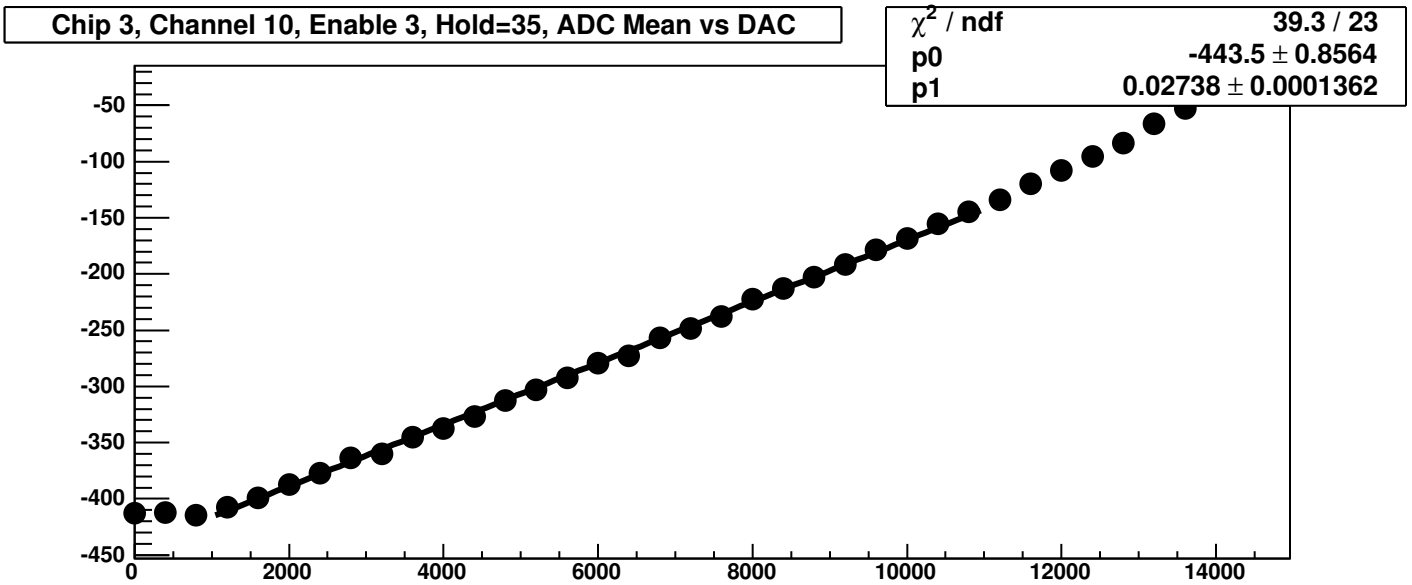

Chip 3, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

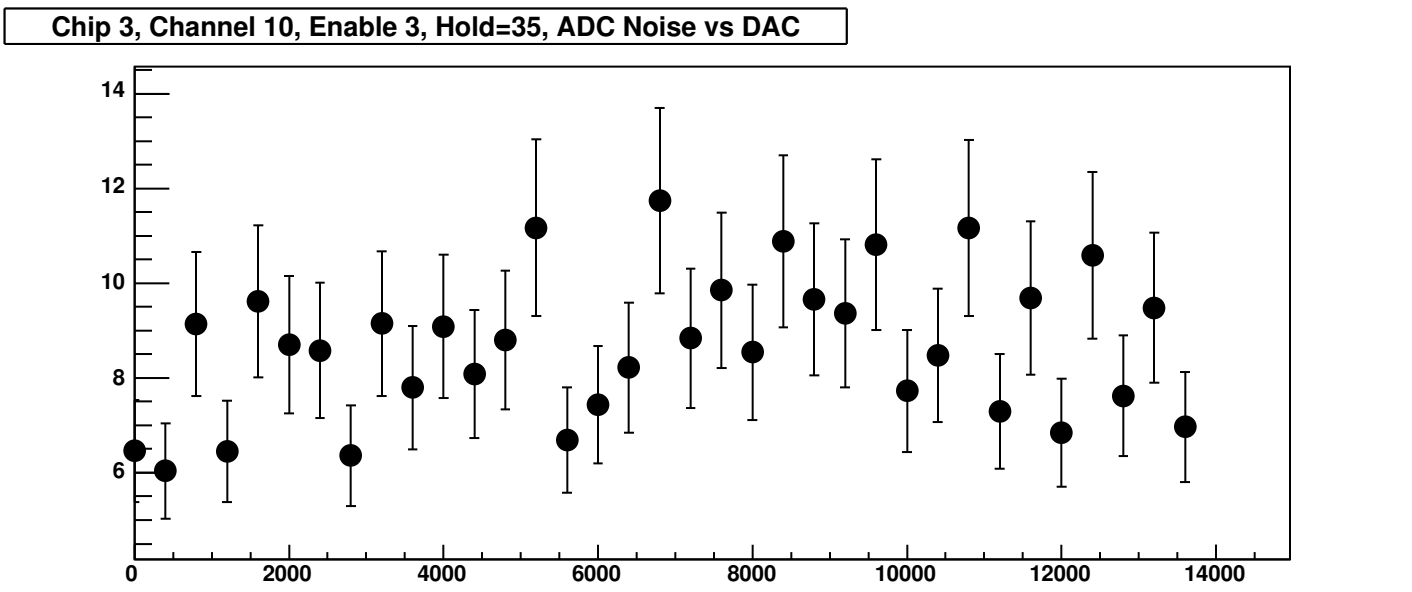

Chip 3, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

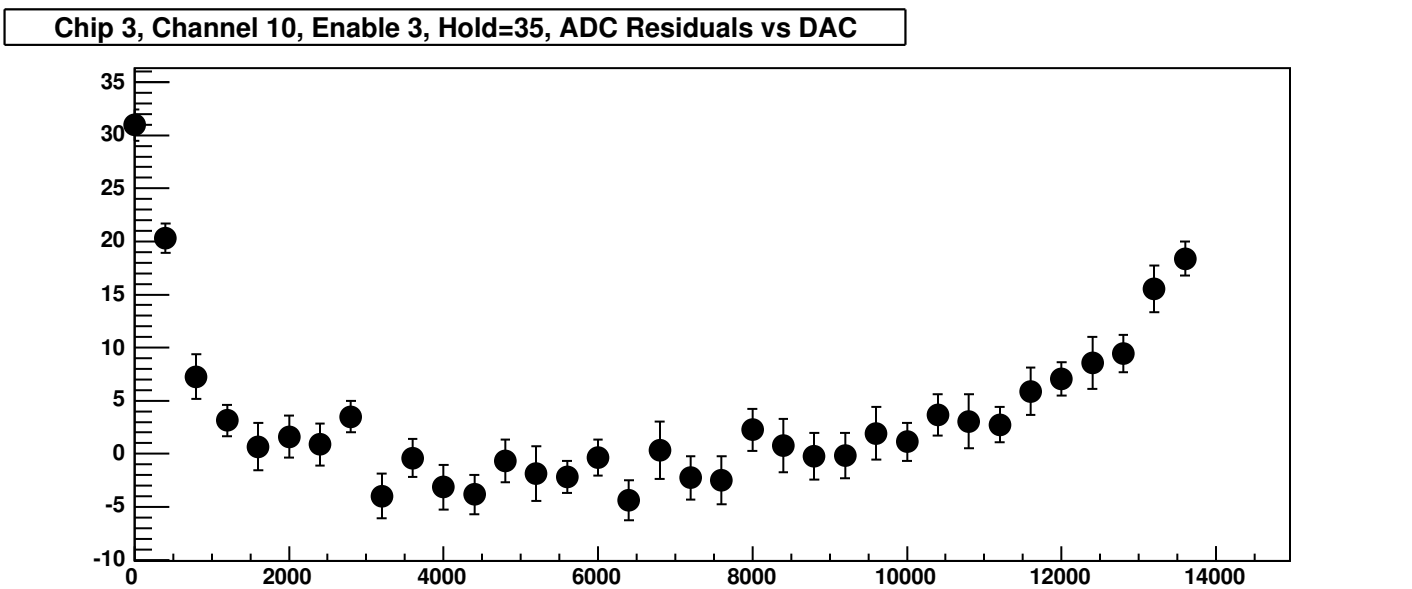

Chip 3, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 45.58 / 23
p0 -442.4 ± 0.7617
p1 0.006642 ± 0.0001168
-340
-360
-380
-400
-420
-440
0
2000
4000
6000
8000
10000
12000
14000

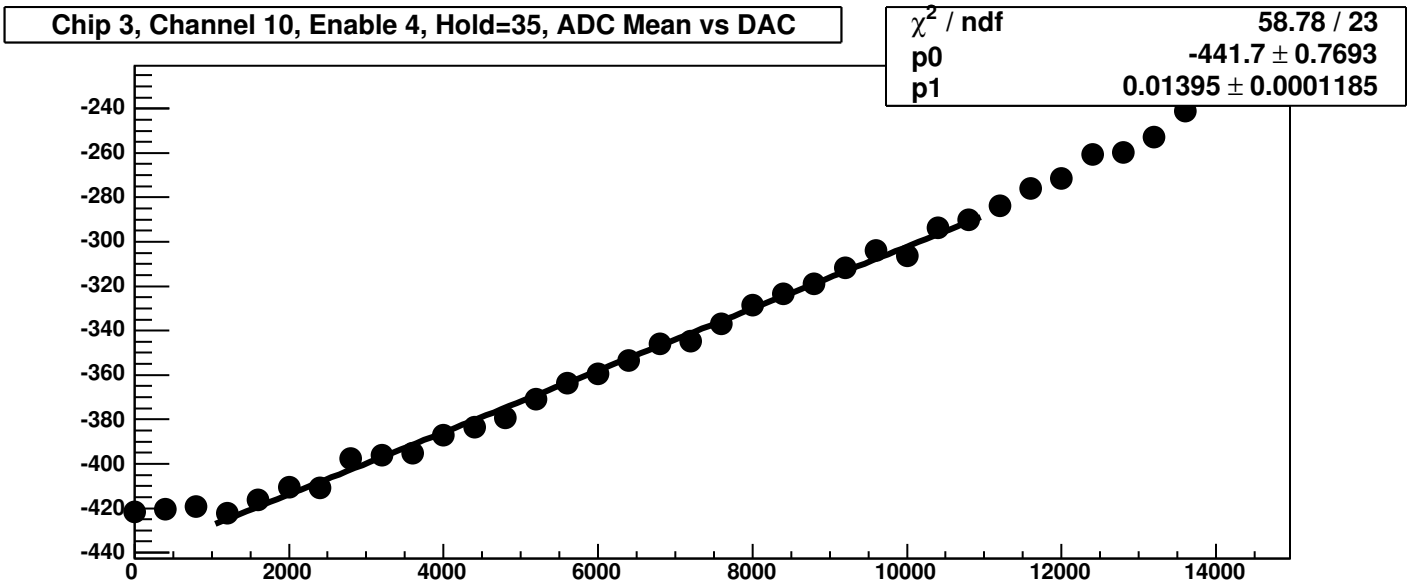

Chip 3, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

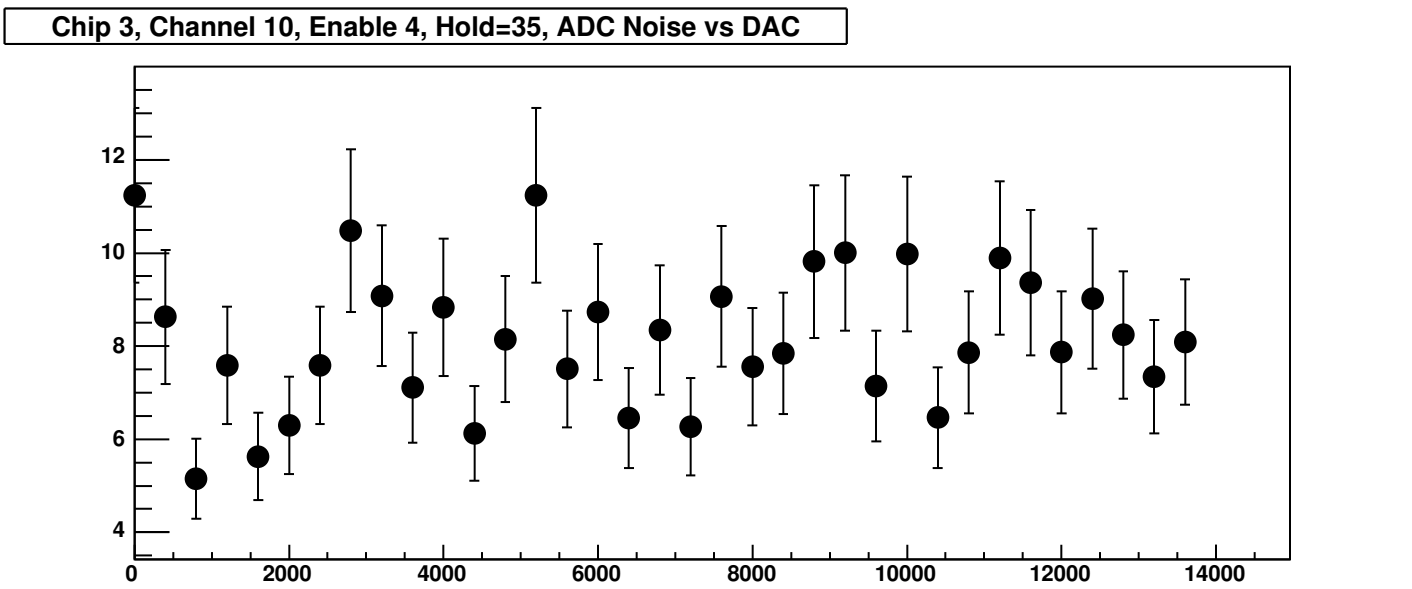

Chip 3, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

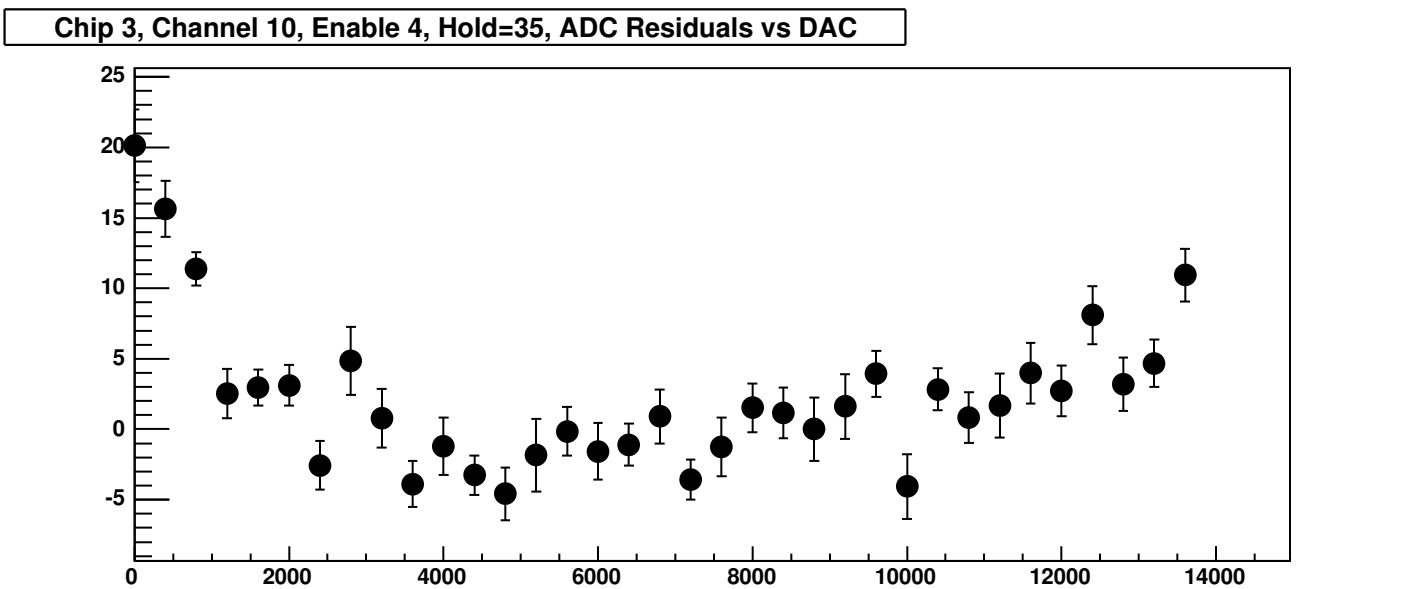

Chip 3, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 39.3 / 23
p0 -443.5 ± 0.8564
p1 0.02738 ± 0.0001362
-50
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

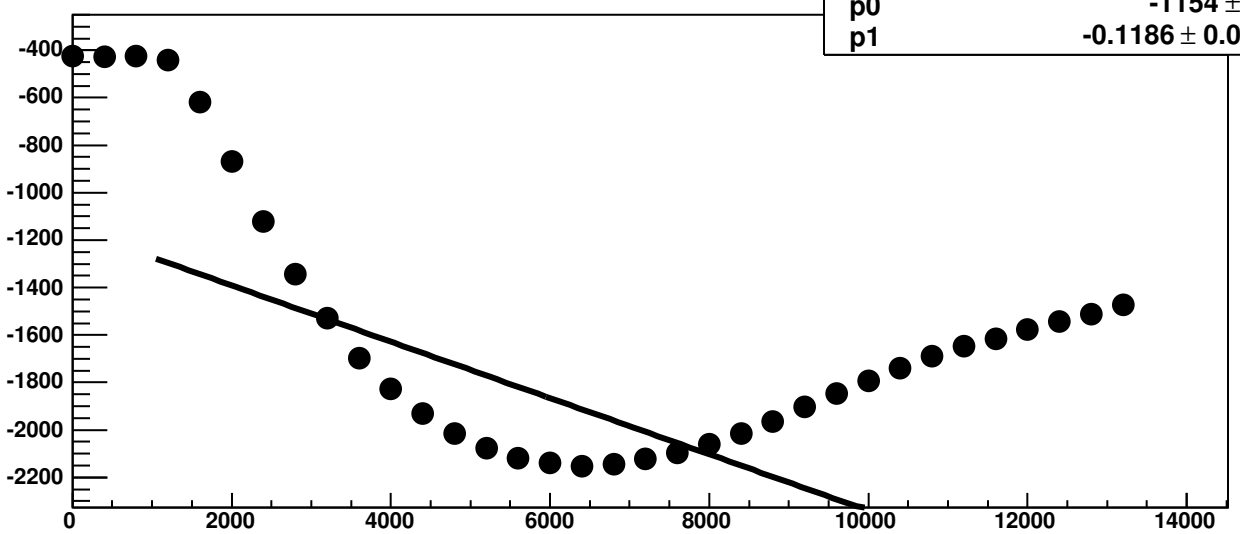

Chip 3, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 58.78 / 23
p0 -441.7 ± 0.7693
p1 0.01395 ± 0.0001185
-240
-260
-280
-300
-320
-340
-360
-380
-400
-420
-440
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

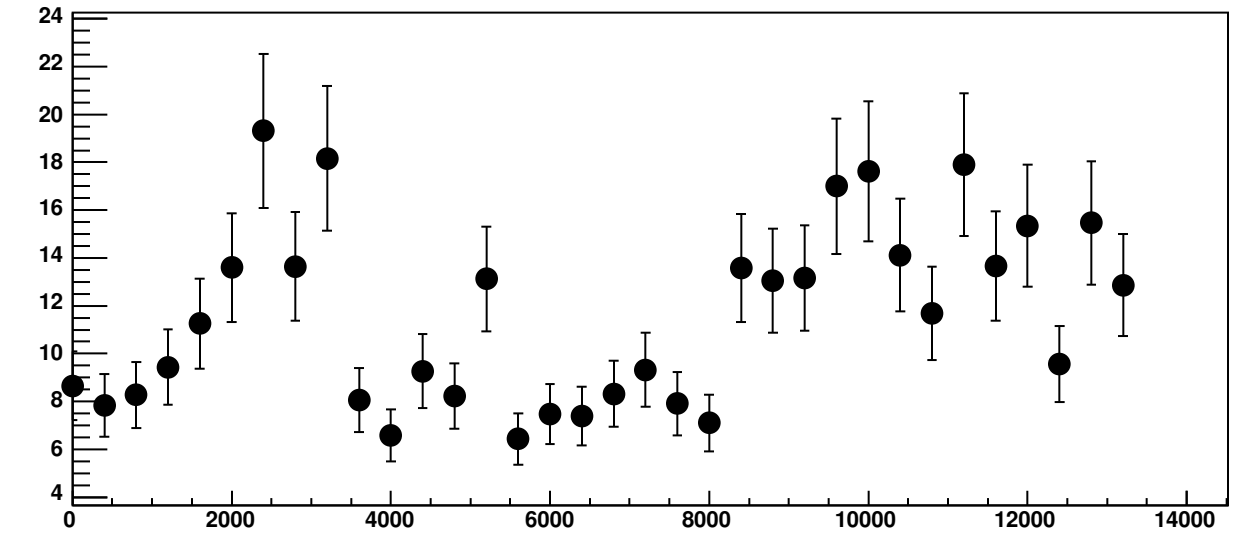

Chip 3, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

Chip 3, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

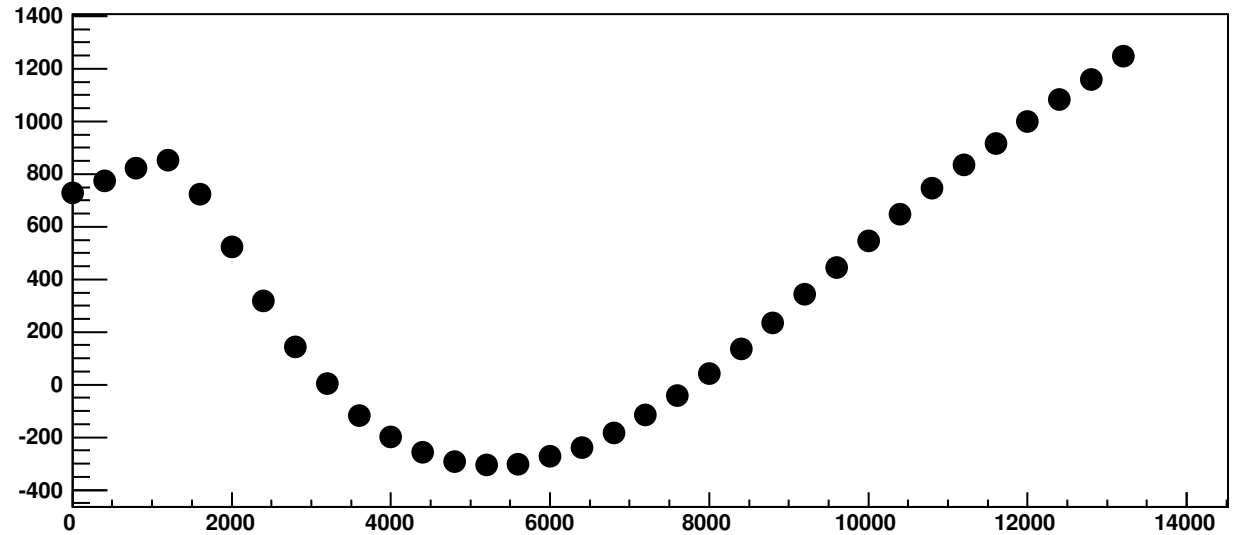
χ^2 / ndf	6.1e+05 / 23
p0	-1154 ± 1.179
p1	-0.1186 ± 0.000188

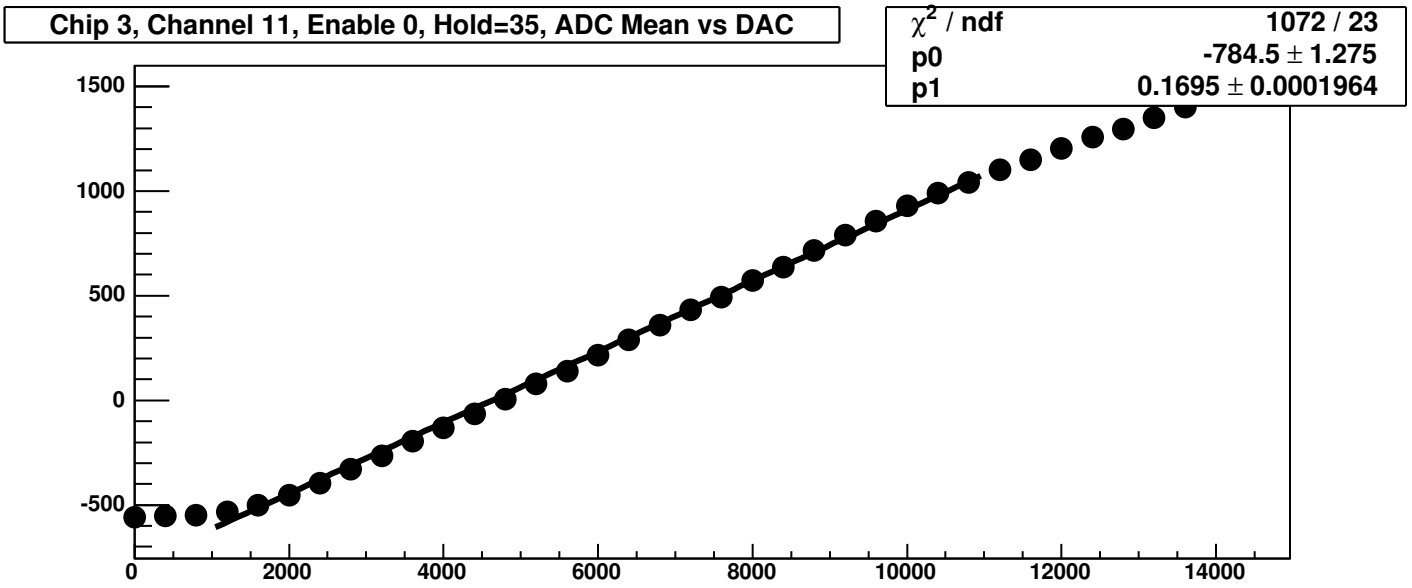


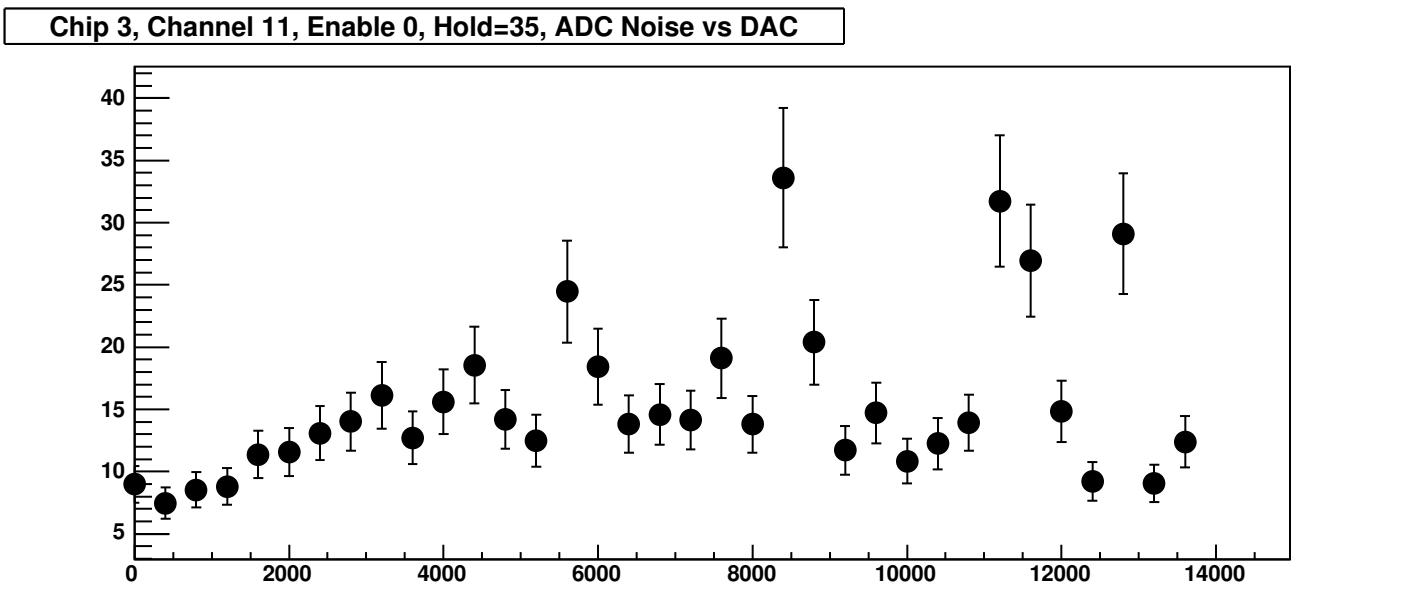
Chip 3, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

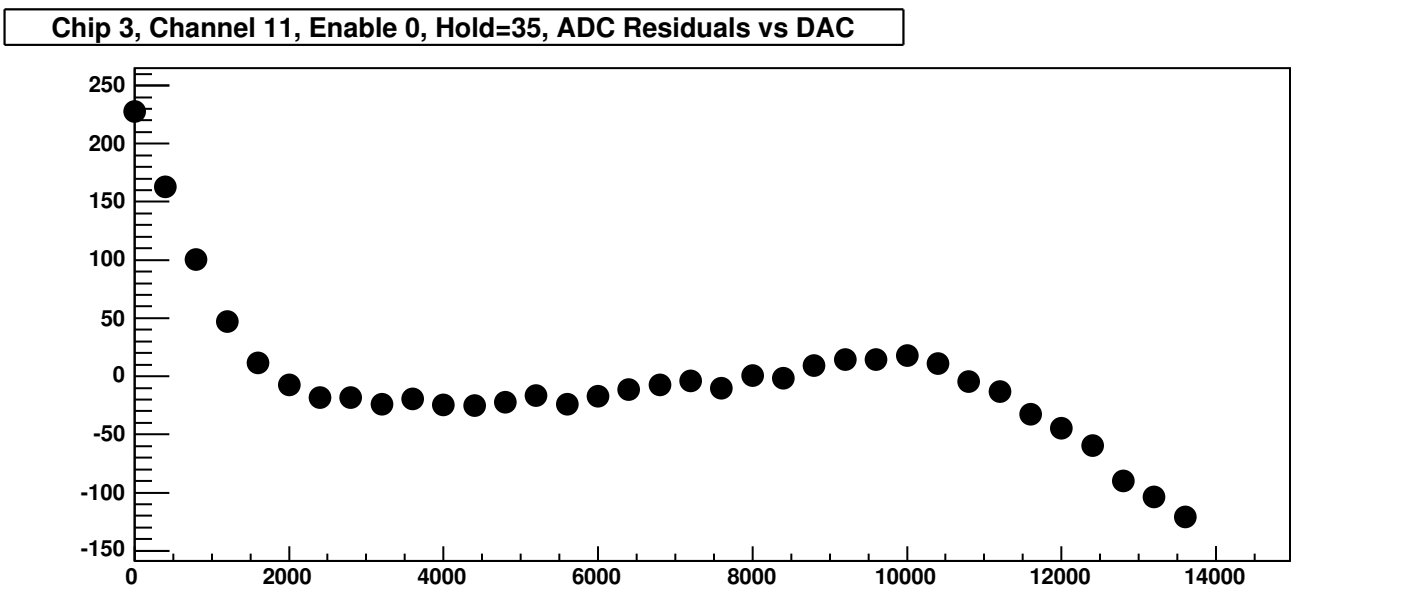


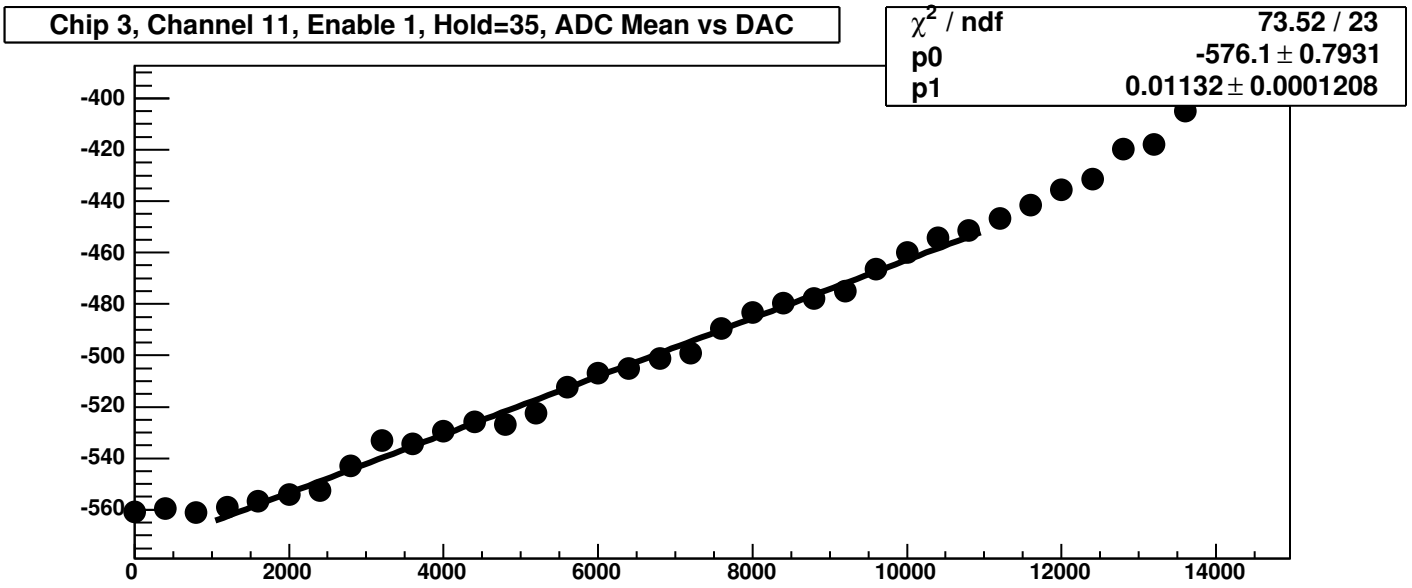
Chip 3, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

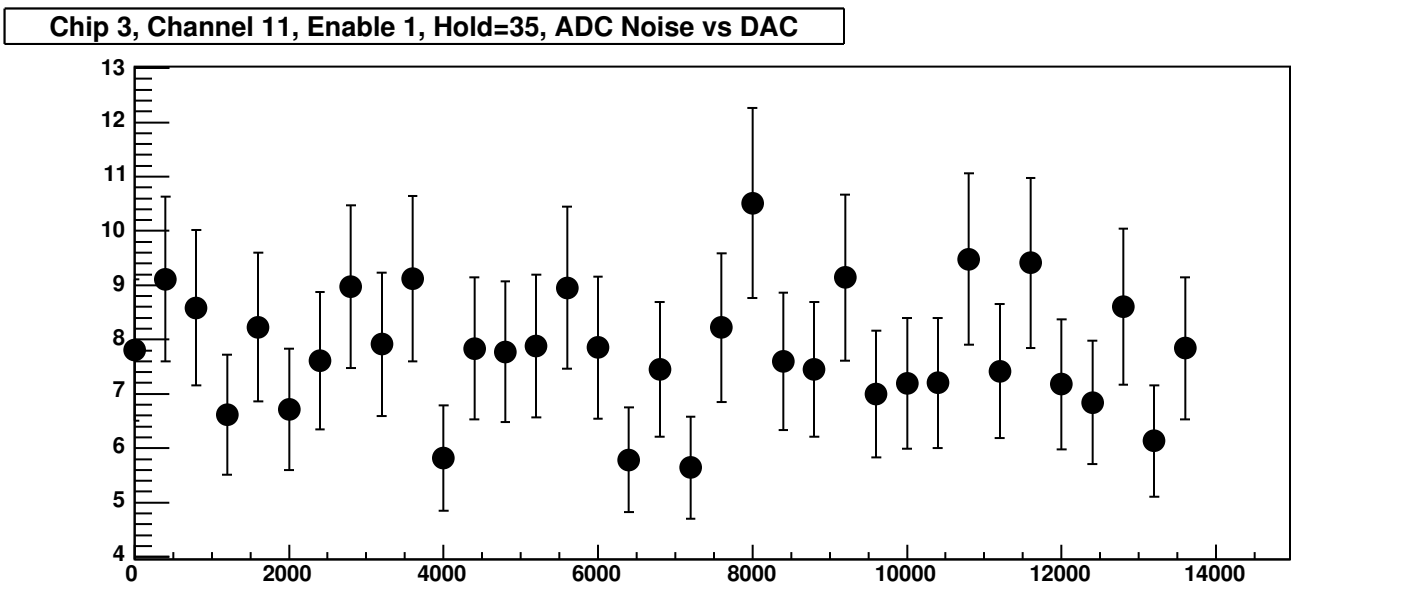


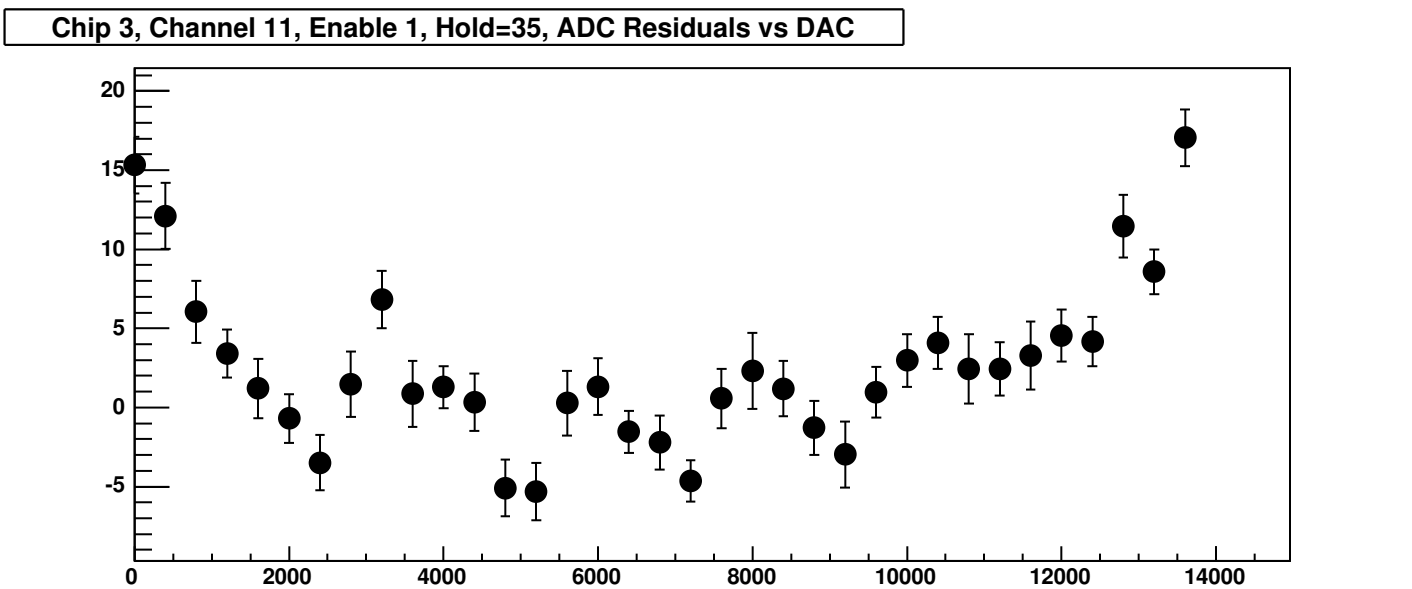

Chip 3, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1072 / 23
p0 -784.5 ± 1.275
p1 0.1695 ± 0.0001964
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

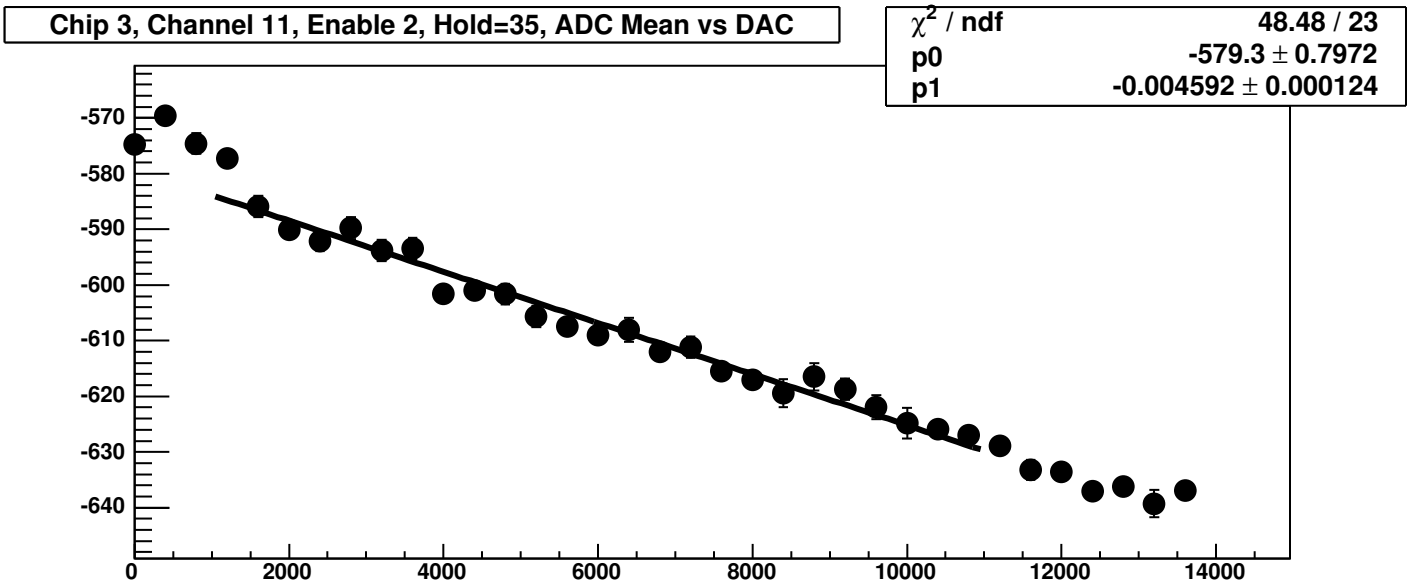

Chip 3, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

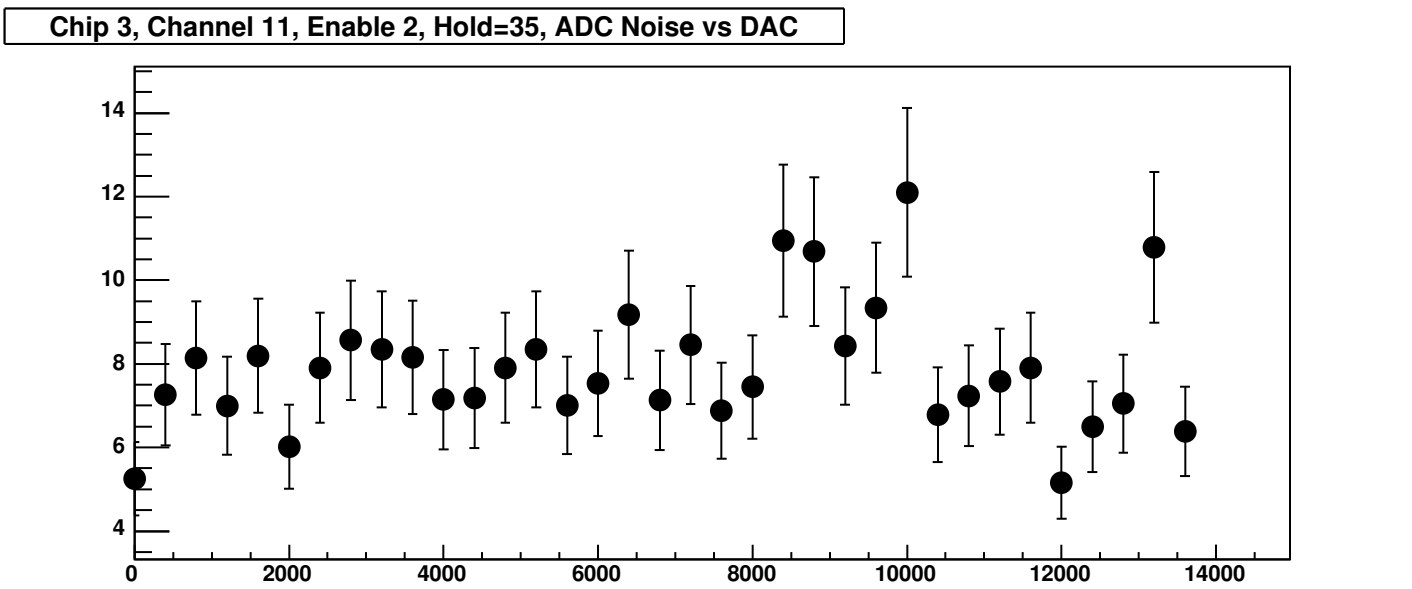

Chip 3, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

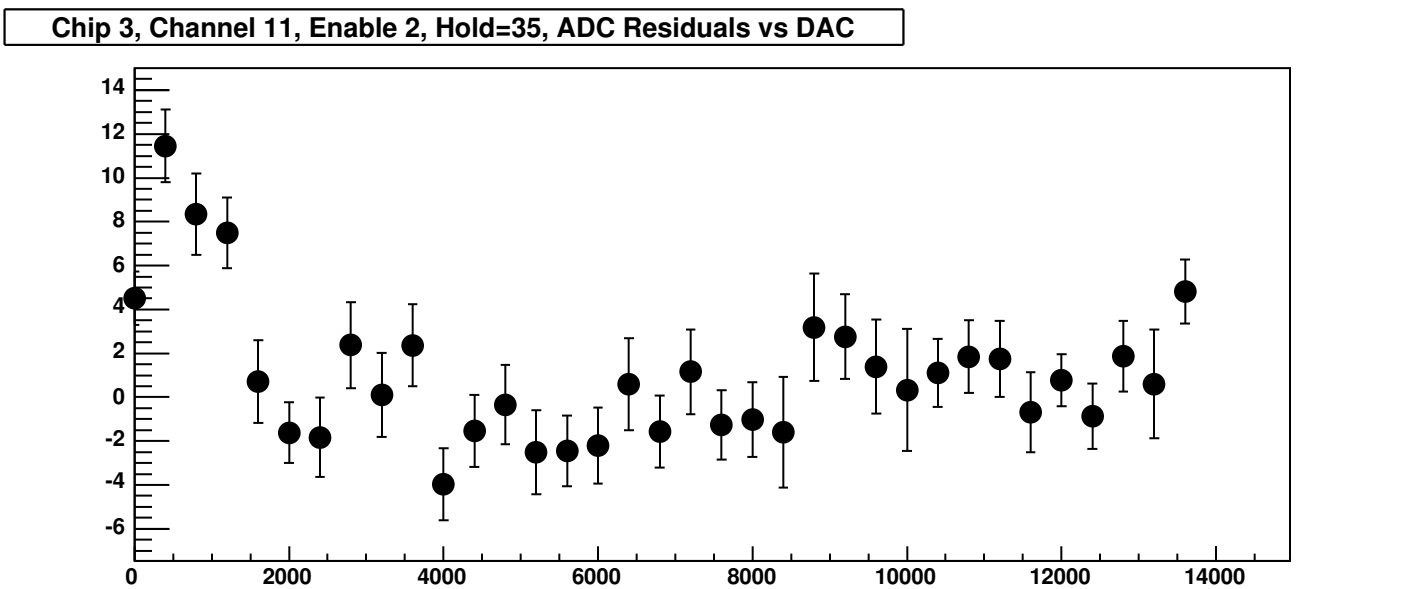

Chip 3, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 73.52 / 23
p0 -576.1 ± 0.7931
p1 0.01132 ± 0.0001208
-400
-420
-440
-460
-480
-500
-520
-540
-560
0
2000
4000
6000
8000
10000
12000
14000

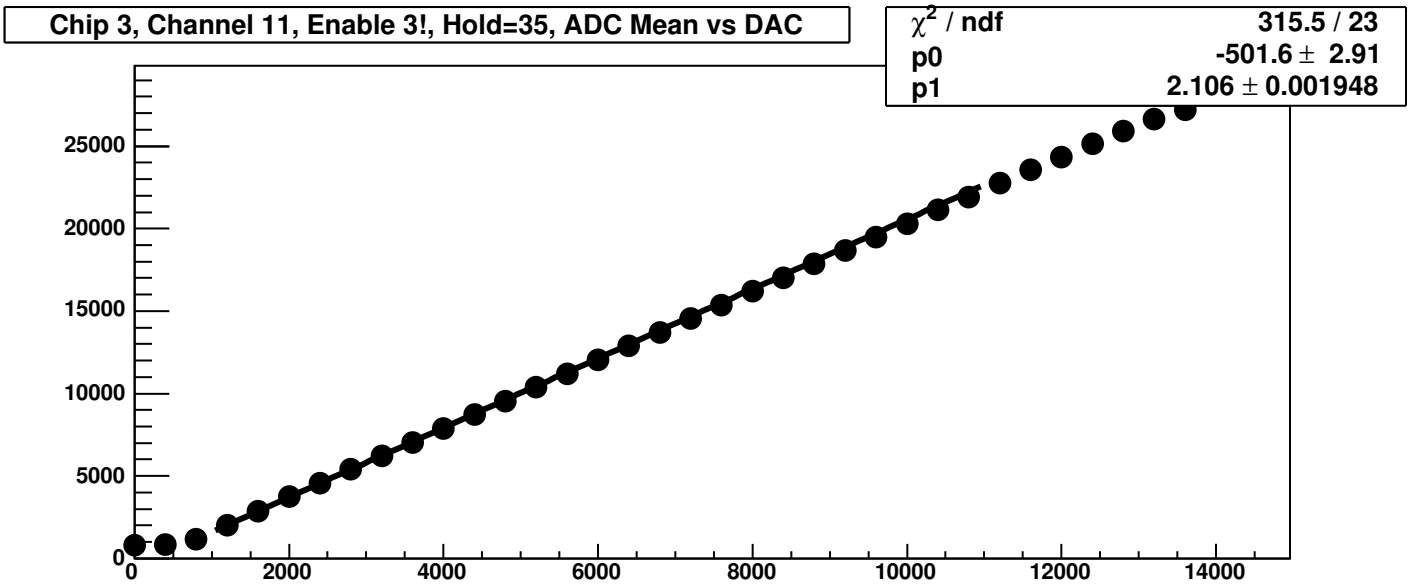

Chip 3, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

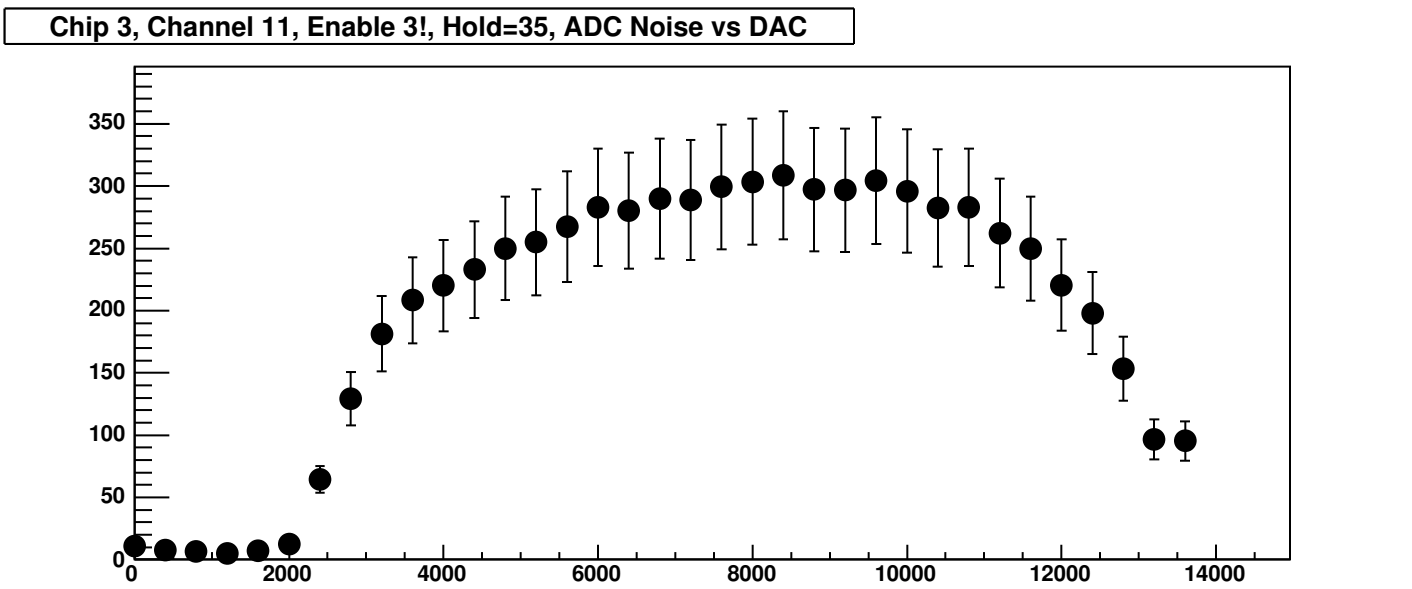

Chip 3, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

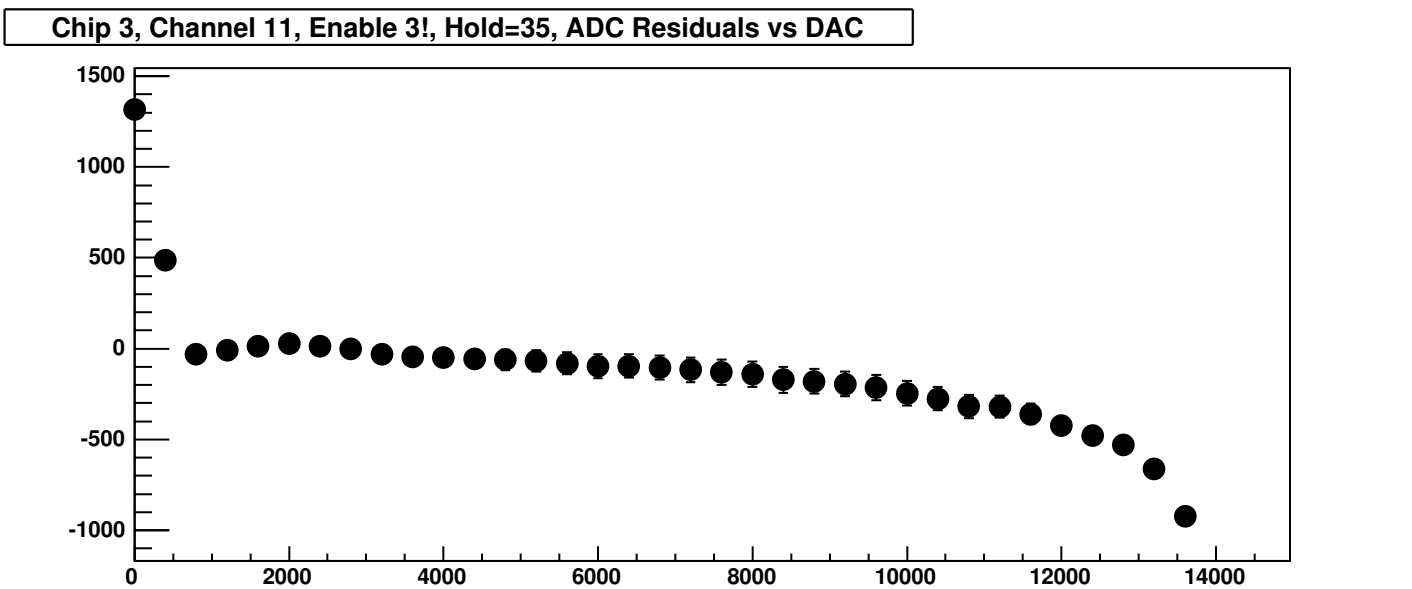

Chip 3, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 48.48 / 23
p0 -579.3 ± 0.7972
p1 -0.004592 ± 0.000124
-570
-580
-590
-600
-610
-620
-630
-640
0
2000
4000
6000
8000
10000
12000
14000

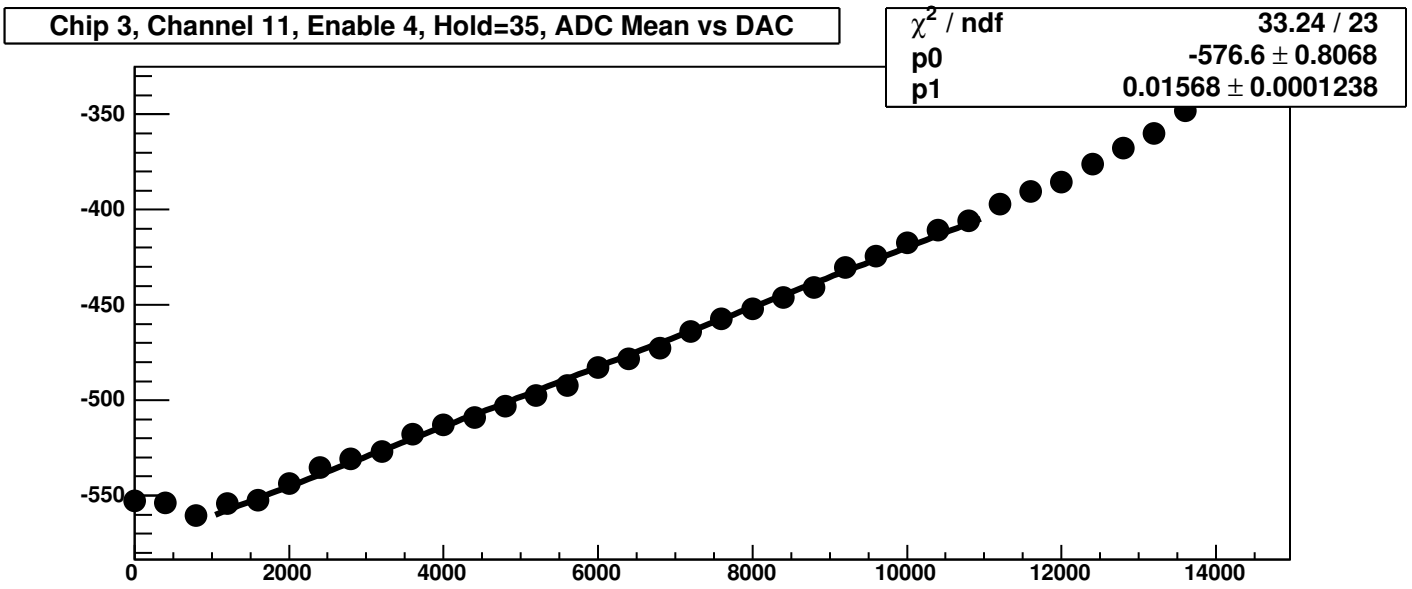

Chip 3, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

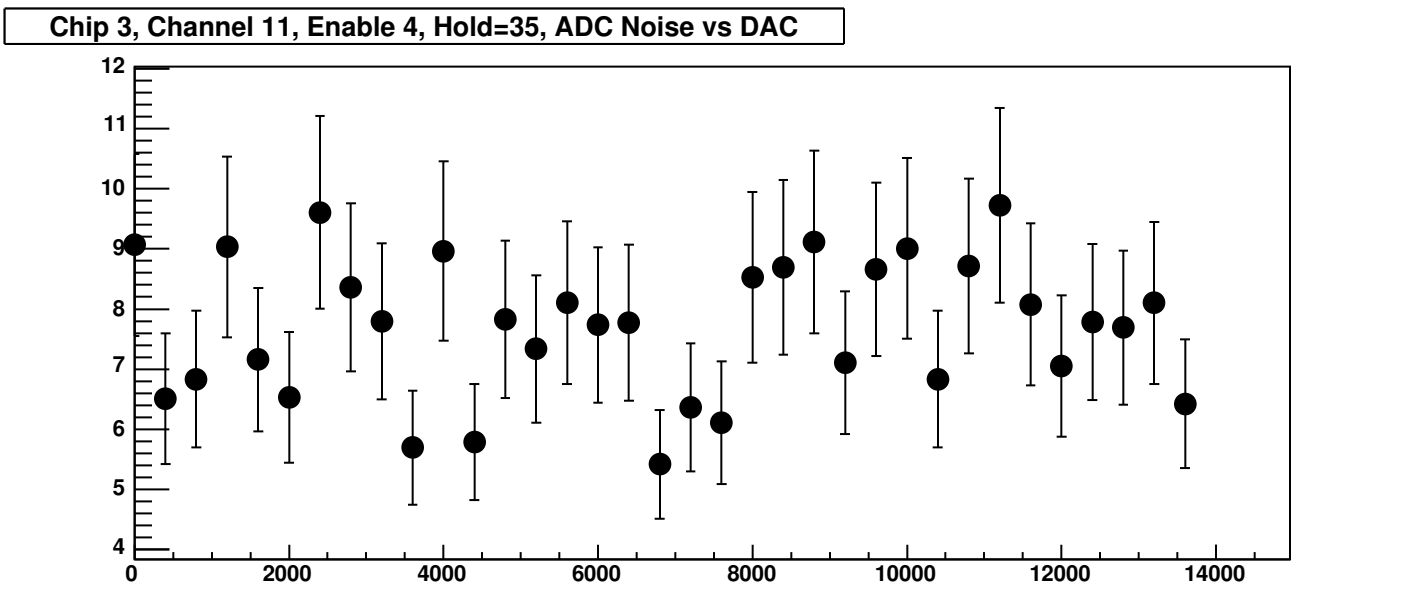

Chip 3, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000

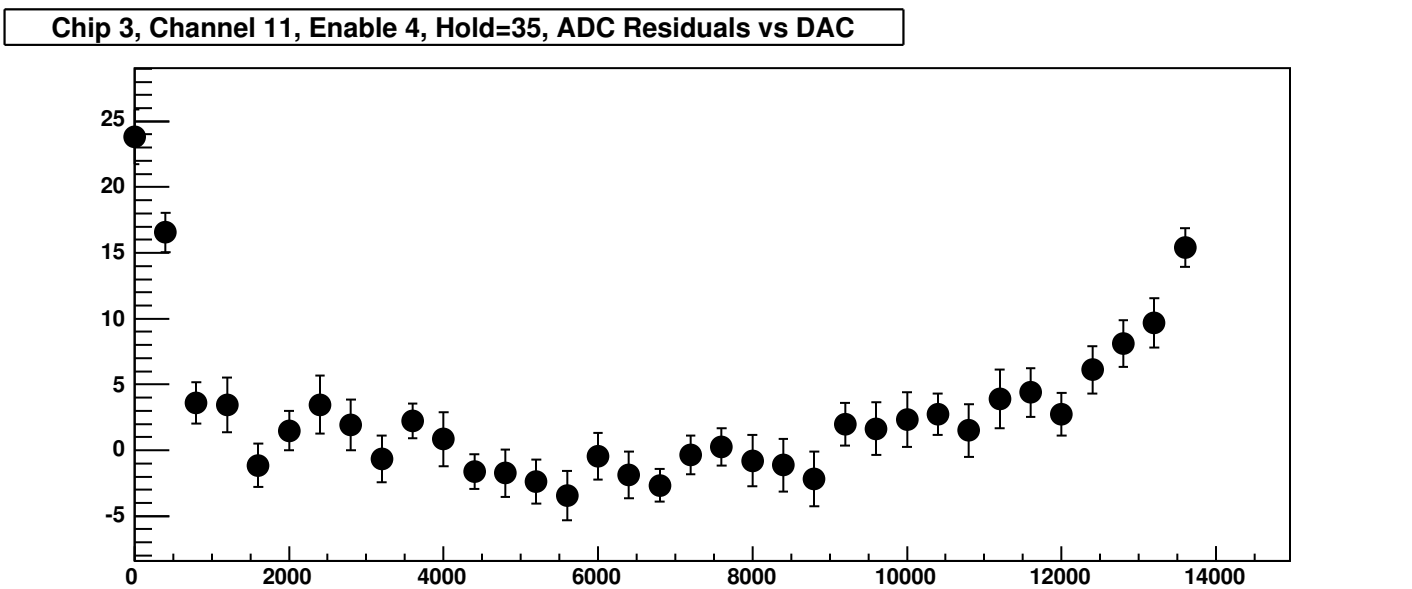

Chip 3, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 315.5 / 23
p0 -501.6 ± 2.91
p1 2.106 ± 0.001948
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

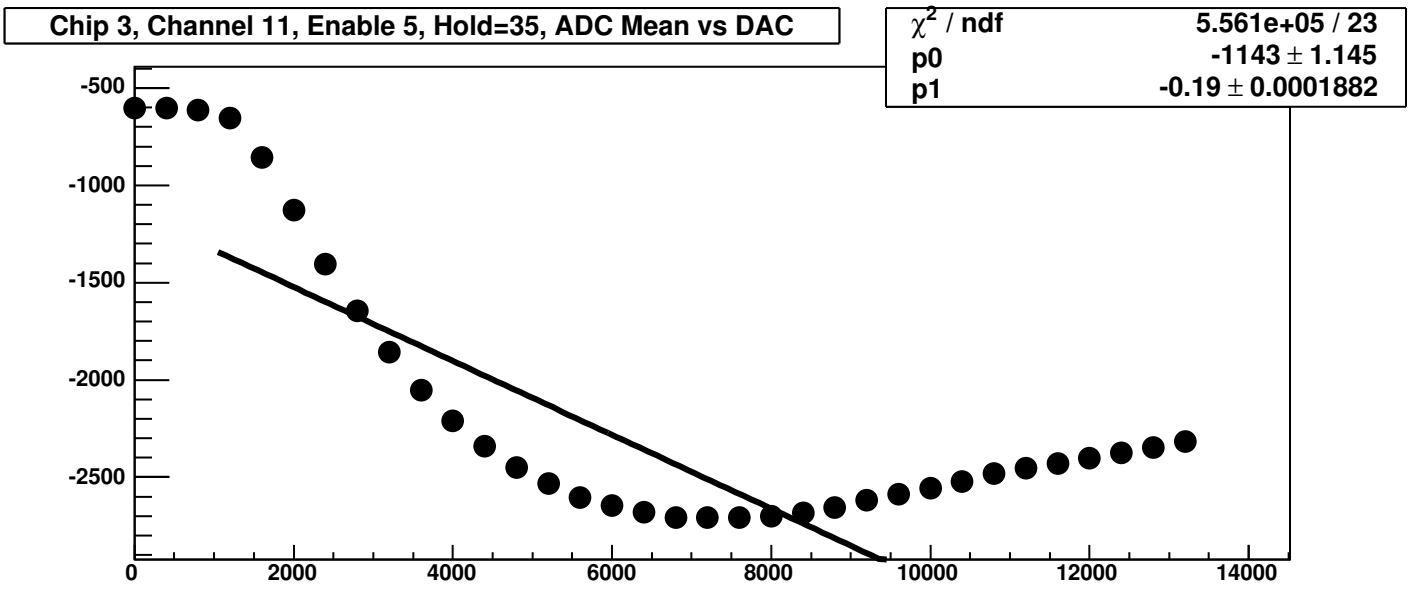

Chip 3, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

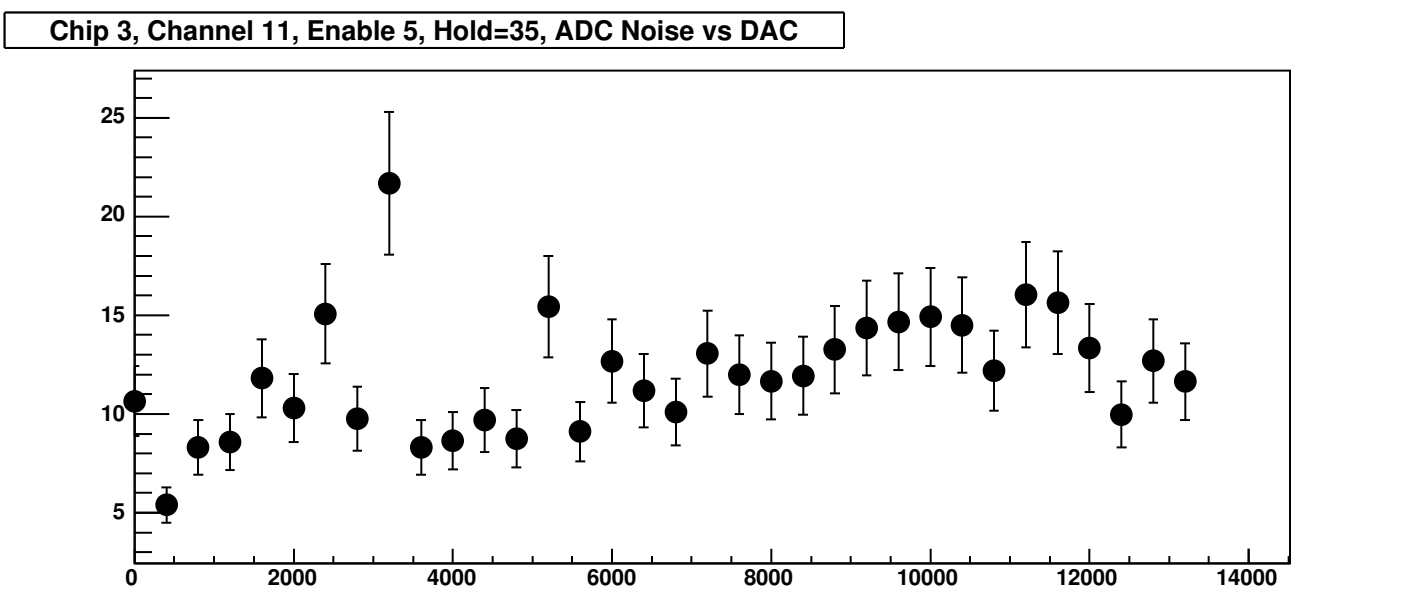

Chip 3, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

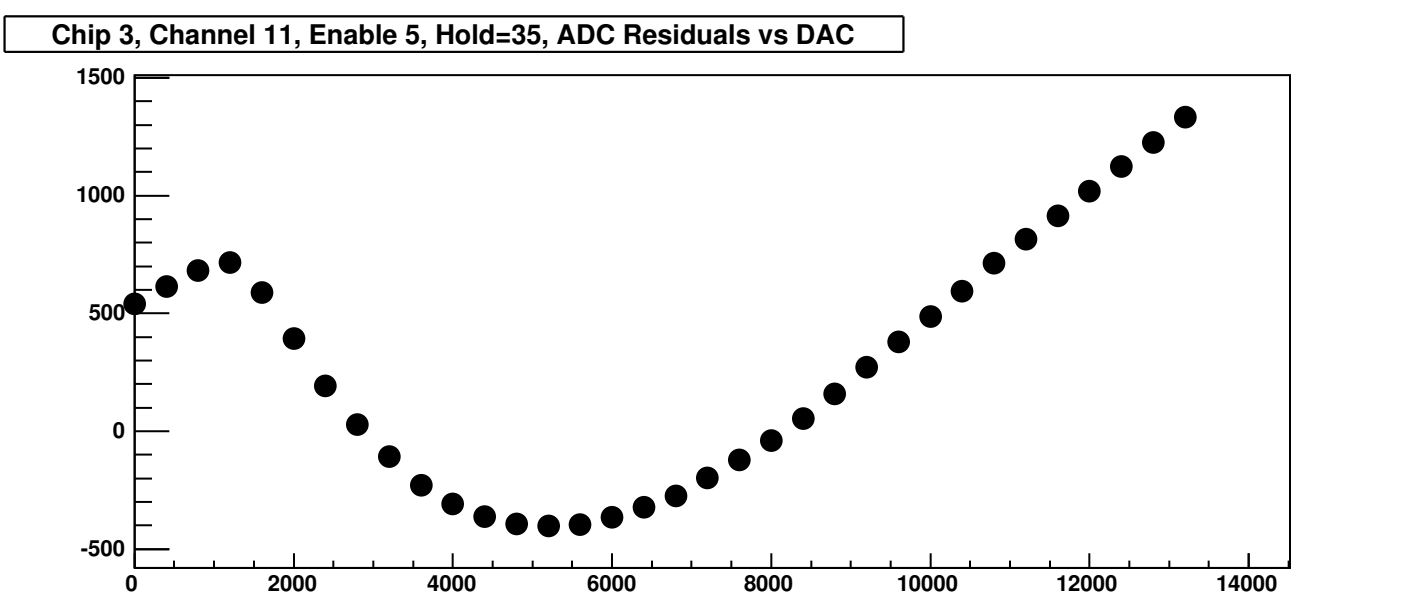

Chip 3, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 33.24 / 23
p0 -576.6 ± 0.8068
p1 0.01568 ± 0.0001238
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

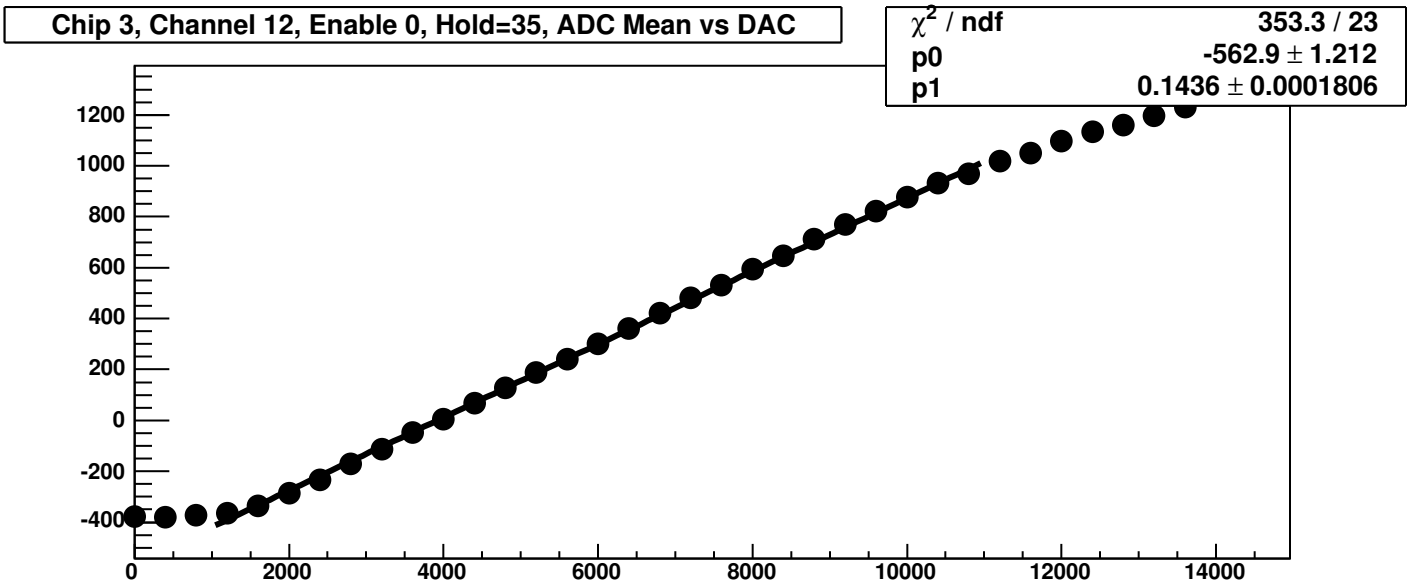

Chip 3, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

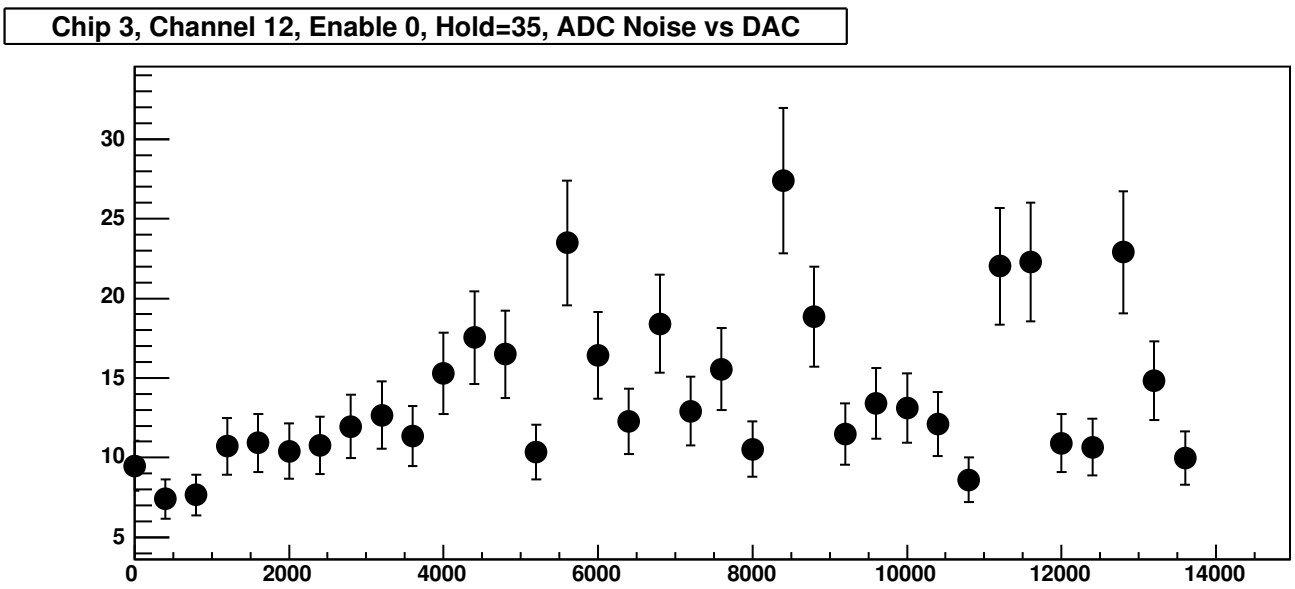

Chip 3, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

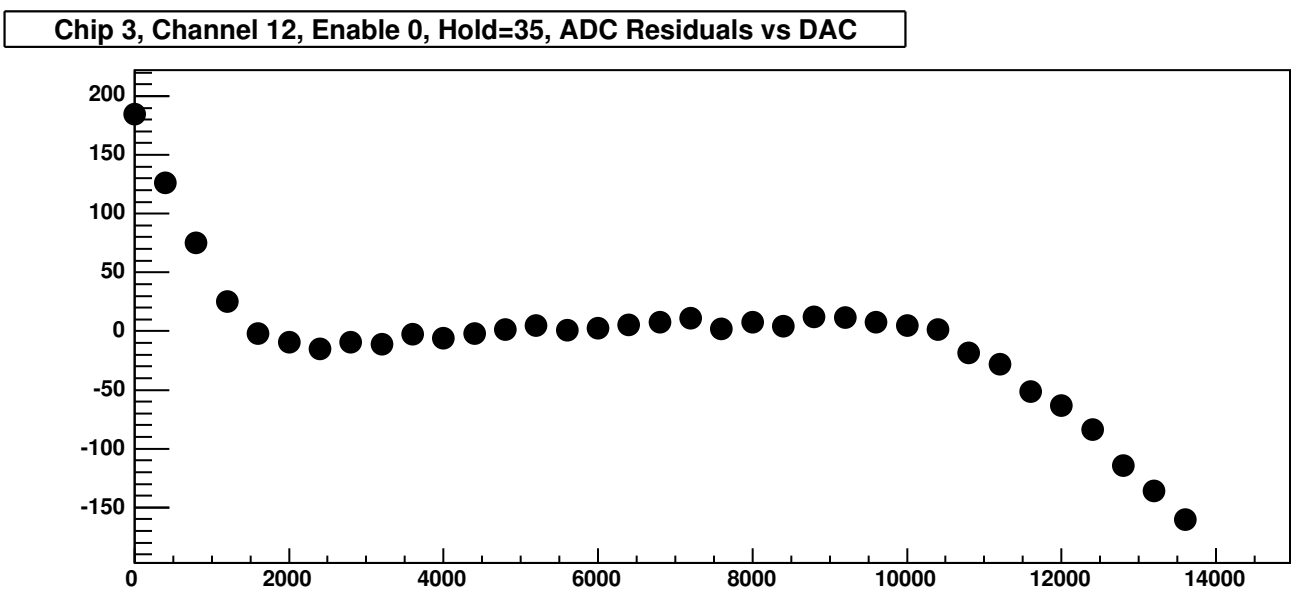

Chip 3, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5.561e+05 / 23
p0 -1143 ± 1.145
p1 -0.19 ± 0.0001882
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

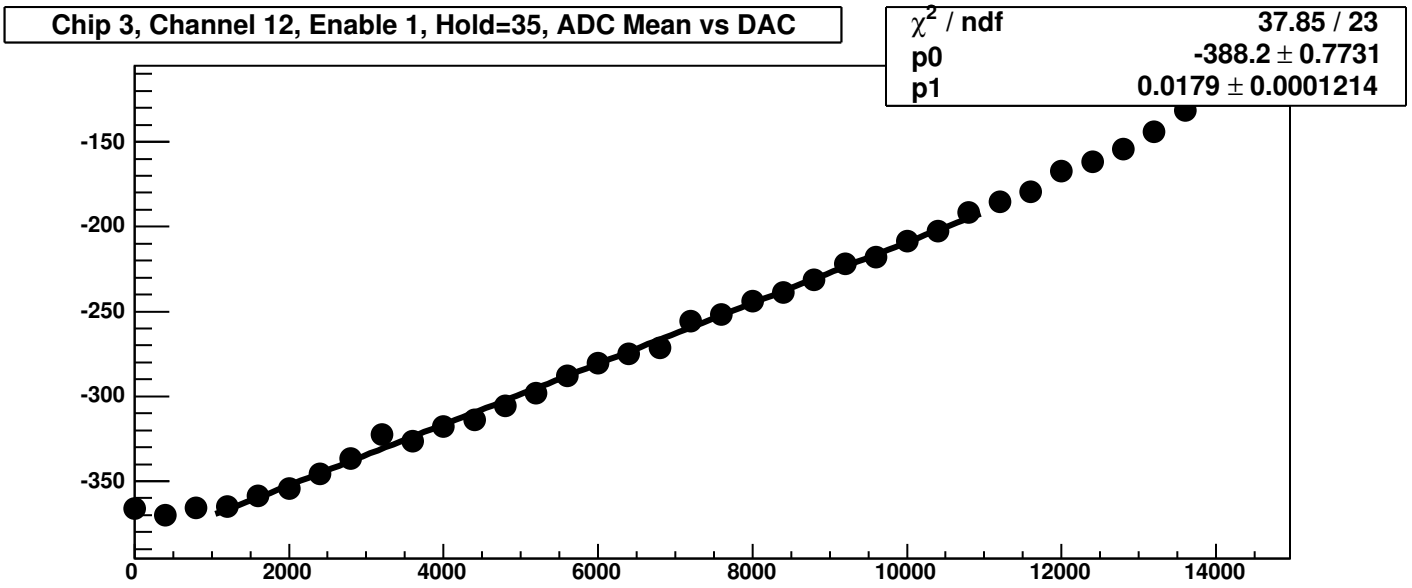

Chip 3, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

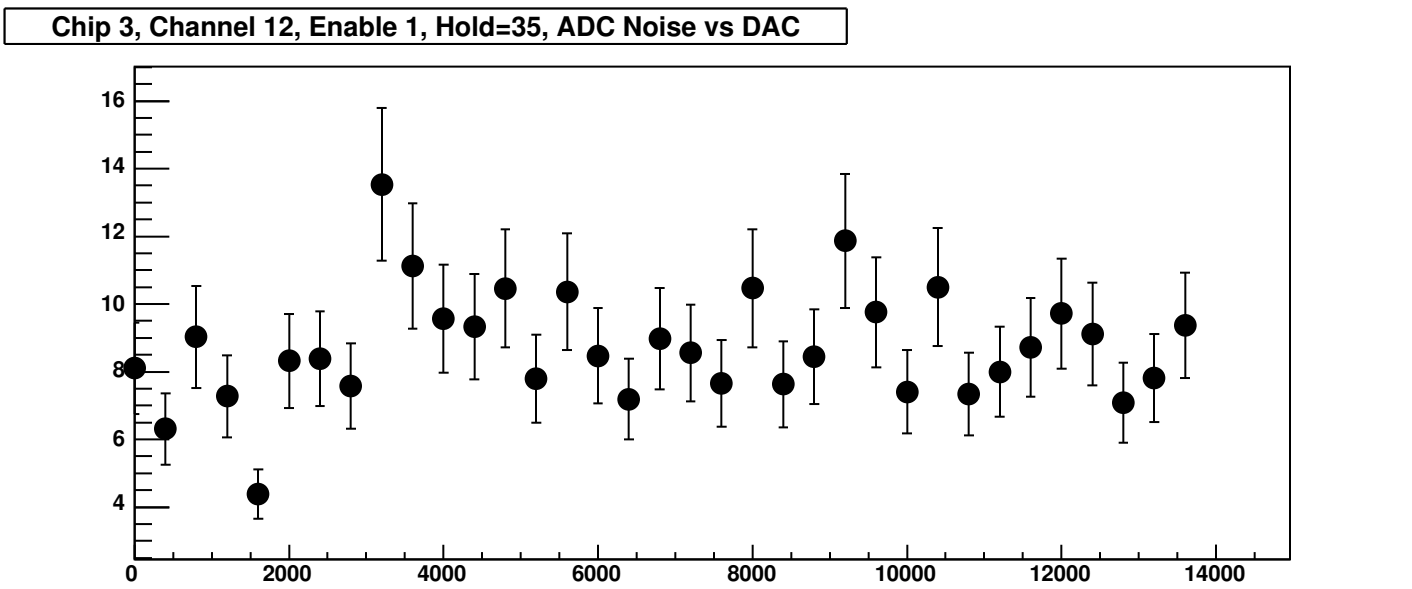

Chip 3, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

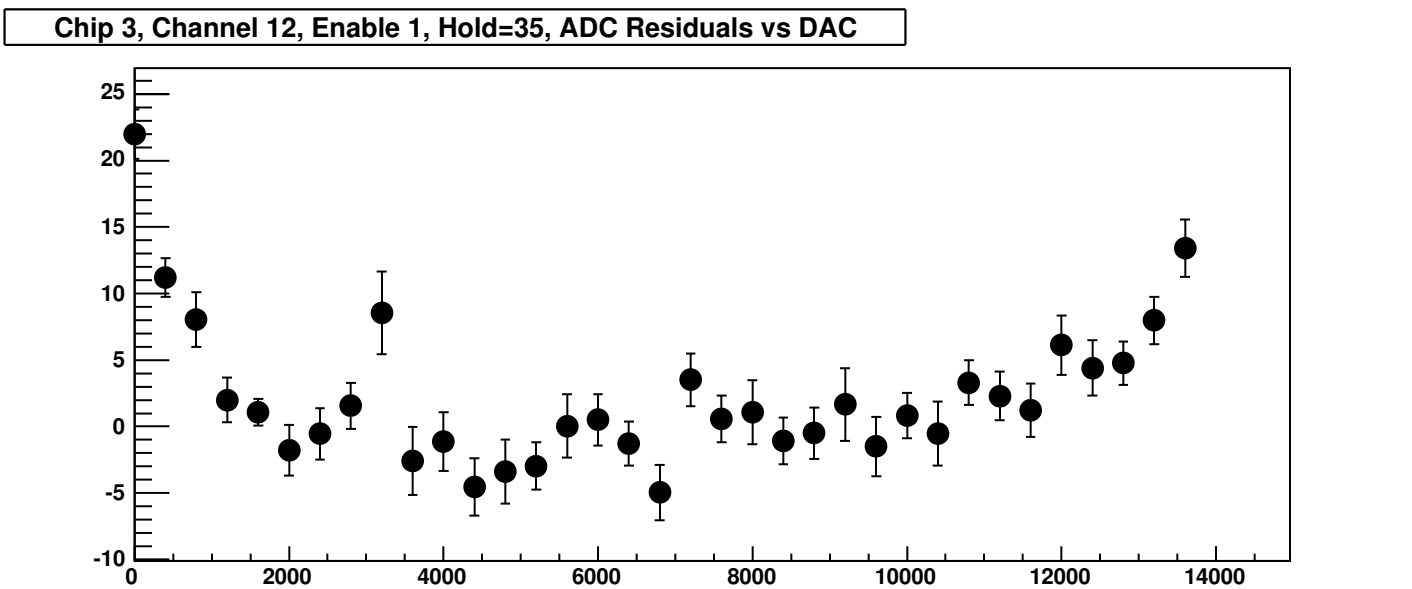

Chip 3, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 353.3 / 23
p0 -562.9 ± 1.212
p1 0.1436 ± 0.0001806
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

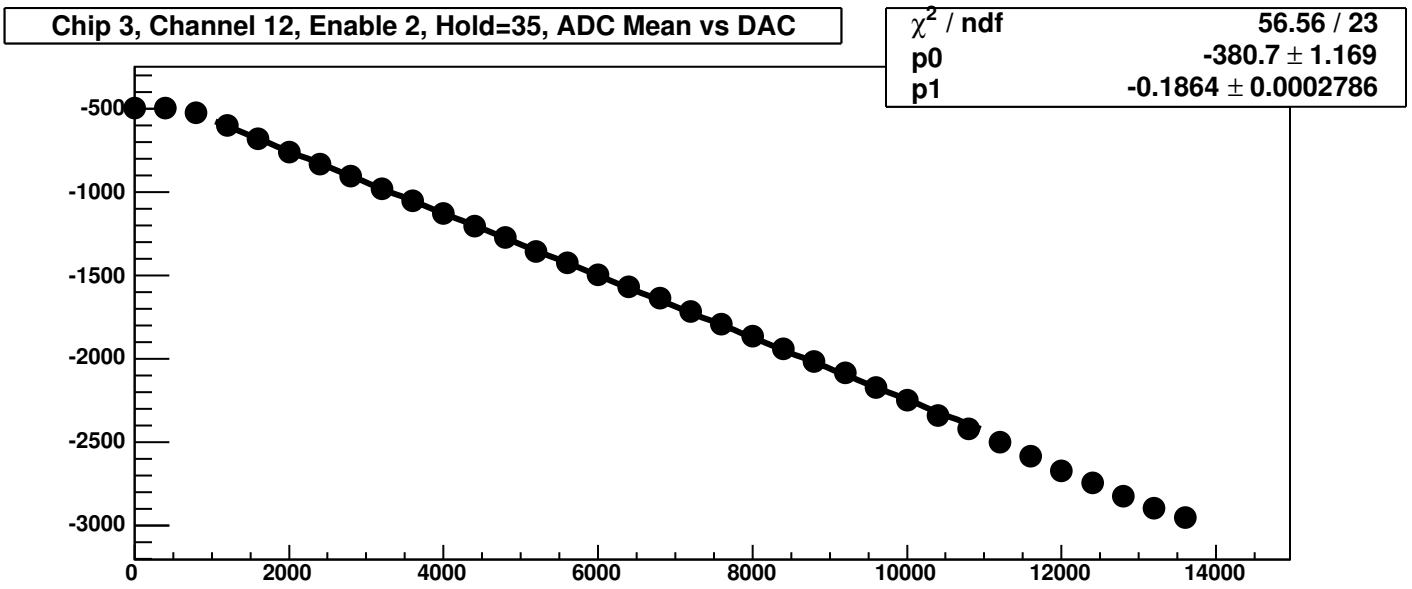

Chip 3, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

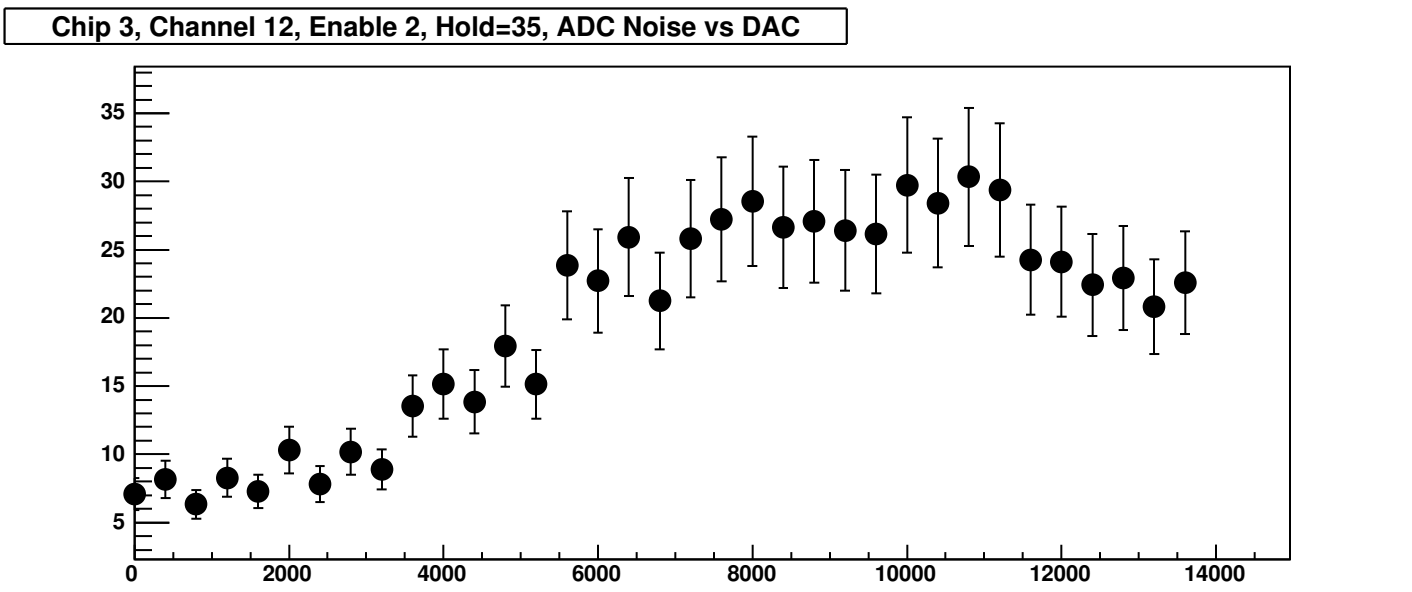

Chip 3, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

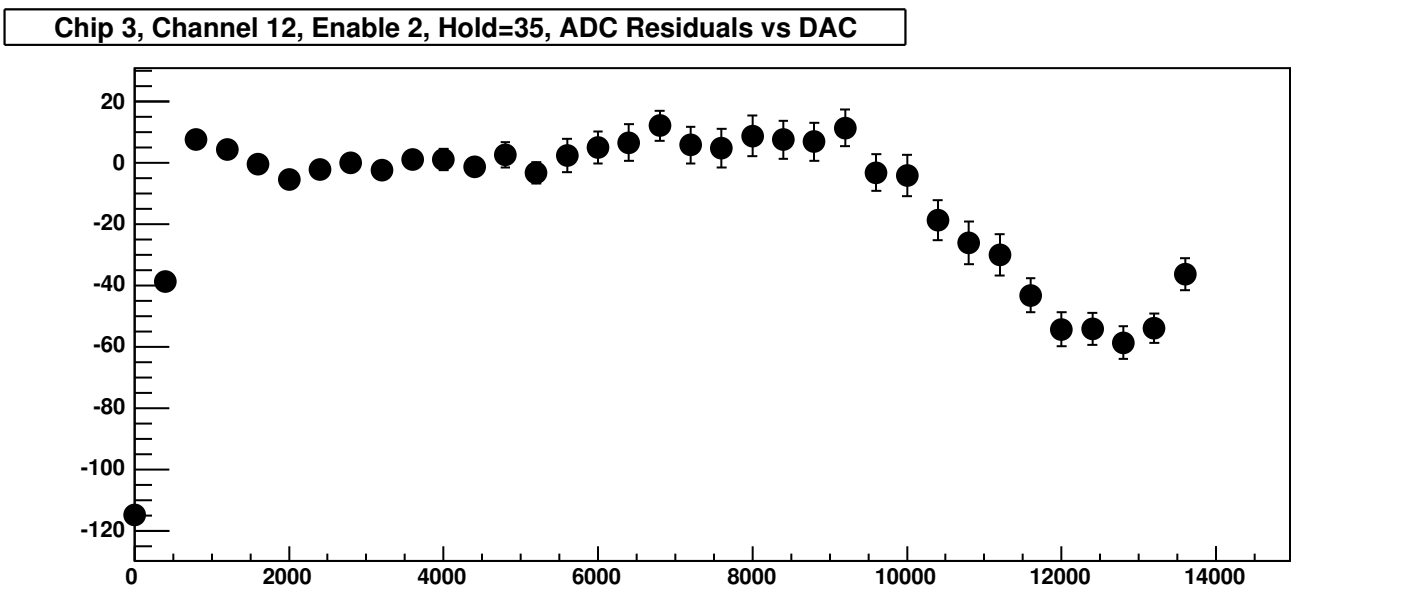

Chip 3, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 37.85 / 23
p0 -388.2 ± 0.7731
p1 0.0179 ± 0.0001214
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

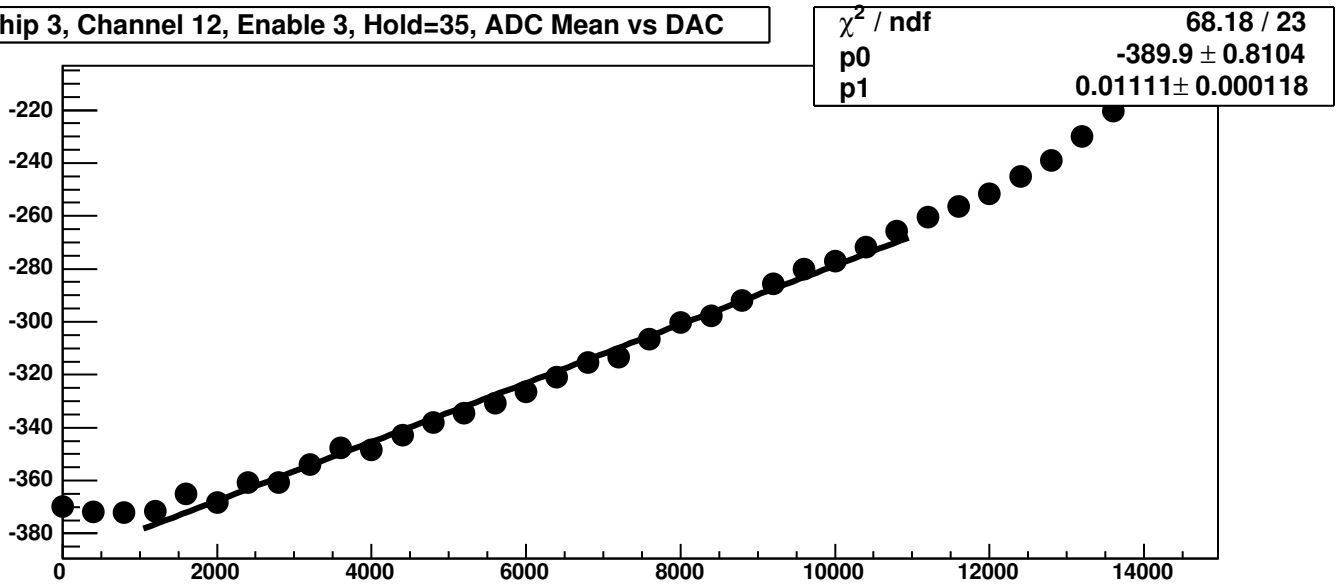

Chip 3, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 56.56 / 23
p0 -380.7 ± 1.169
p1 -0.1864 ± 0.0002786
-500
-1000
-1500
-2000
-2500
-3000
0
2000
4000
6000
8000
10000
12000
14000

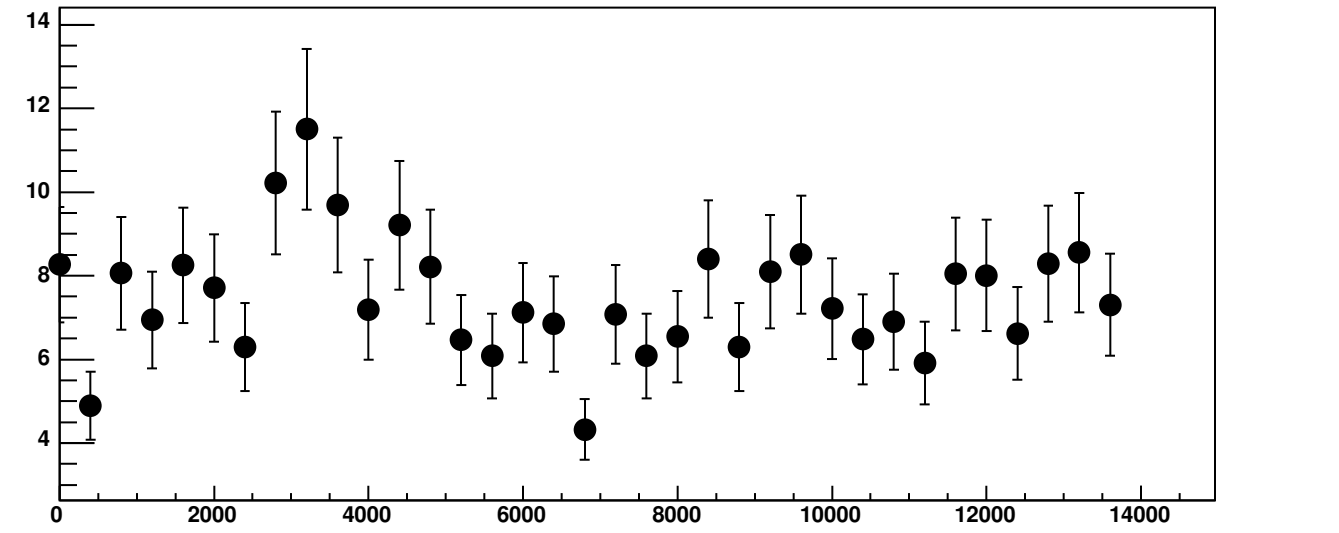

Chip 3, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
2000
4000
6000
8000
10000
12000
14000

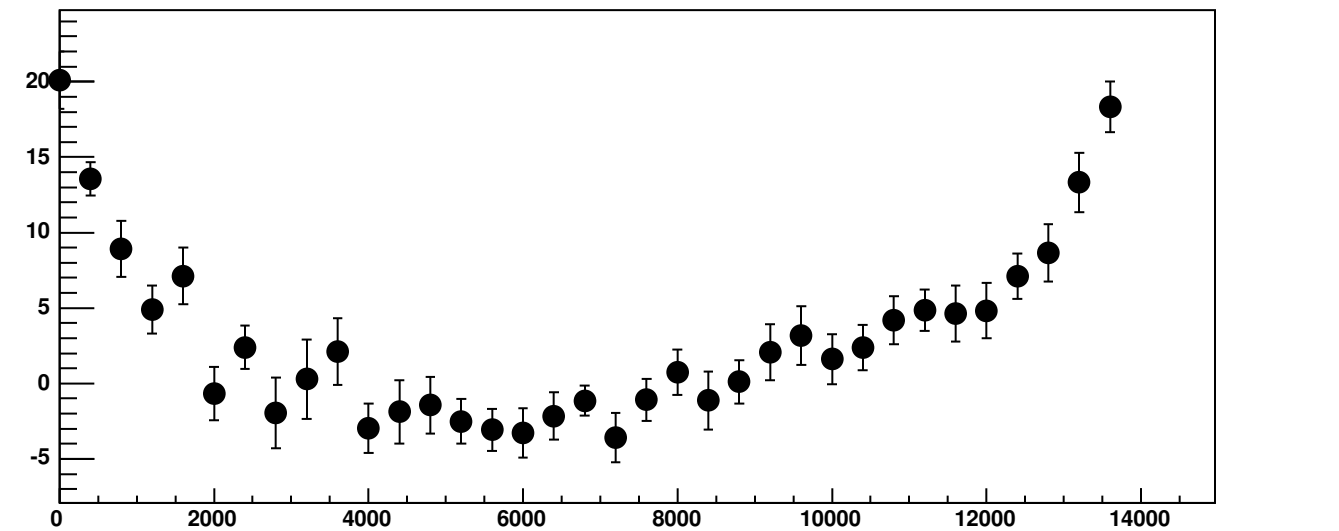
Chip 3, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

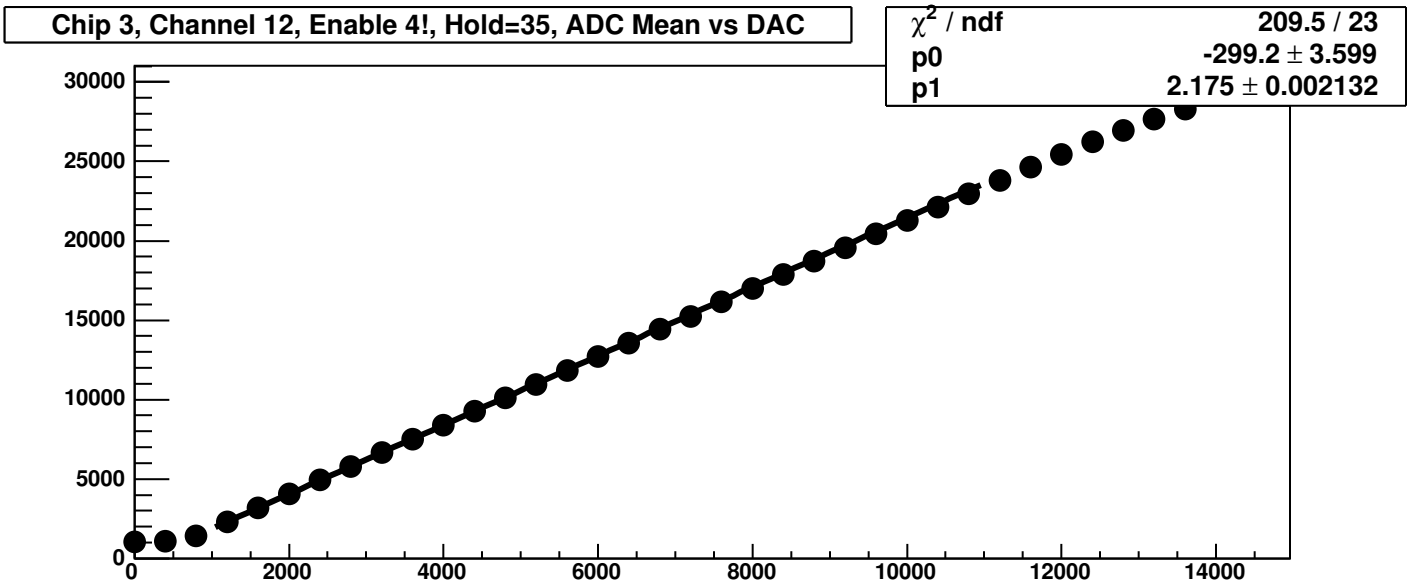


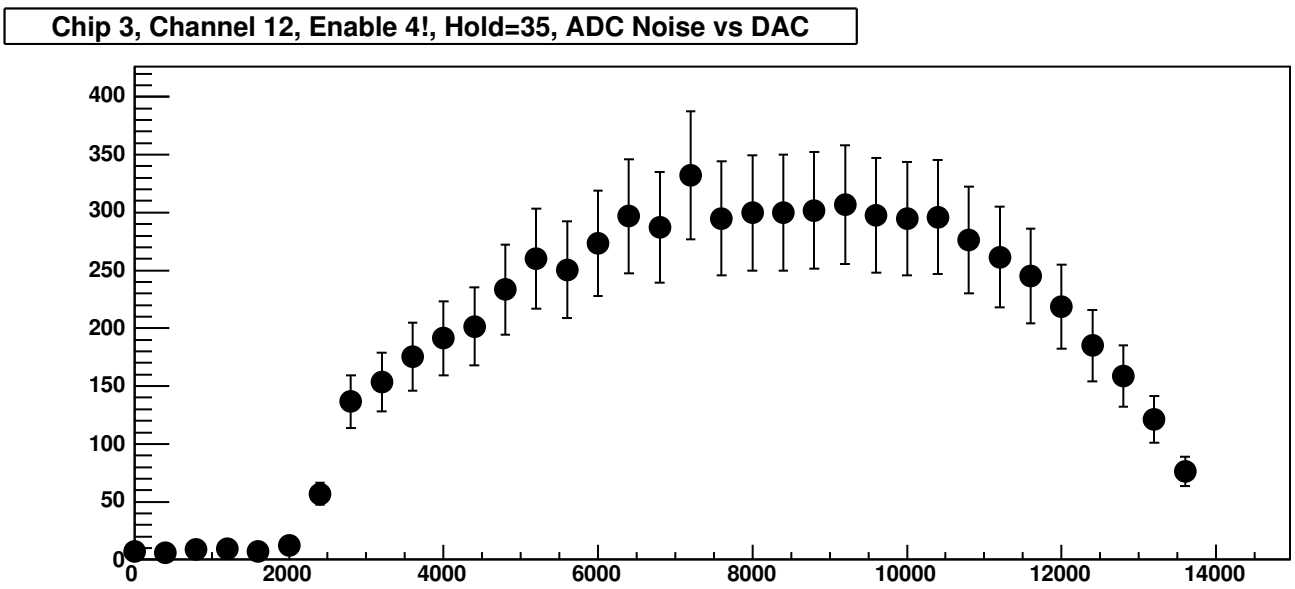
Chip 3, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC

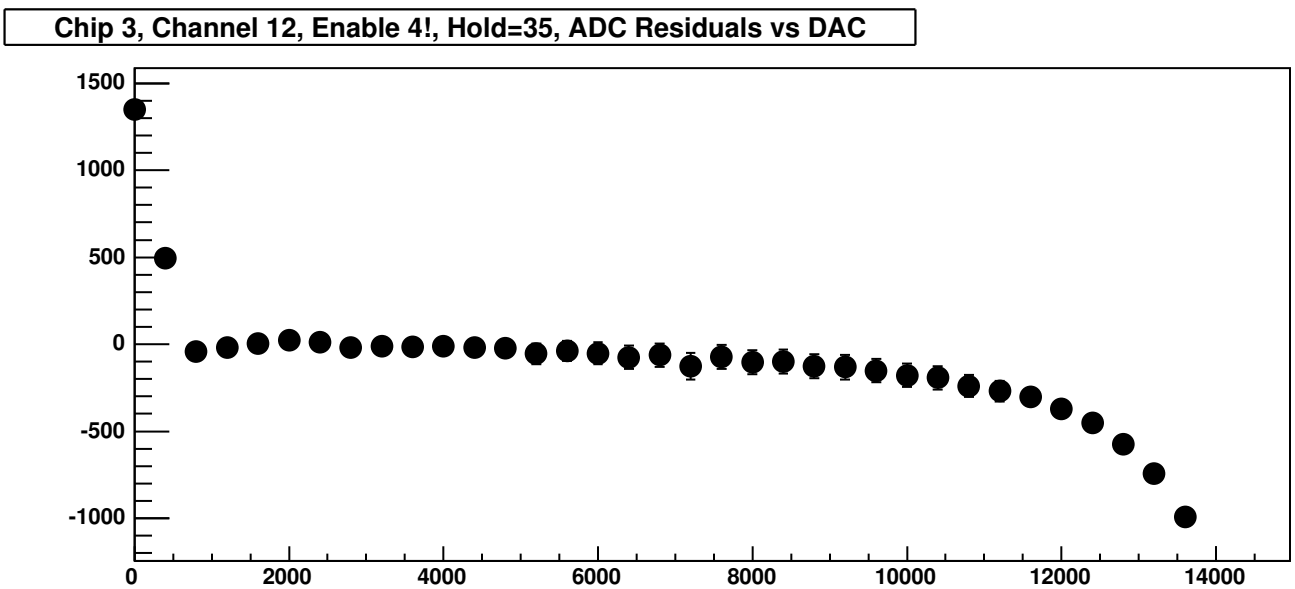


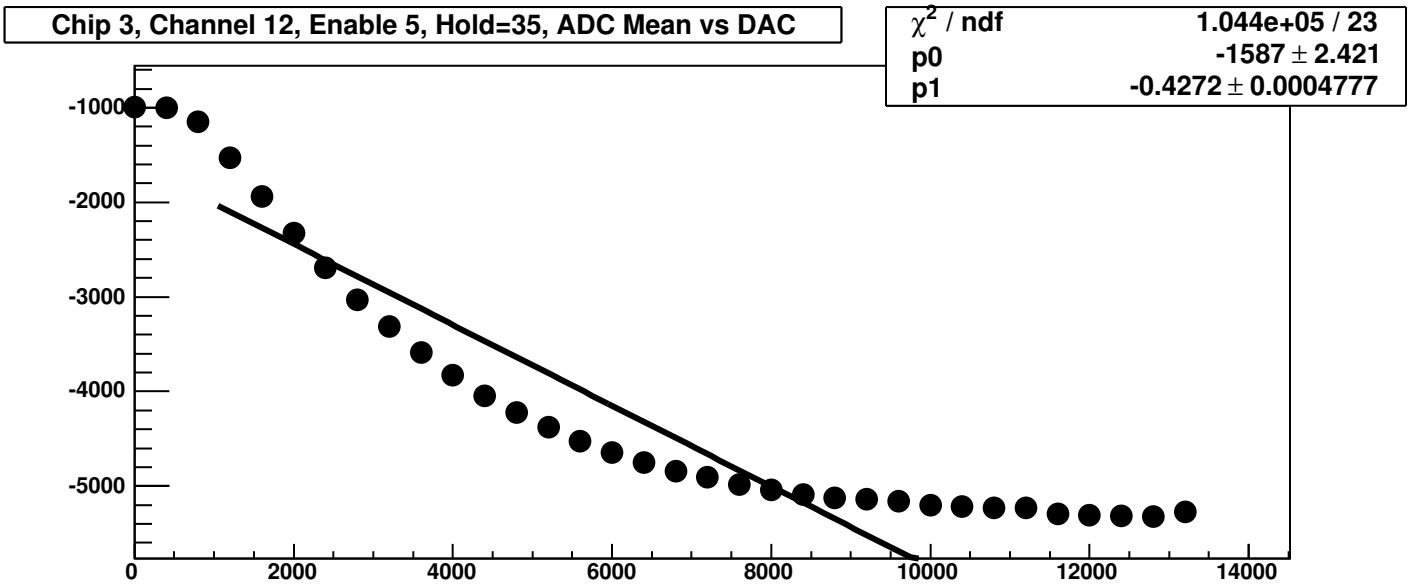
Chip 3, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

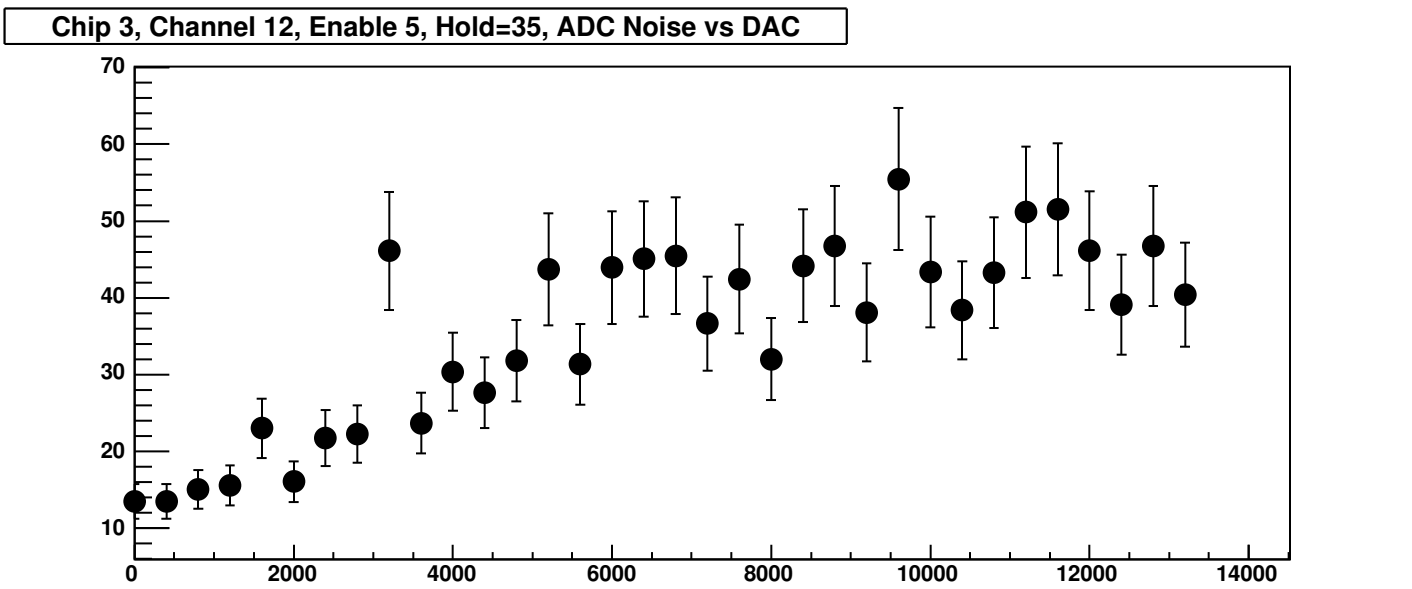


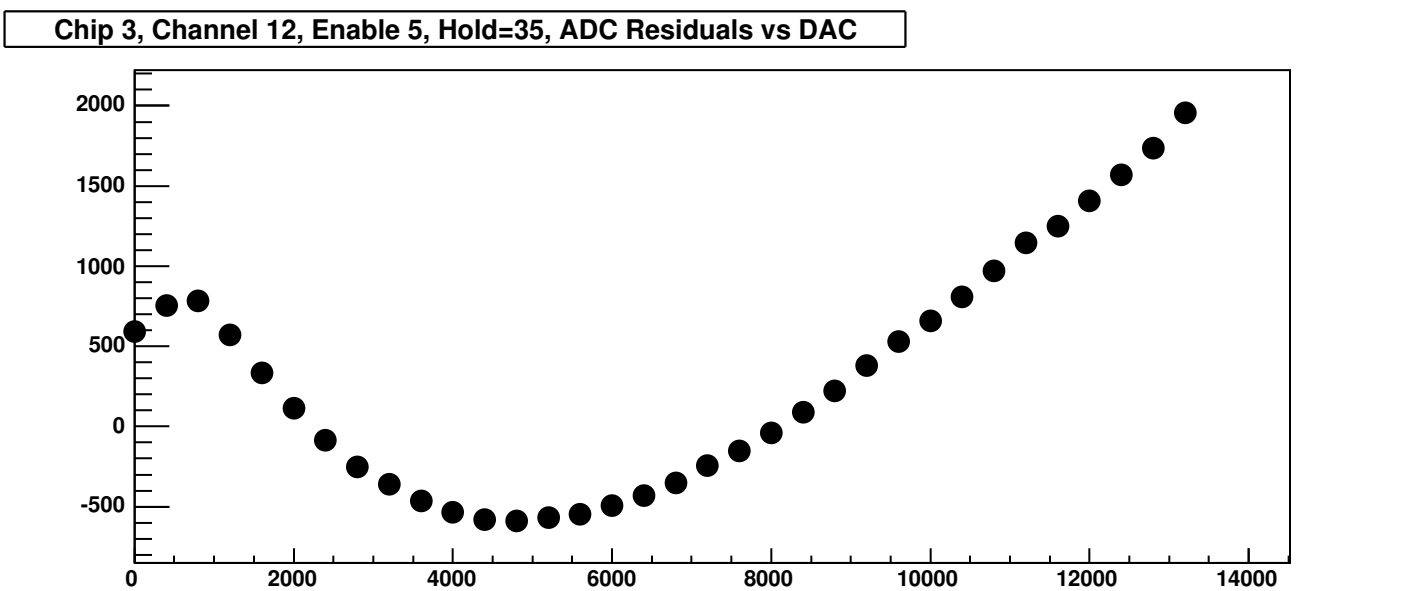

Chip 3, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 209.5 / 23
p0 -299.2 ± 3.599
p1 2.175 ± 0.002132
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

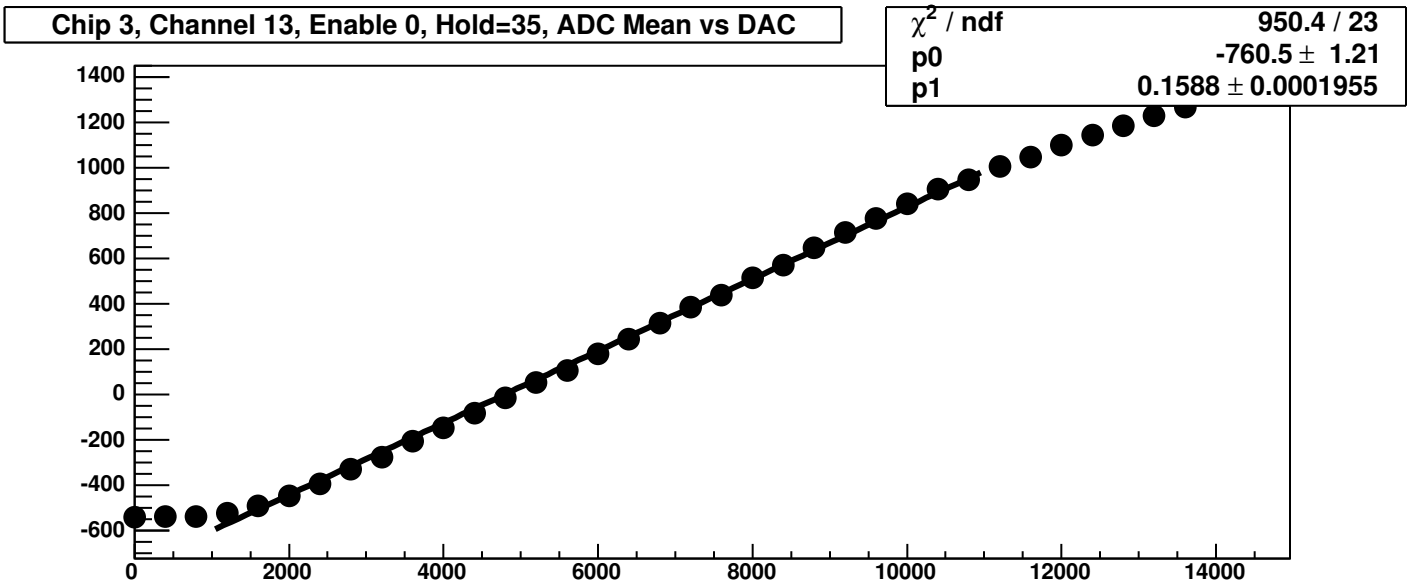

Chip 3, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

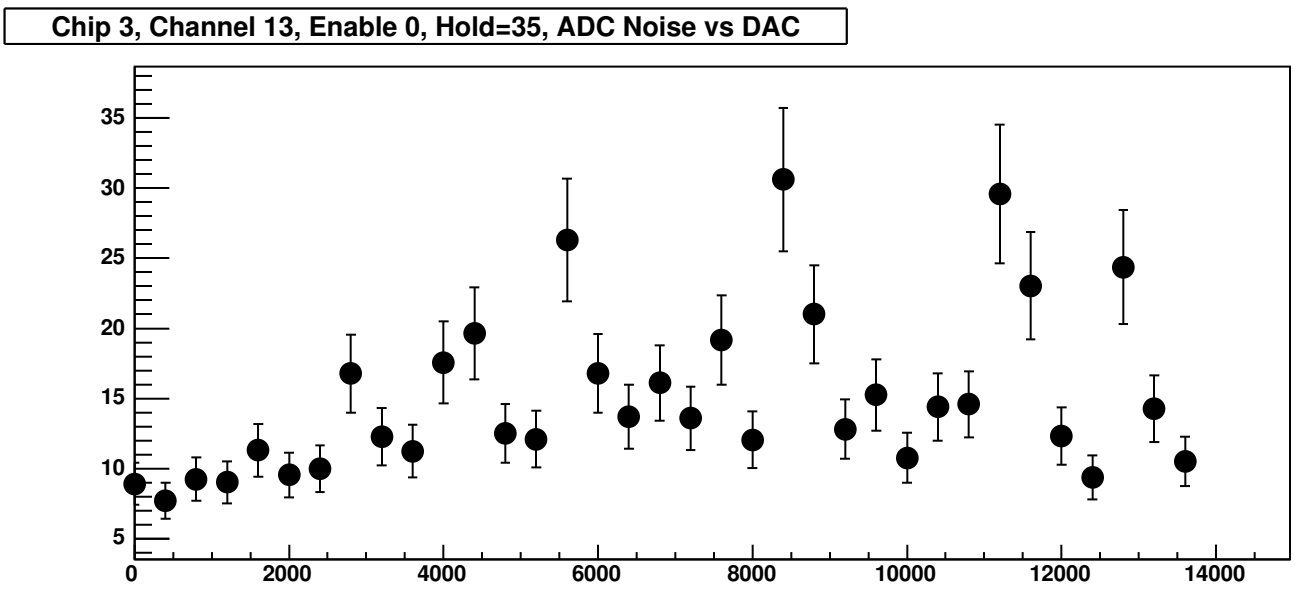

Chip 3, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

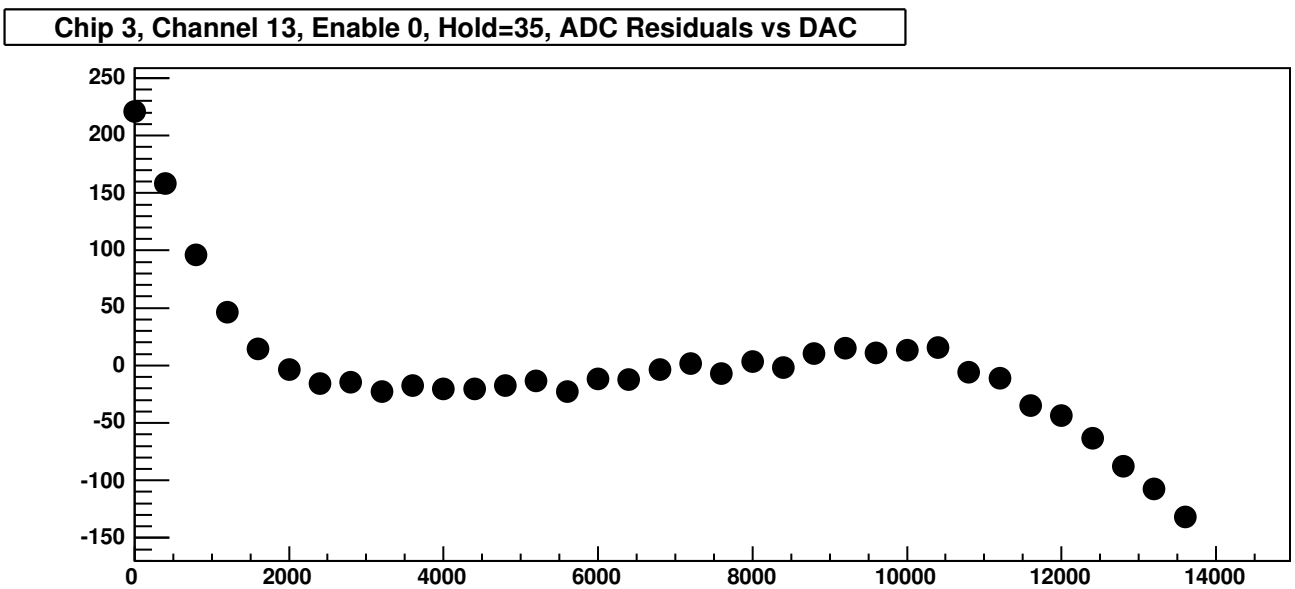

Chip 3, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.044e+05 / 23
p0 -1587 ± 2.421
p1 -0.4272 ± 0.0004777
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000

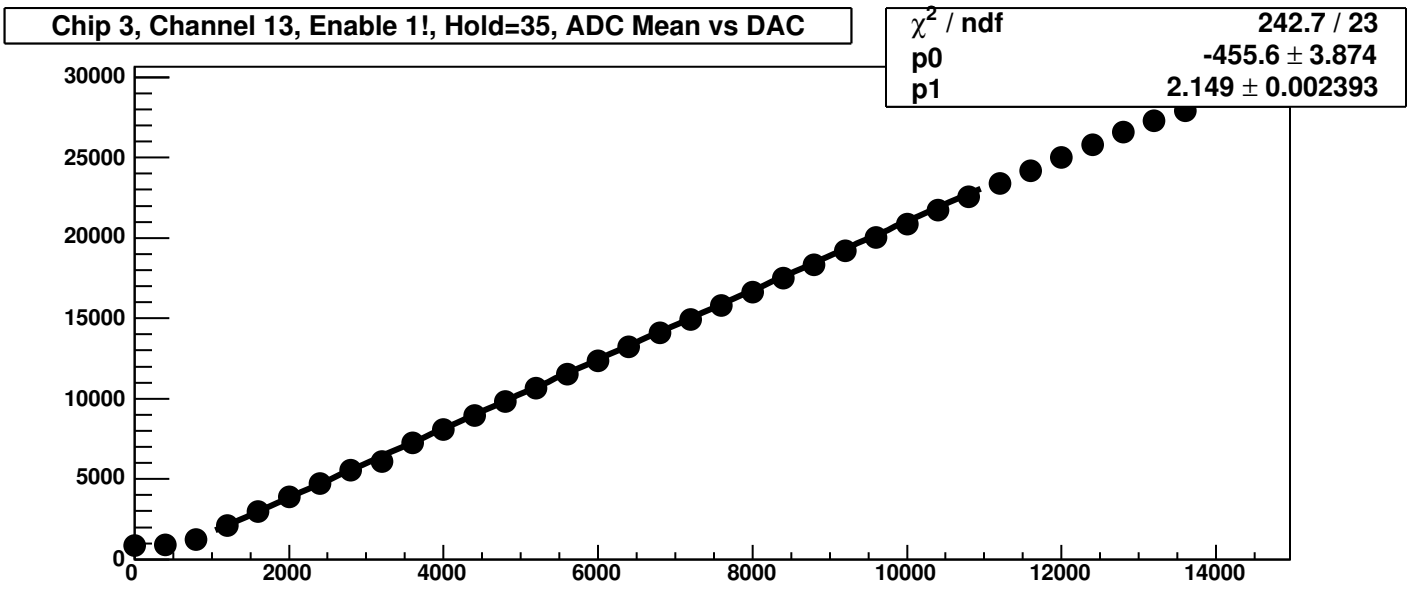

Chip 3, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

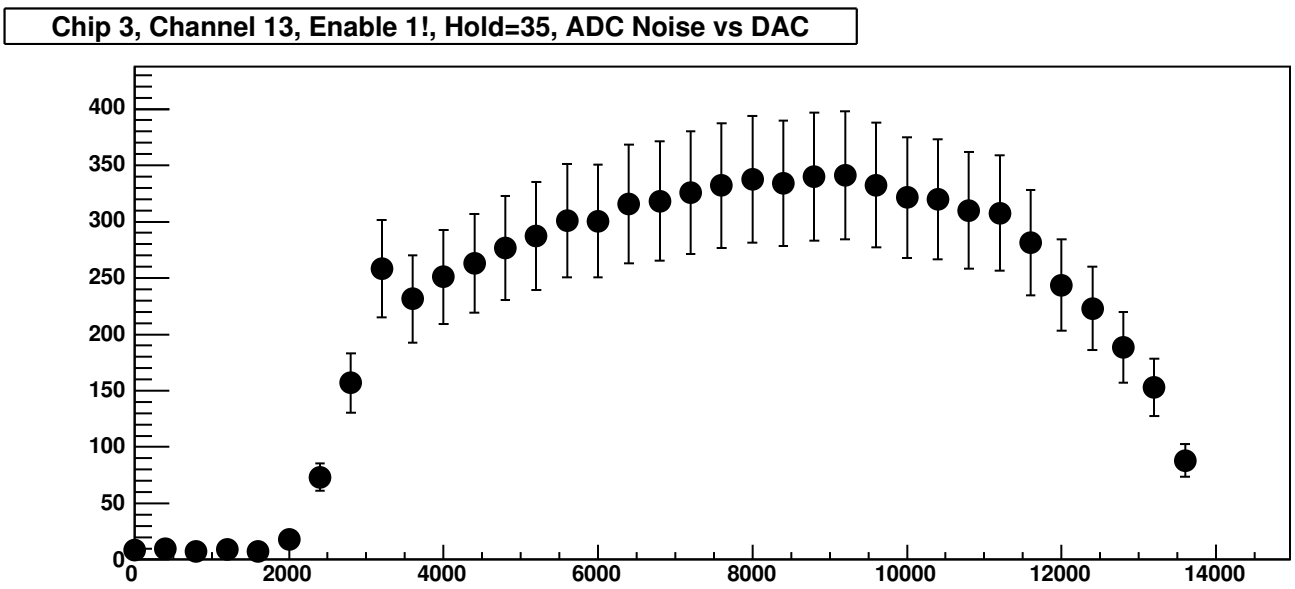

Chip 3, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

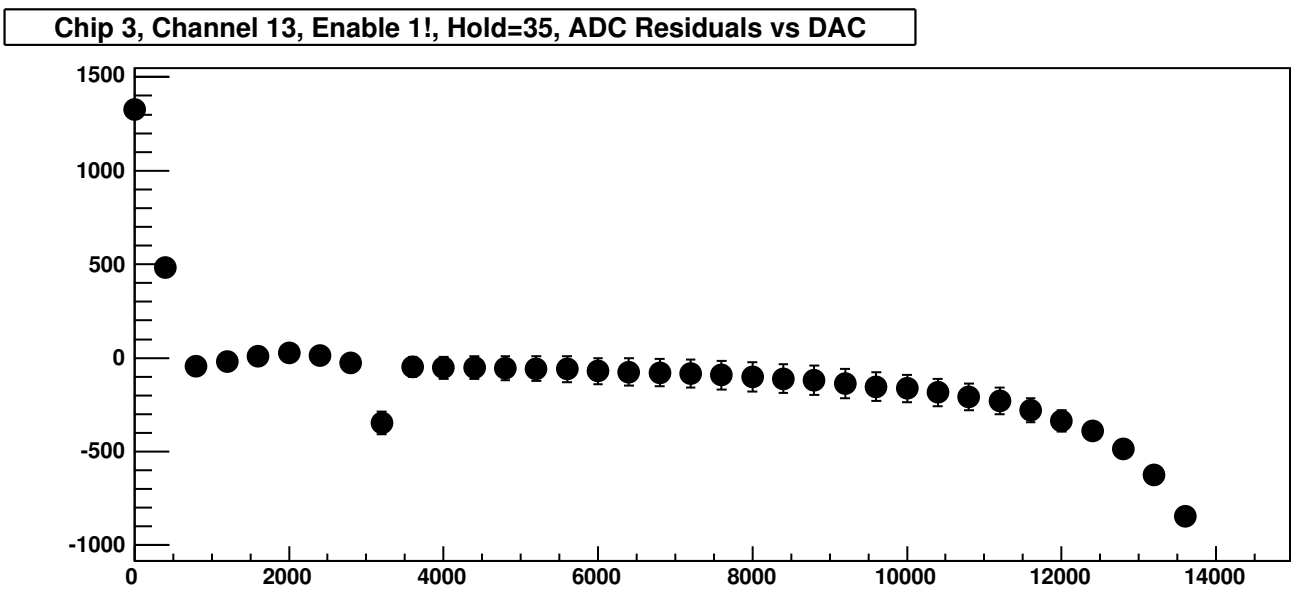

Chip 3, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 950.4 / 23
p0 -760.5 ± 1.21
p1 0.1588 ± 0.0001955
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

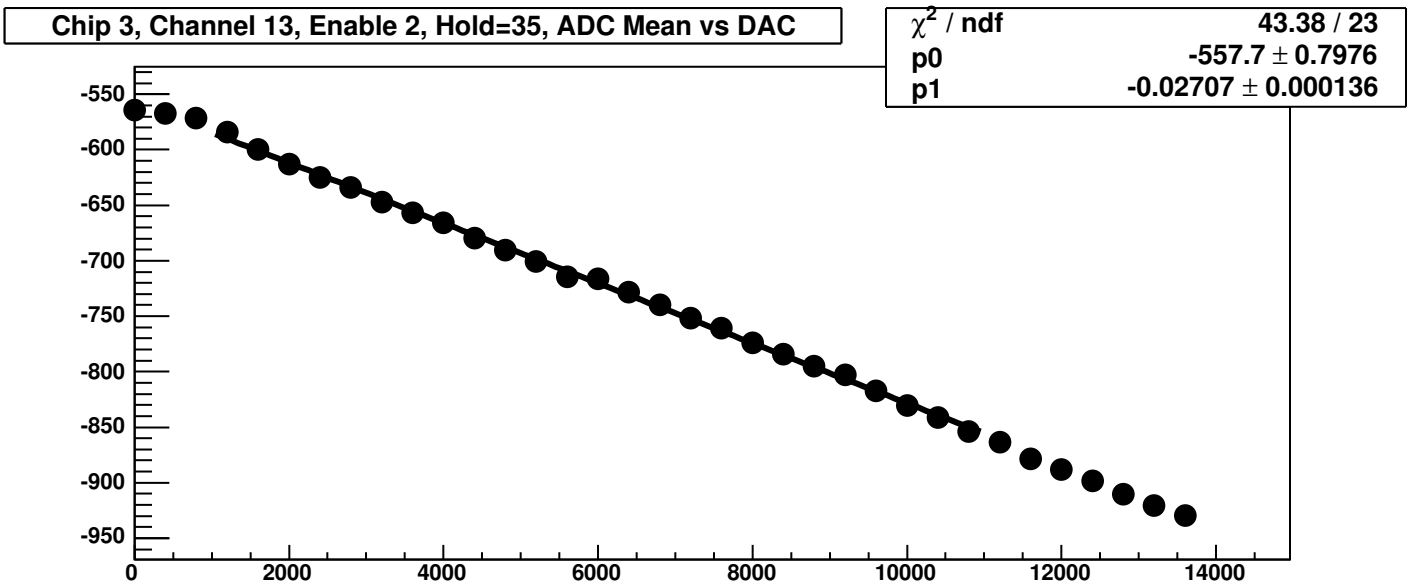

Chip 3, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

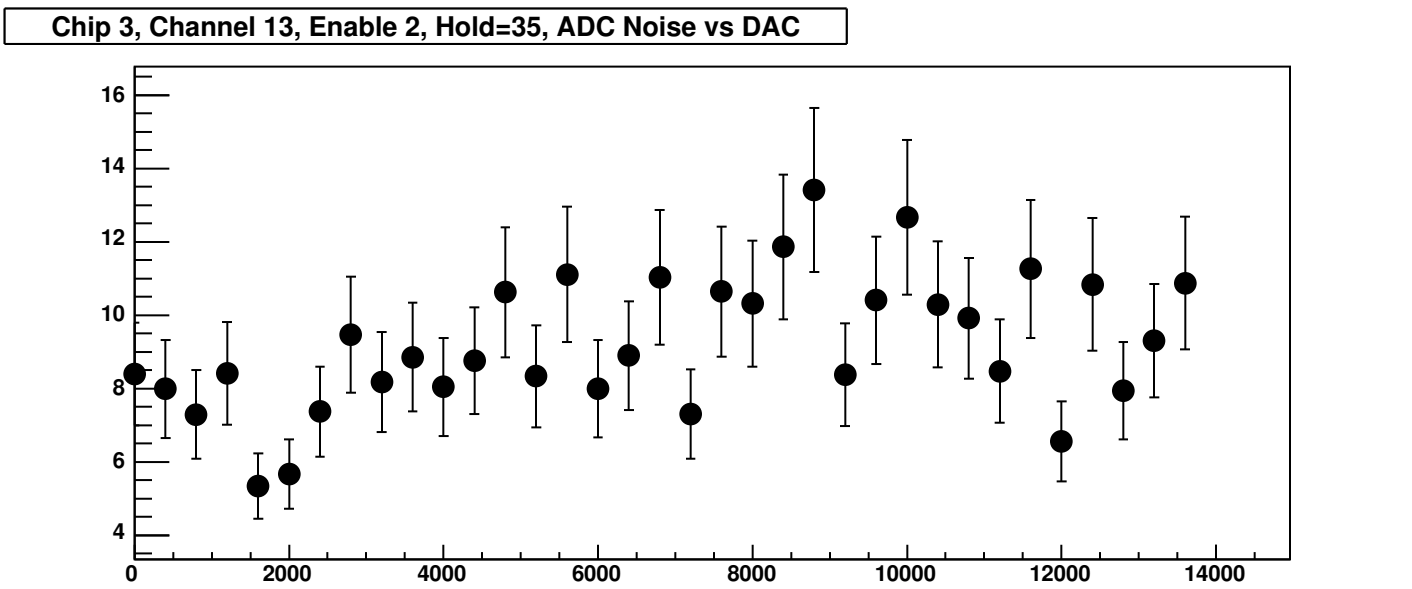

Chip 3, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

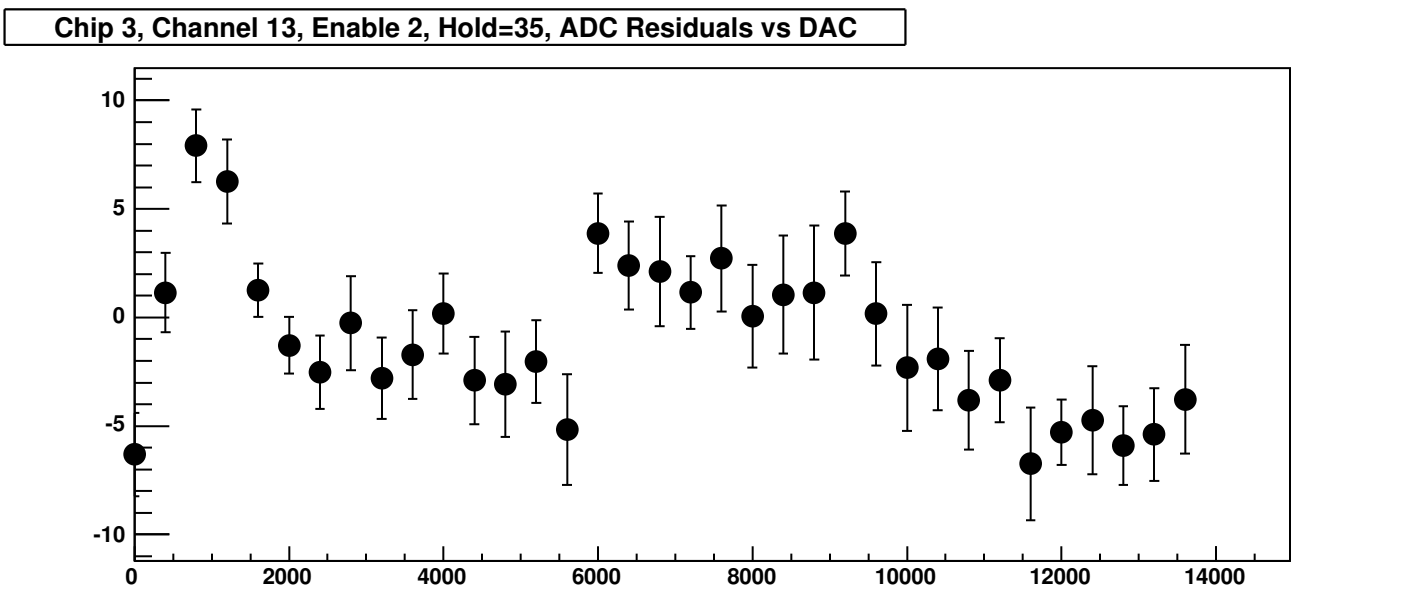

Chip 3, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 242.7 / 23
p0 -455.6 ± 3.874
p1 2.149 ± 0.002393
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

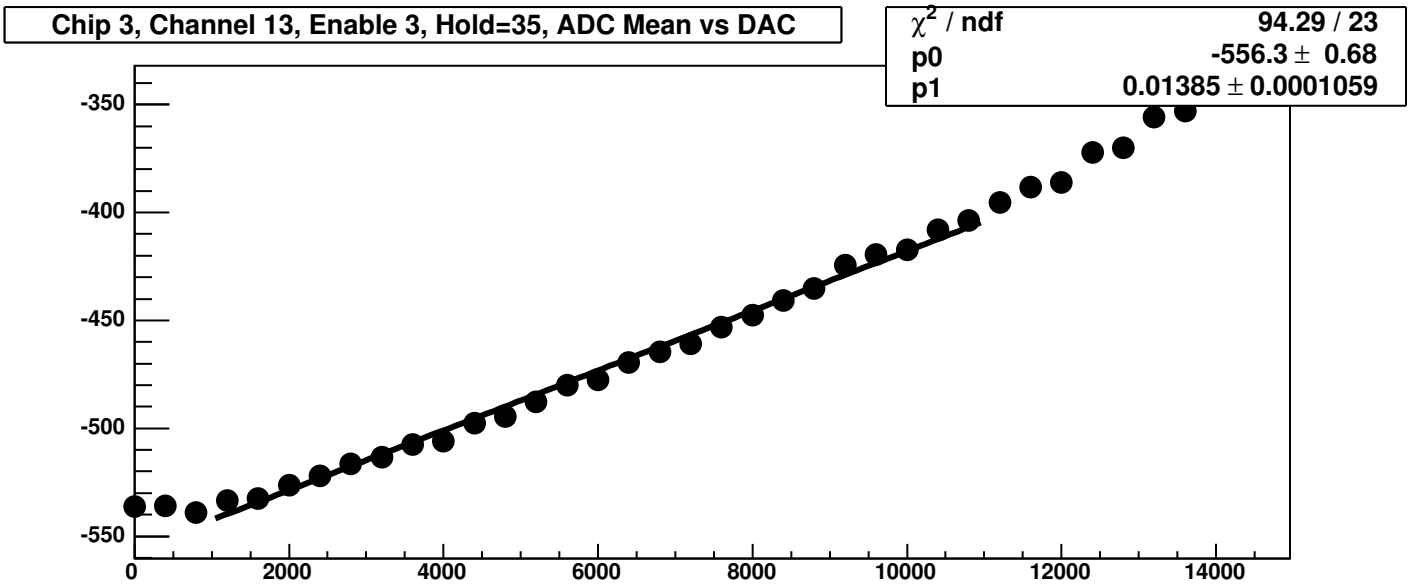

Chip 3, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

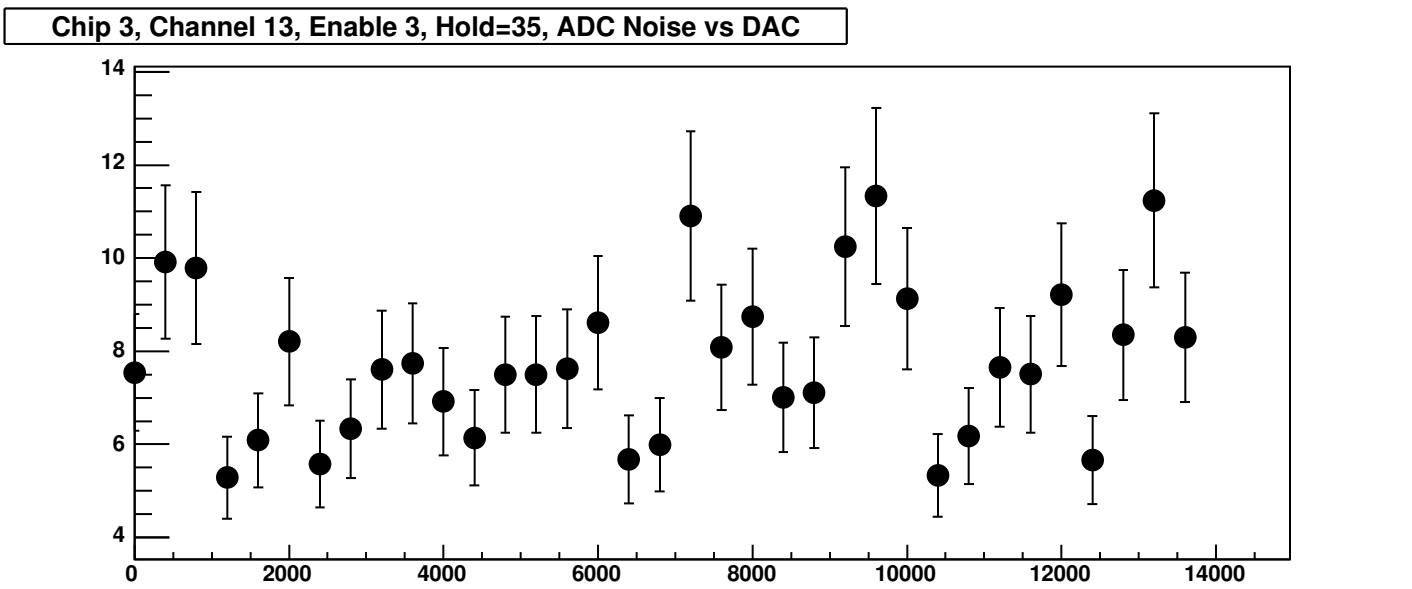

Chip 3, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

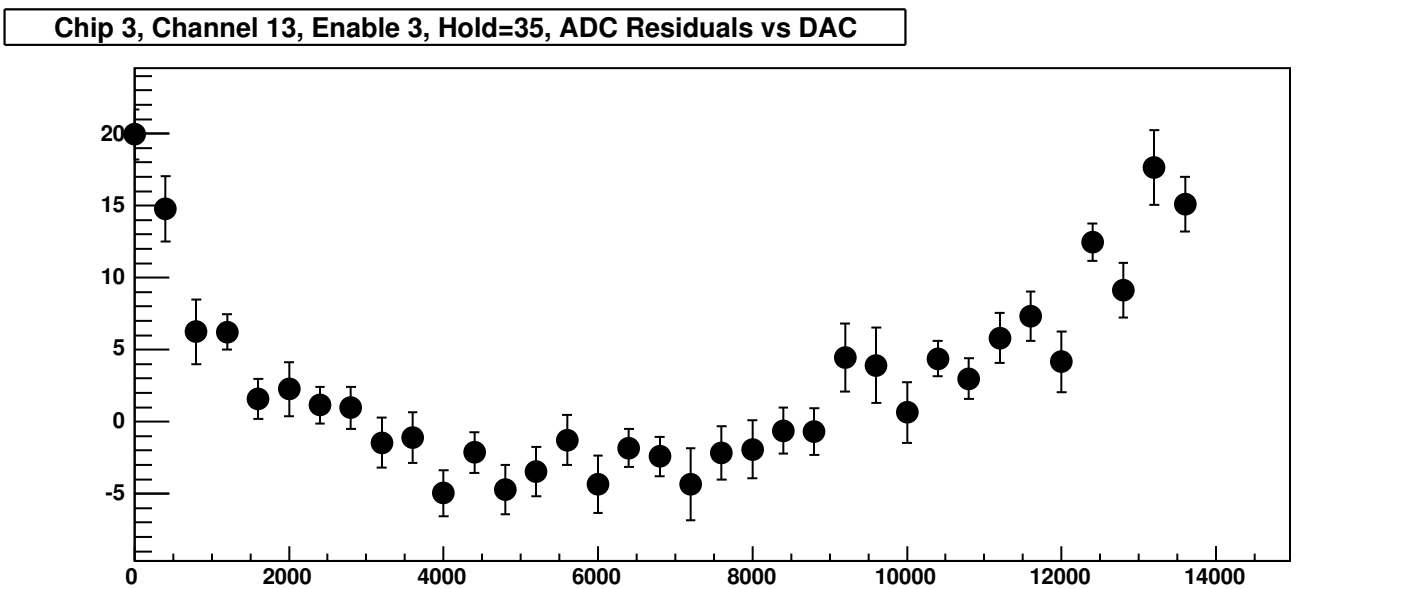

Chip 3, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 43.38 / 23
p0 -557.7 ± 0.7976
p1 -0.02707 ± 0.000136
-550
-600
-650
-700
-750
-800
-850
-900
-950
0
2000
4000
6000
8000
10000
12000
14000

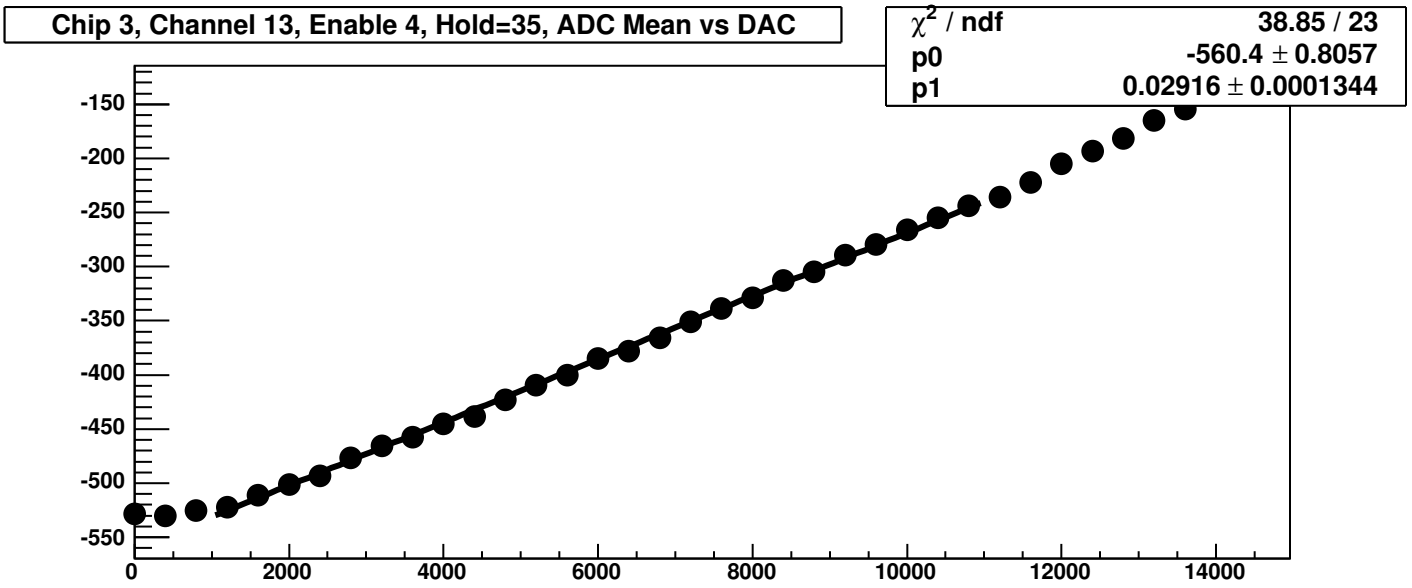

Chip 3, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

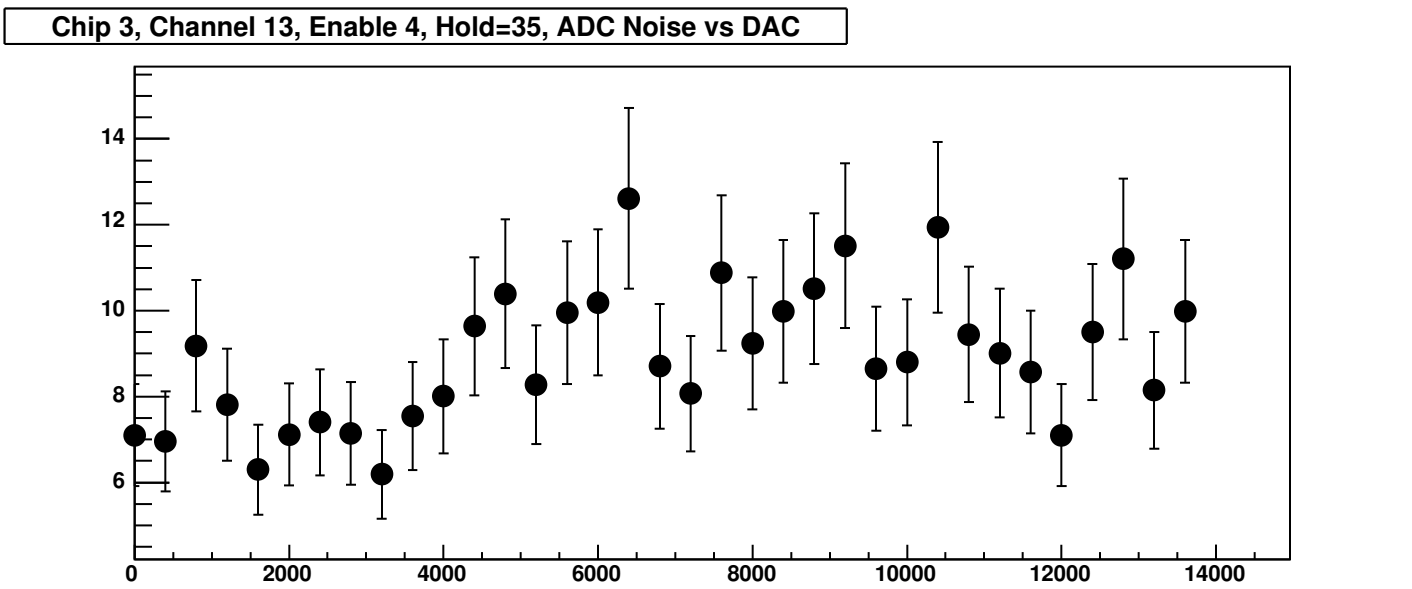

Chip 3, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

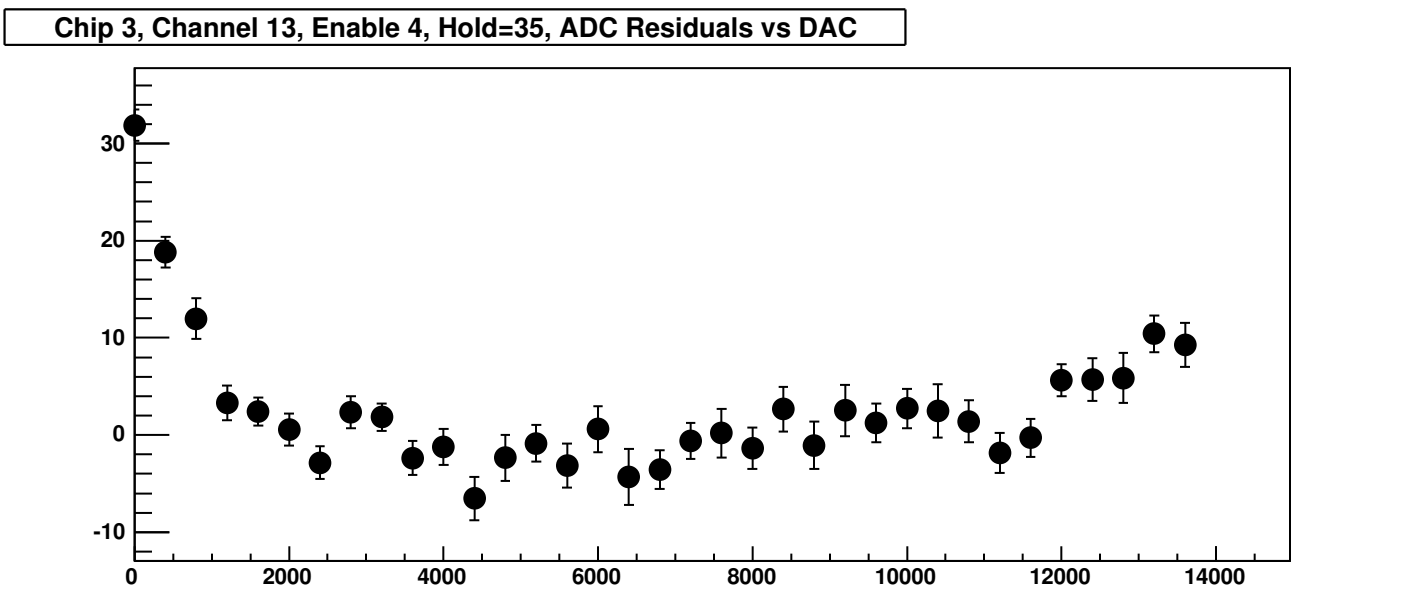

Chip 3, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 94.29 / 23
p0 -556.3 ± 0.68
p1 0.01385 ± 0.0001059
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

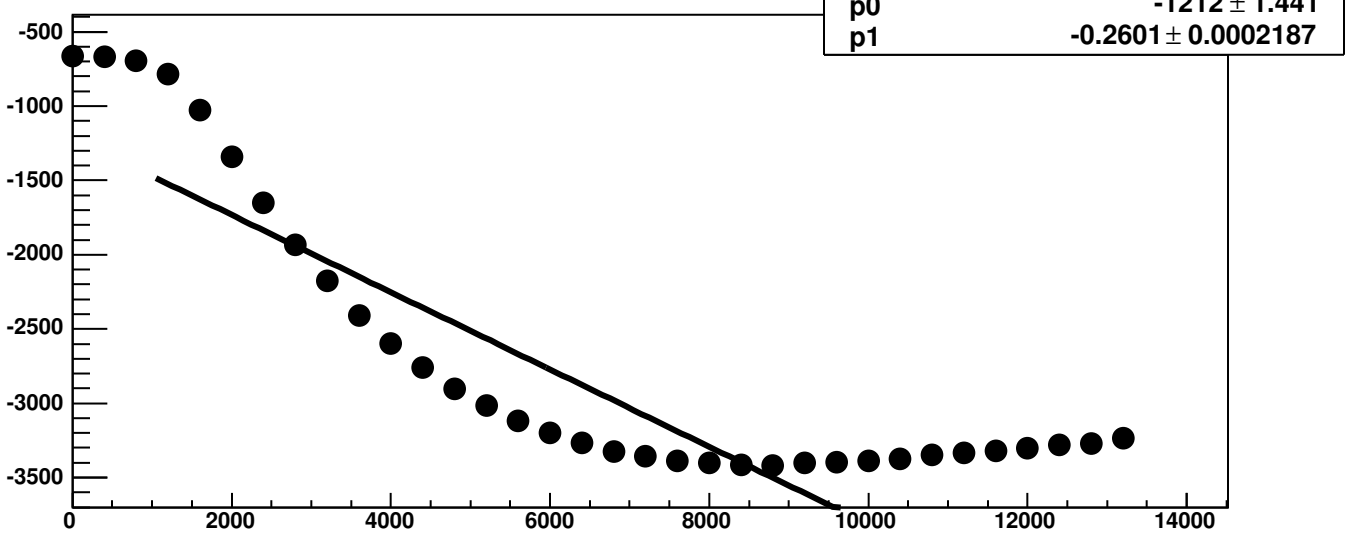

Chip 3, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 38.85 / 23
p0 -560.4 ± 0.8057
p1 0.02916 ± 0.0001344
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

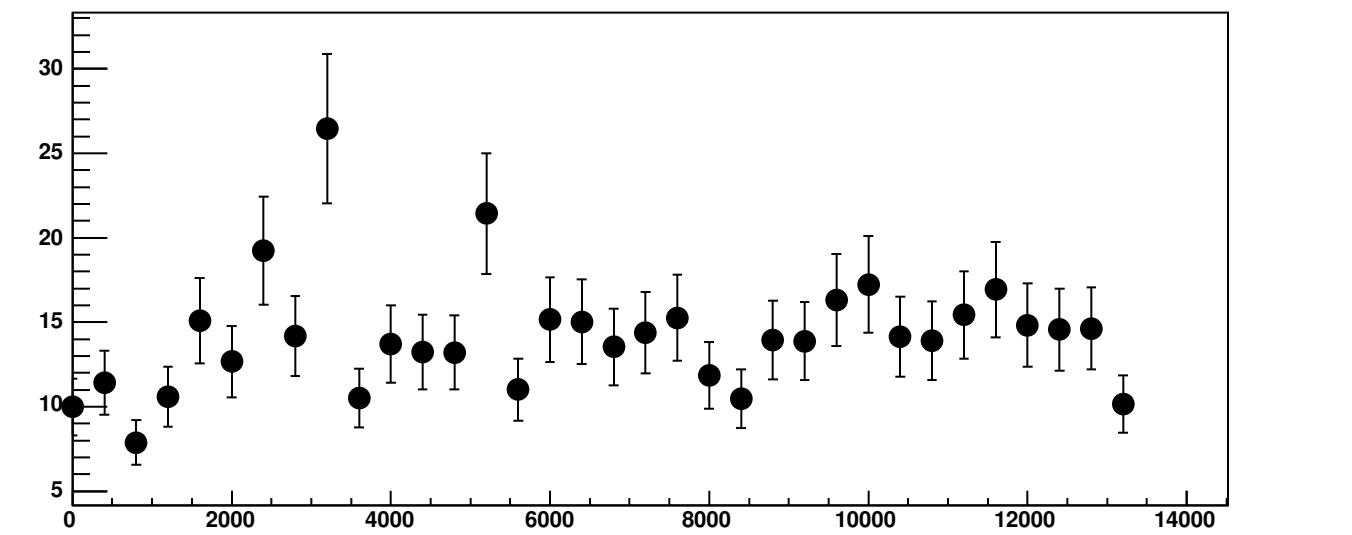

Chip 3, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000

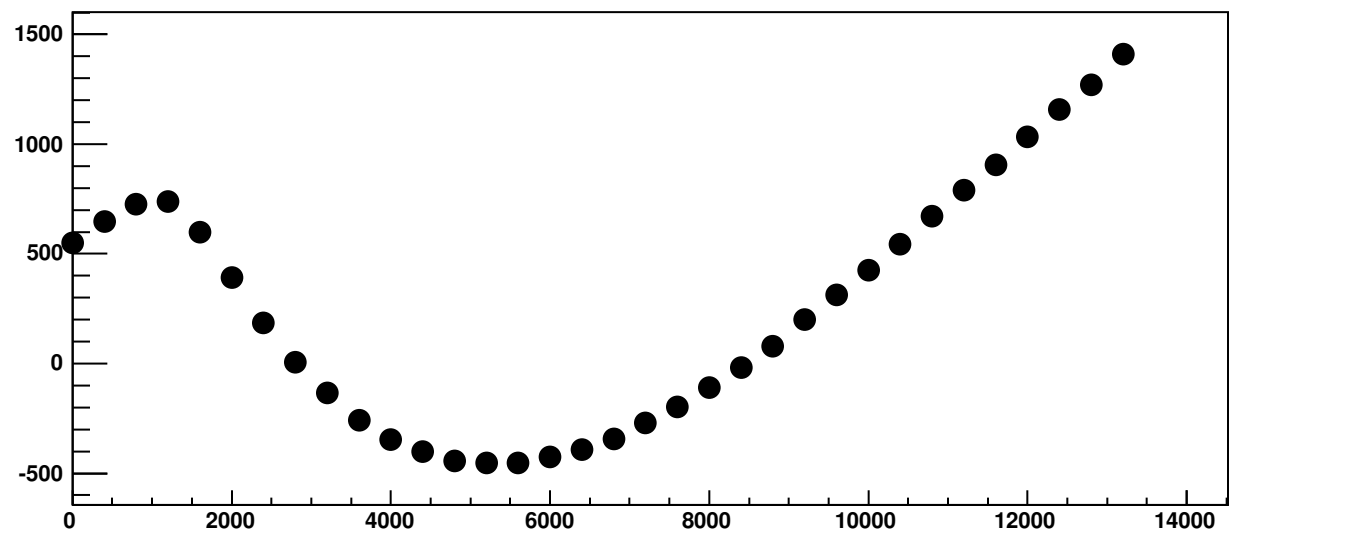
Chip 3, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

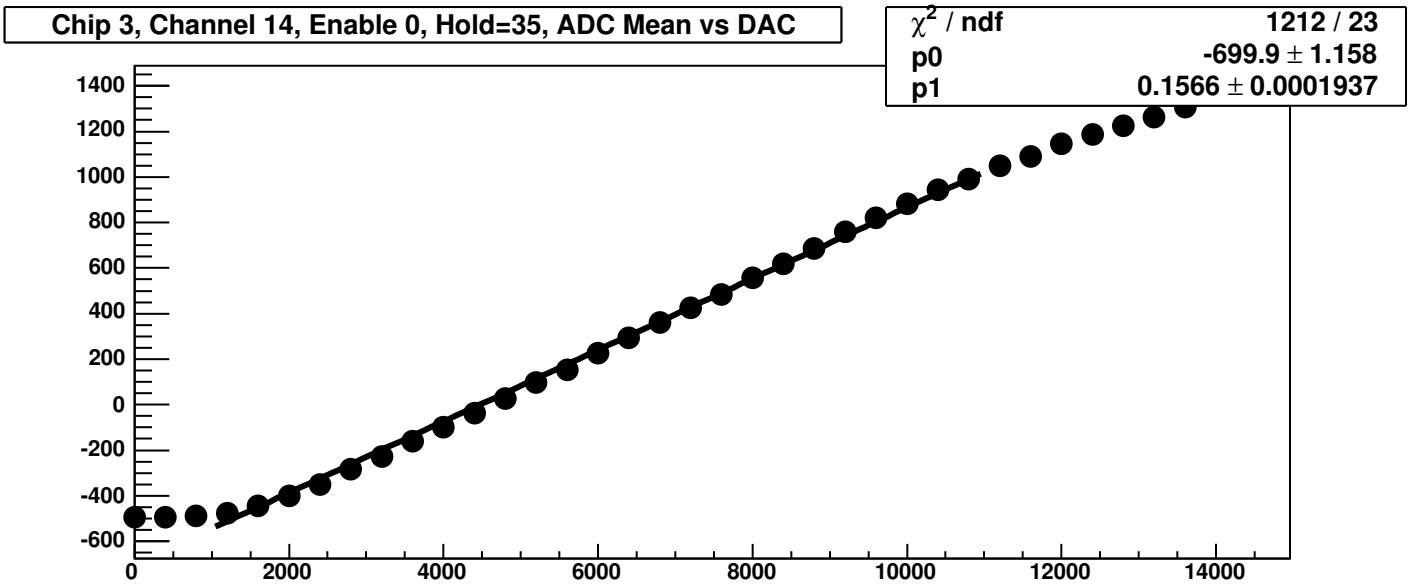


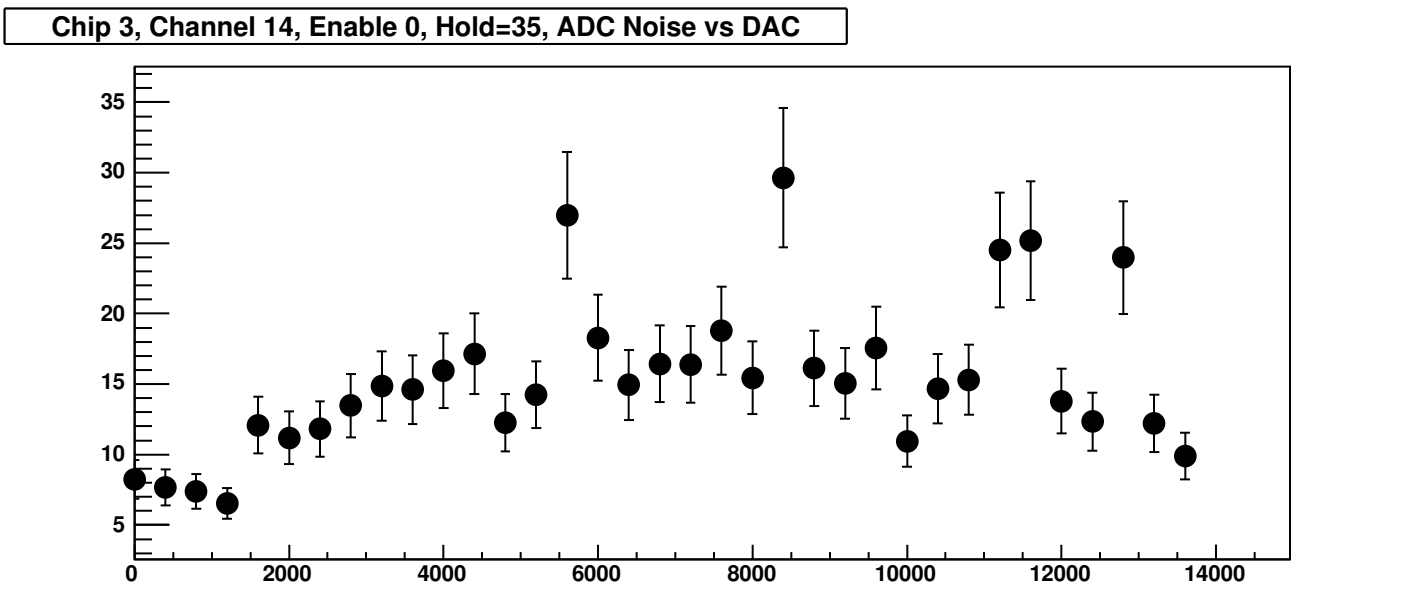
Chip 3, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

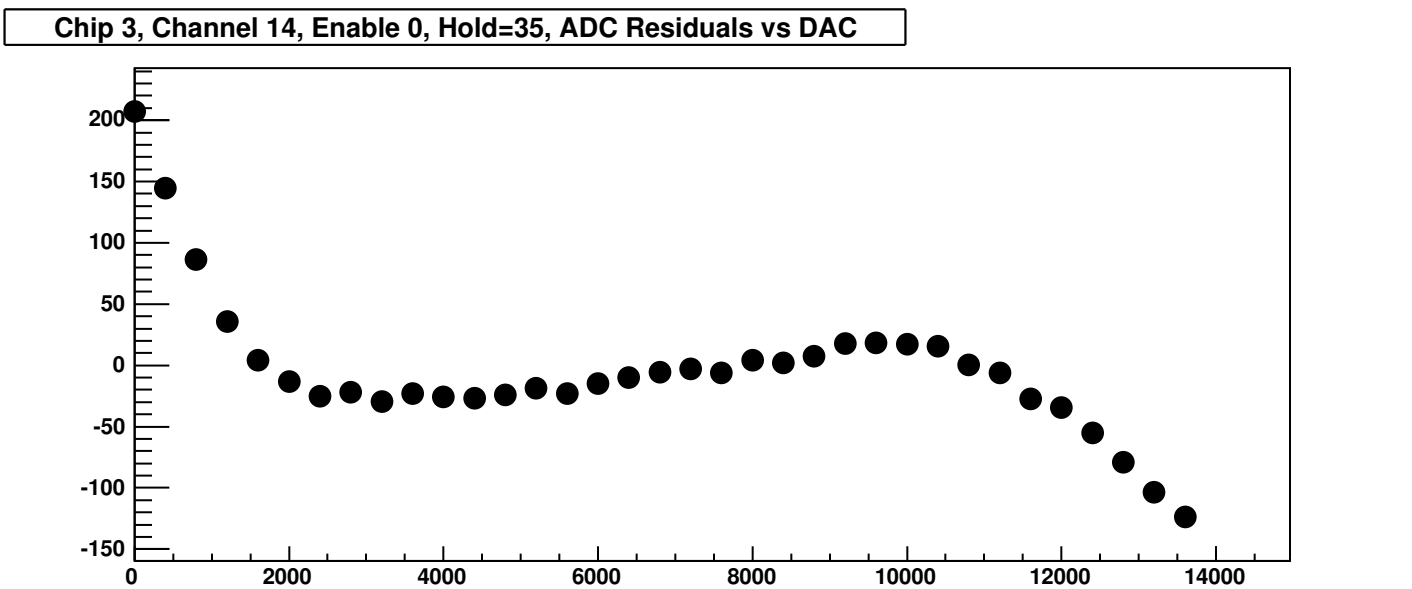


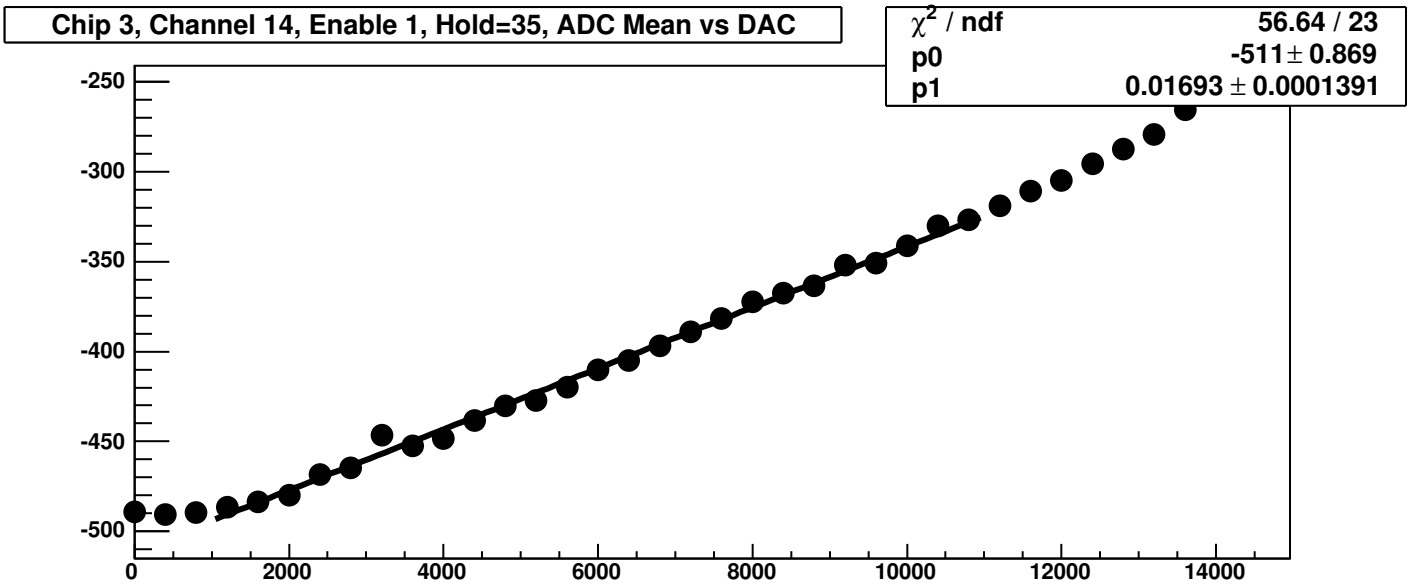
Chip 3, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

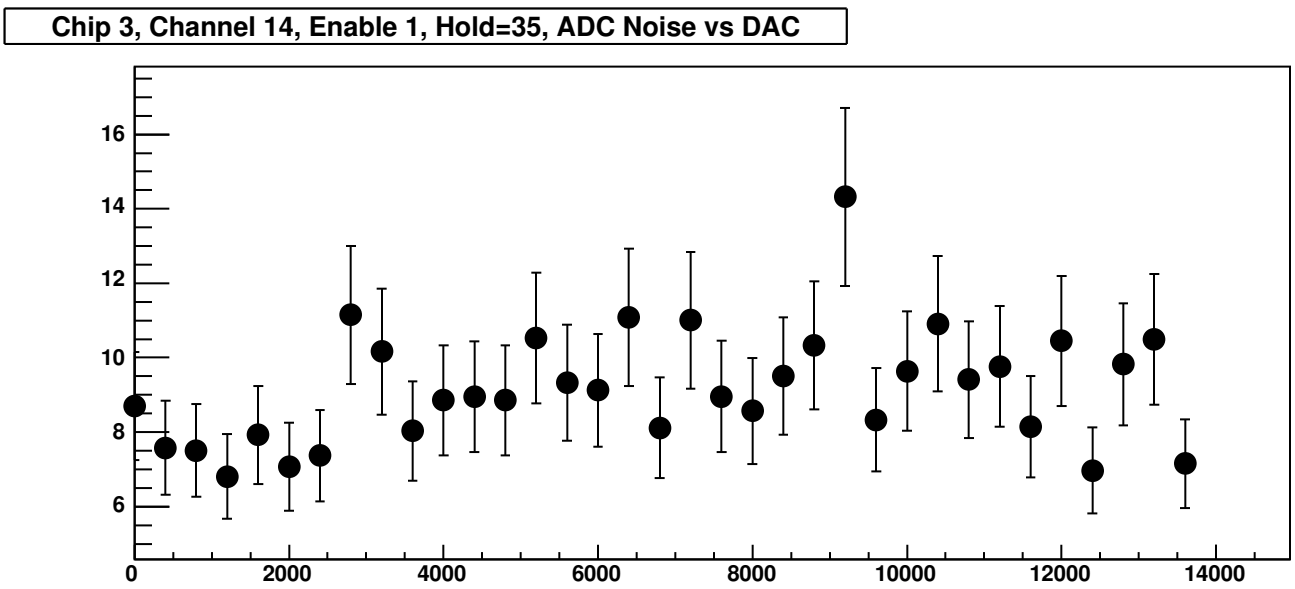


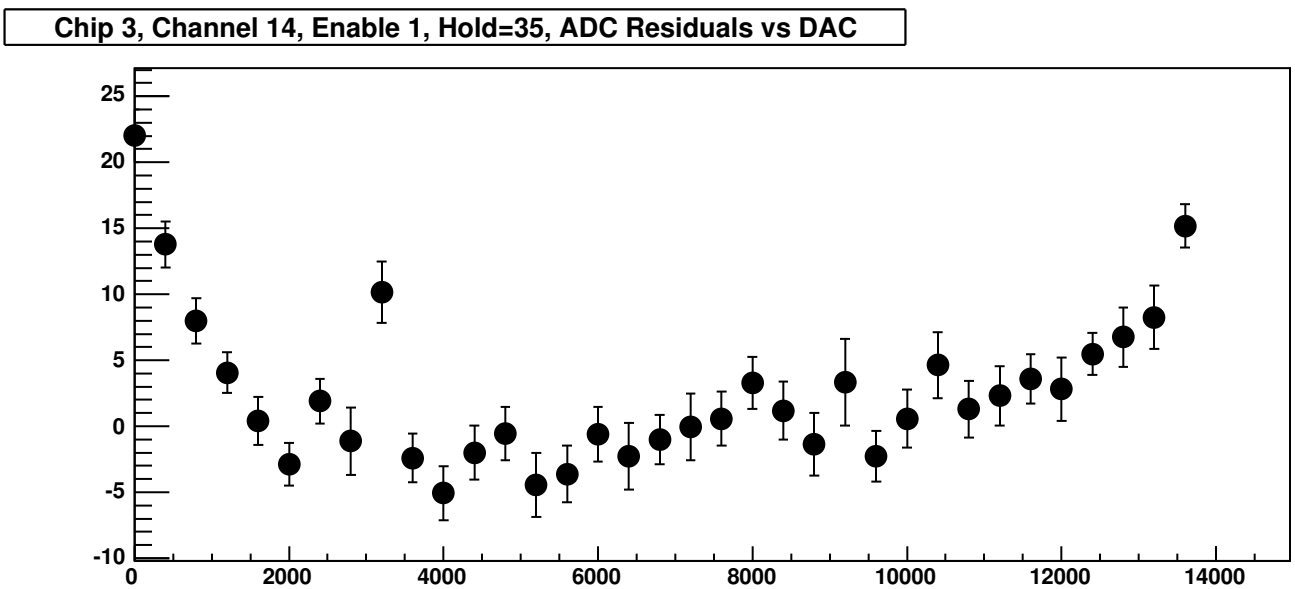

Chip 3, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1212 / 23
p0 -699.9 ± 1.158
p1 0.1566 ± 0.0001937
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

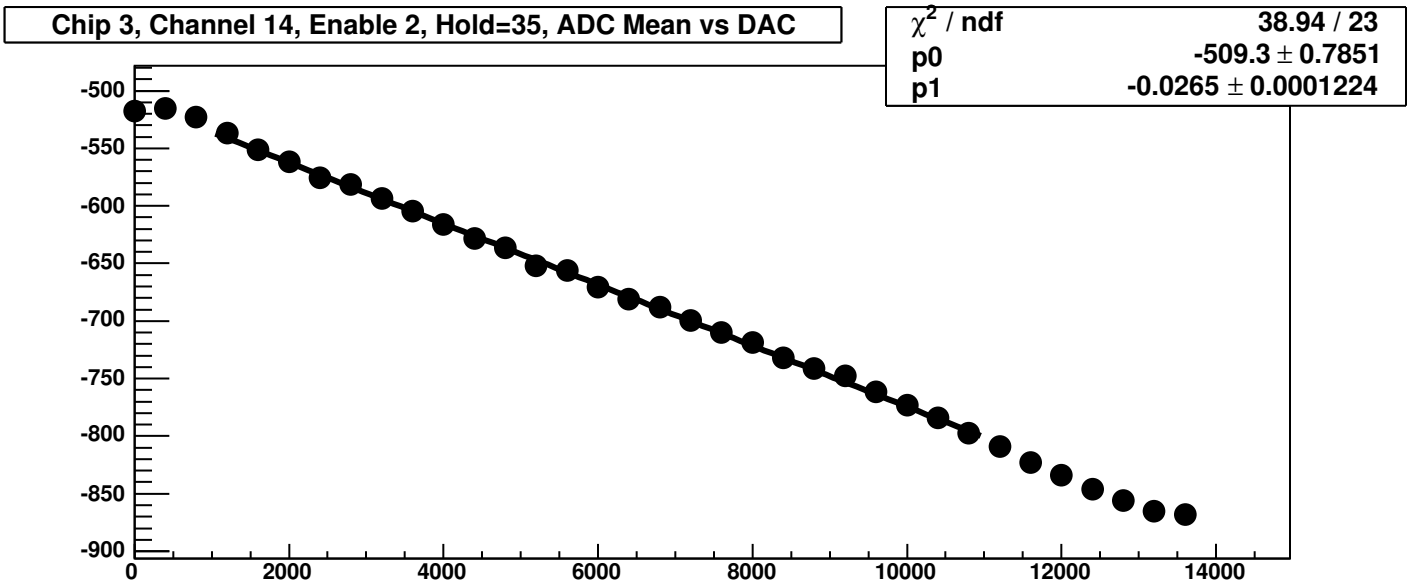

Chip 3, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

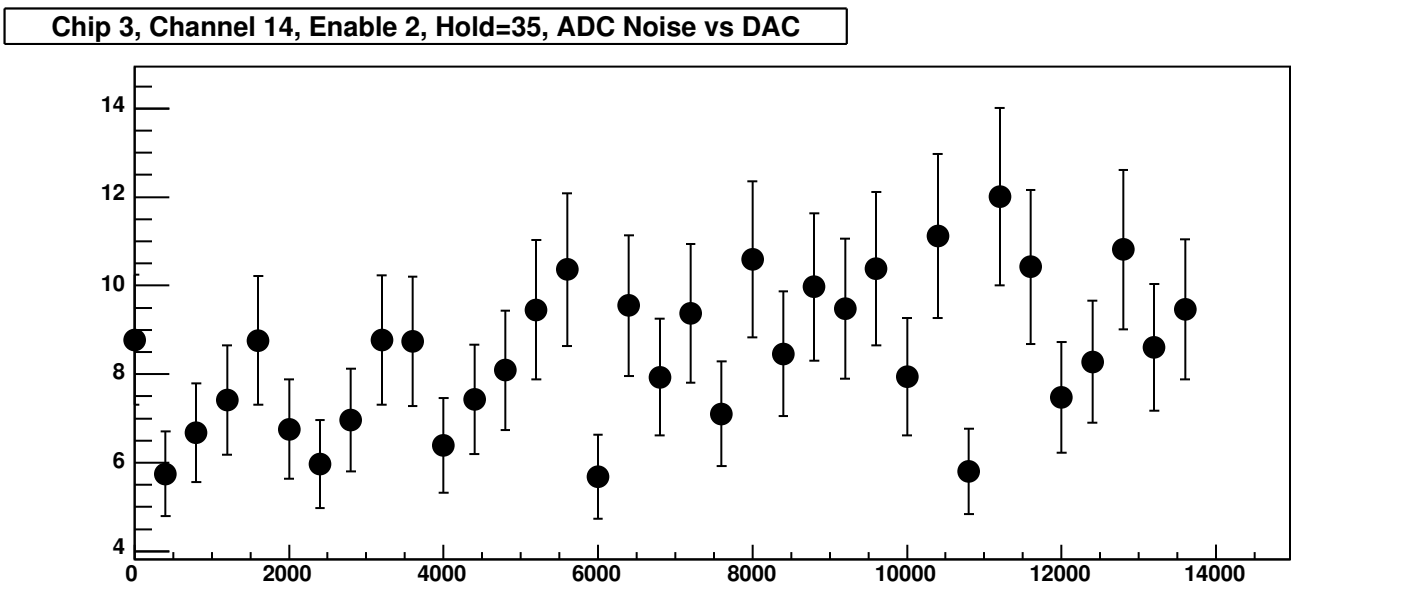

Chip 3, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

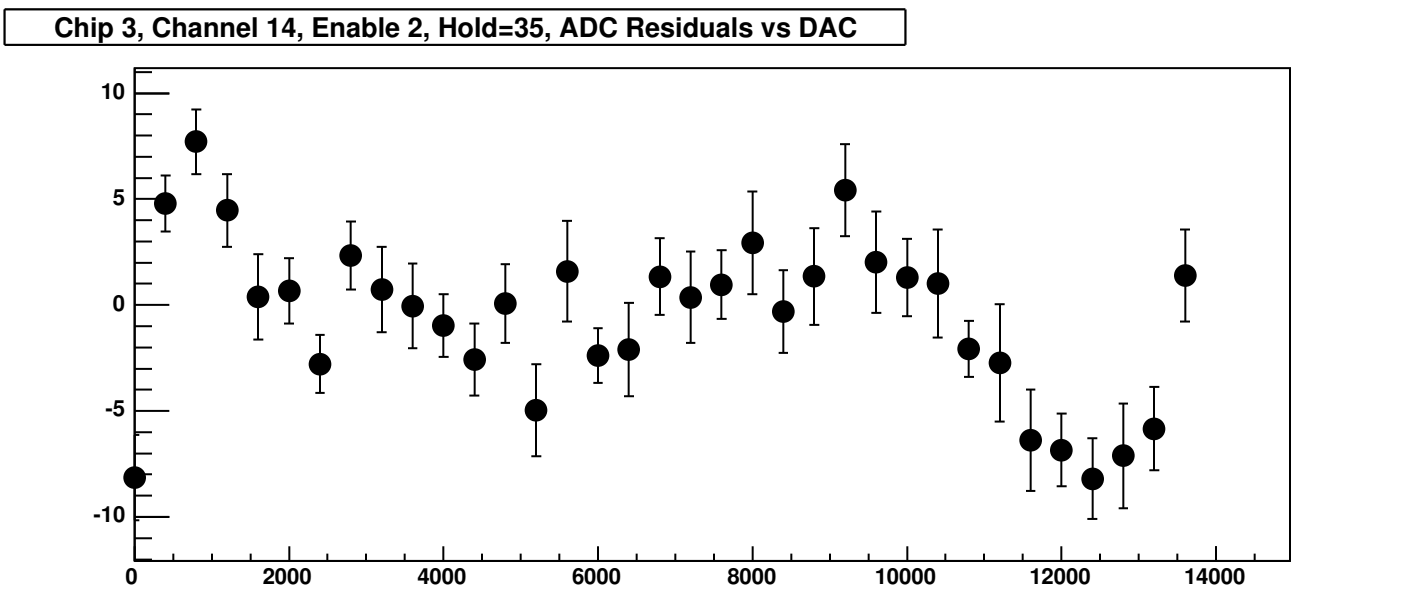

Chip 3, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 56.64 / 23
p0 -511 ± 0.869
p1 0.01693 ± 0.0001391
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

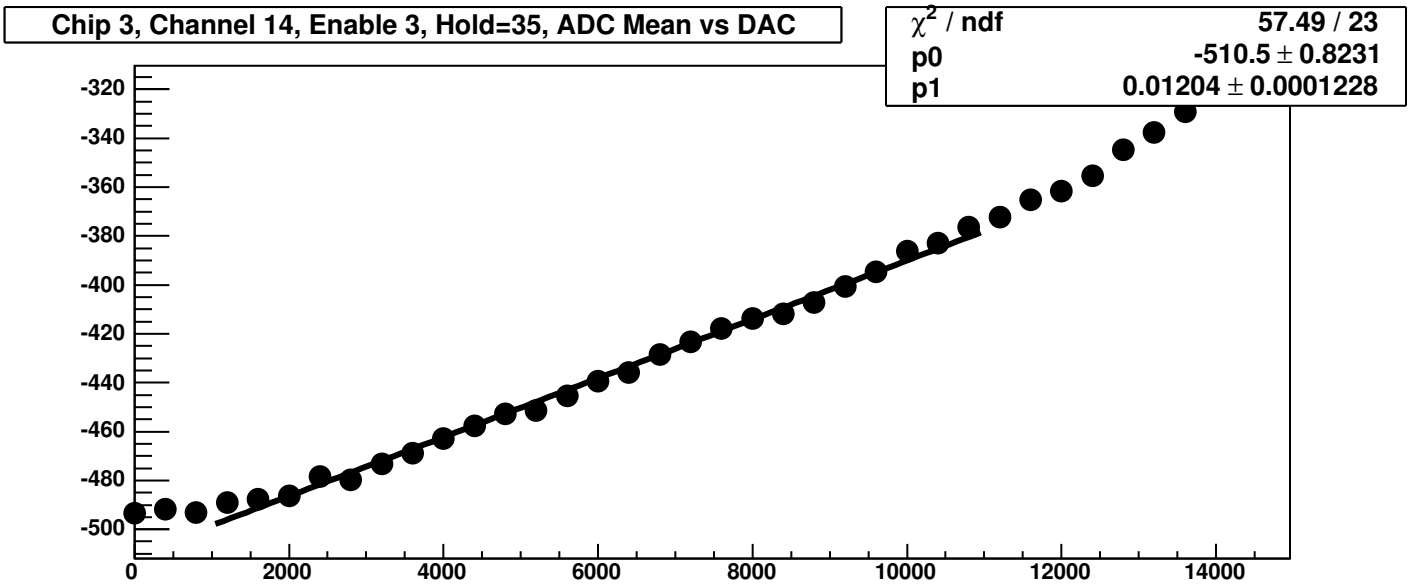

Chip 3, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

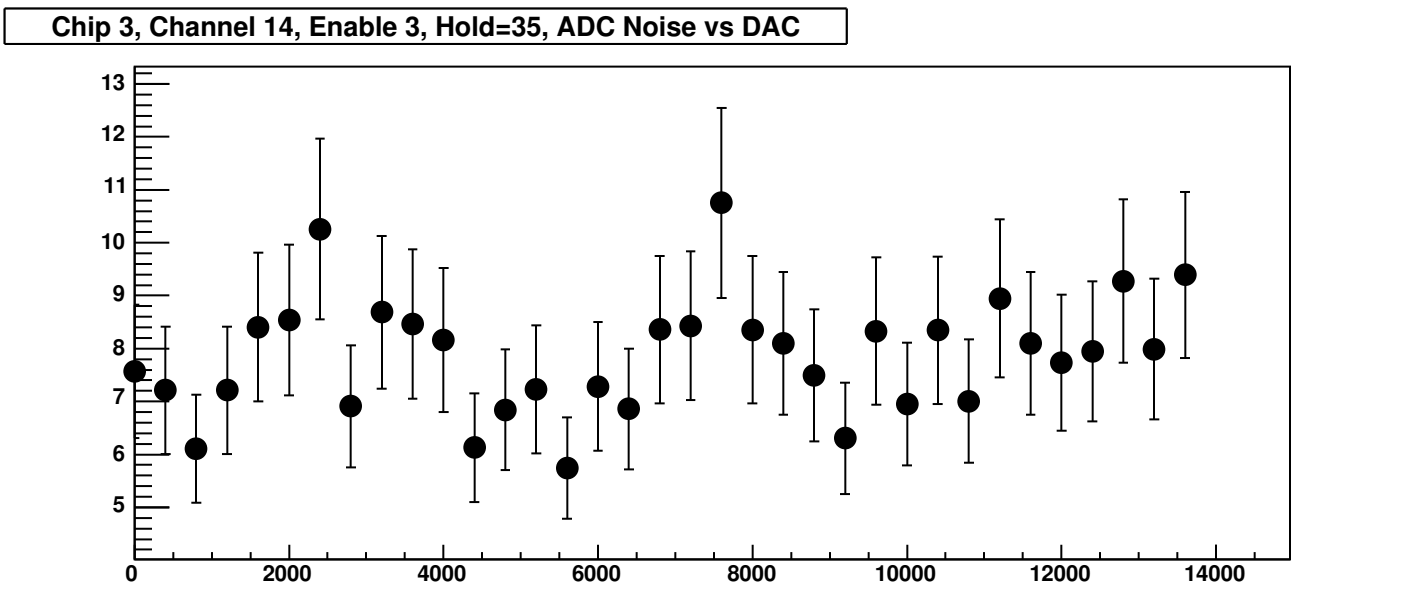

Chip 3, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

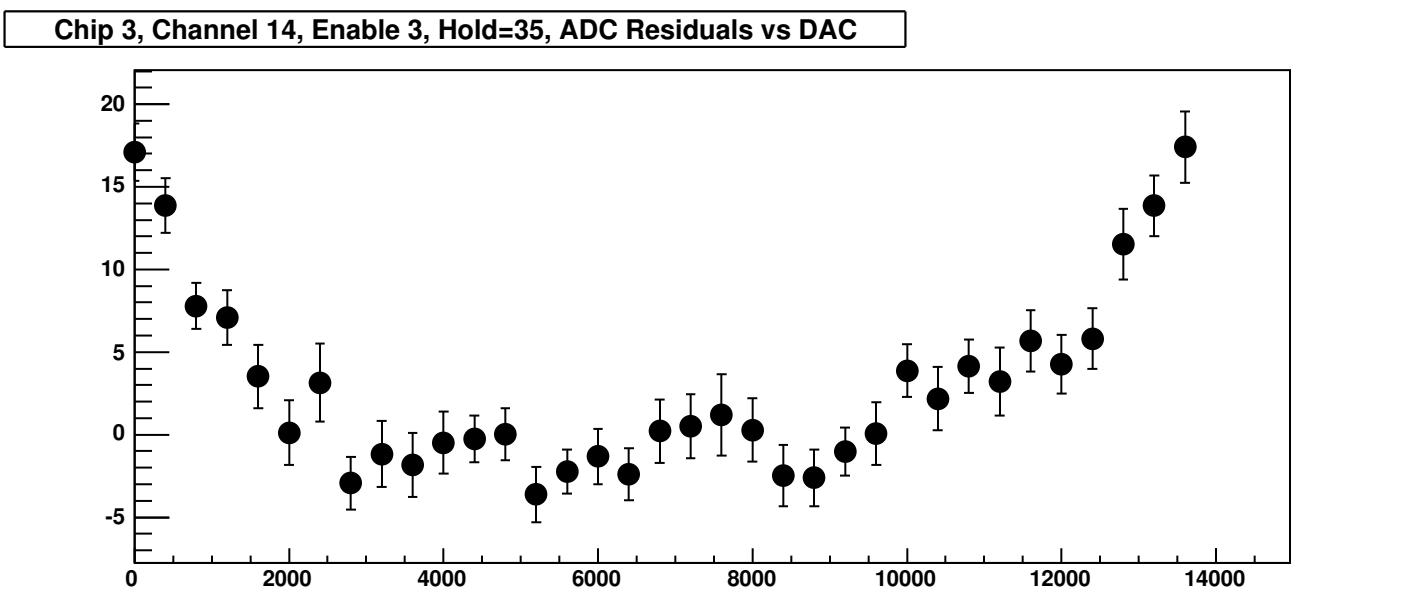

Chip 3, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 38.94 / 23
p0 -509.3 ± 0.7851
p1 -0.0265 ± 0.0001224
-500
-550
-600
-650
-700
-750
-800
-850
-900
0
2000
4000
6000
8000
10000
12000
14000

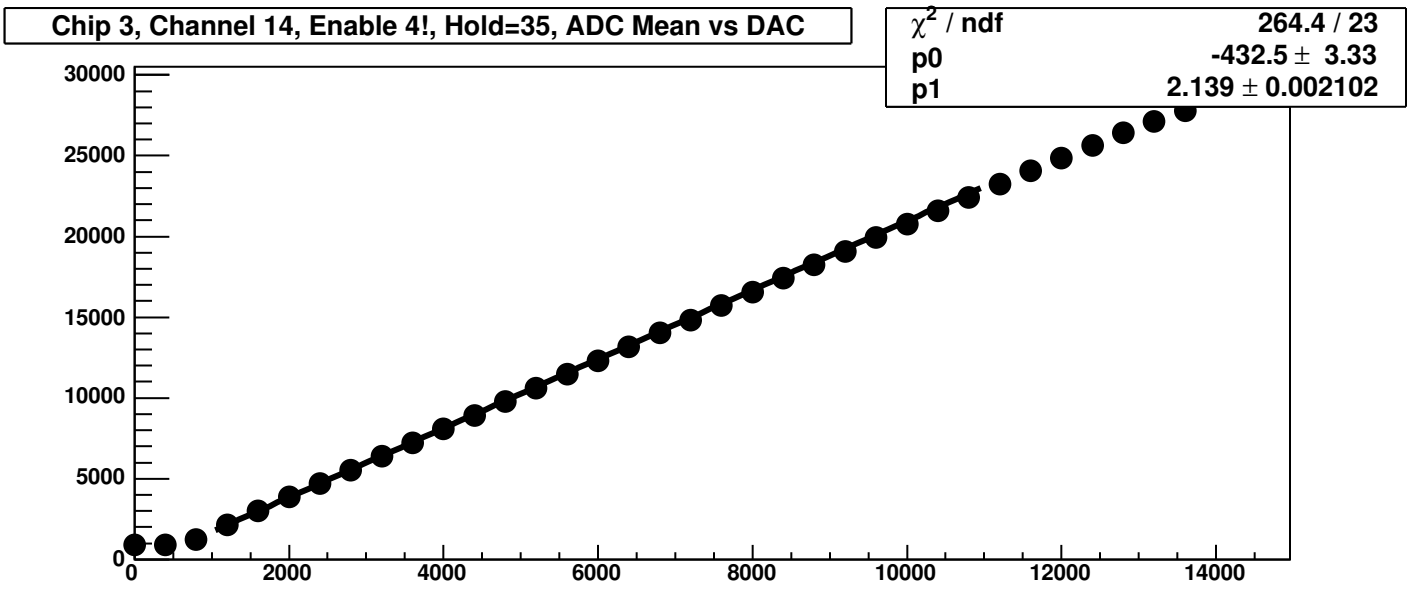

Chip 3, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

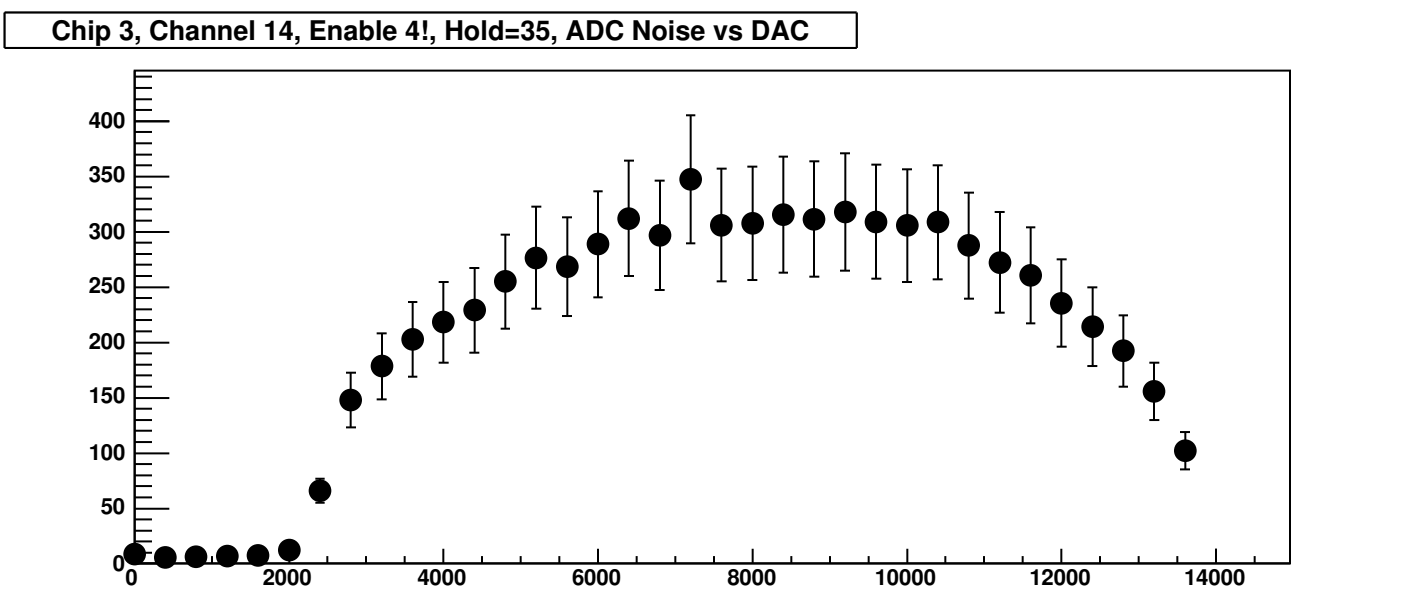

Chip 3, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

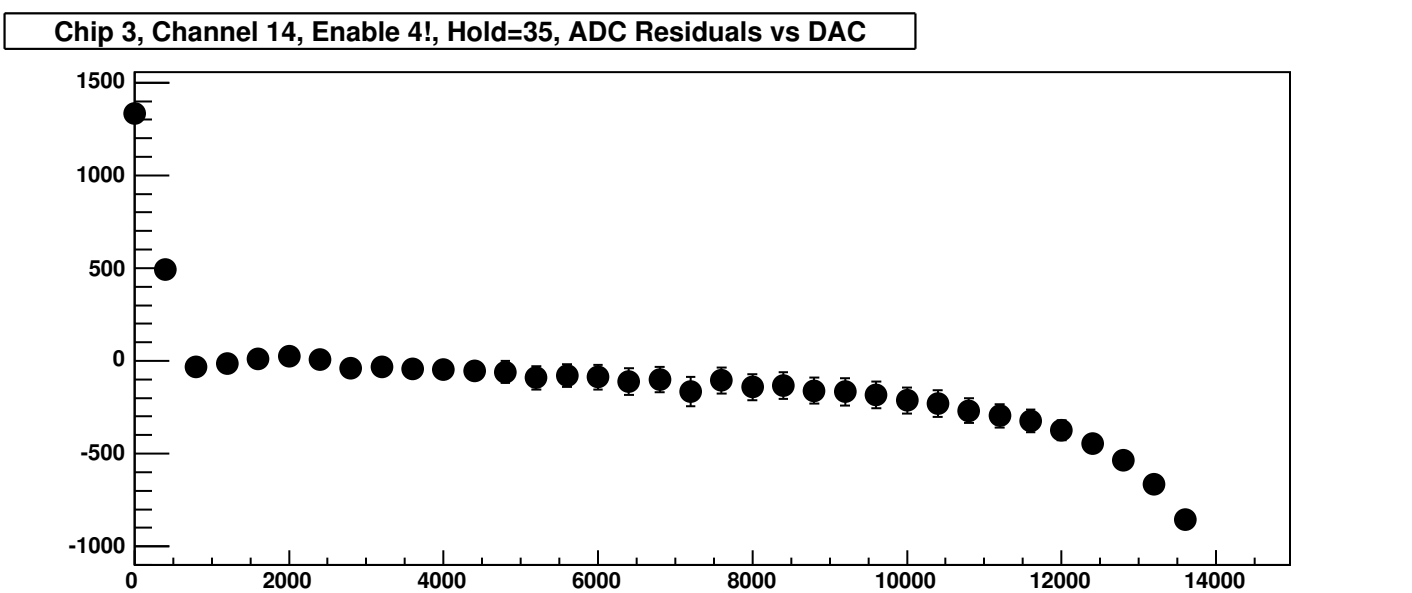

Chip 3, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 57.49 / 23
p0 -510.5 ± 0.8231
p1 0.01204 ± 0.0001228
-320
-340
-360
-380
-400
-420
-440
-460
-480
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

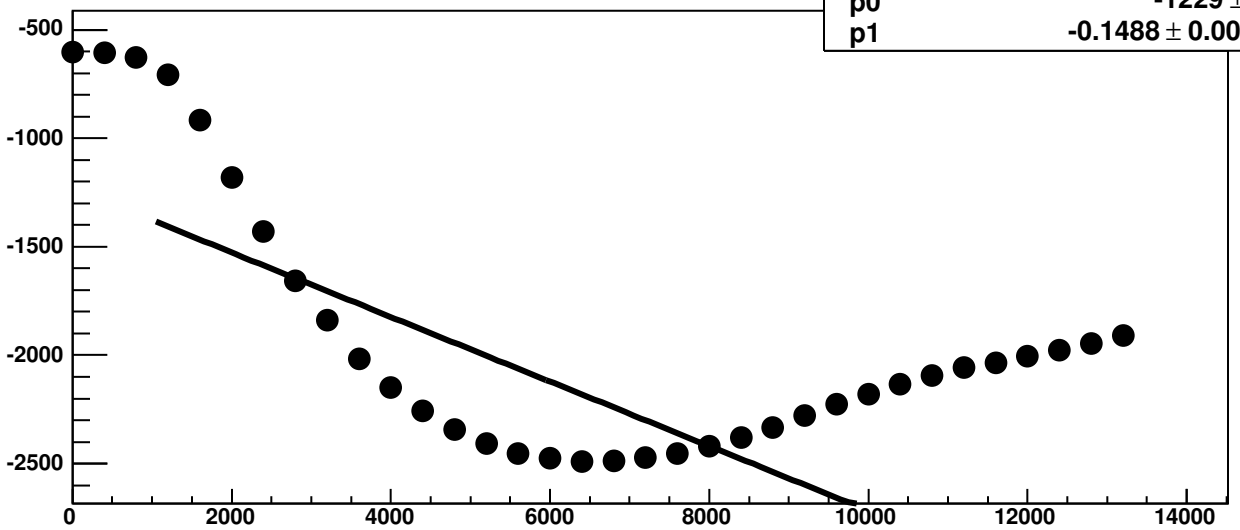

Chip 3, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 264.4 / 23
p0 -432.5 ± 3.33
p1 2.139 ± 0.002102
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

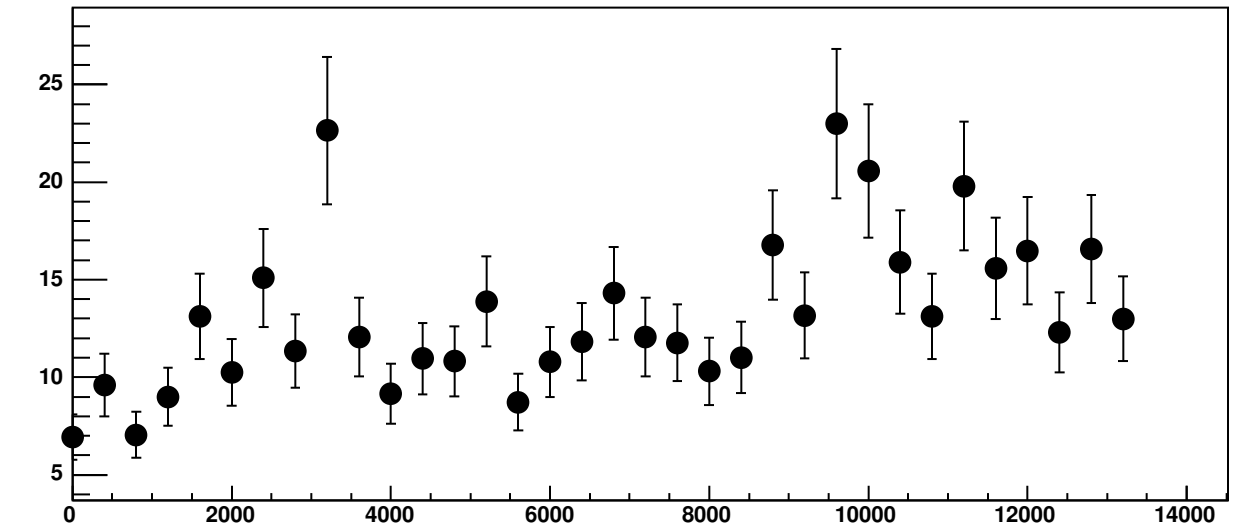

Chip 3, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

Chip 3, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

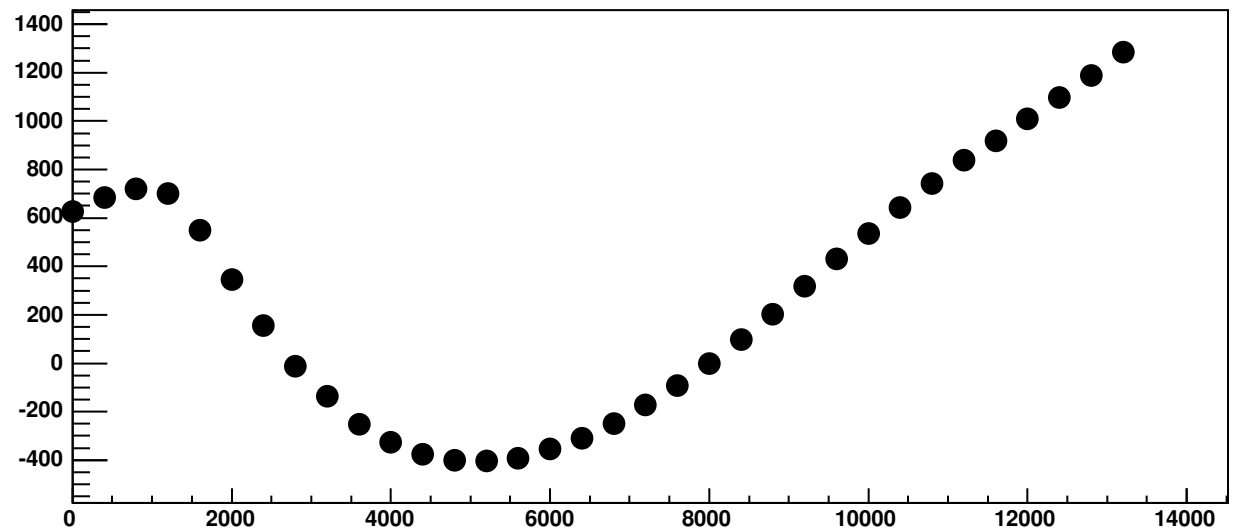
χ^2 / ndf	4.796e+05 / 23
p0	-1229 ± 1.251
p1	-0.1488 ± 0.0002043

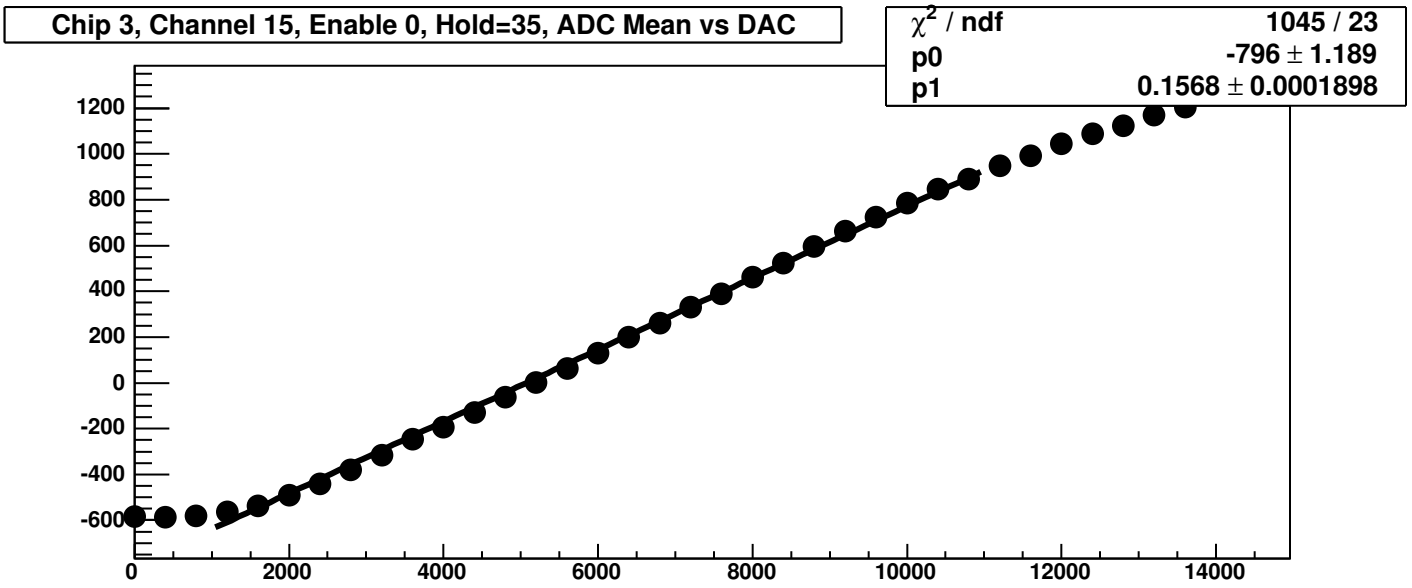


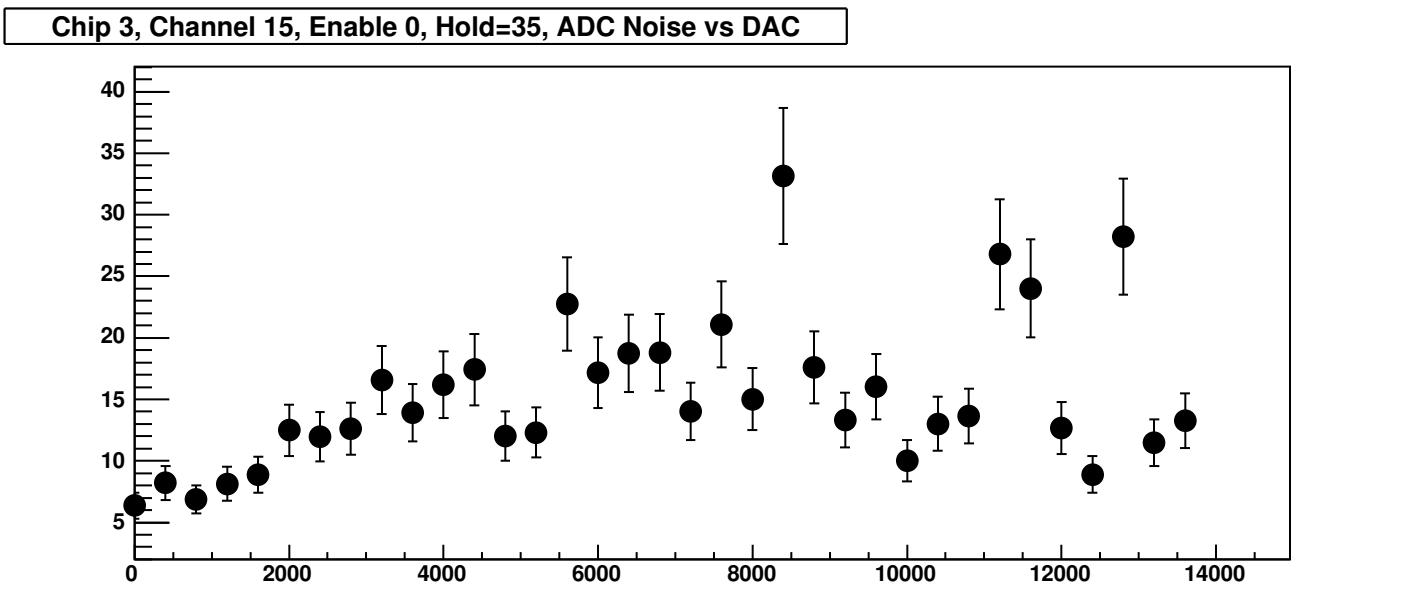
Chip 3, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC

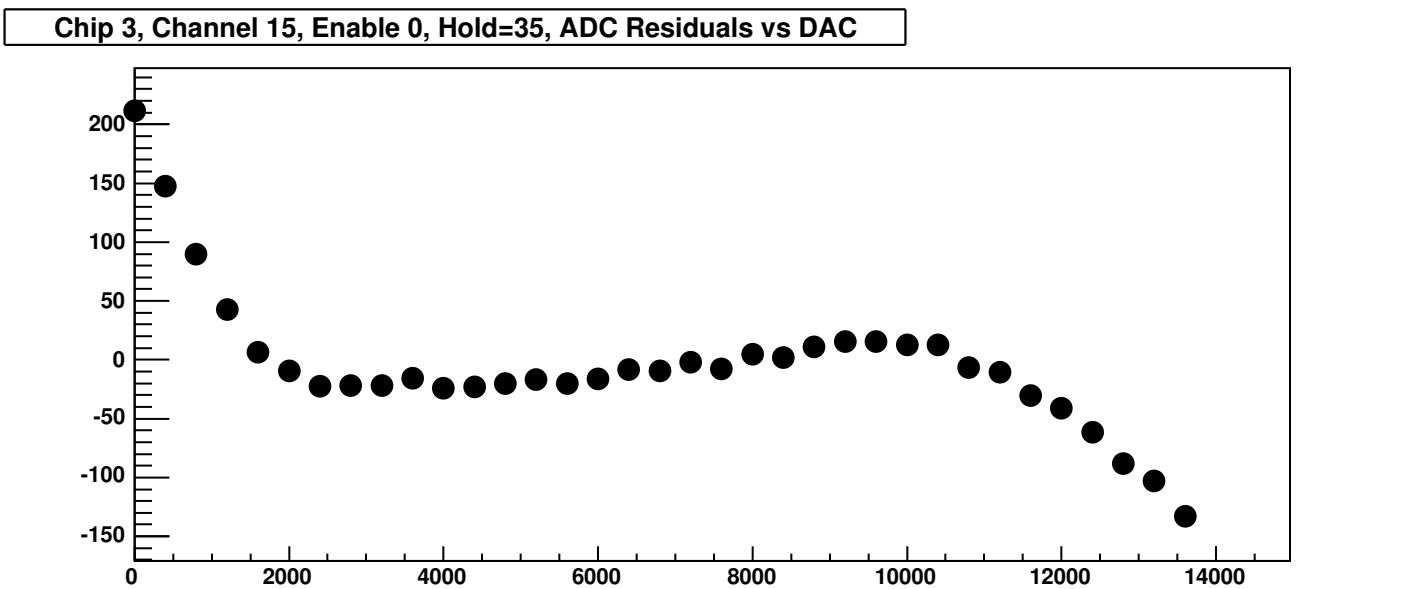


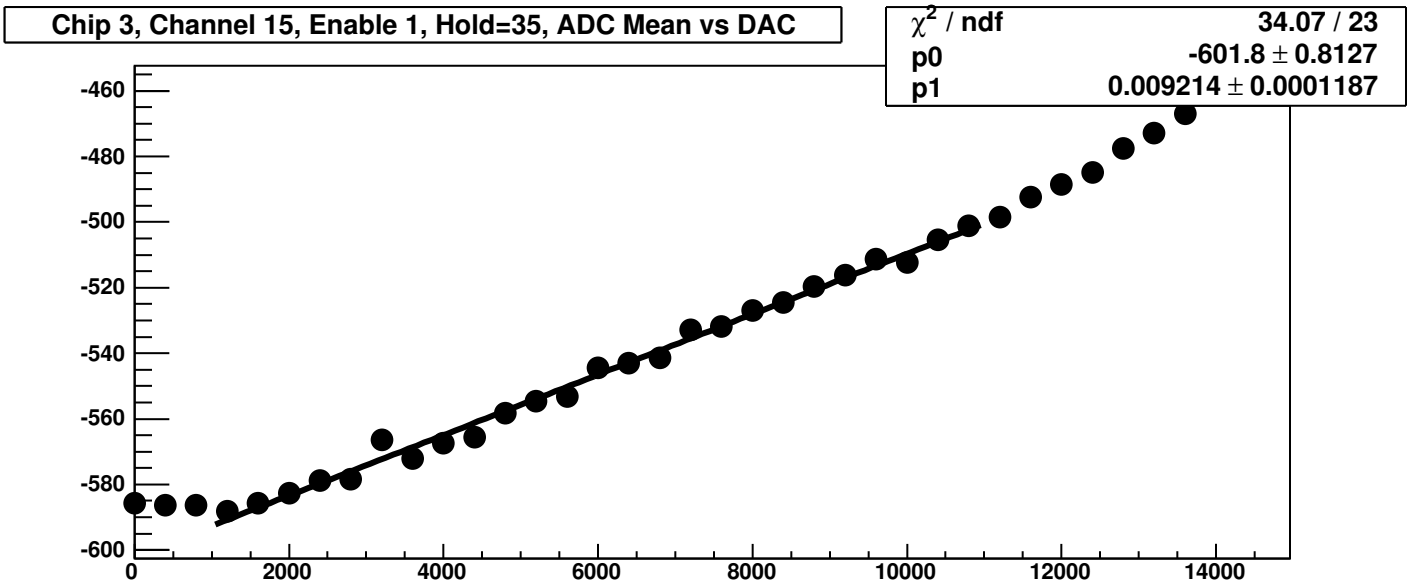
Chip 3, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

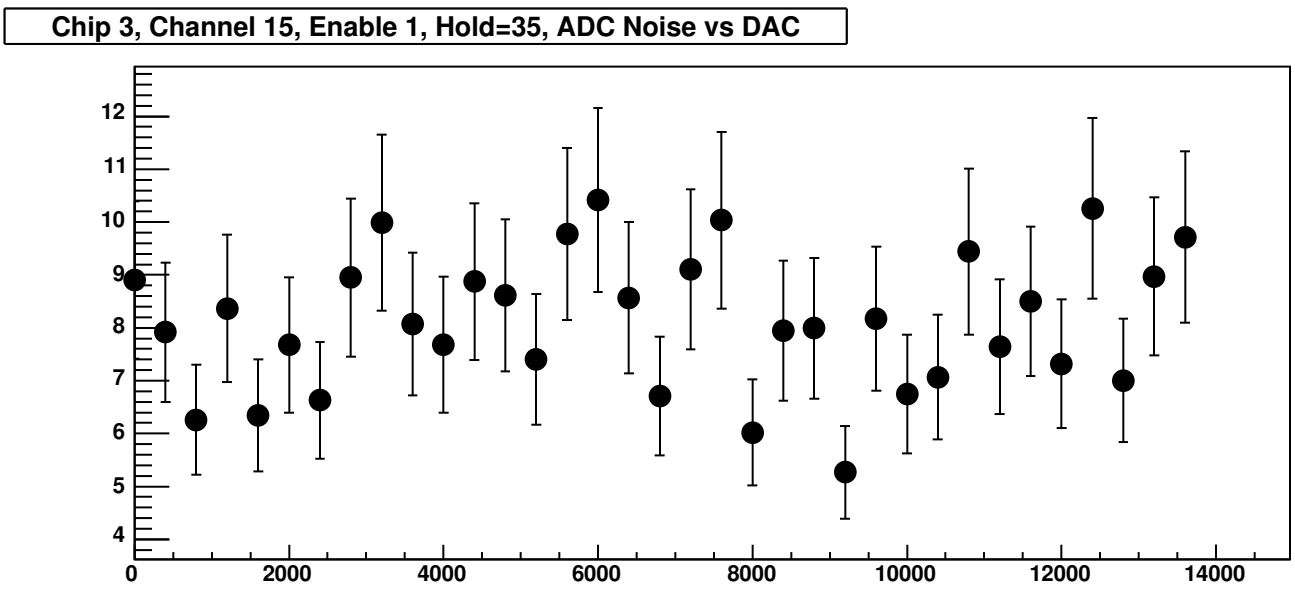


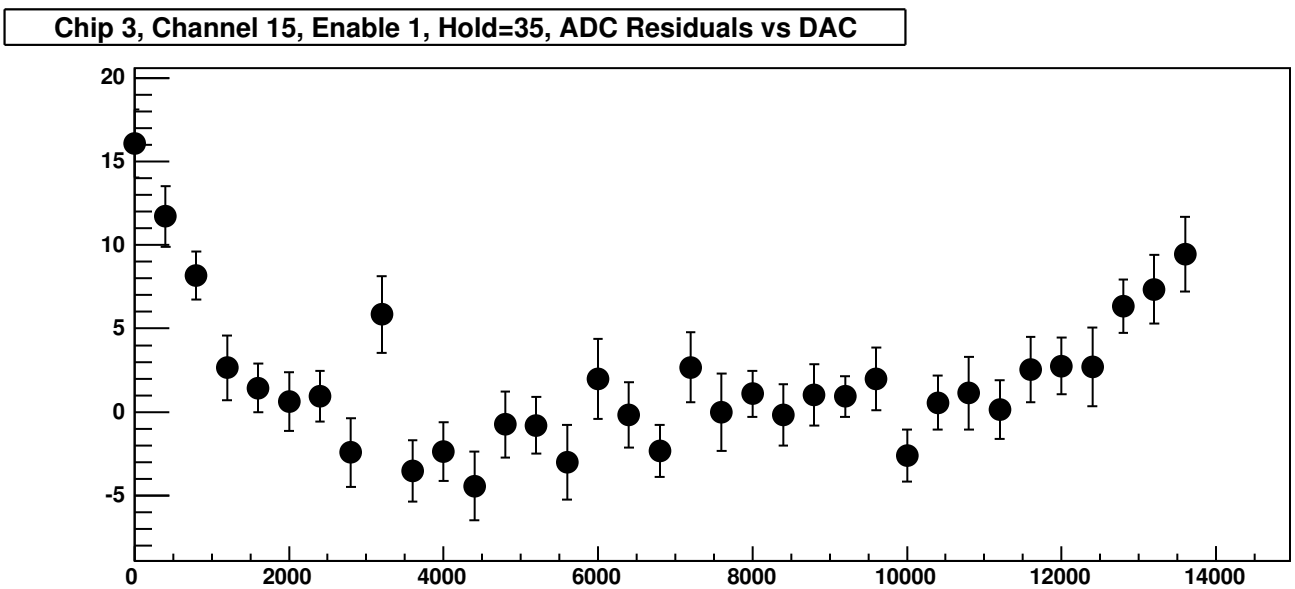

Chip 3, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1045 / 23
p0 -796 ± 1.189
p1 0.1568 ± 0.0001898
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

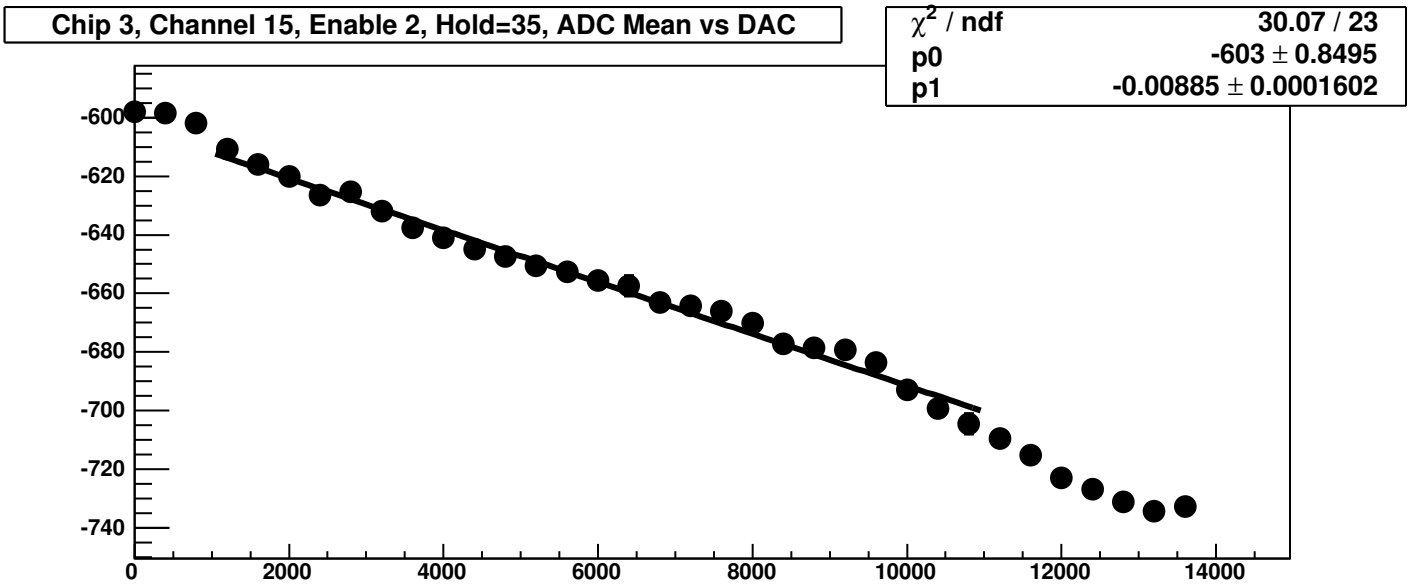

Chip 3, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

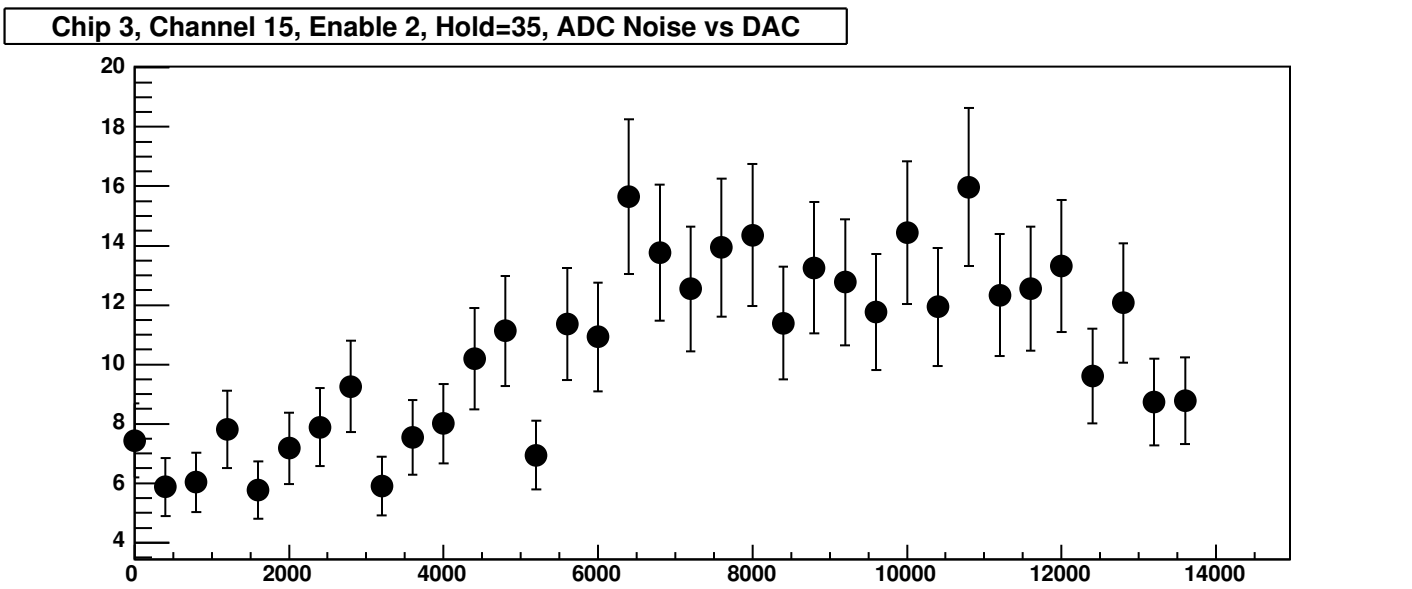

Chip 3, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

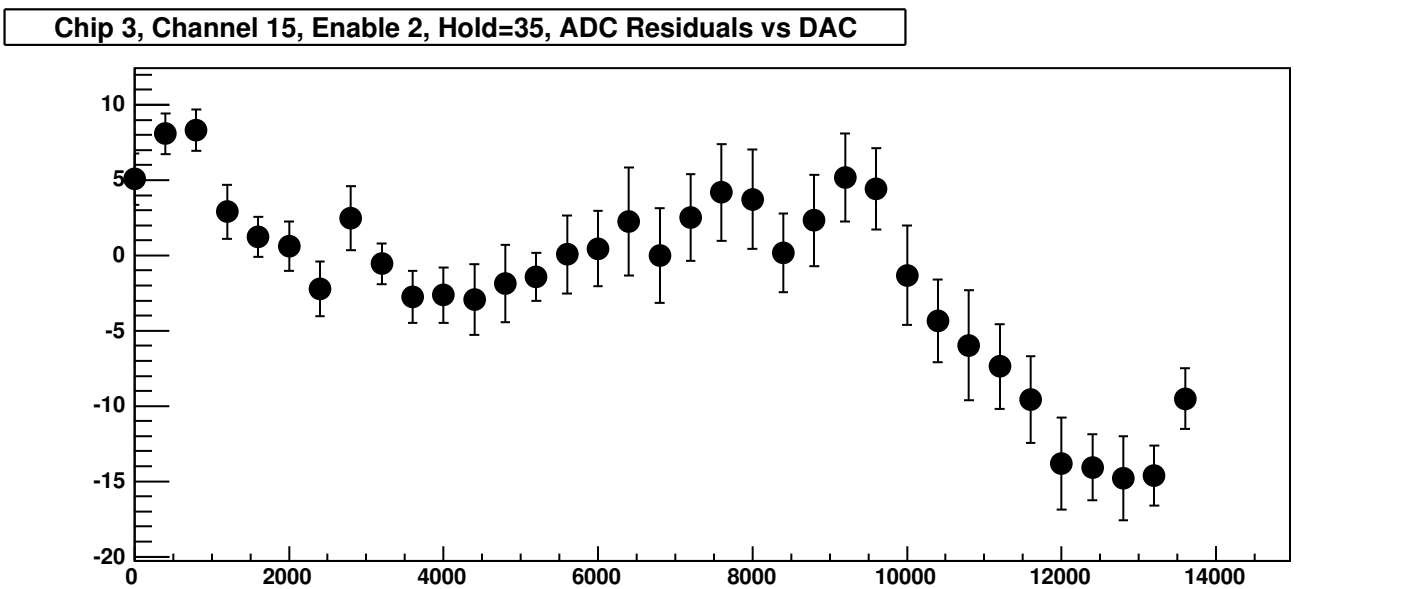

Chip 3, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 34.07 / 23
p0 -601.8 ± 0.8127
p1 0.009214 ± 0.0001187
-460
-480
-500
-520
-540
-560
-580
-600
0
2000
4000
6000
8000
10000
12000
14000

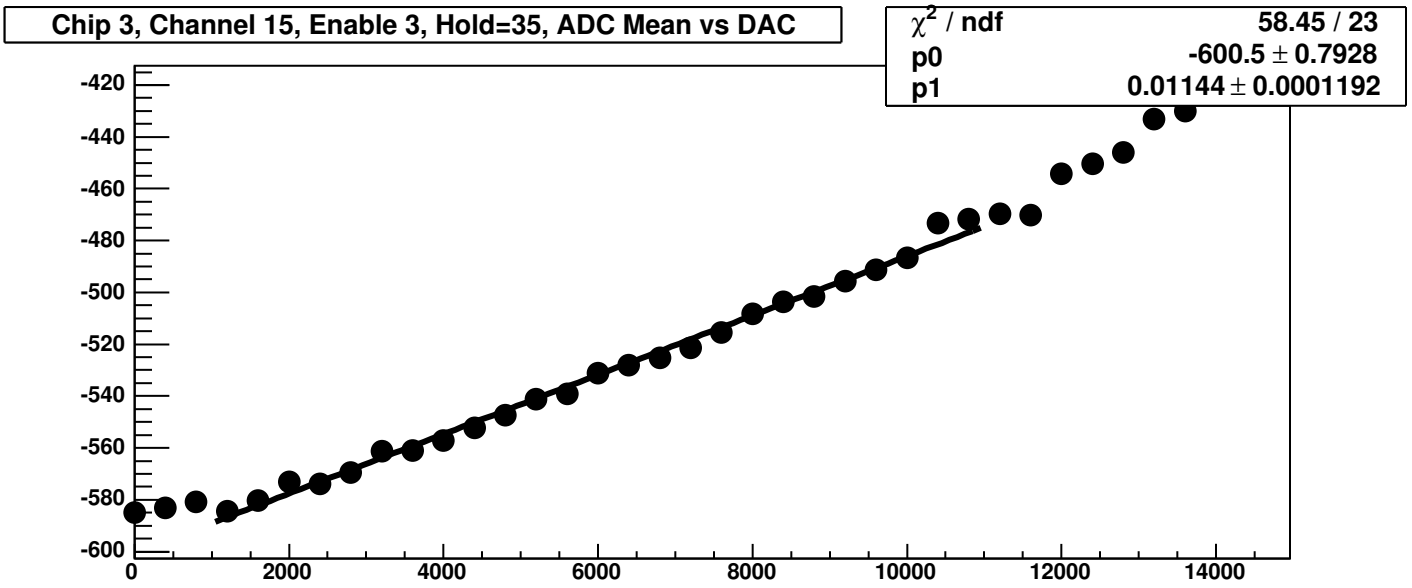

Chip 3, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

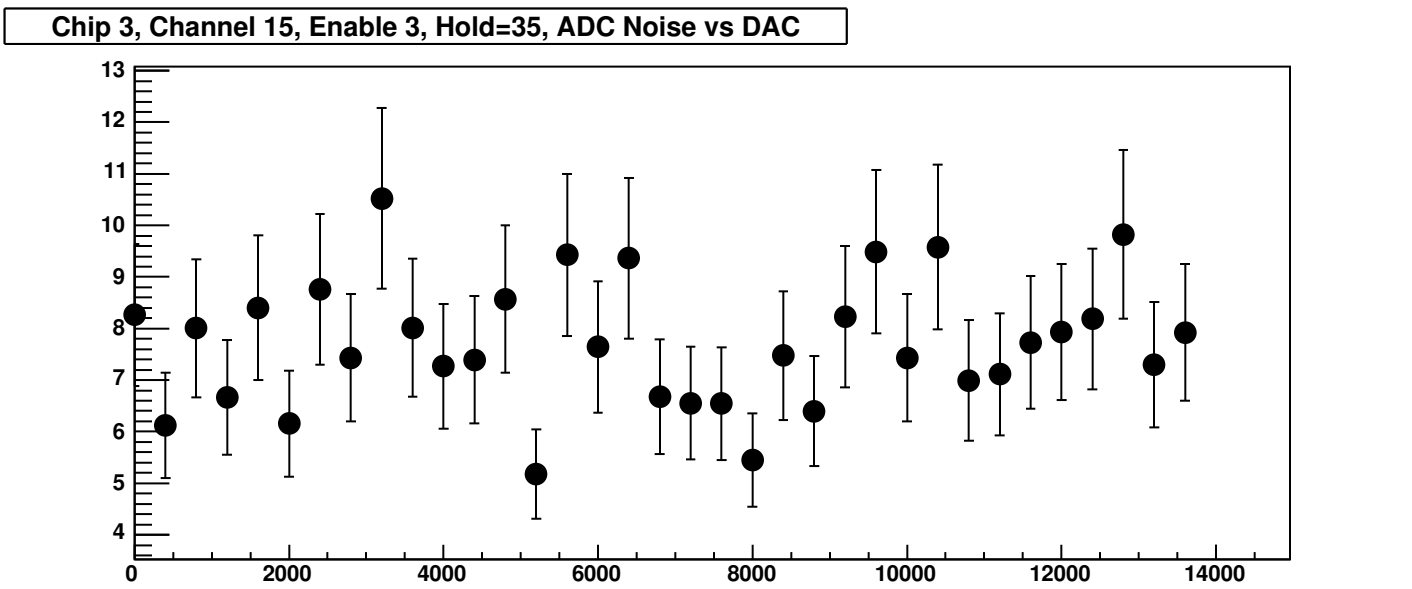

Chip 3, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

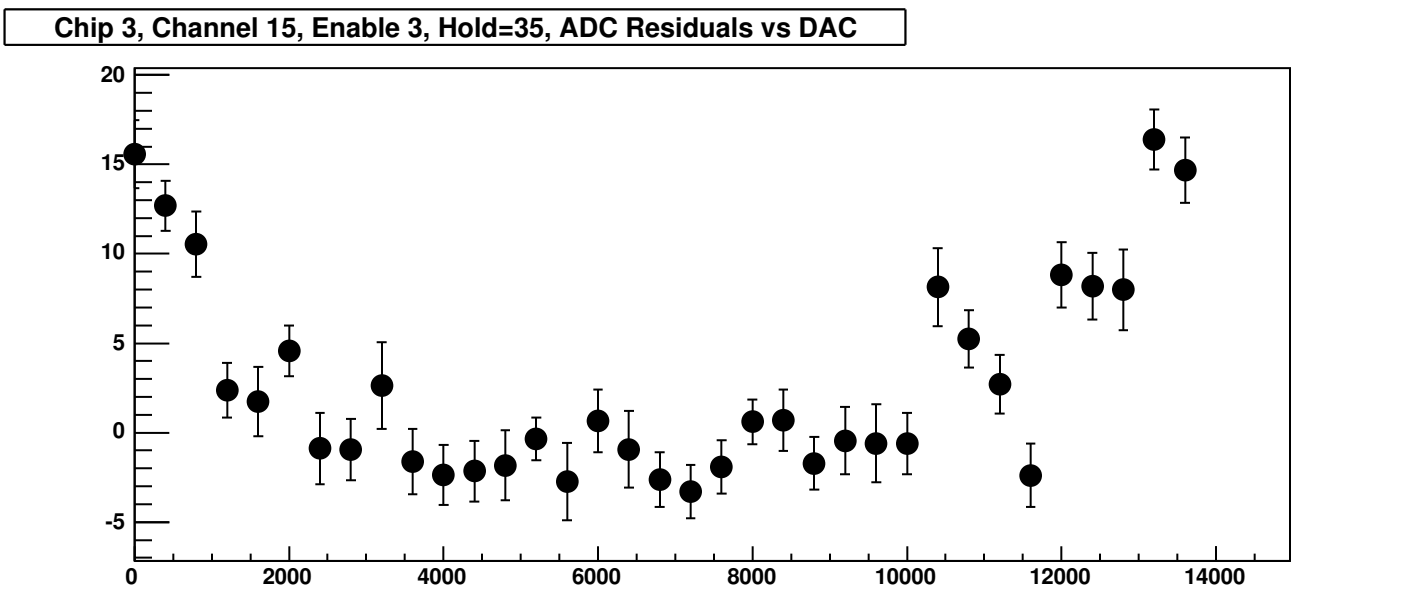

Chip 3, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 30.07 / 23
p0 -603 ± 0.8495
p1 -0.00885 ± 0.0001602
-600
-620
-640
-660
-680
-700
-720
-740
0
2000
4000
6000
8000
10000
12000
14000

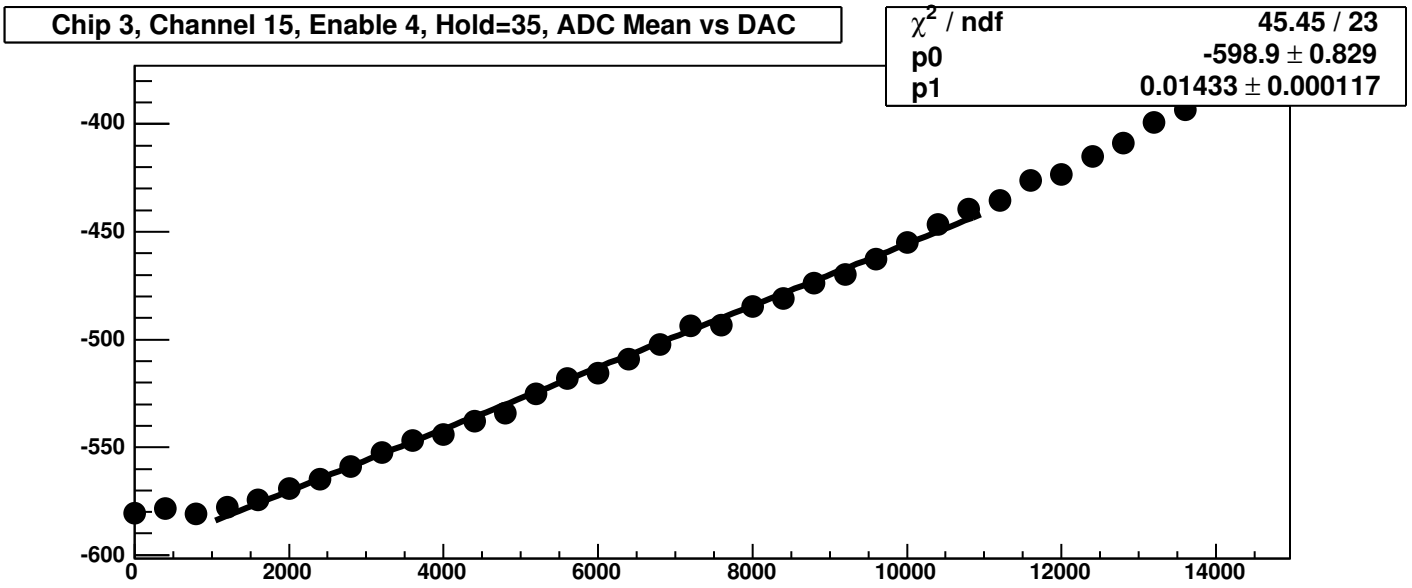

Chip 3, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

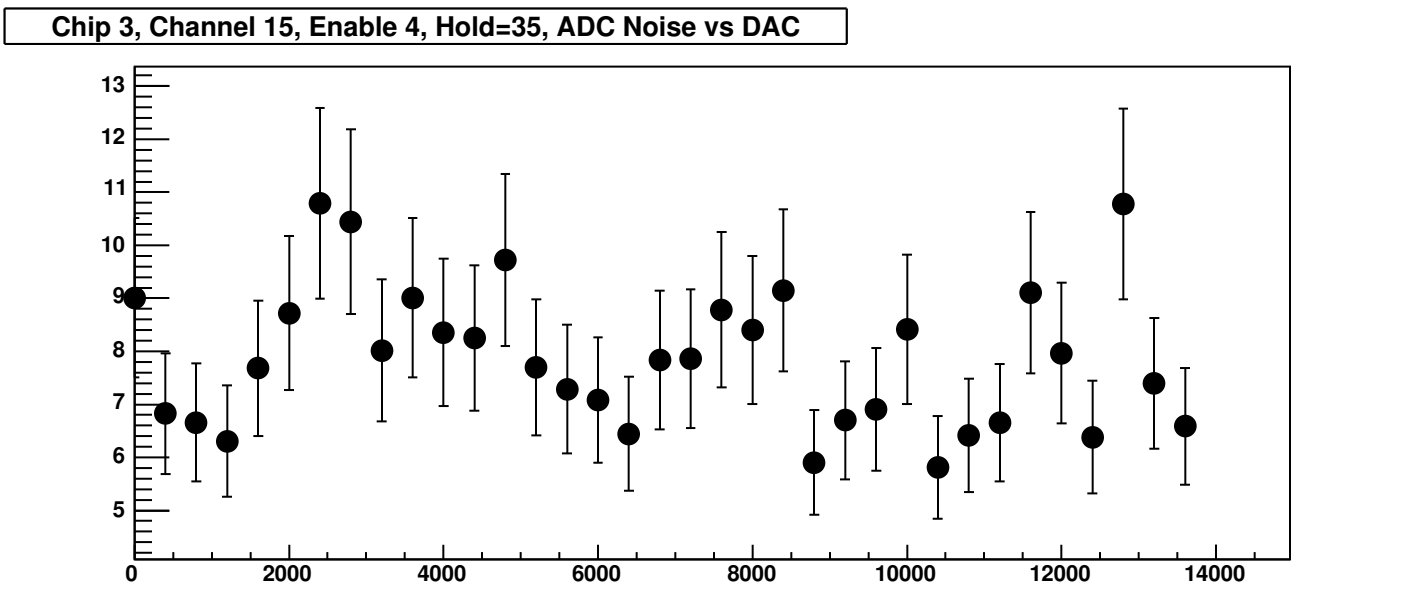

Chip 3, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

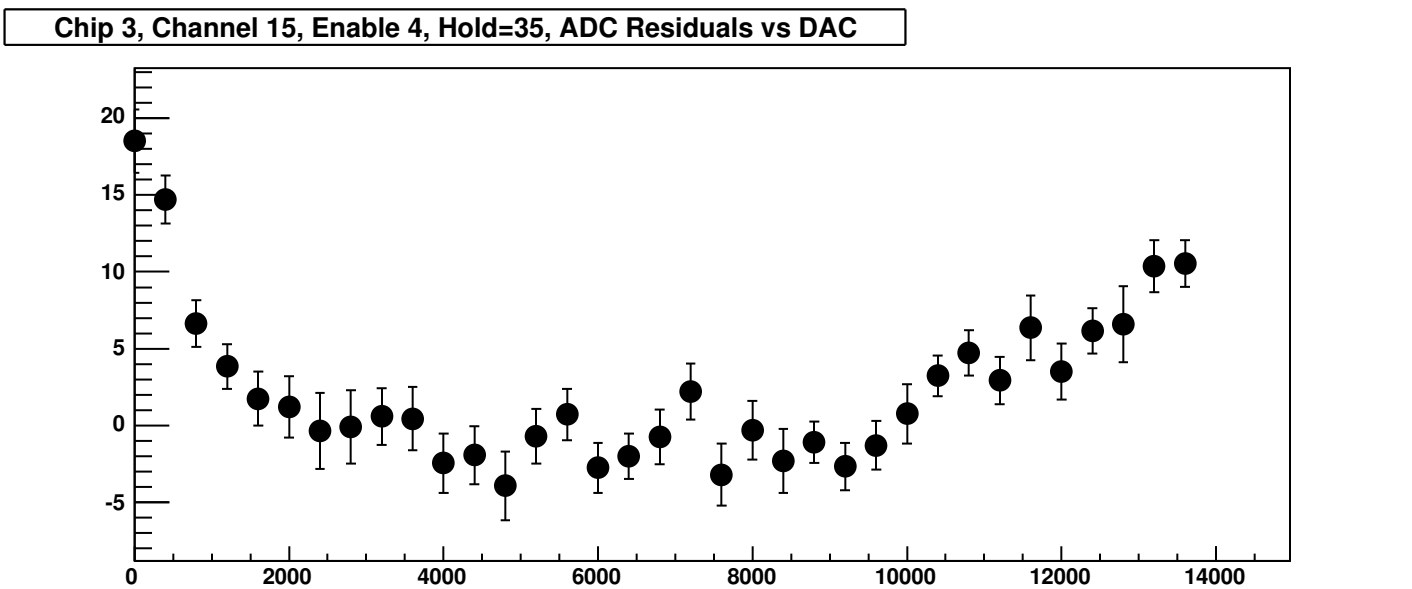

Chip 3, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 58.45 / 23
p0 -600.5 ± 0.7928
p1 0.01144 ± 0.0001192
-420
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
2000
4000
6000
8000
10000
12000
14000

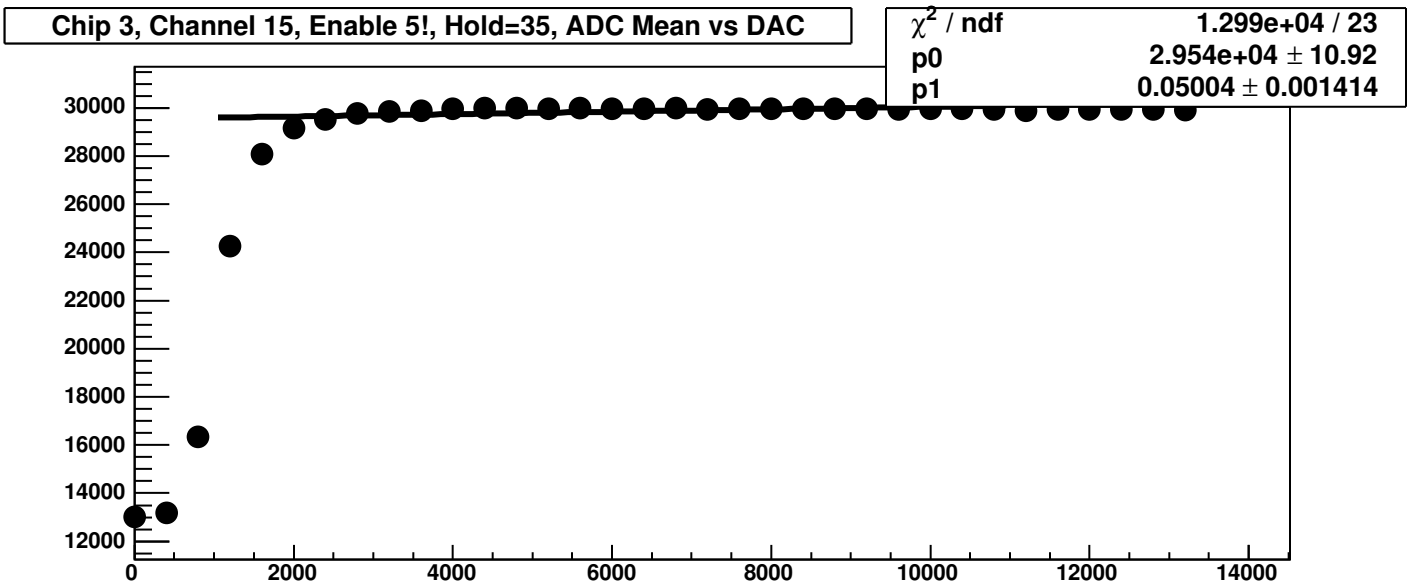

Chip 3, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

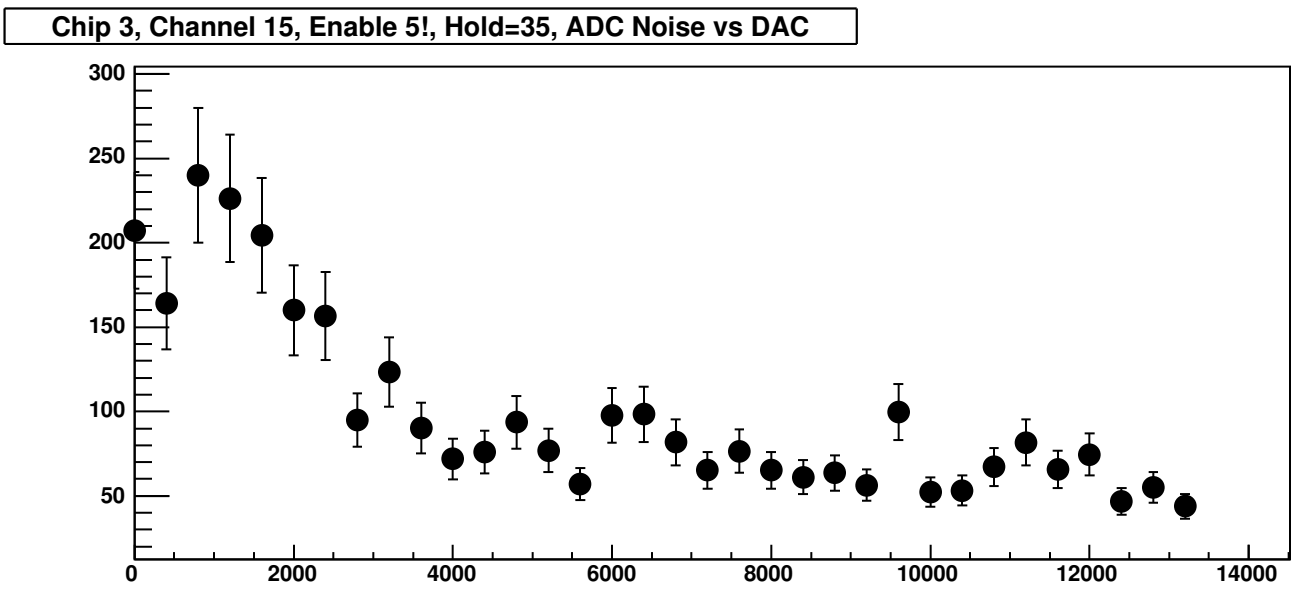

Chip 3, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

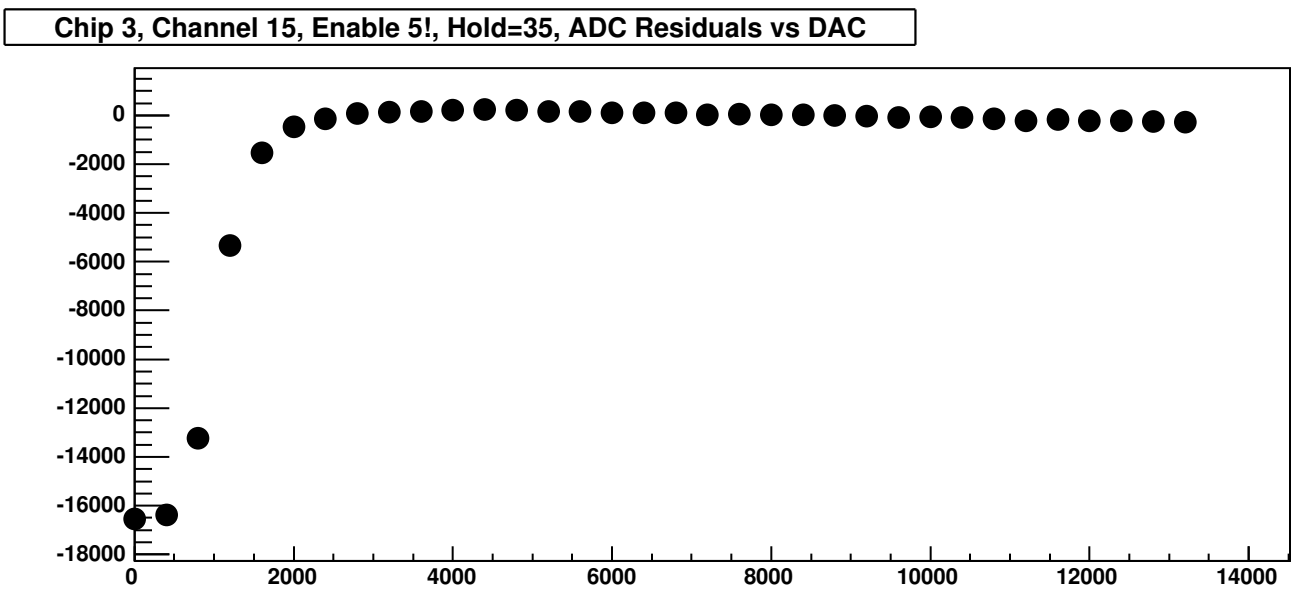

Chip 3, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 45.45 / 23
p0 -598.9 ± 0.829
p1 0.01433 ± 0.000117
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

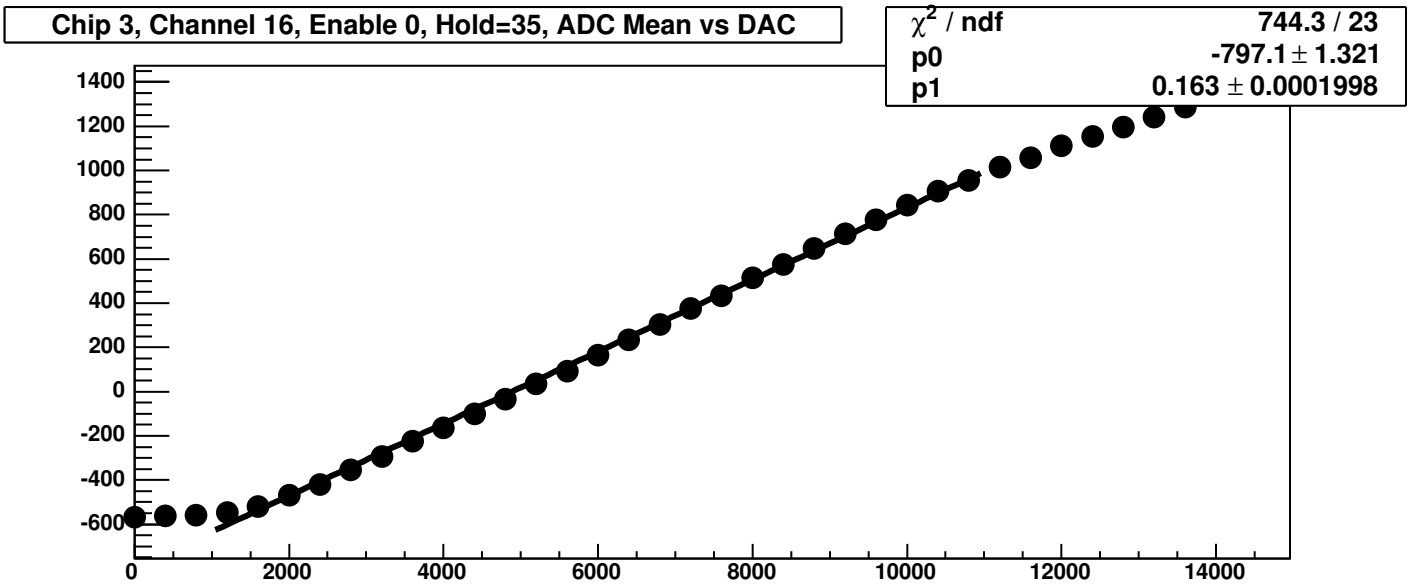

Chip 3, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

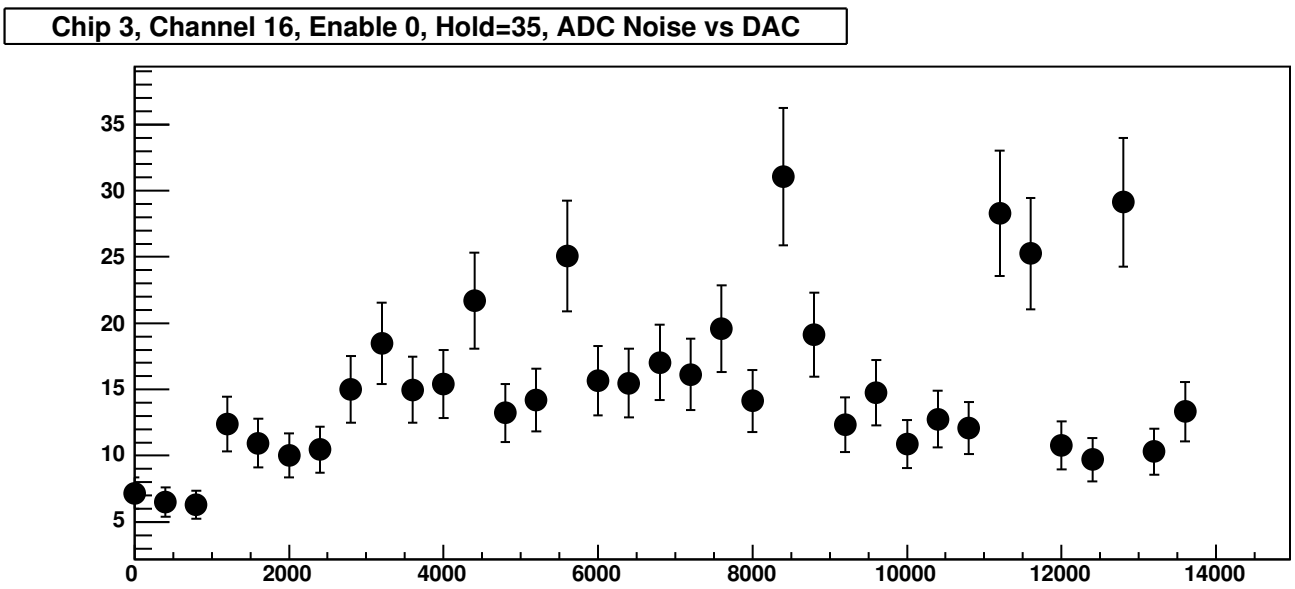

Chip 3, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

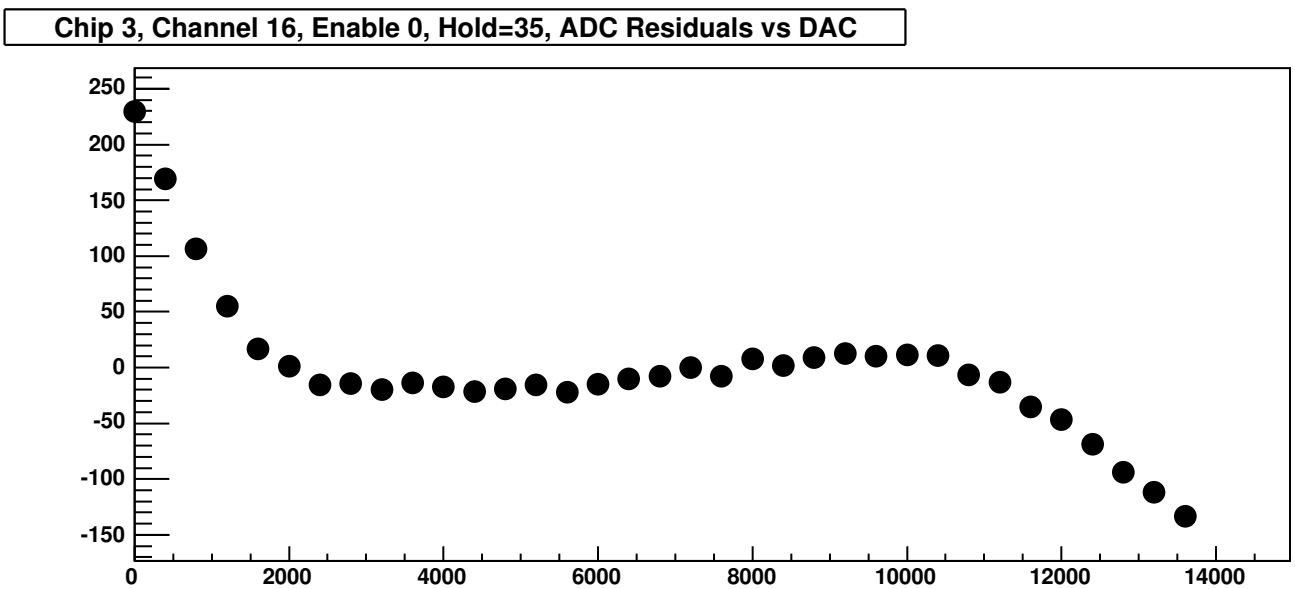

Chip 3, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1.299e+04 / 23
p0 2.954e+04 ± 10.92
p1 0.05004 ± 0.001414
30000
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
2000
4000
6000
8000
10000
12000
14000

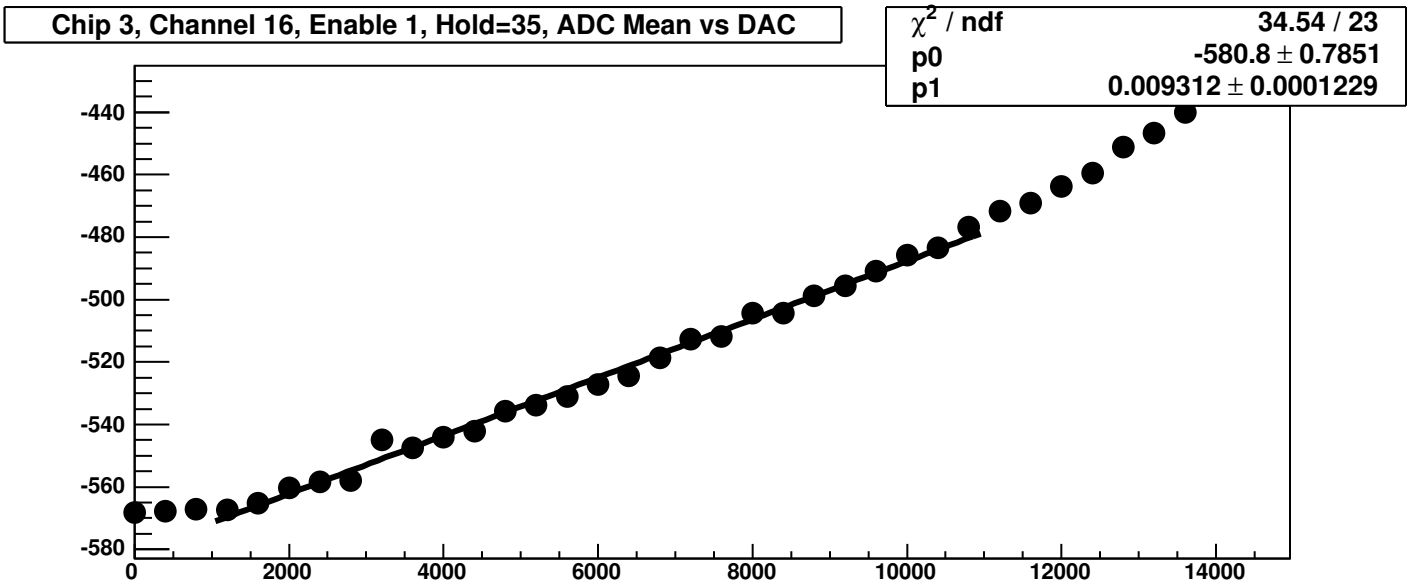

Chip 3, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

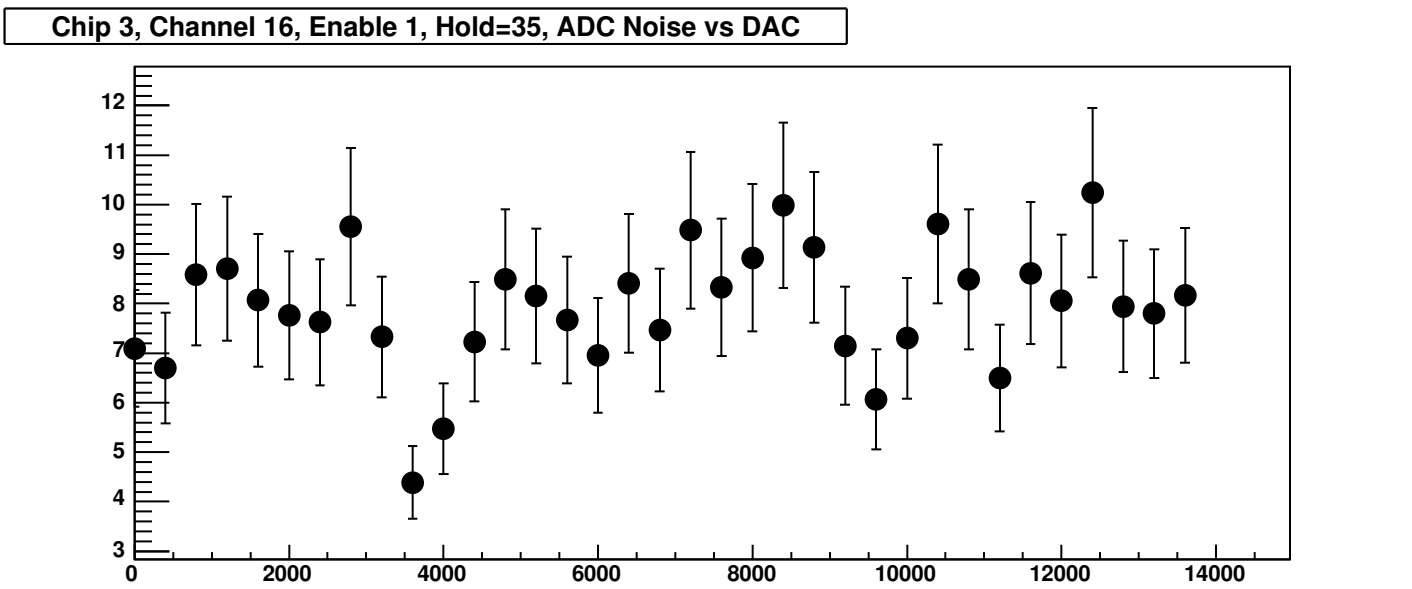

Chip 3, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
-18000
0
2000
4000
6000
8000
10000
12000
14000

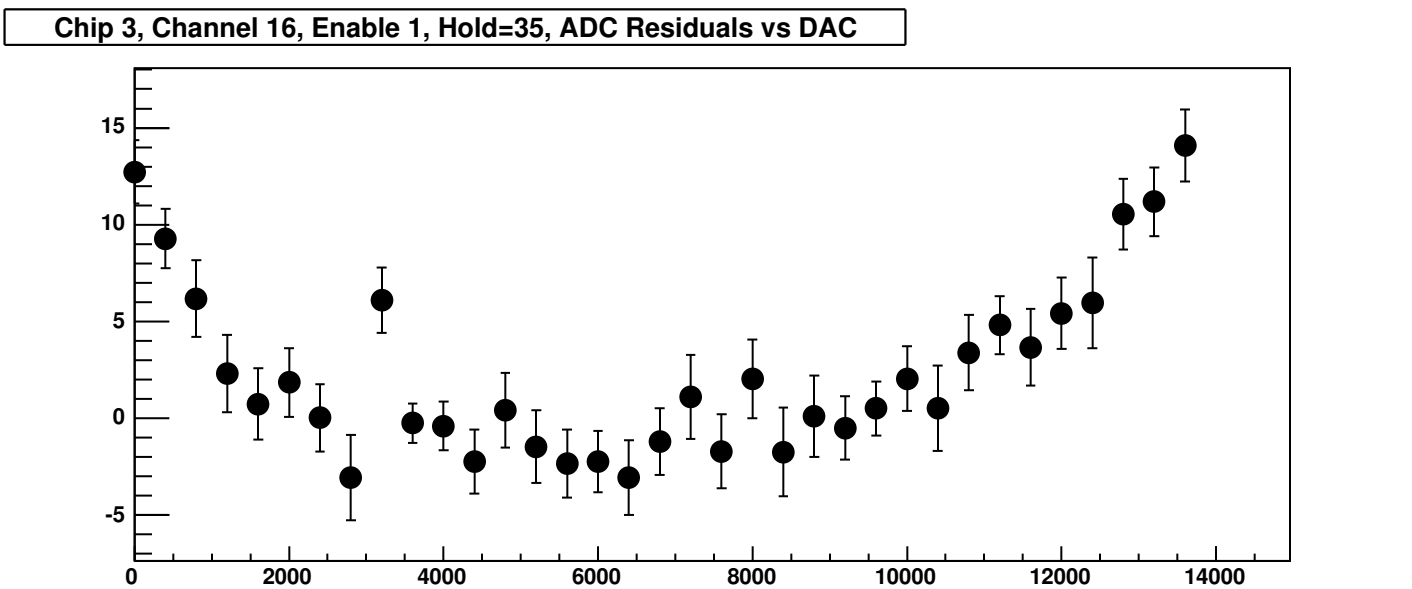

Chip 3, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 744.3 / 23
p0 -797.1 ± 1.321
p1 0.163 ± 0.0001998
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

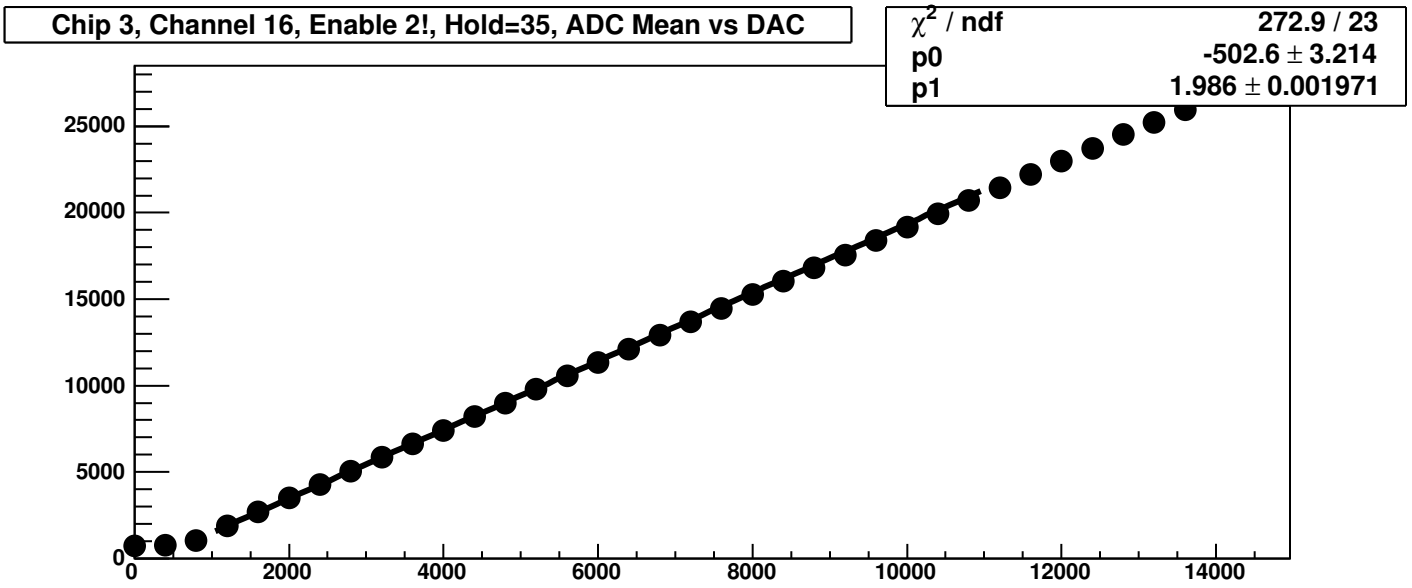

Chip 3, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

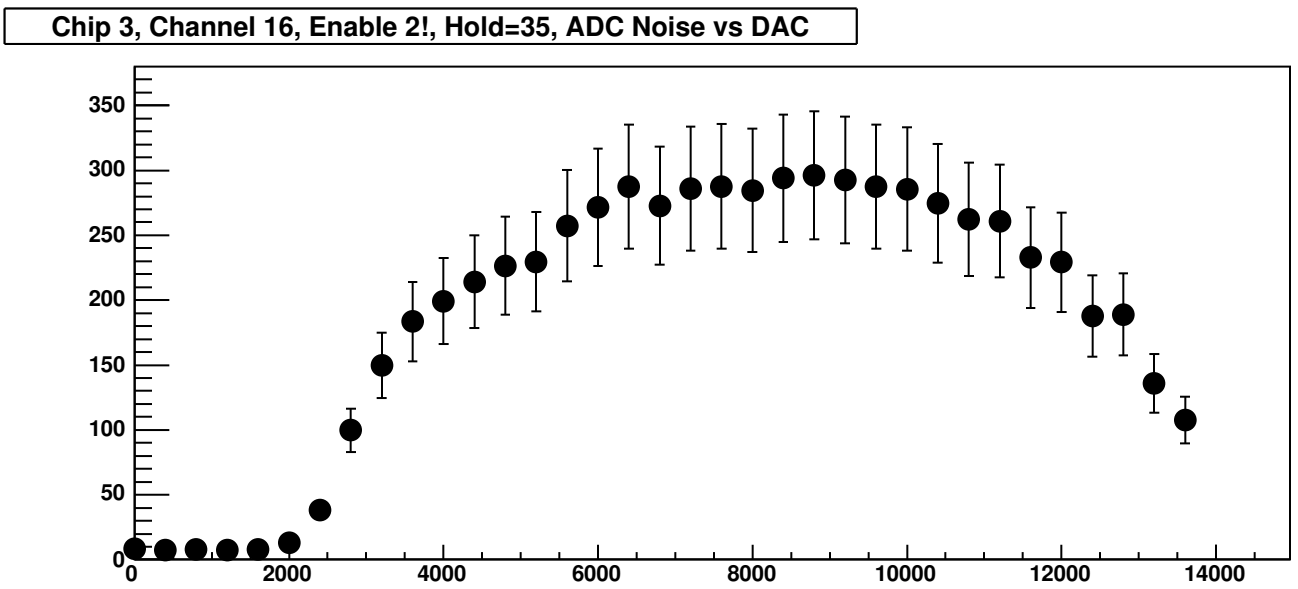

Chip 3, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

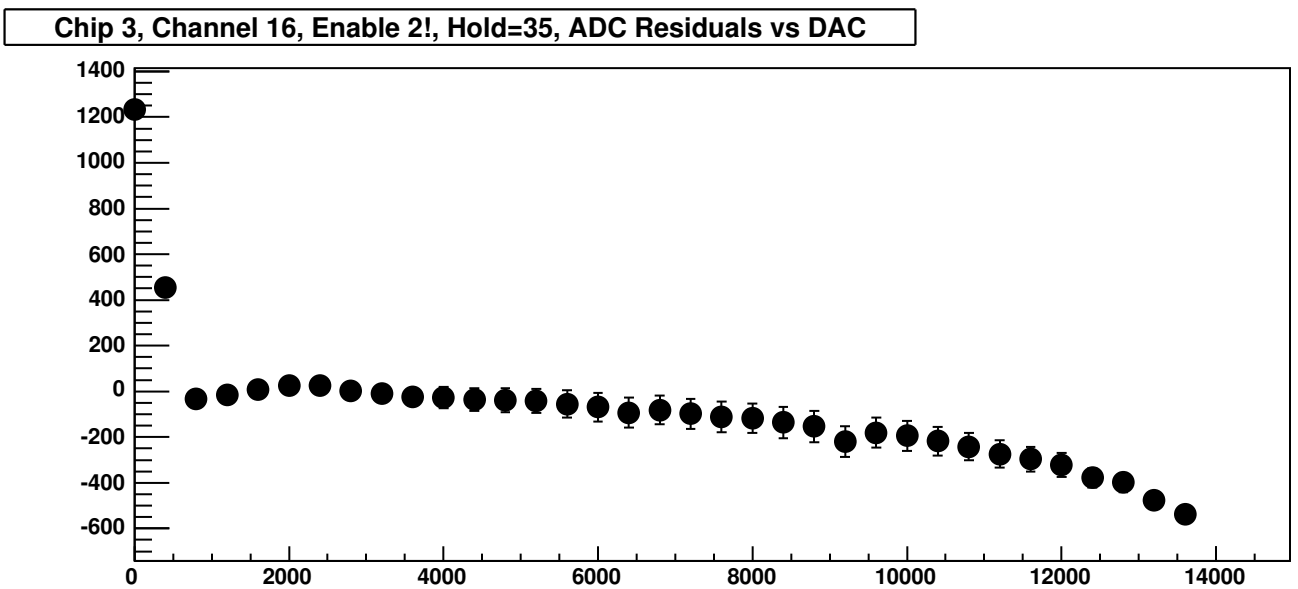

Chip 3, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 34.54 / 23
p0 -580.8 ± 0.7851
p1 0.009312 ± 0.0001229
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

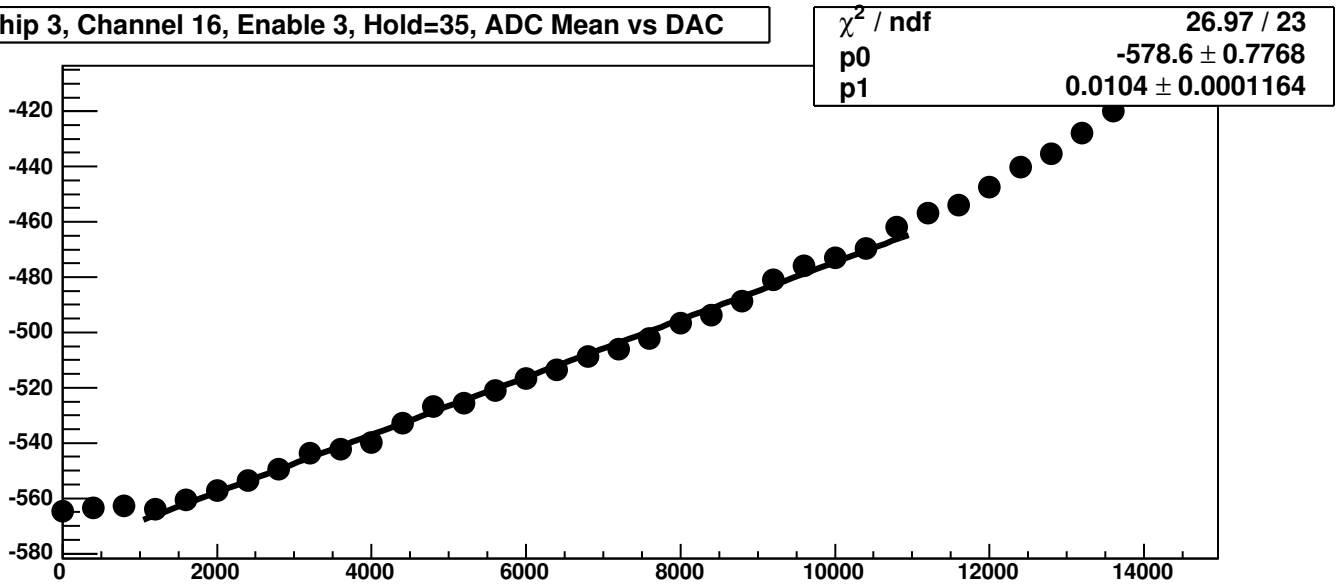

Chip 3, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 272.9 / 23
p0 -502.6 ± 3.214
p1 1.986 ± 0.001971
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

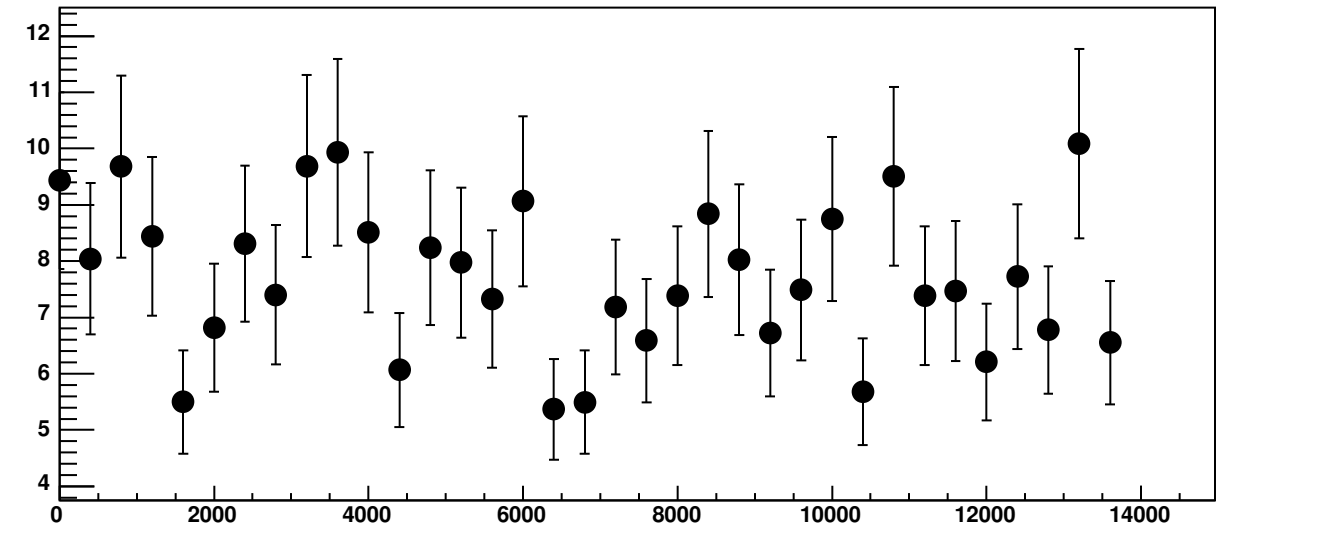

Chip 3, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

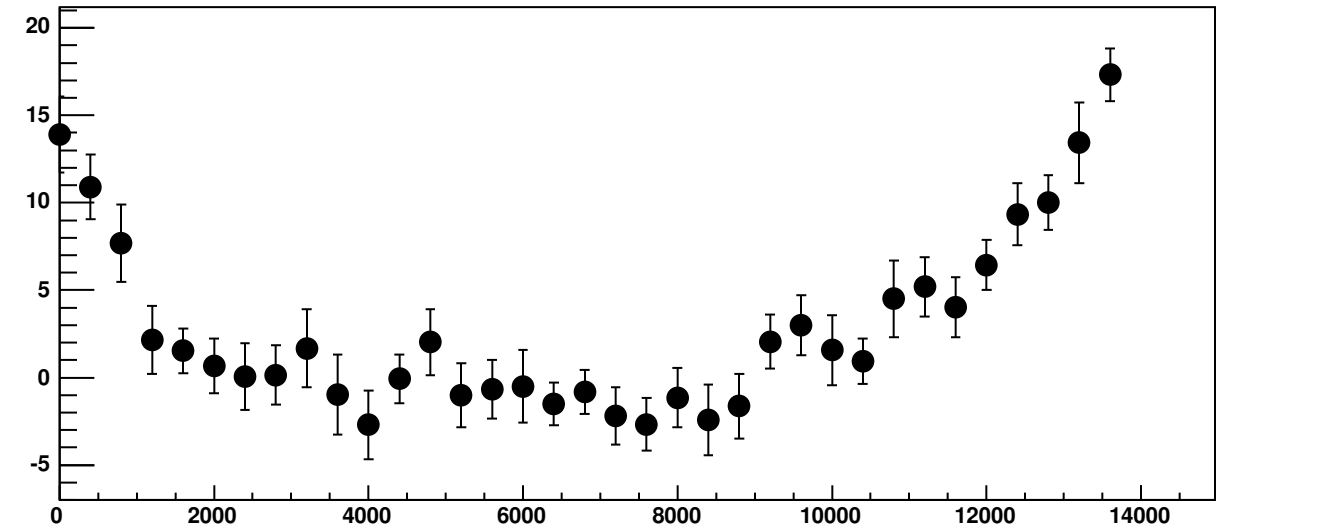
Chip 3, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

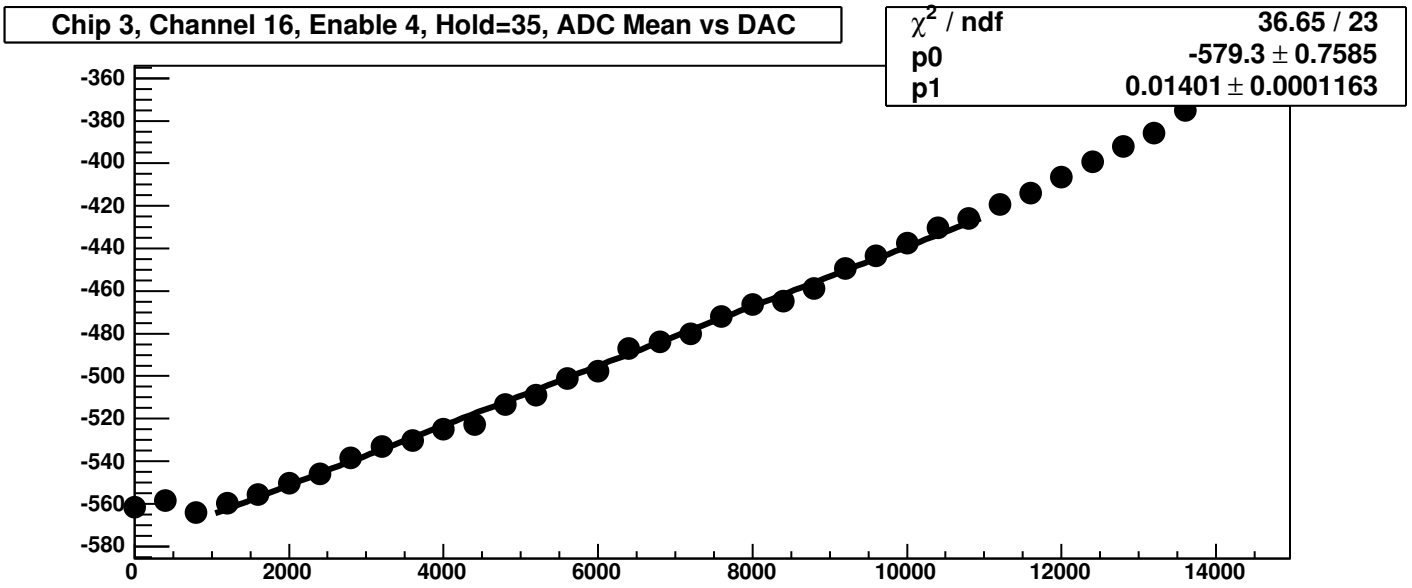


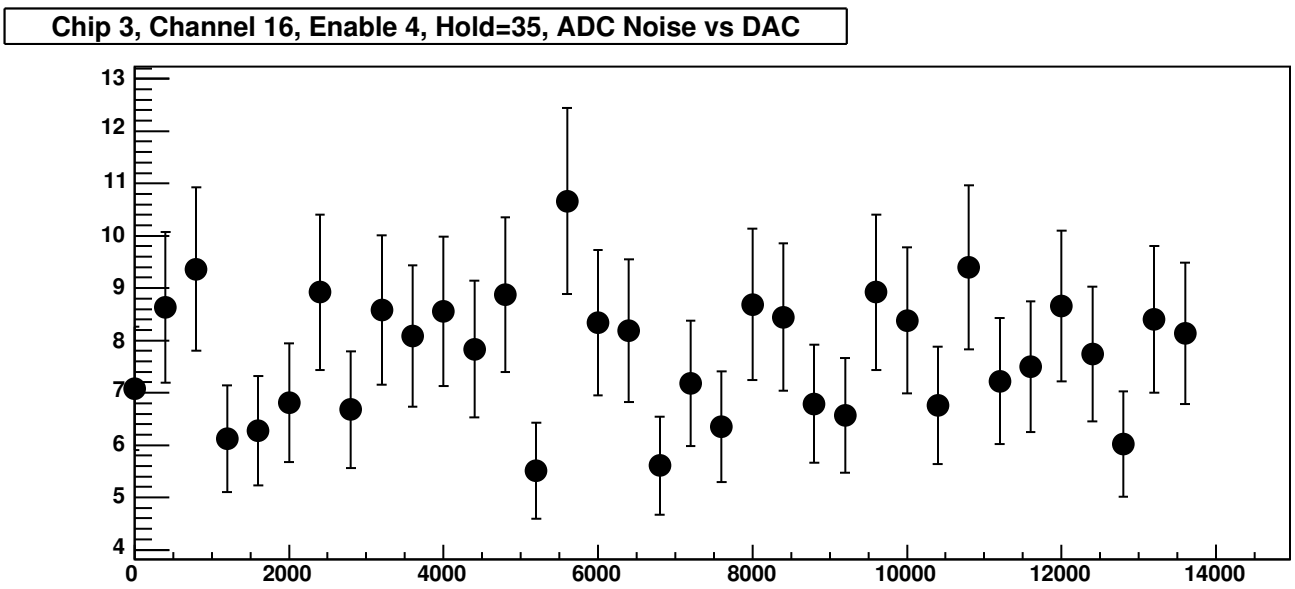
Chip 3, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC

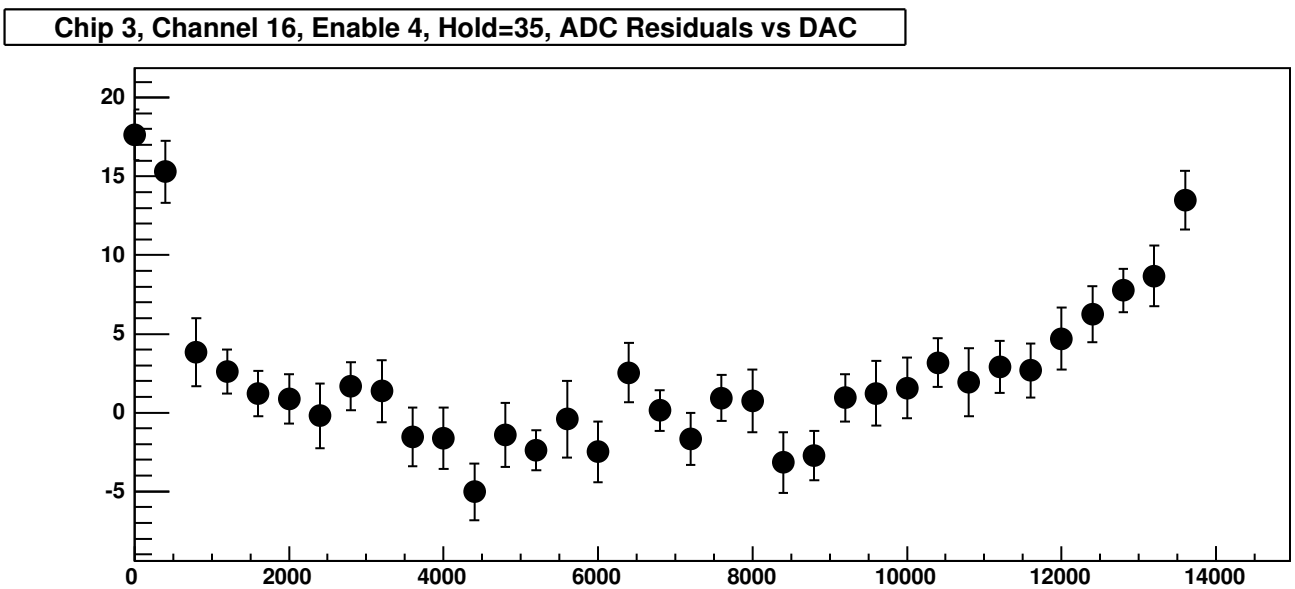


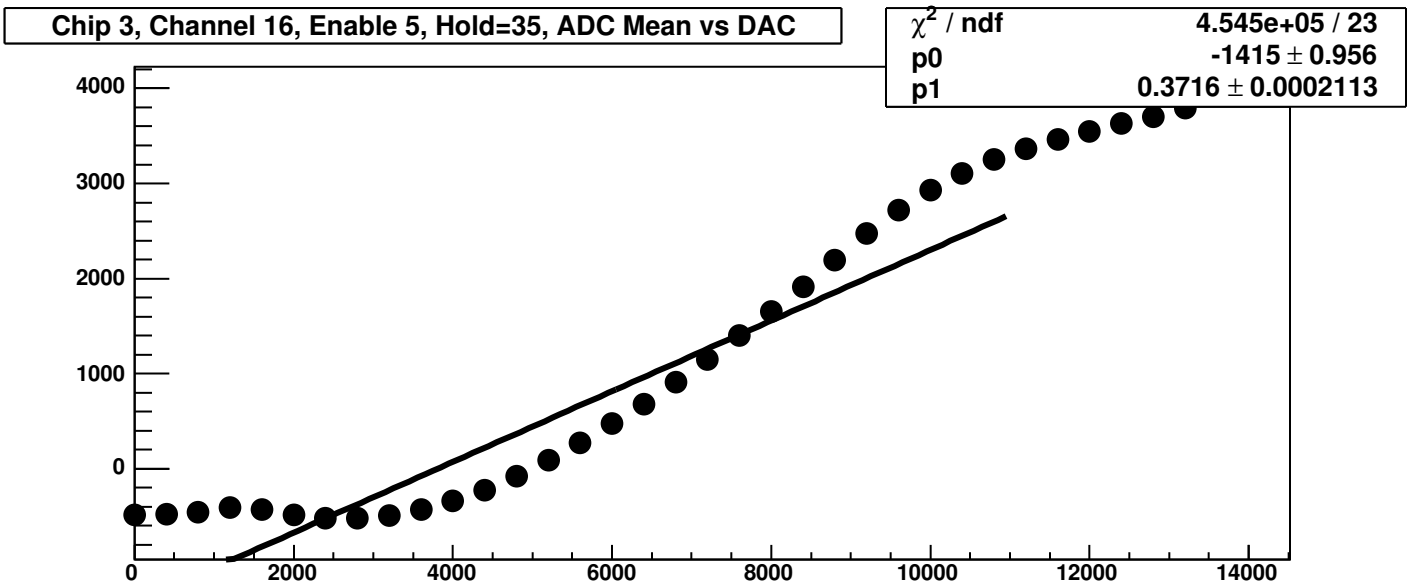
Chip 3, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

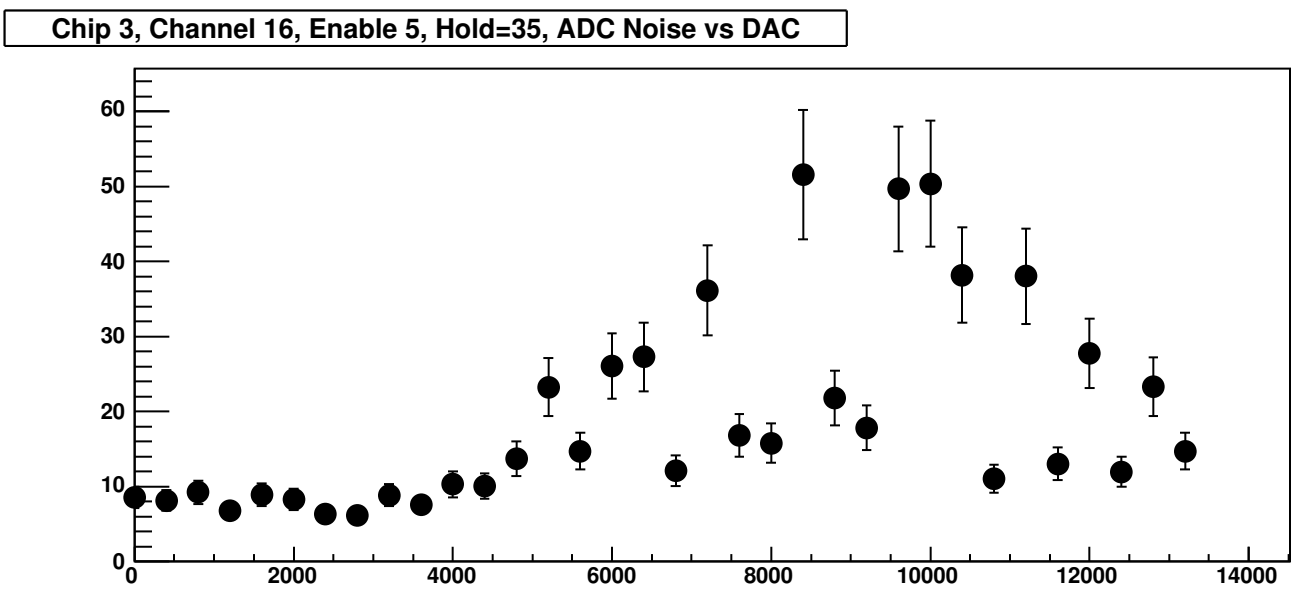


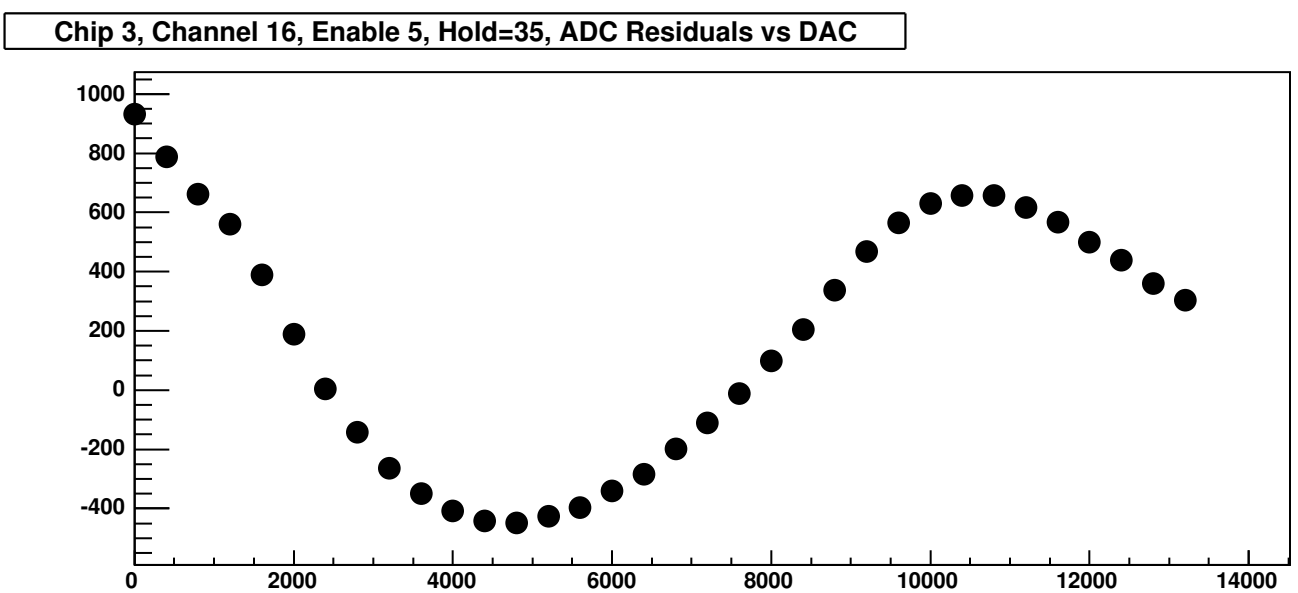

Chip 3, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 36.65 / 23
p0 -579.3 ± 0.7585
p1 0.01401 ± 0.0001163
-360
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000

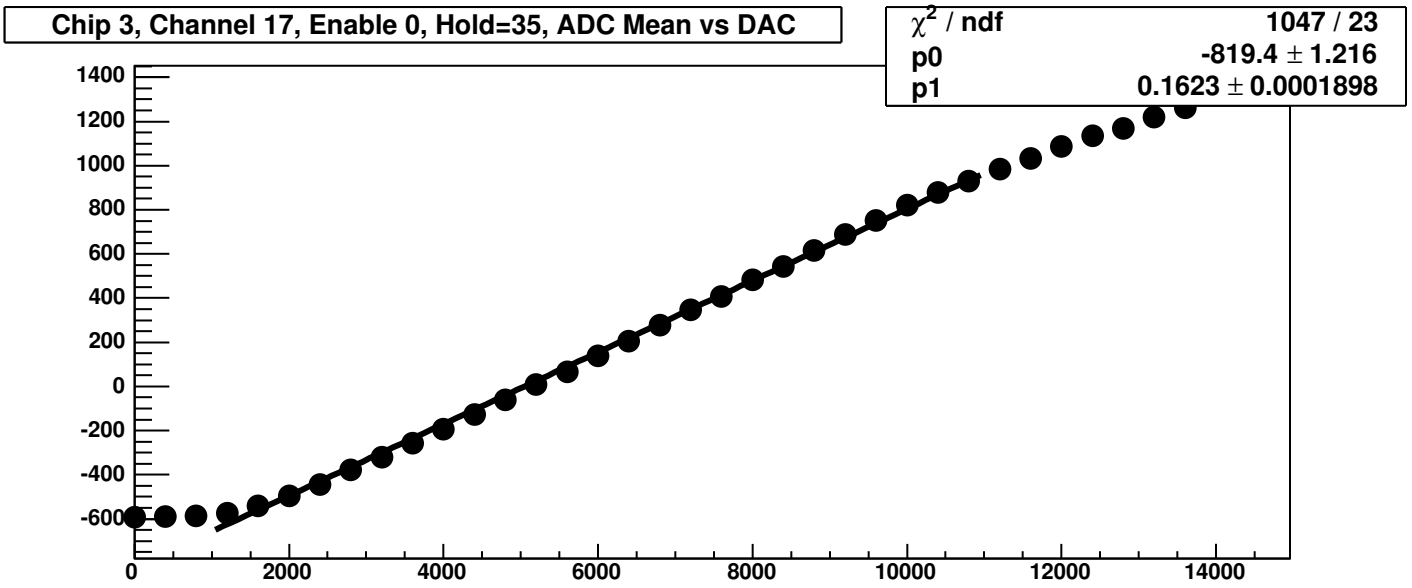

Chip 3, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

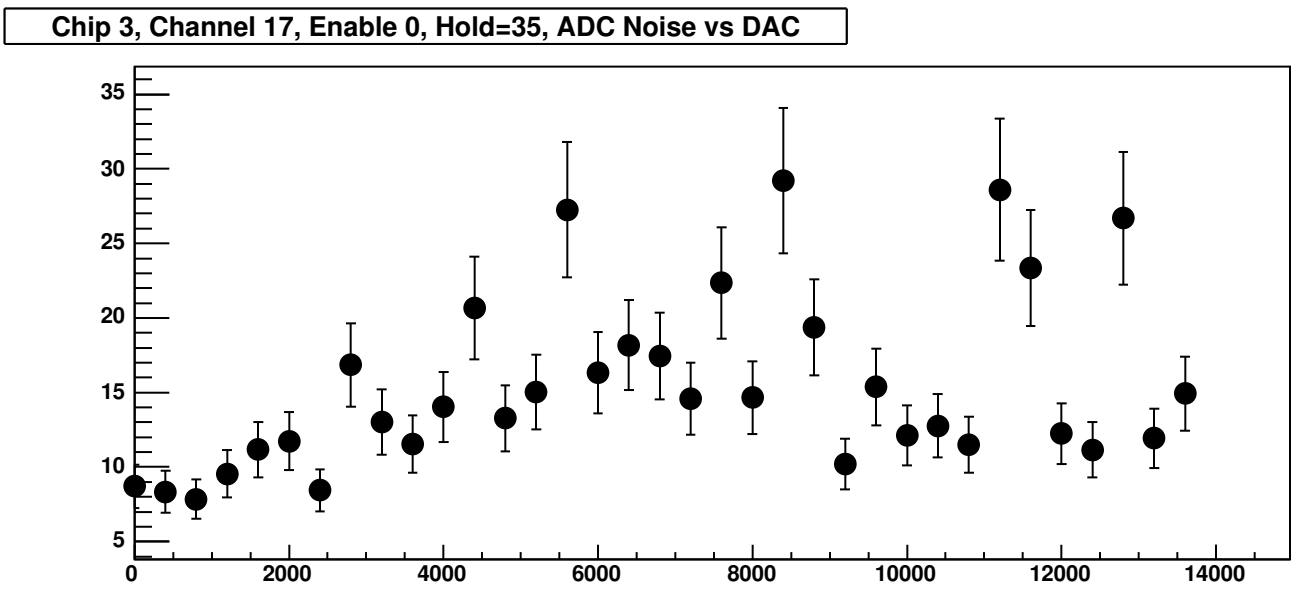

Chip 3, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

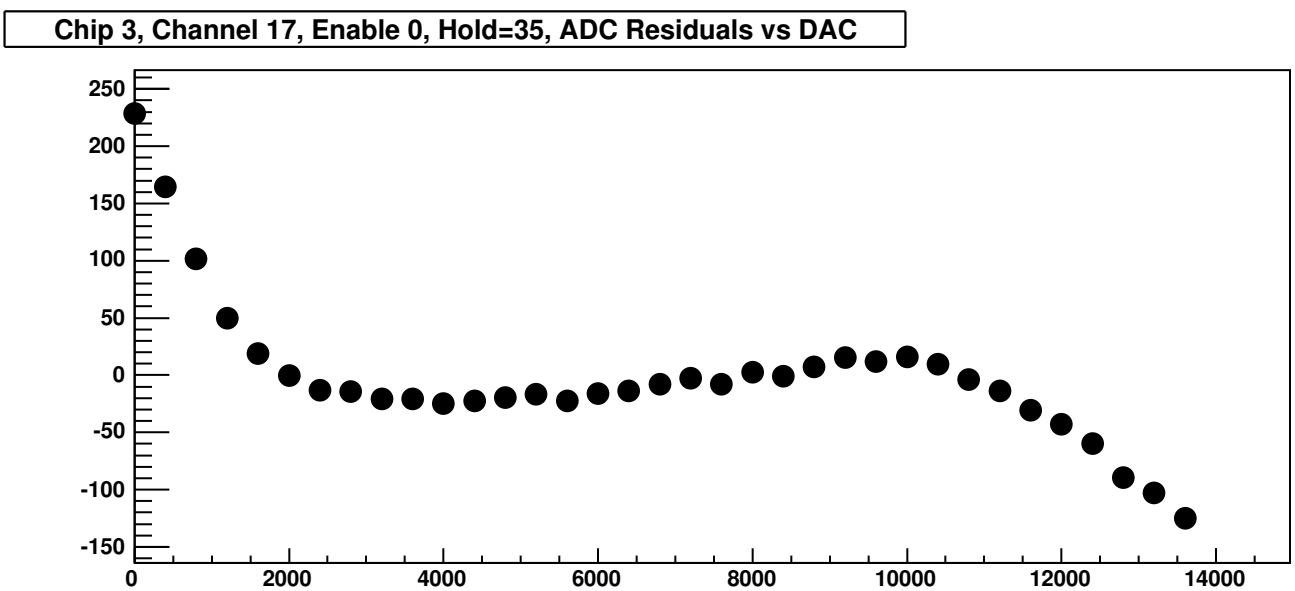

Chip 3, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4.545e+05 / 23
p0 -1415 ± 0.956
p1 0.3716 ± 0.0002113
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

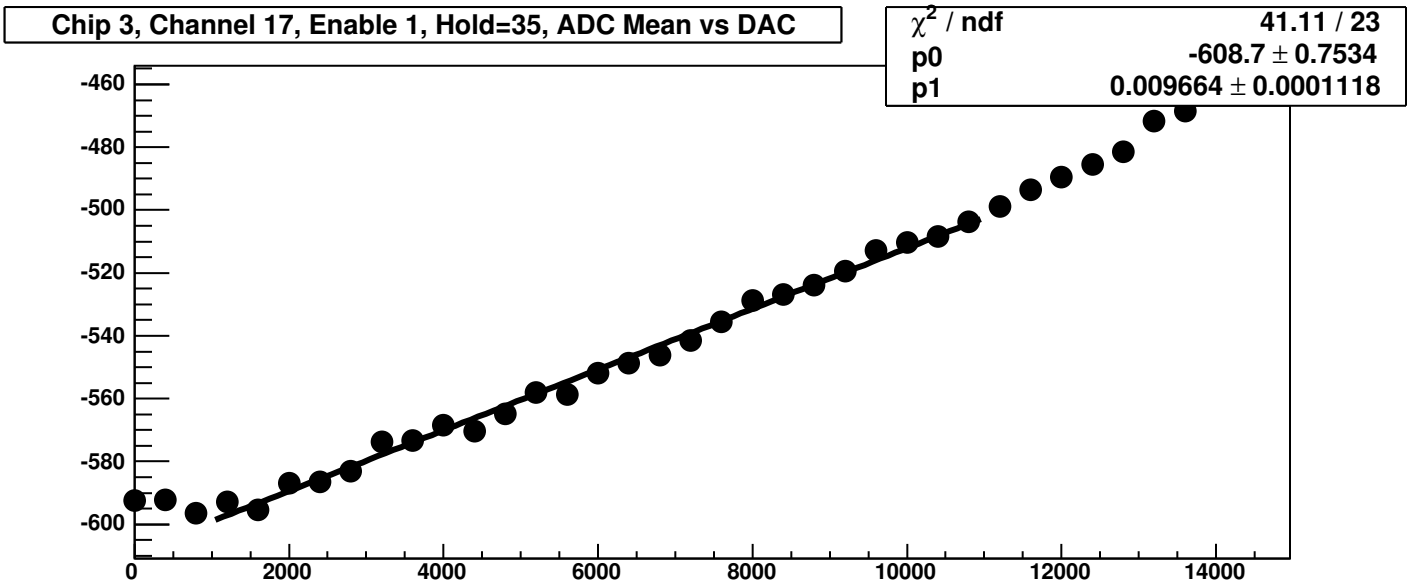

Chip 3, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

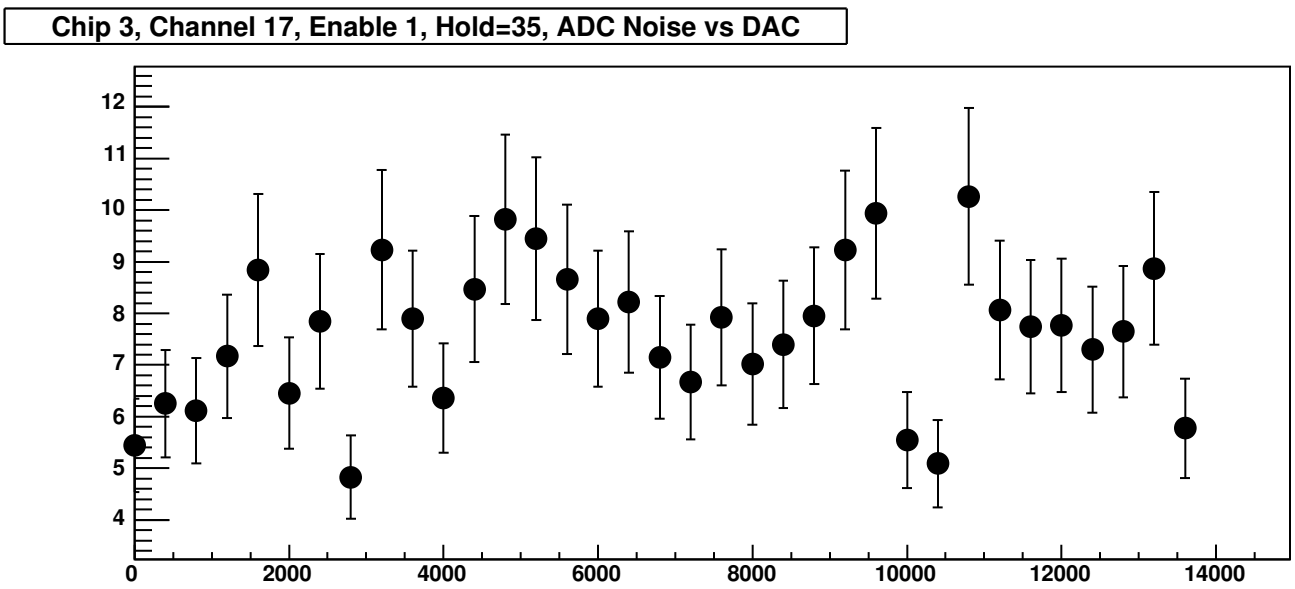

Chip 3, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

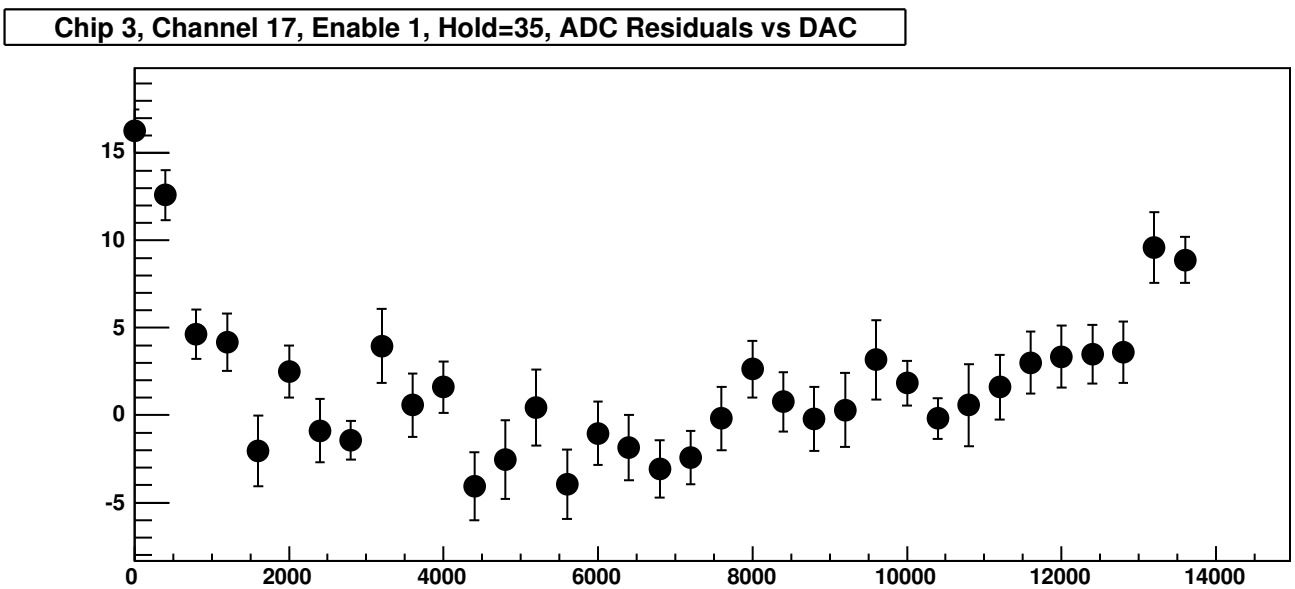

Chip 3, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1047 / 23
p0 -819.4 ± 1.216
p1 0.1623 ± 0.0001898
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

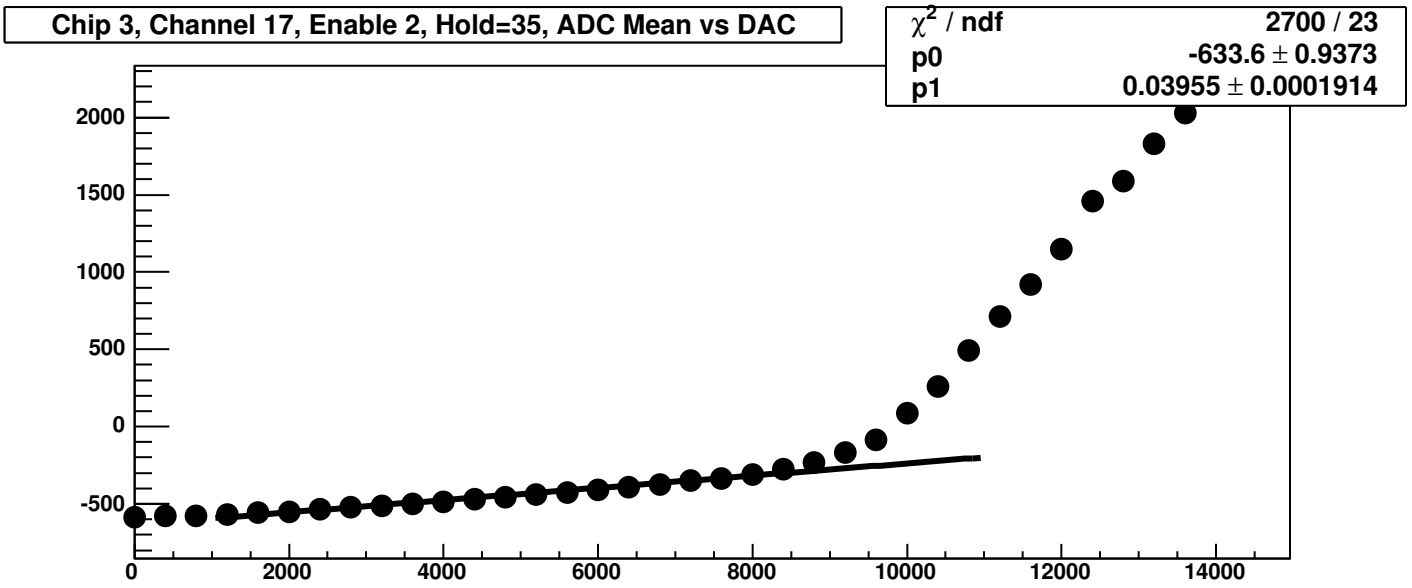

Chip 3, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

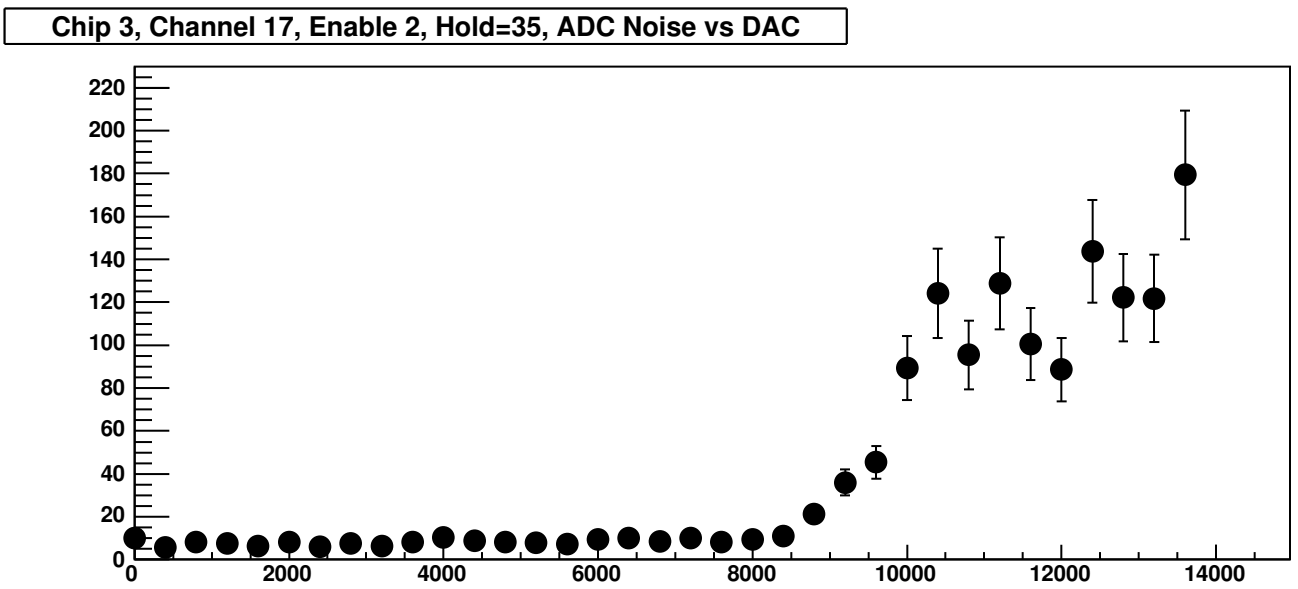

Chip 3, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

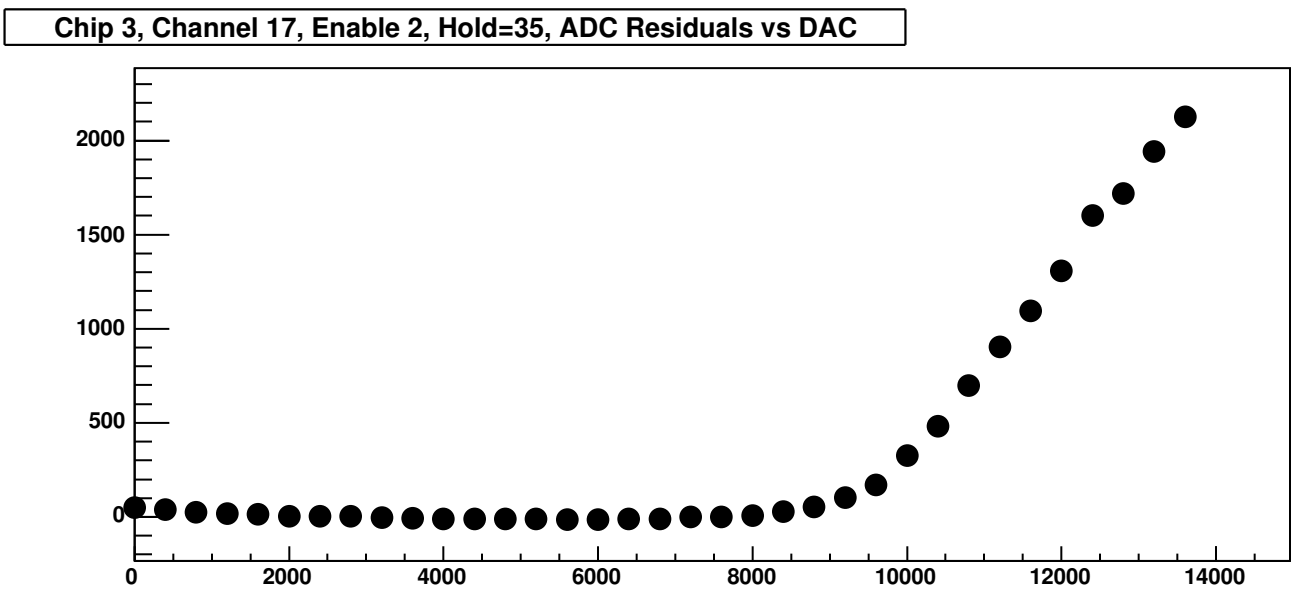

Chip 3, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 41.11 / 23
p0 -608.7 ± 0.7534
p1 0.009664 ± 0.0001118
-460
-480
-500
-520
-540
-560
-580
-600
0
2000
4000
6000
8000
10000
12000
14000

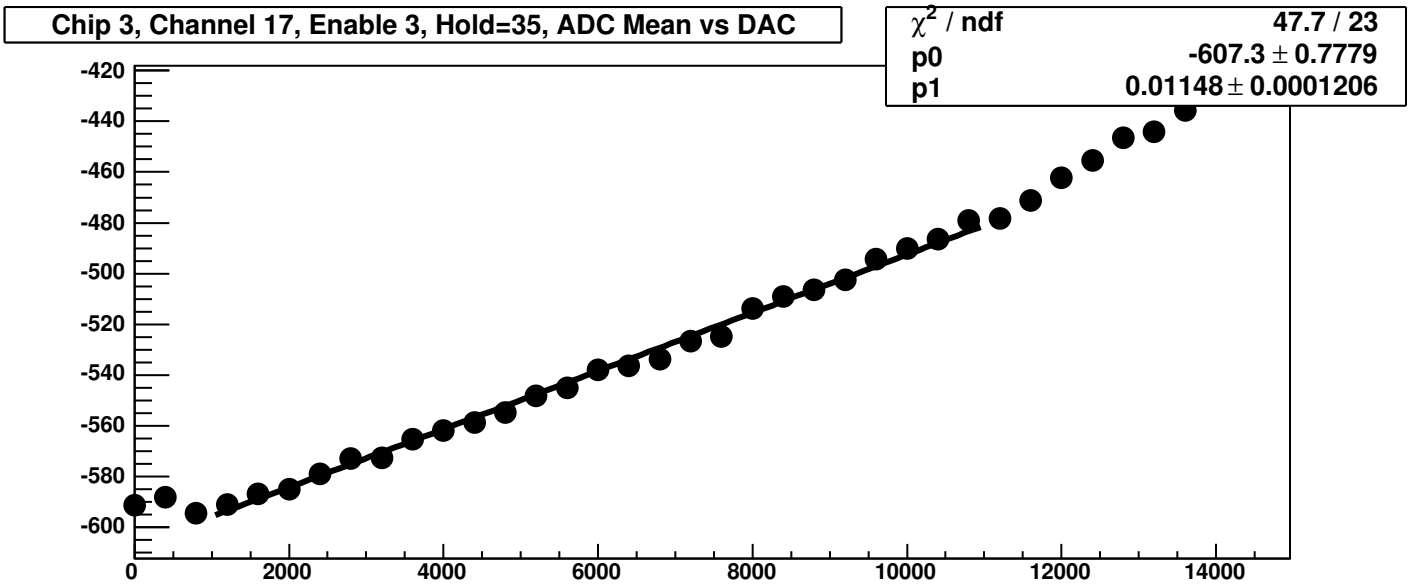

Chip 3, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

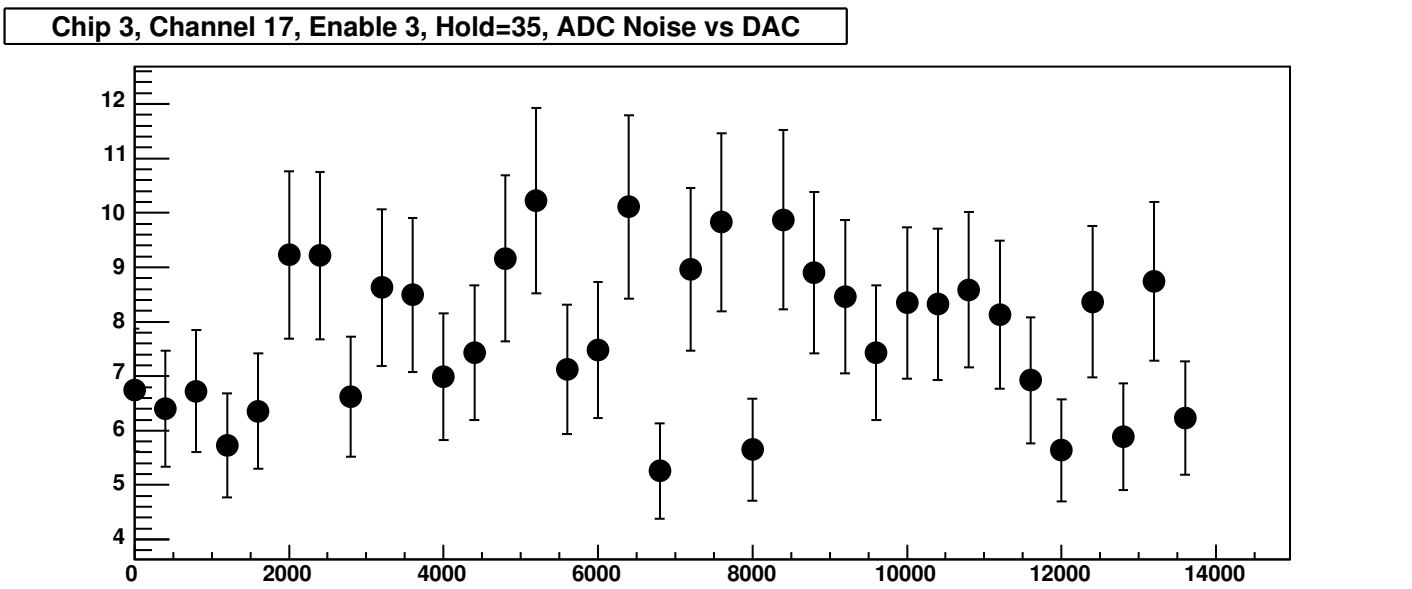

Chip 3, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

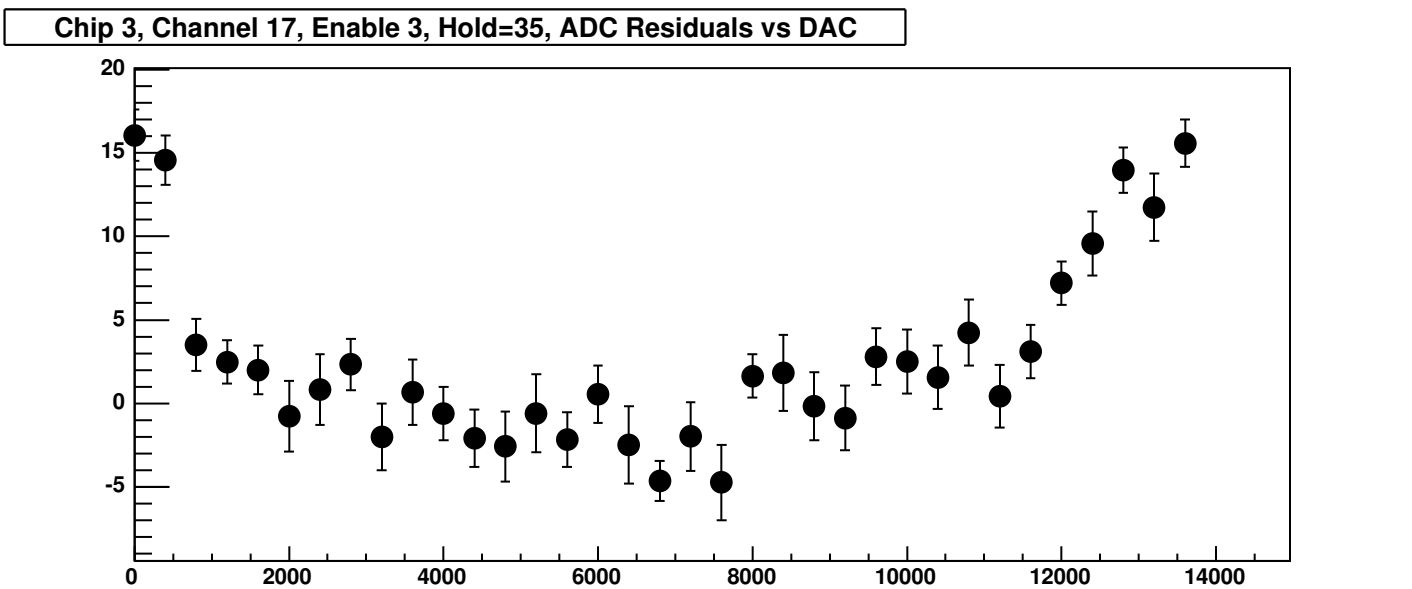

Chip 3, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 2700 / 23
p0 -633.6 ± 0.9373
p1 0.03955 ± 0.0001914
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

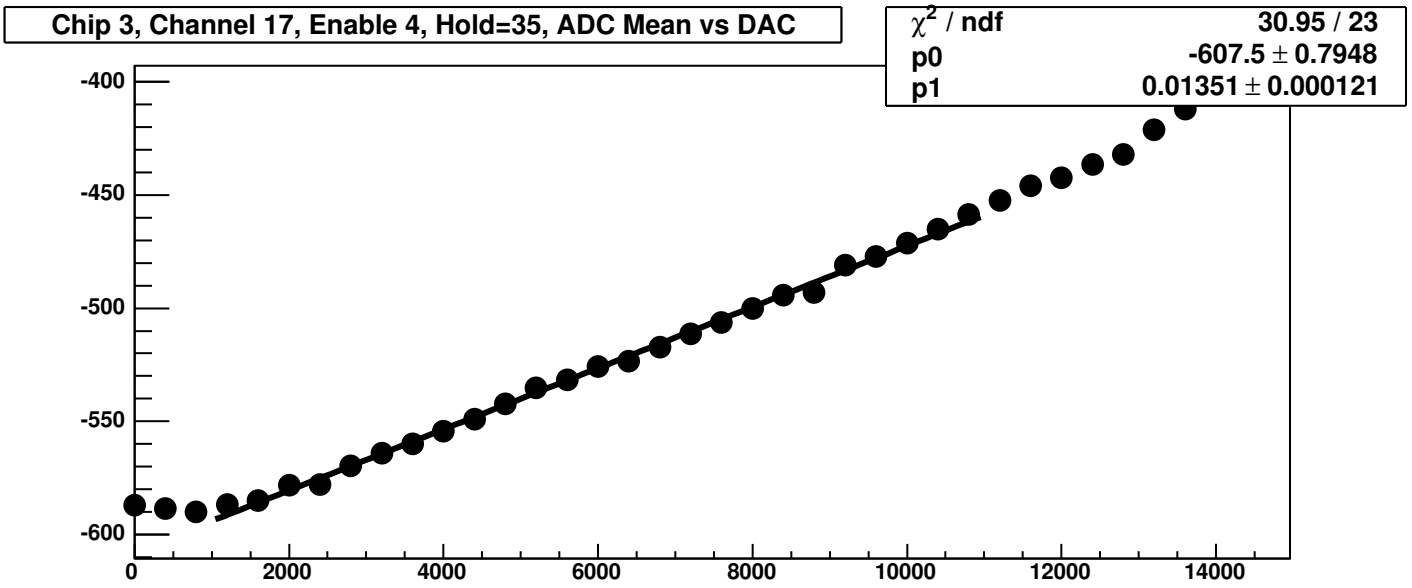

Chip 3, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

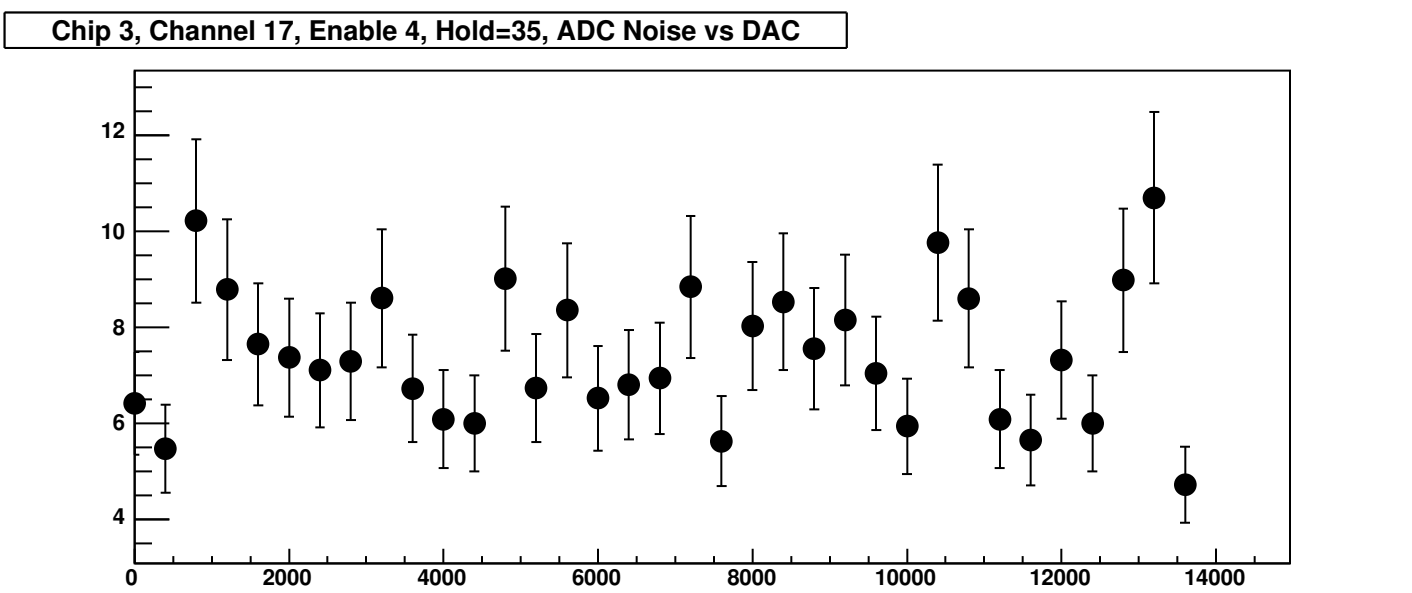

Chip 3, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

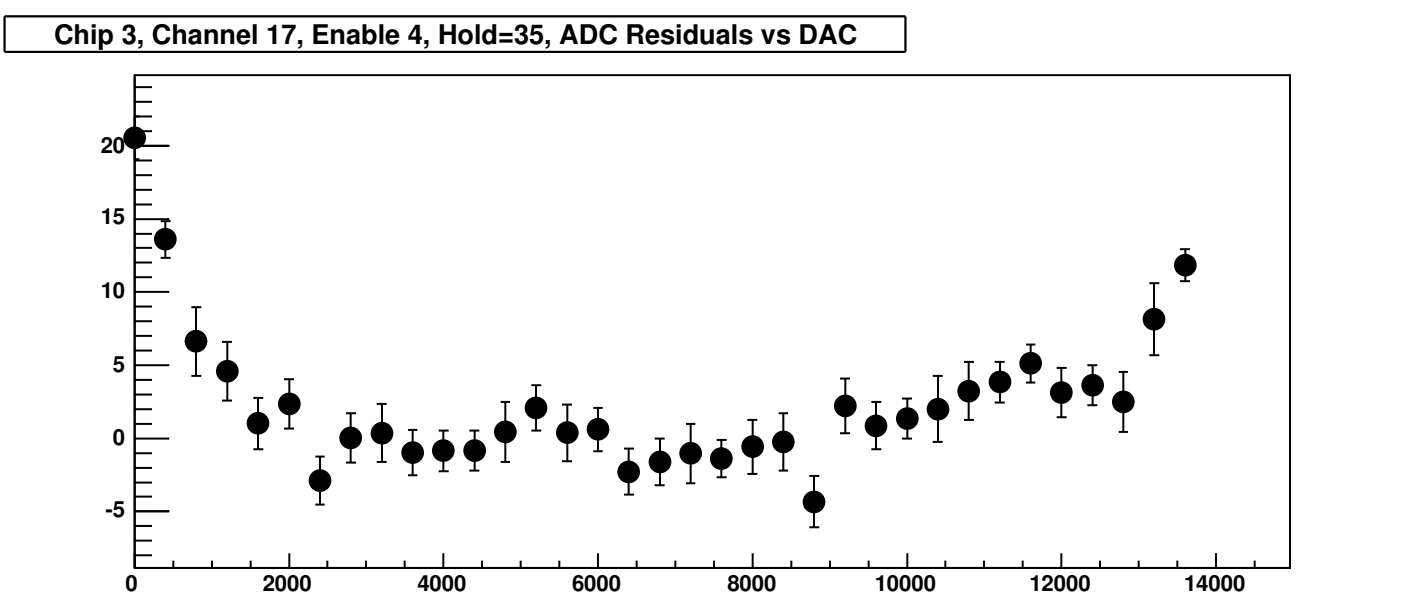

Chip 3, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 47.7 / 23
p0 -607.3 ± 0.7779
p1 0.01148 ± 0.0001206
-420
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
2000
4000
6000
8000
10000
12000
14000

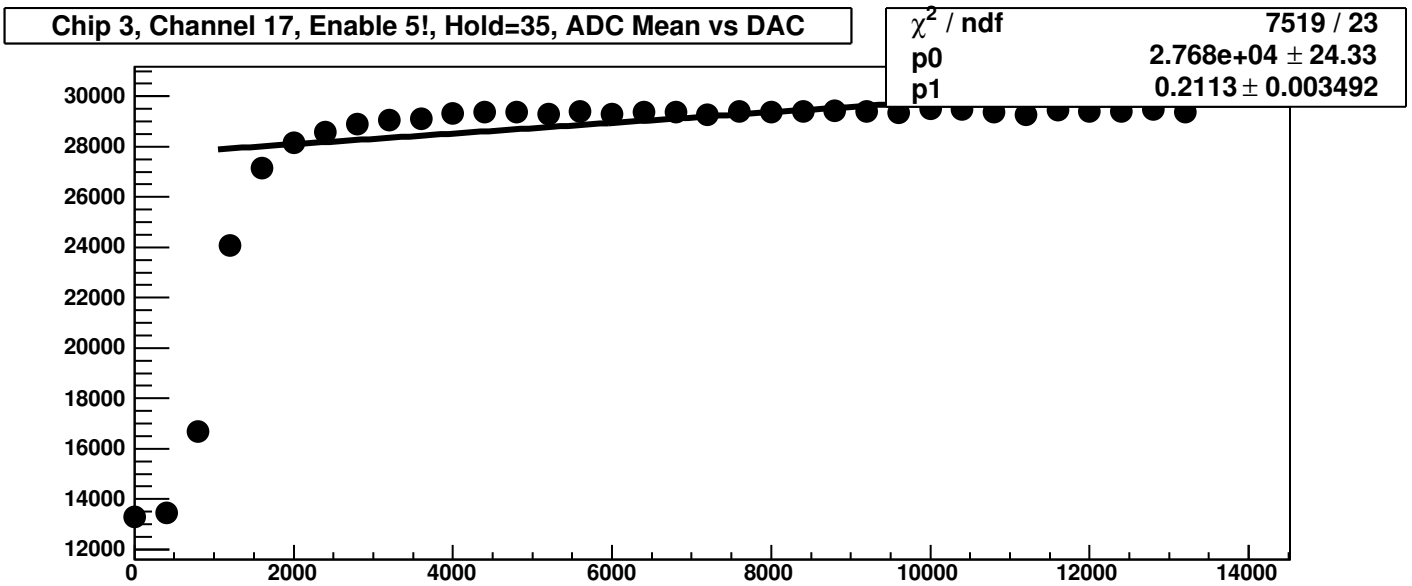

Chip 3, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

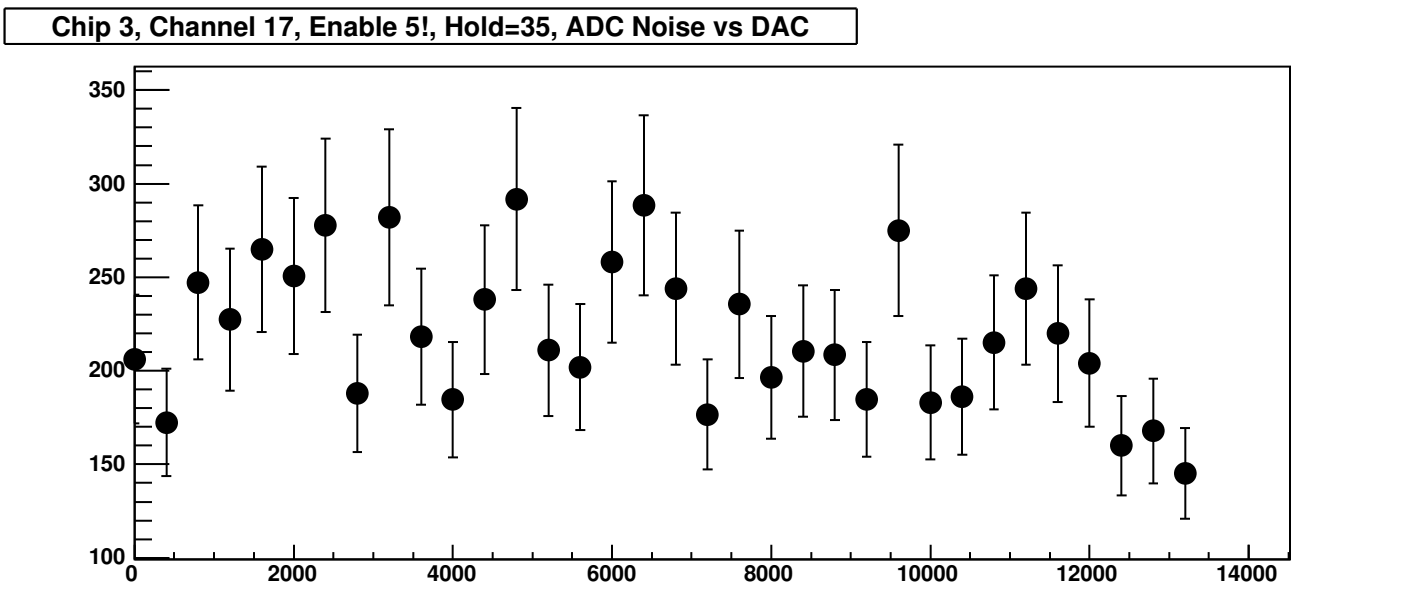

Chip 3, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

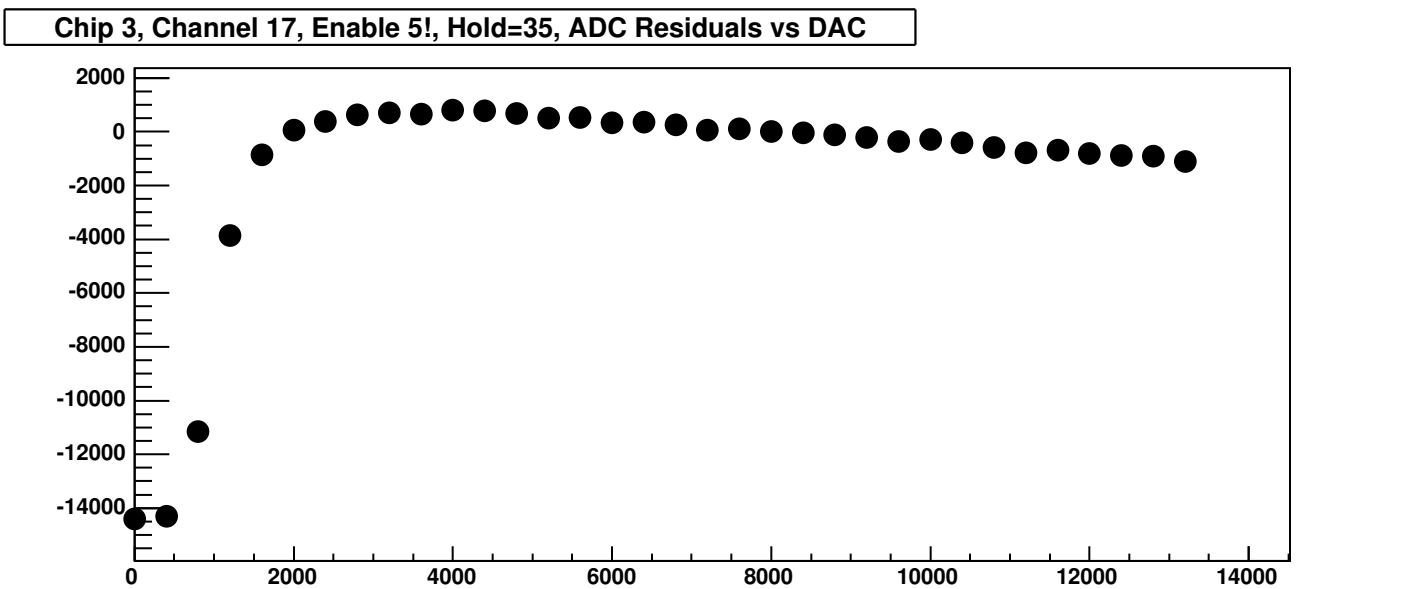

Chip 3, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.95 / 23
p0 -607.5 ± 0.7948
p1 0.01351 ± 0.000121
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

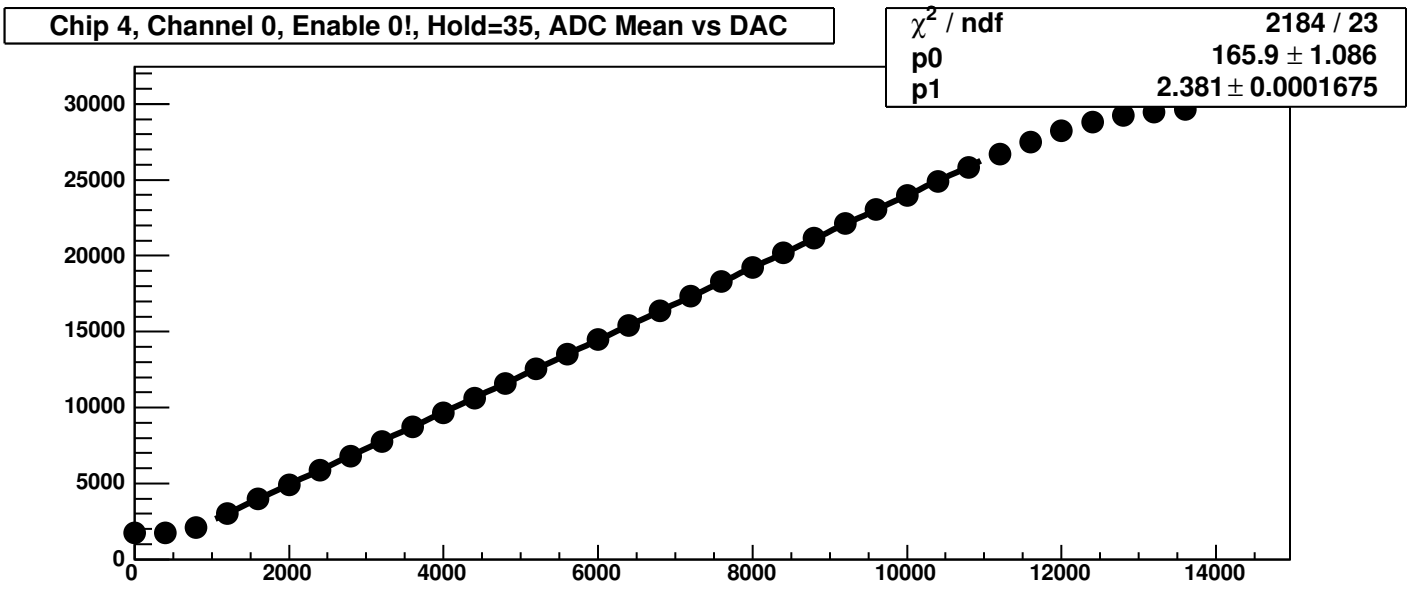

Chip 3, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

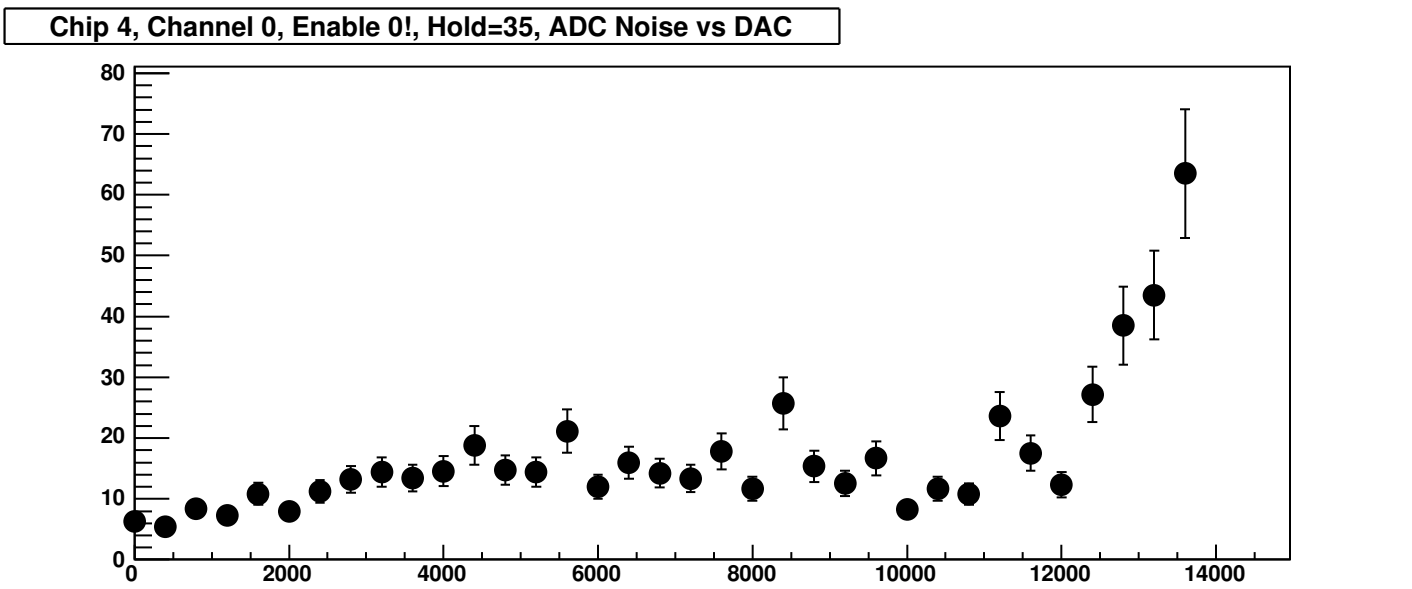

Chip 3, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

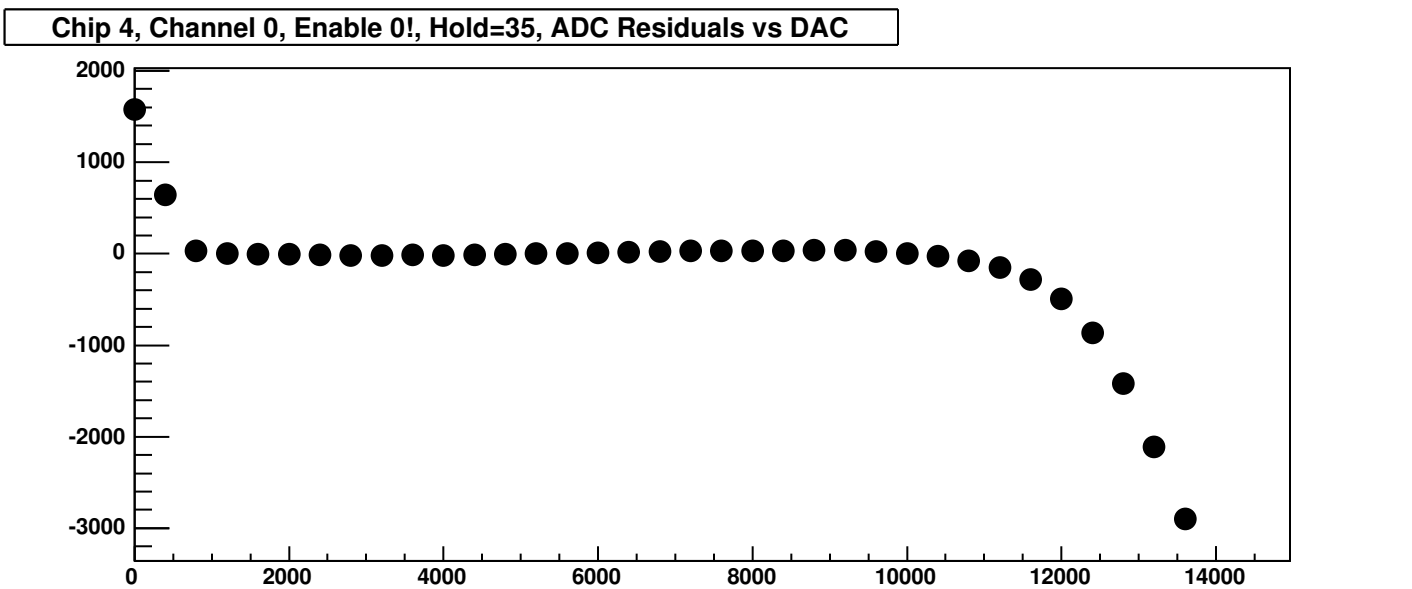

Chip 3, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 7519 / 23
p0 2.768e+04 ± 24.33
p1 0.2113 ± 0.003492
30000
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
2000
4000
6000
8000
10000
12000
14000

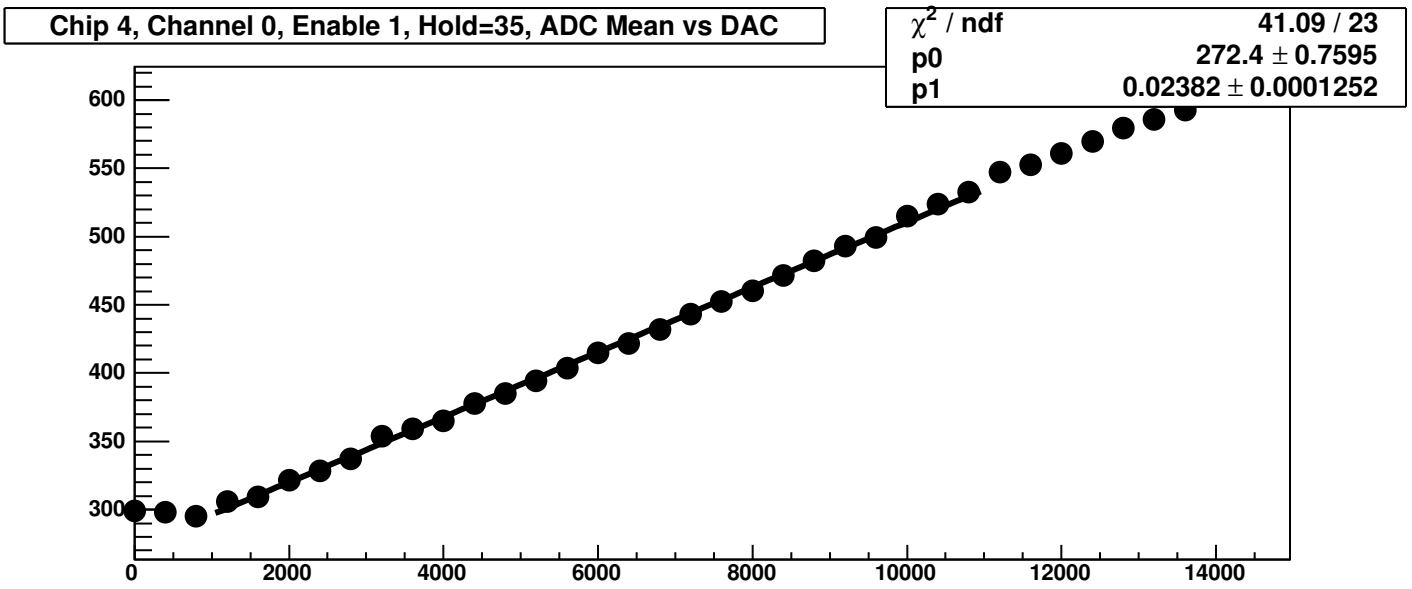

Chip 3, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

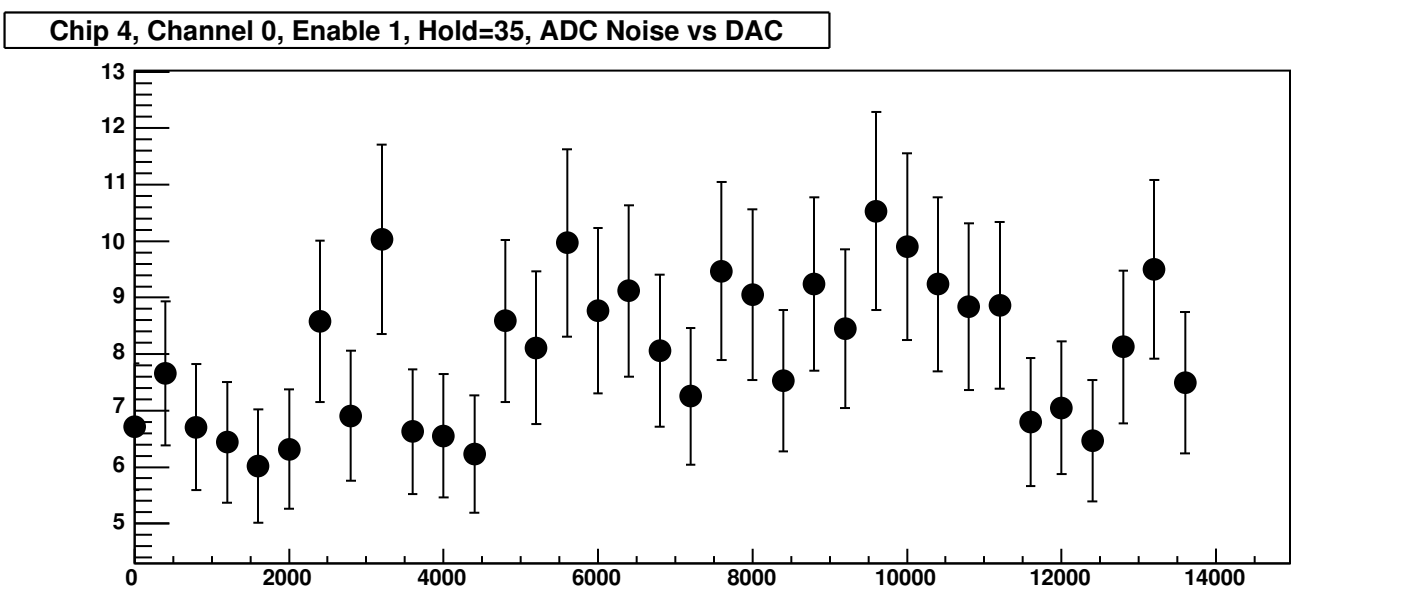

Chip 3, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

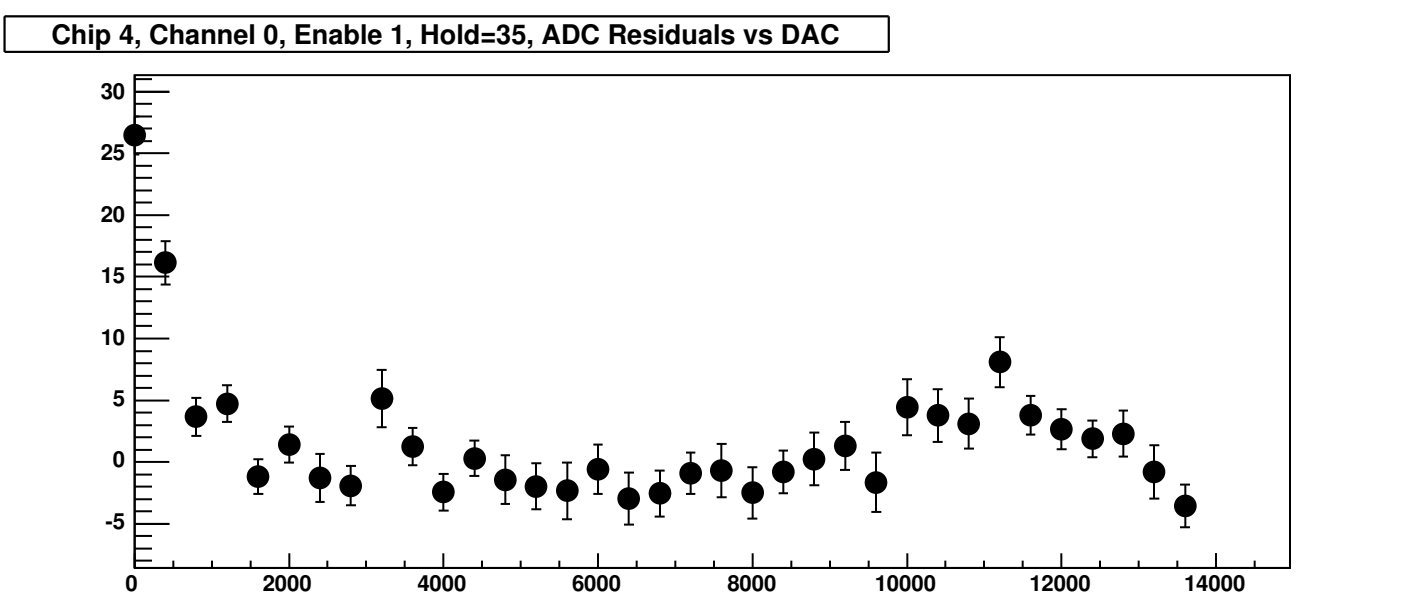

Chip 4, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 2184 / 23
p0 165.9 ± 1.086
p1 2.381 ± 0.0001675
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

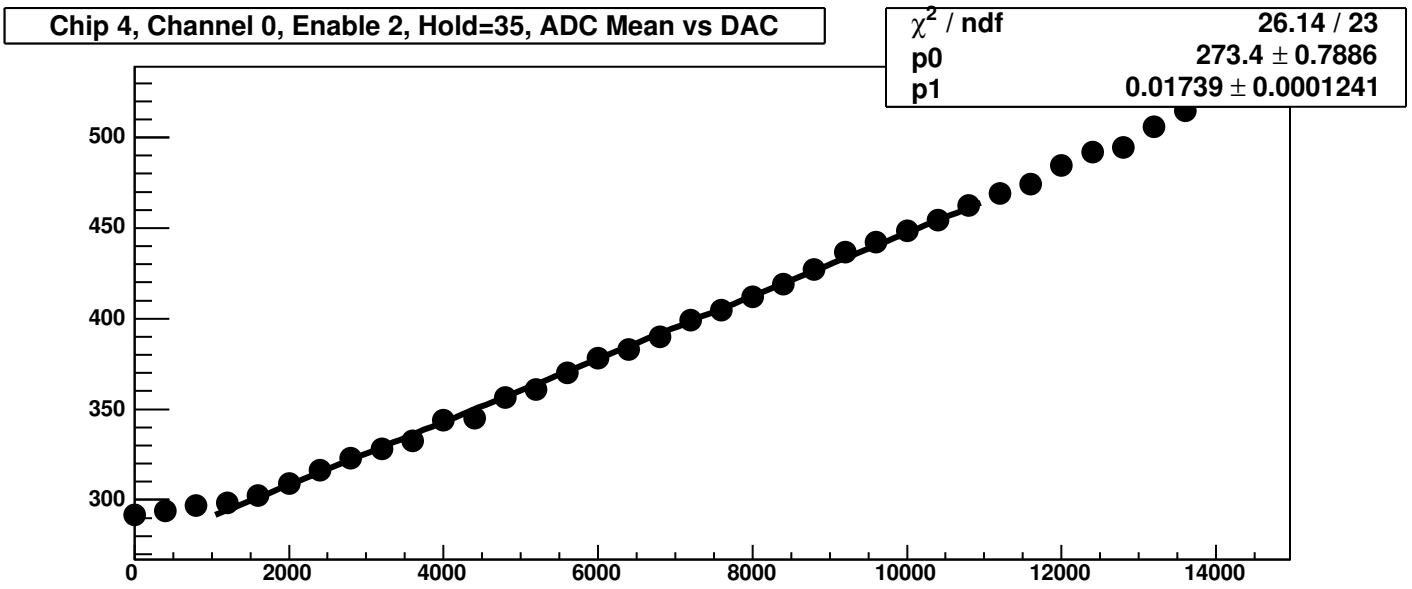

Chip 4, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

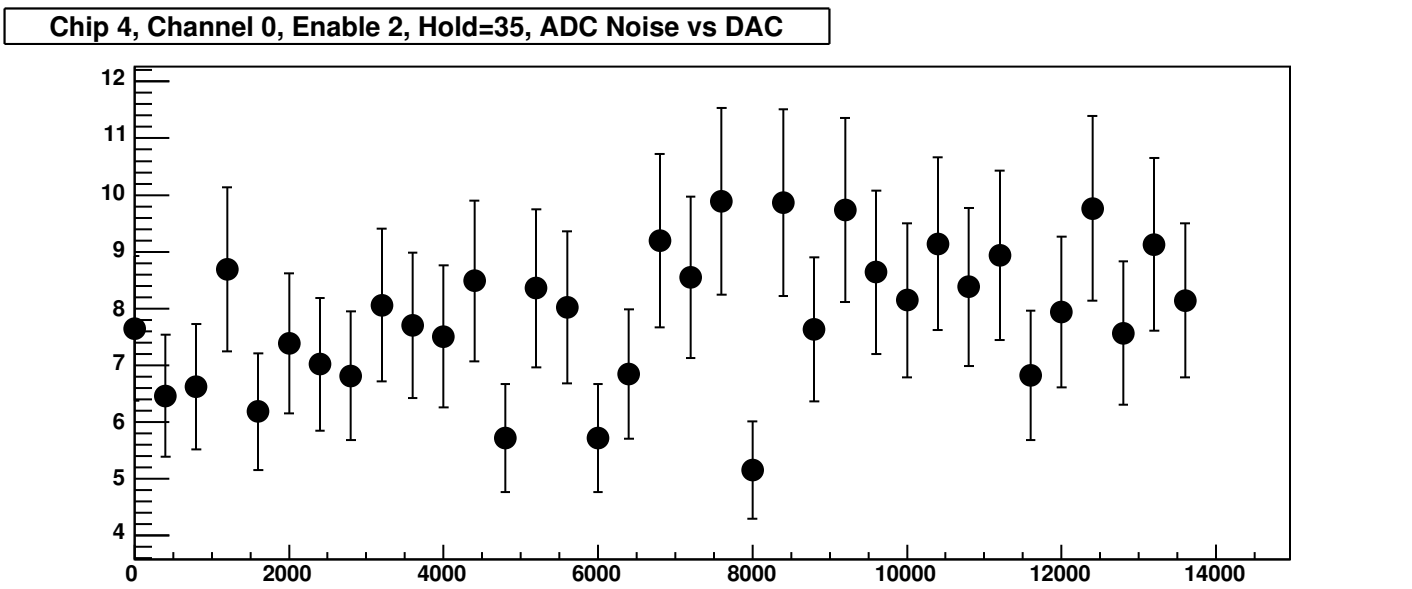

Chip 4, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

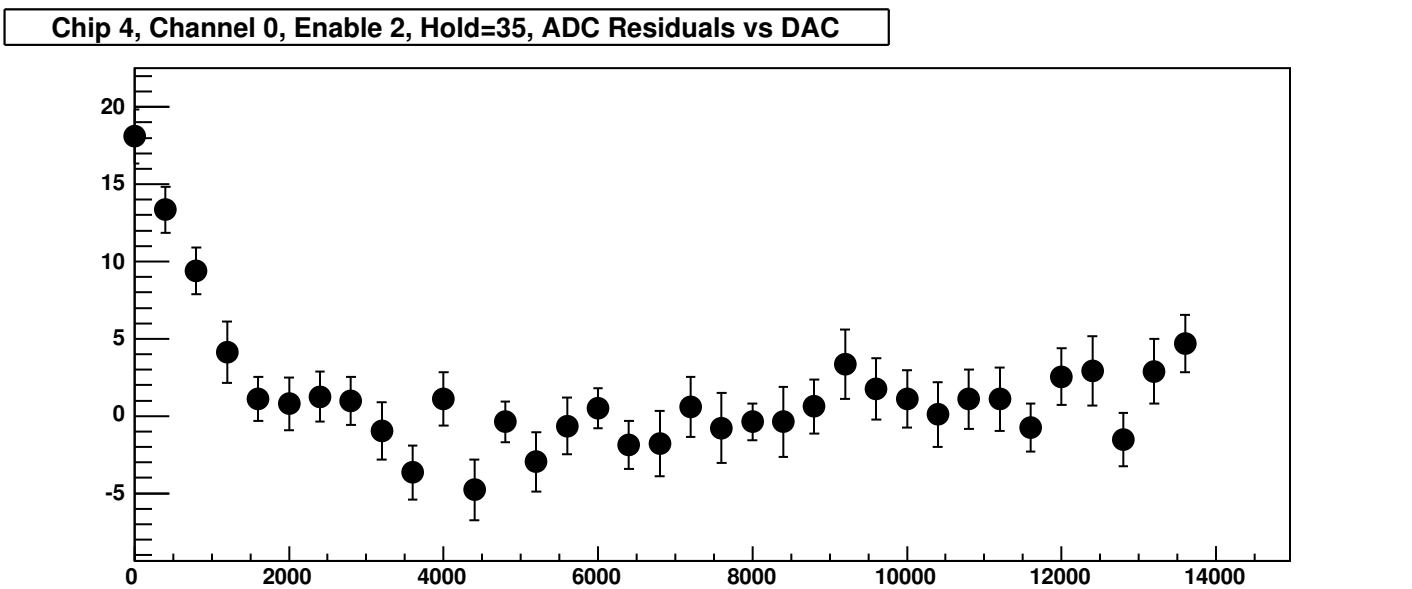

Chip 4, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 41.09 / 23
p0 272.4 ± 0.7595
p1 0.02382 ± 0.0001252
600
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

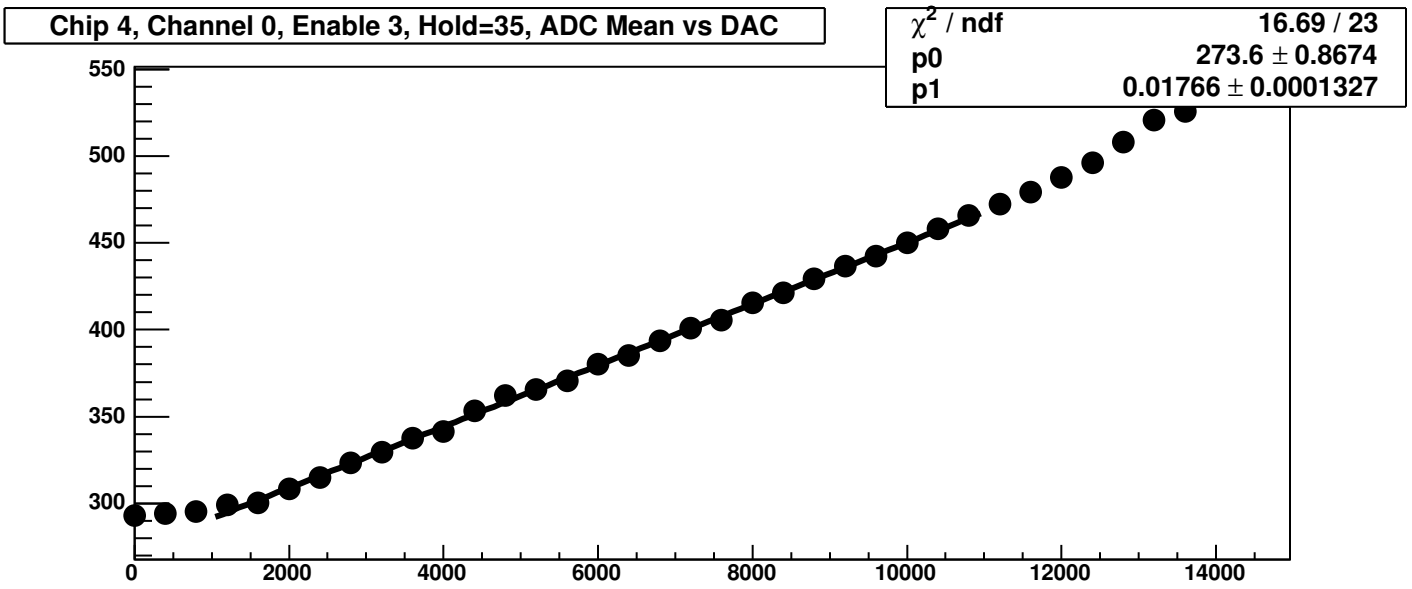

Chip 4, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

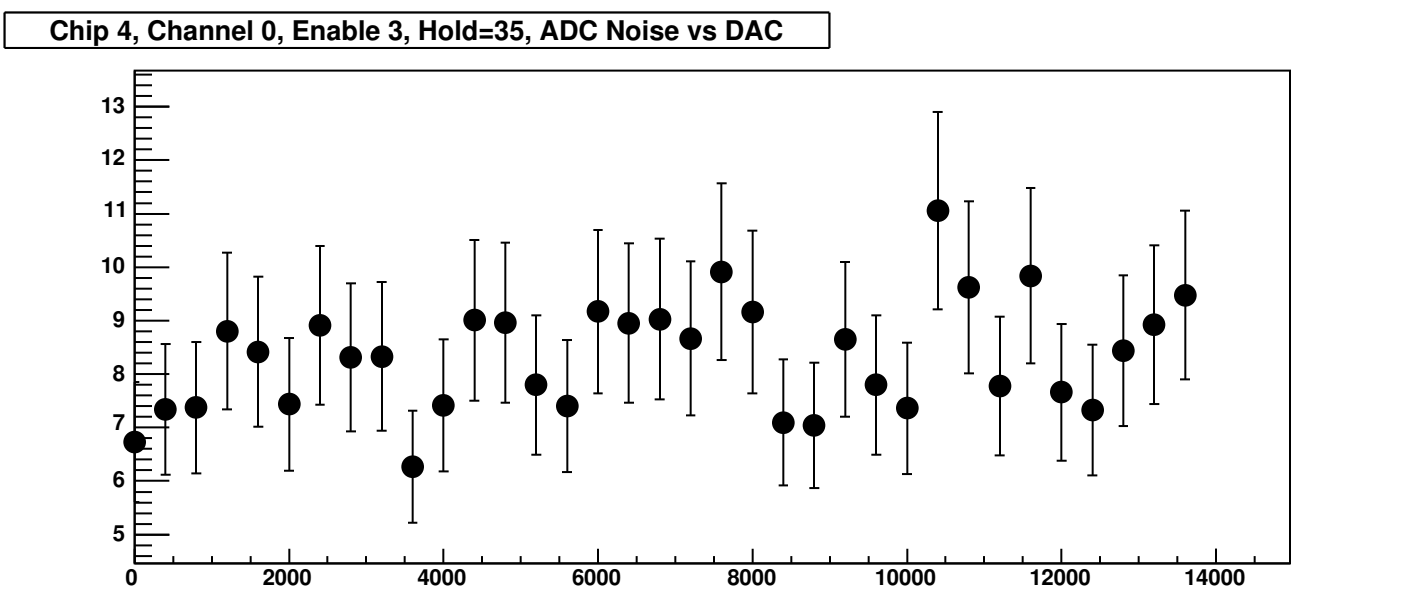

Chip 4, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

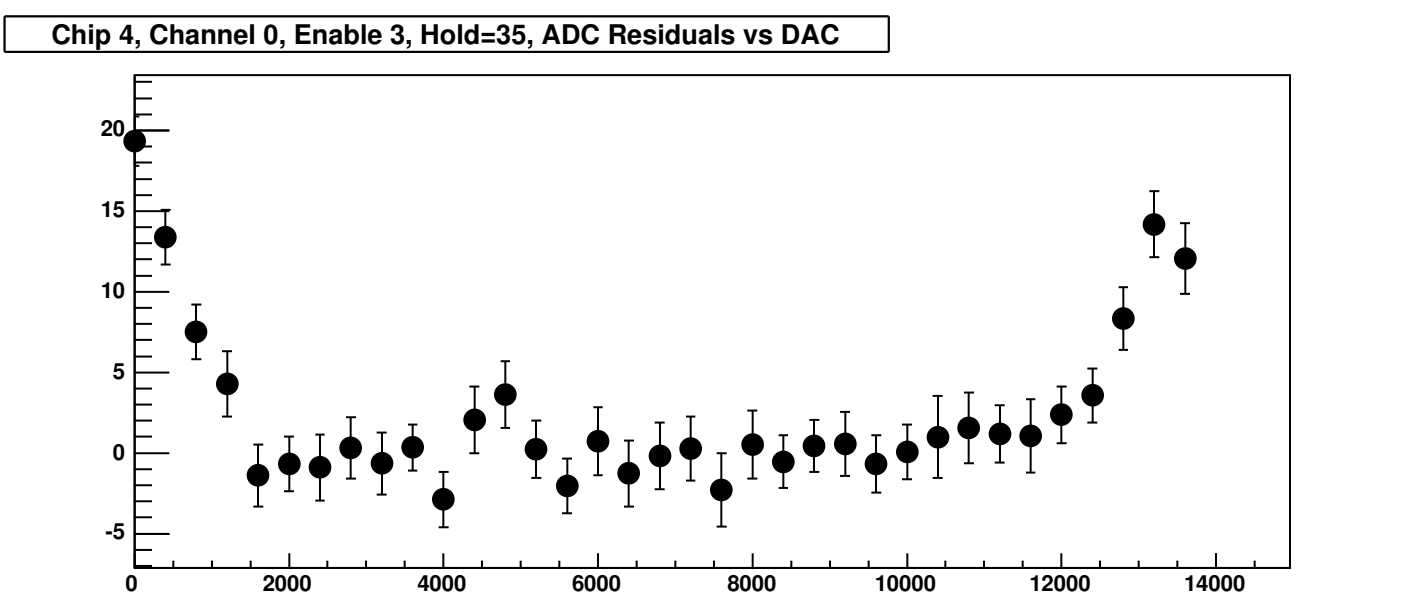

Chip 4, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 26.14 / 23
p0 273.4 ± 0.7886
p1 0.01739 ± 0.0001241
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

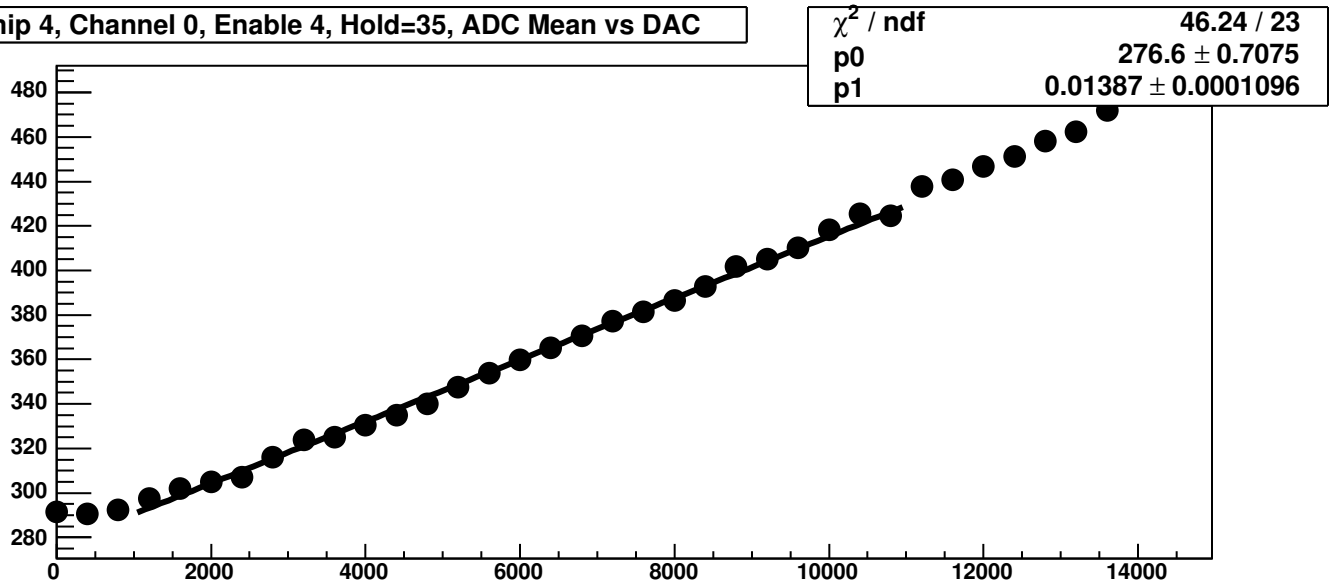

Chip 4, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 16.69 / 23
p0 273.6 ± 0.8674
p1 0.01766 ± 0.0001327
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

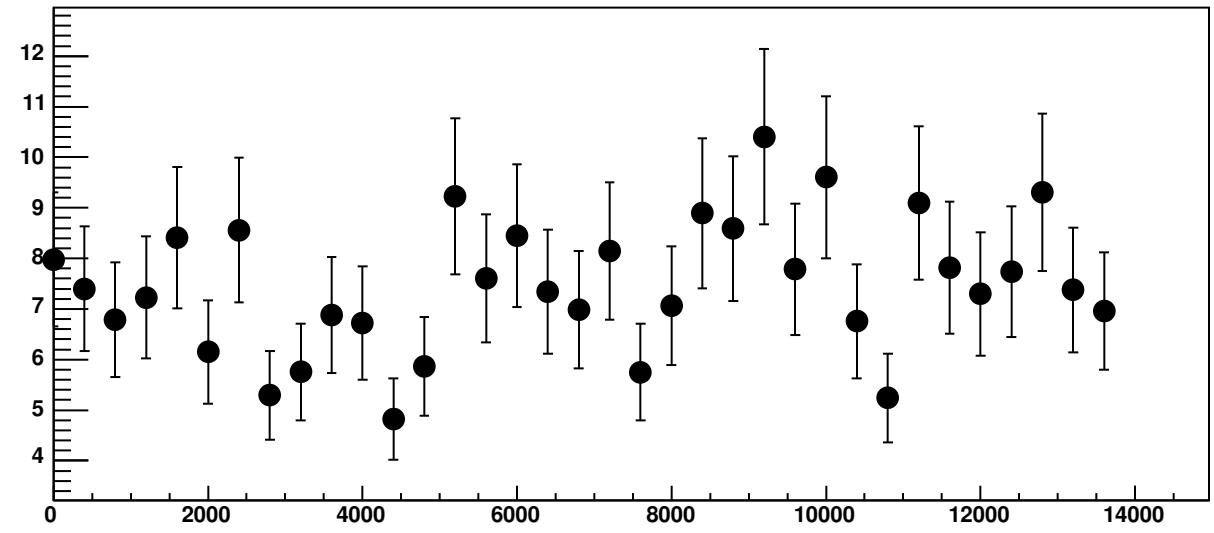

Chip 4, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

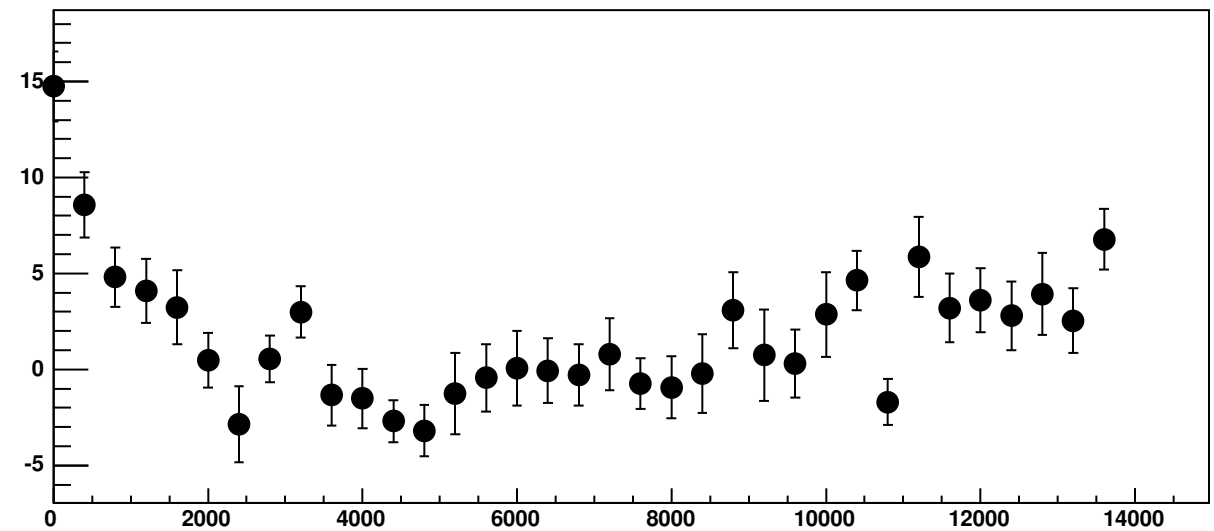
Chip 4, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

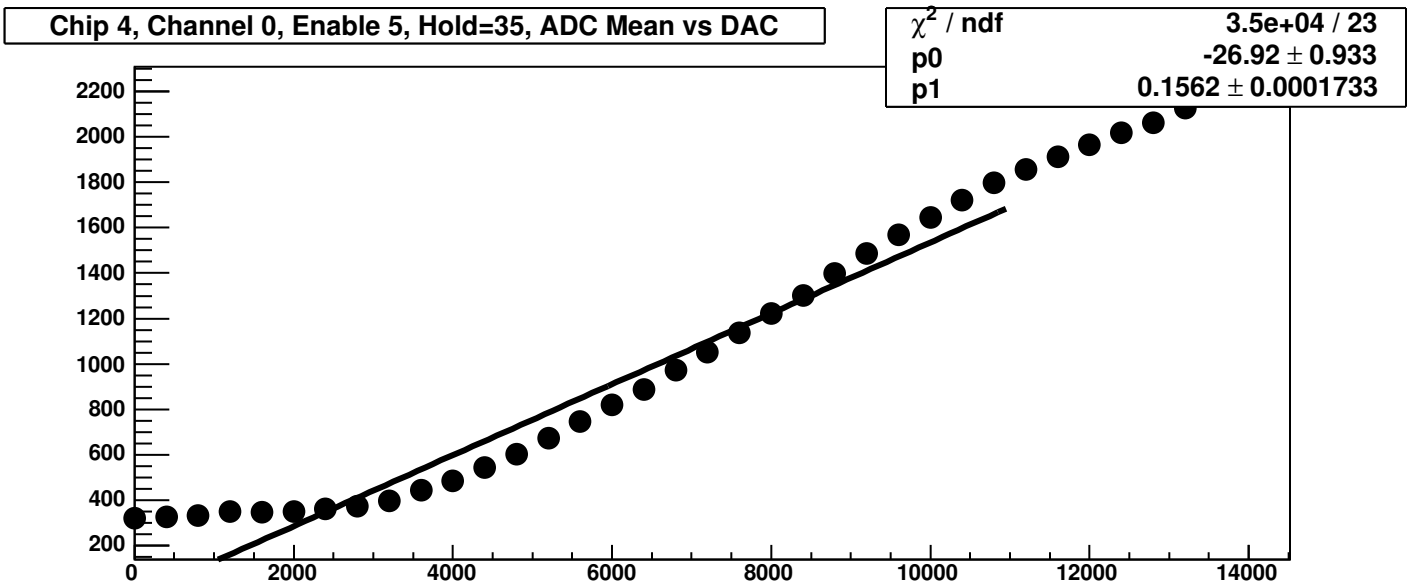


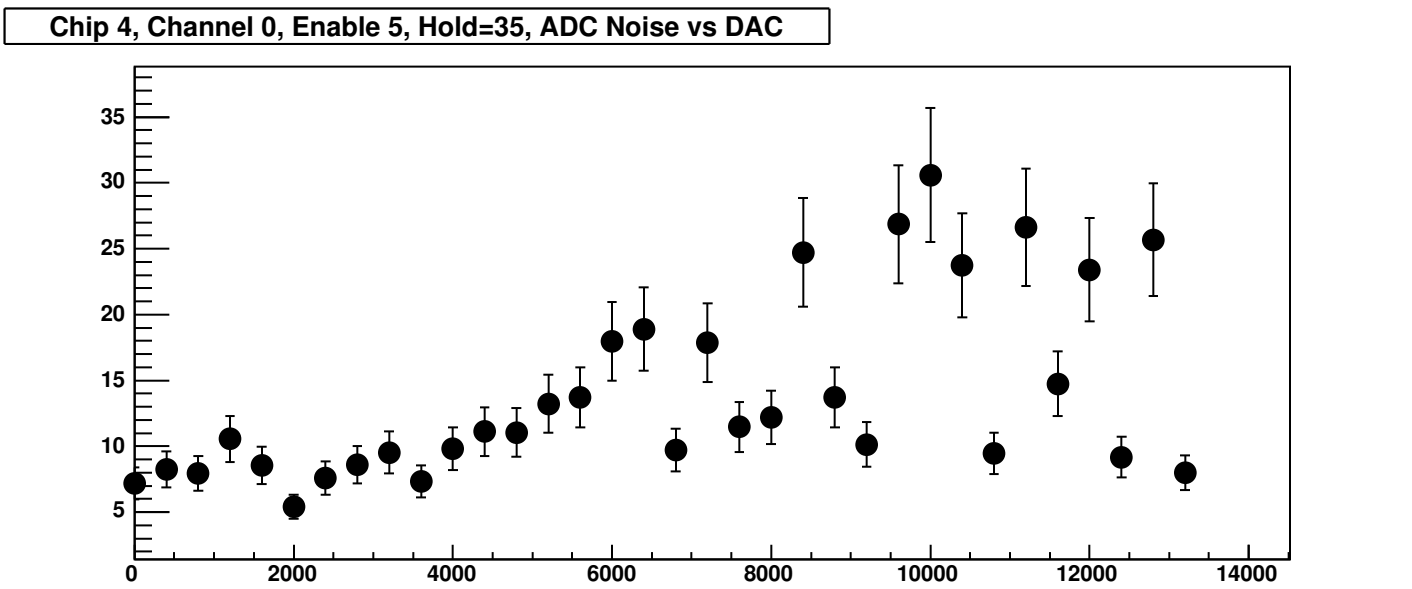
Chip 4, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

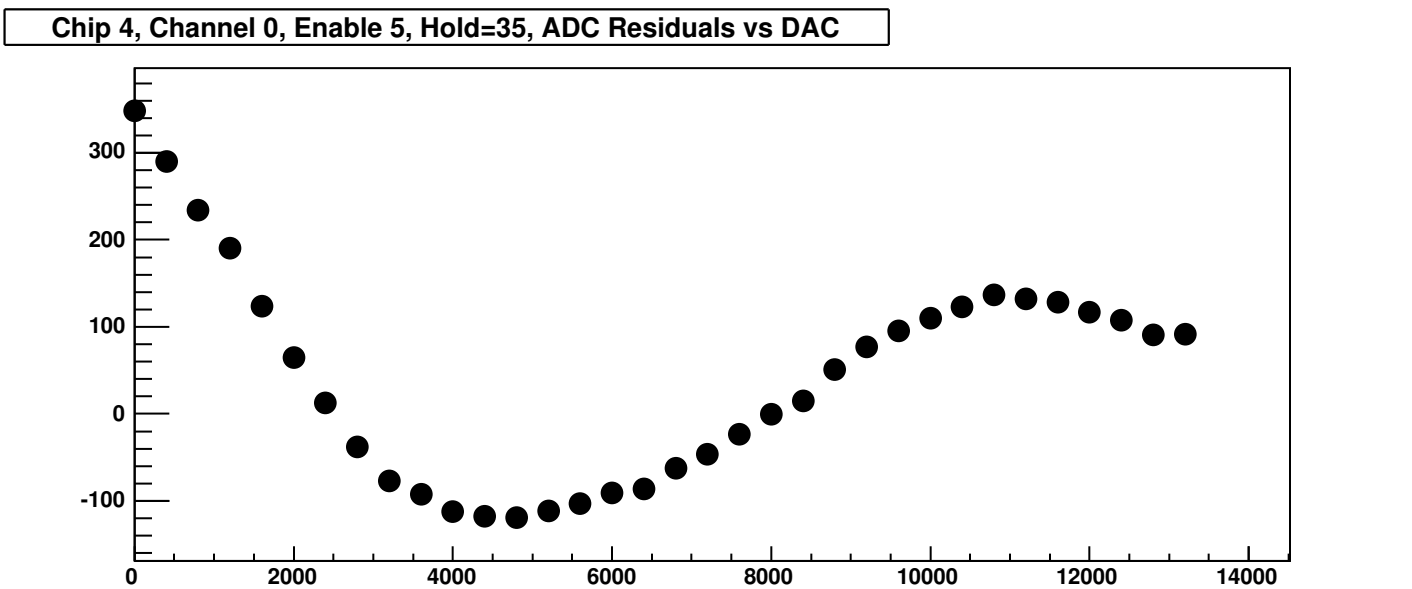


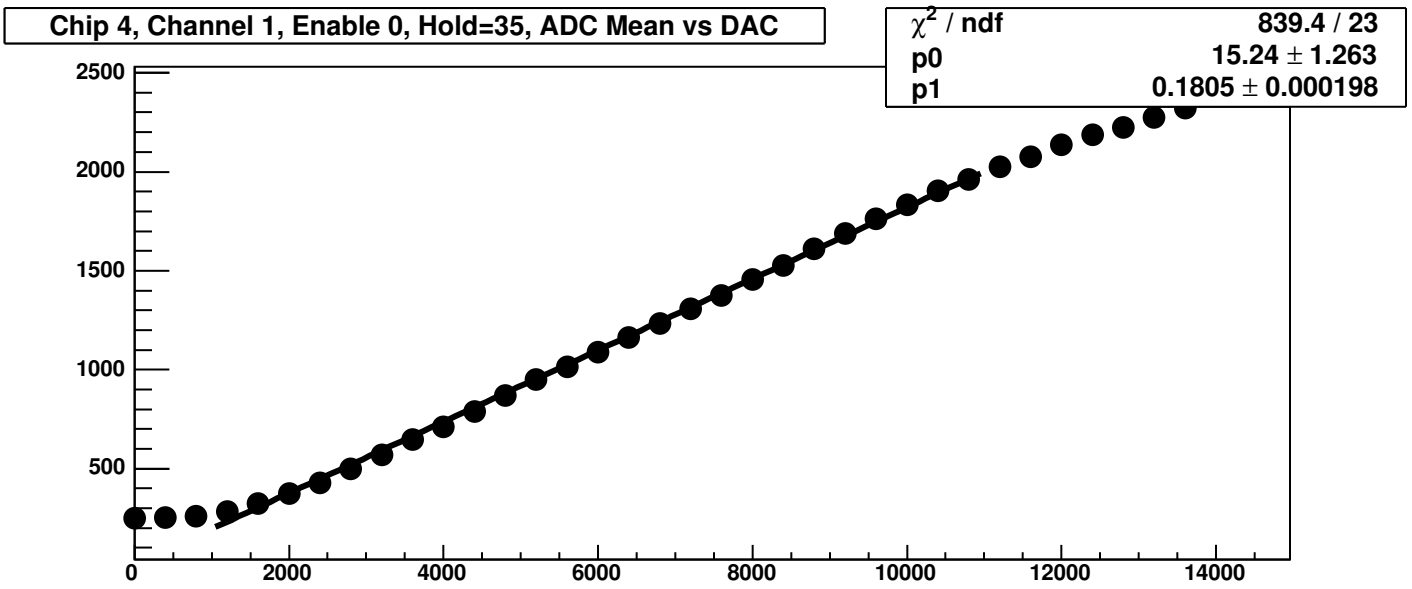
Chip 4, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

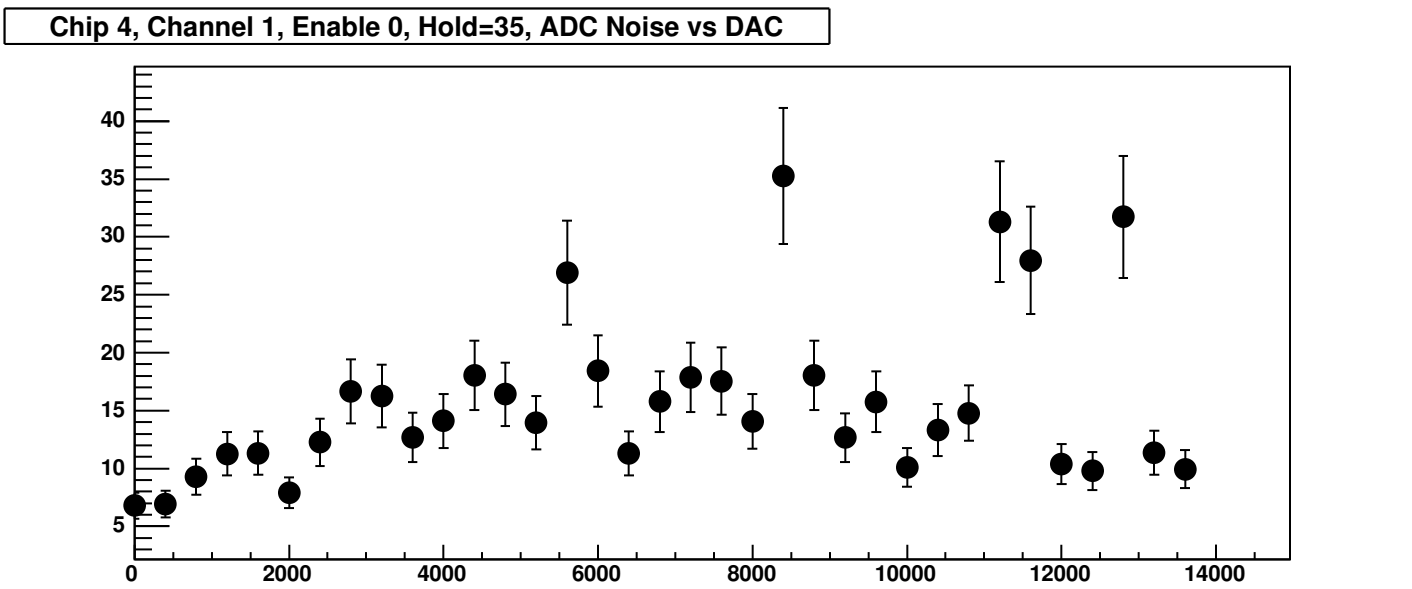


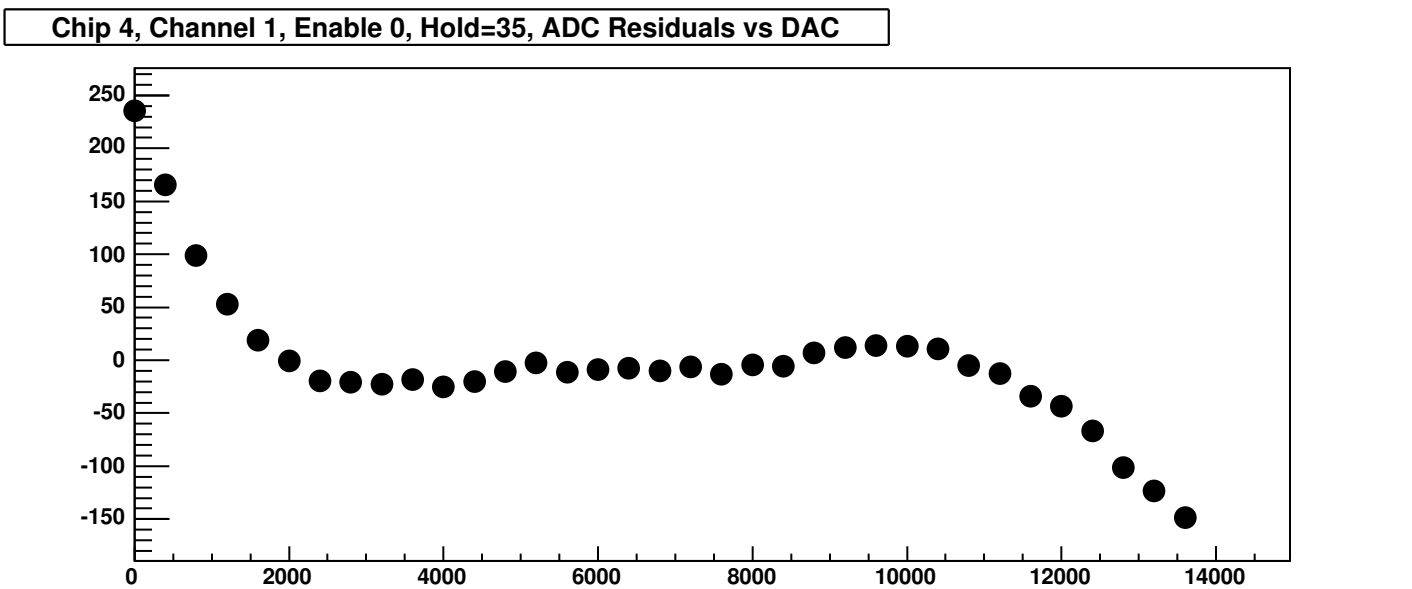

Chip 4, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.5e+04 / 23
p0 -26.92 ± 0.933
p1 0.1562 ± 0.0001733
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

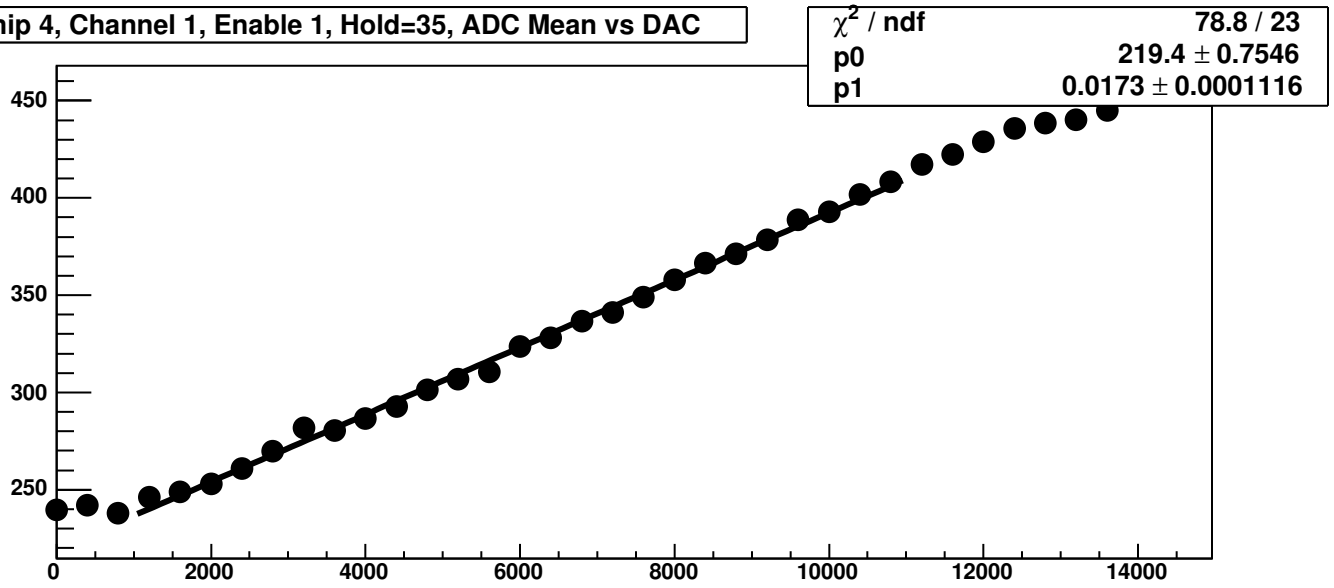

Chip 4, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 839.4 / 23
p0 15.24 ± 1.263
p1 0.1805 ± 0.000198
2500
2000
1500
1000
500
0
2000
4000
6000
8000
10000
12000
14000

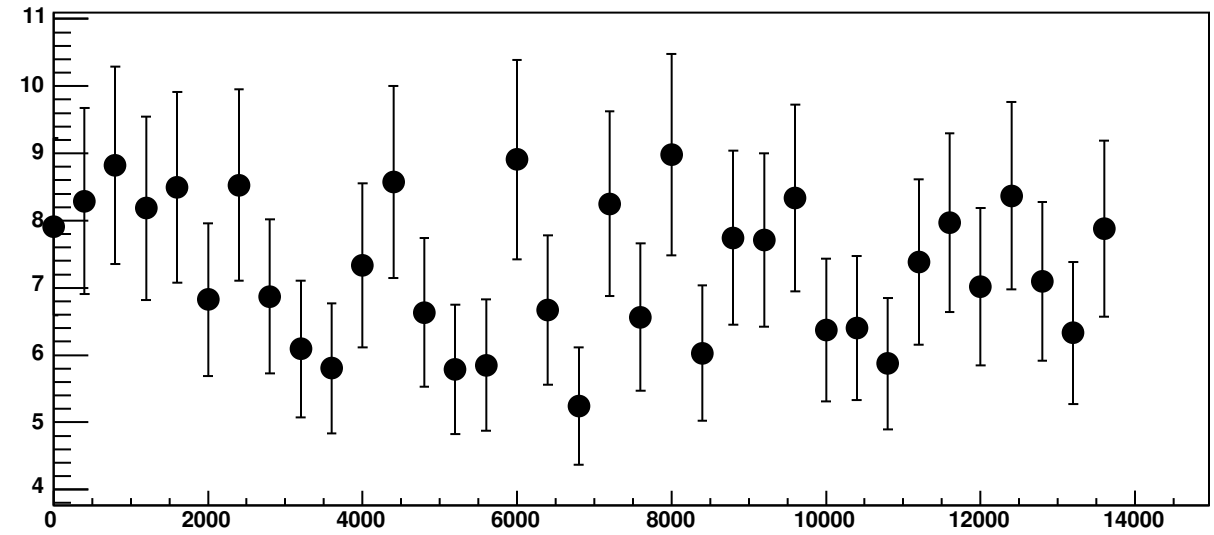

Chip 4, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

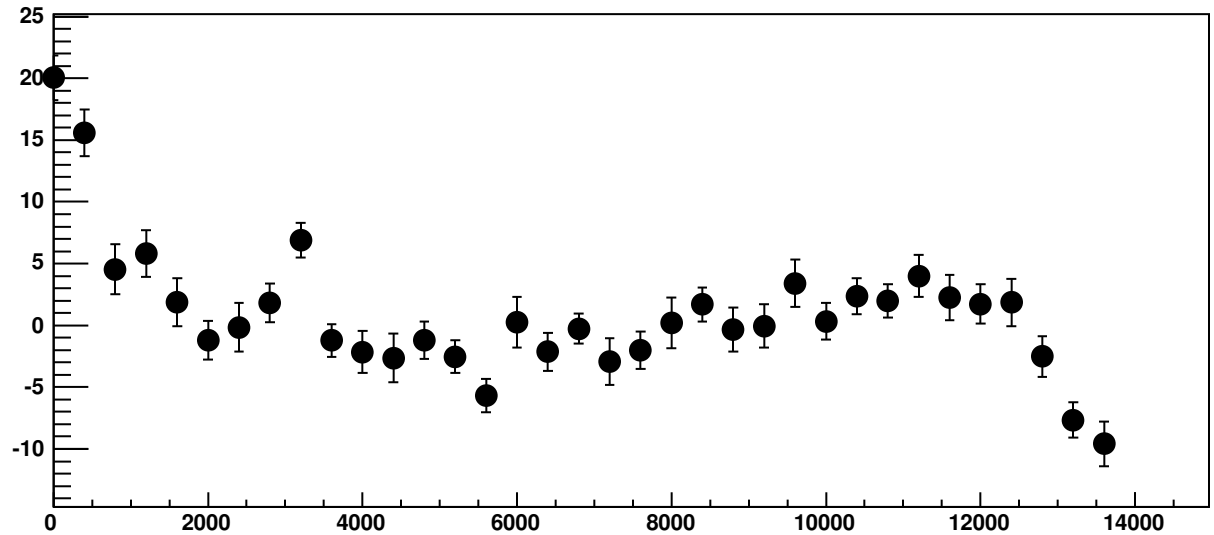
Chip 4, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



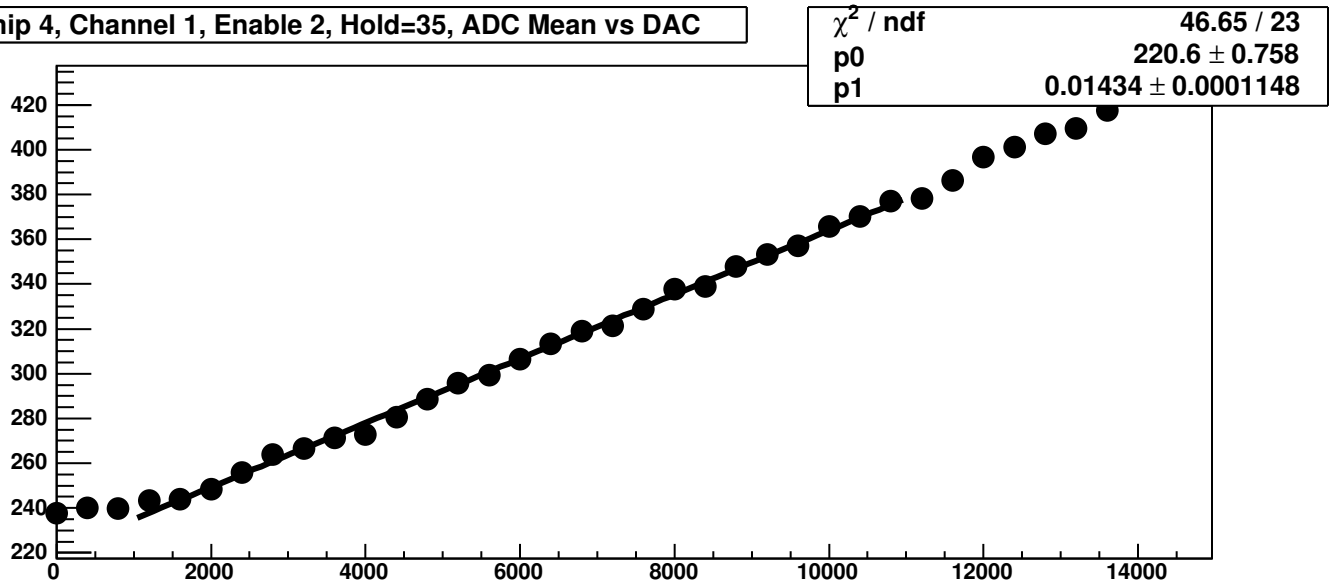
Chip 4, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



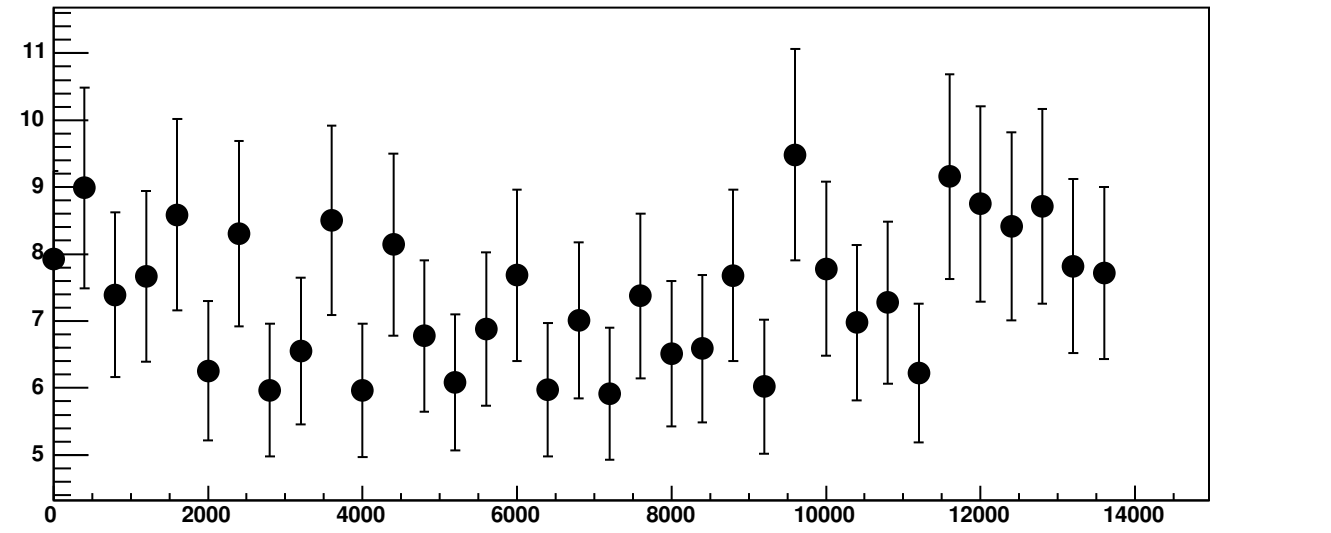
Chip 4, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



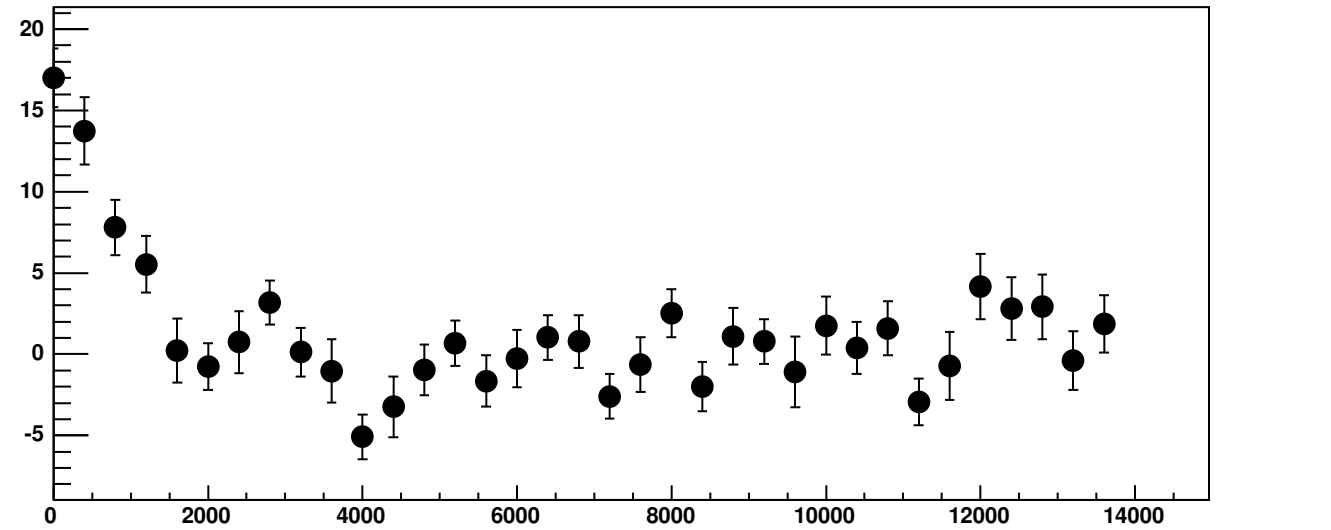
Chip 4, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

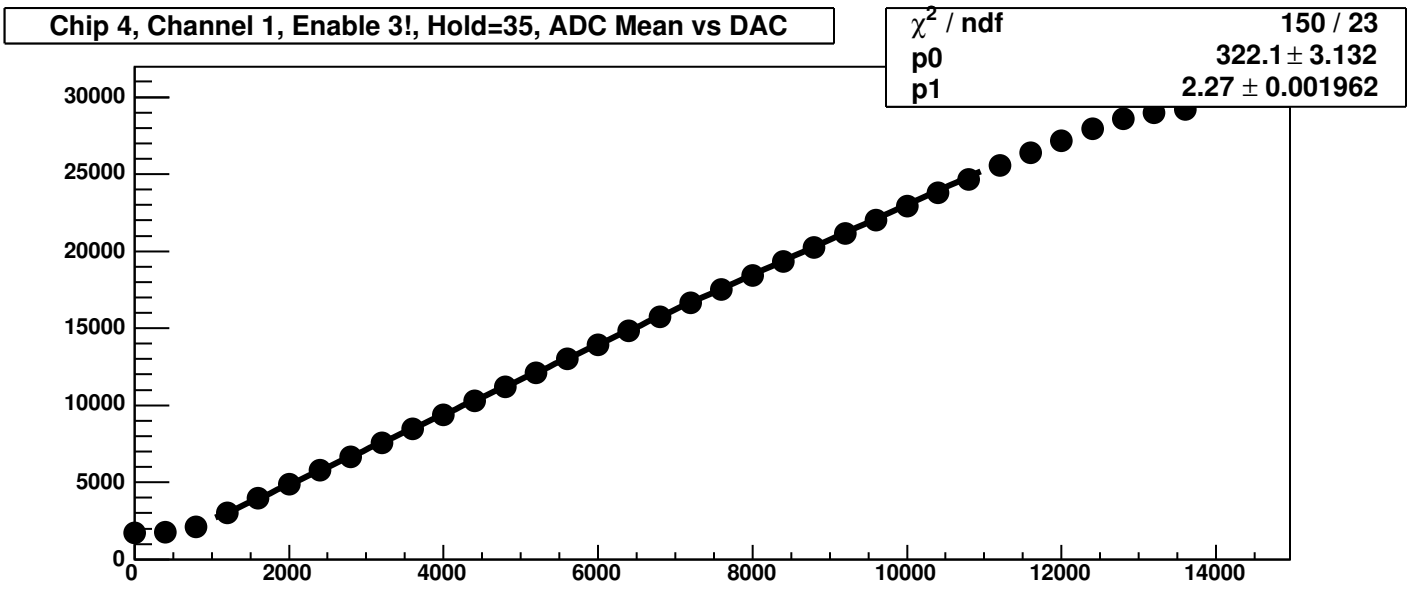


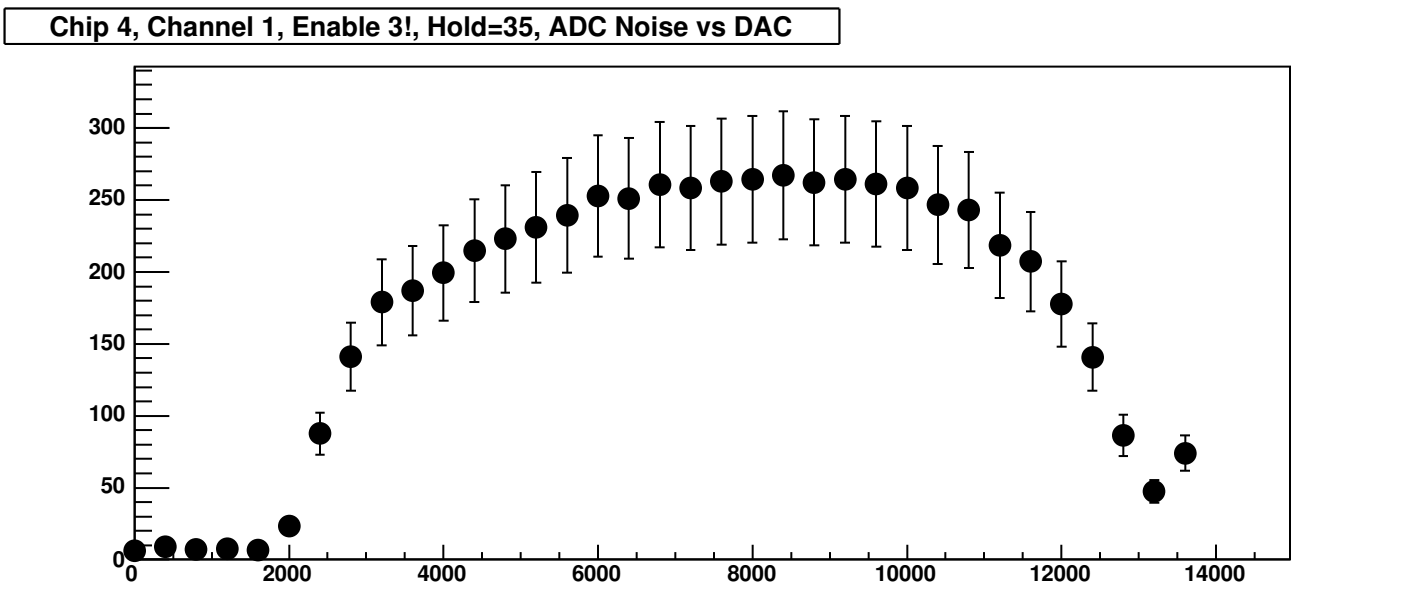
Chip 4, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC

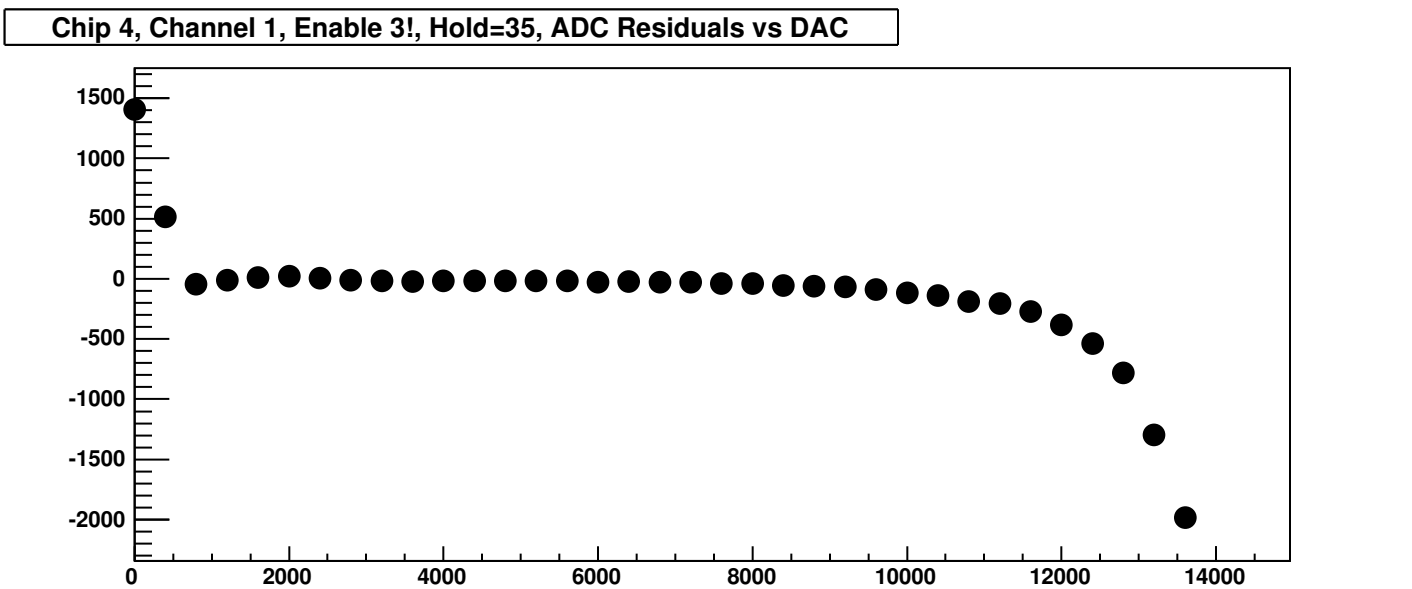


Chip 4, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

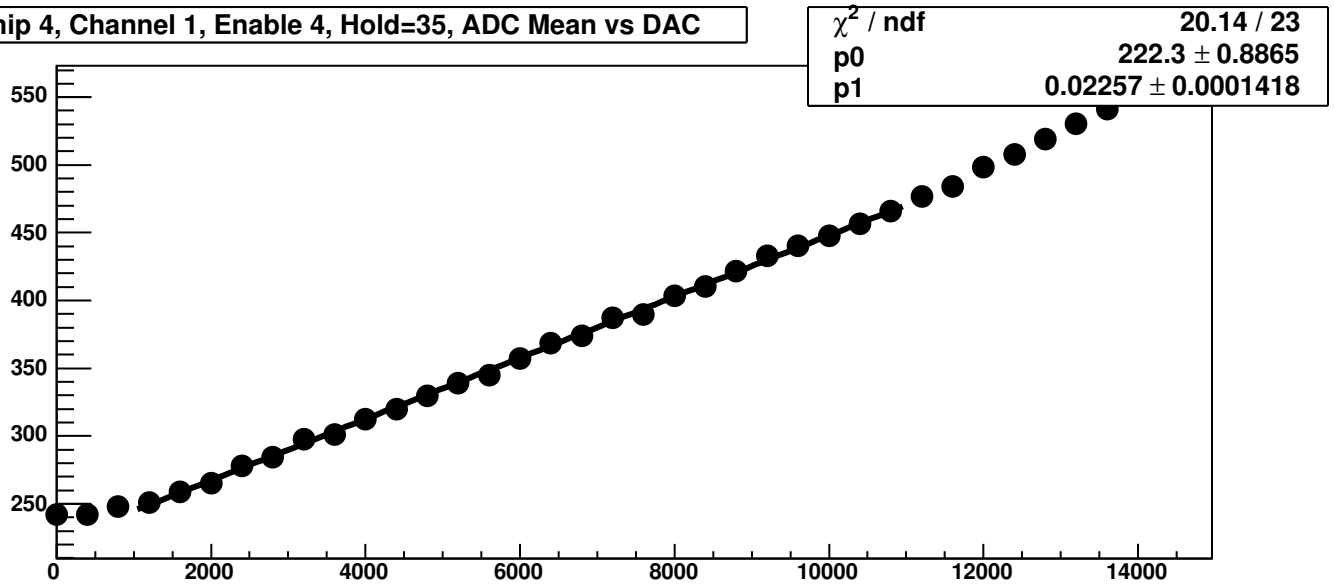



Chip 4, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 150 / 23
p0 322.1 ± 3.132
p1 2.27 ± 0.001962
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

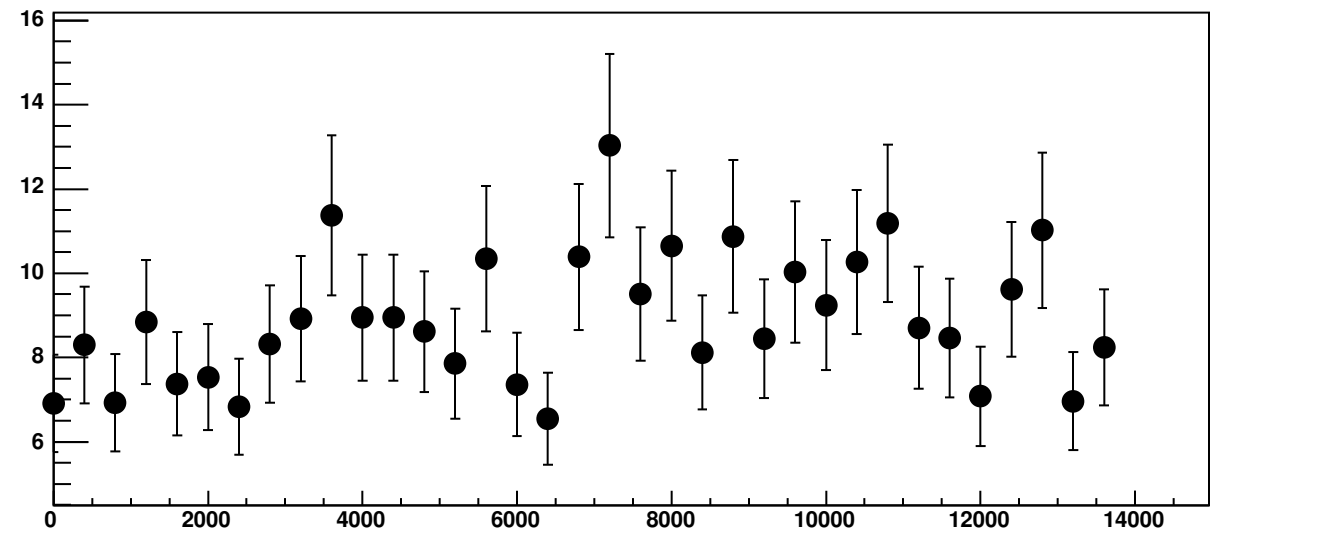

Chip 4, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

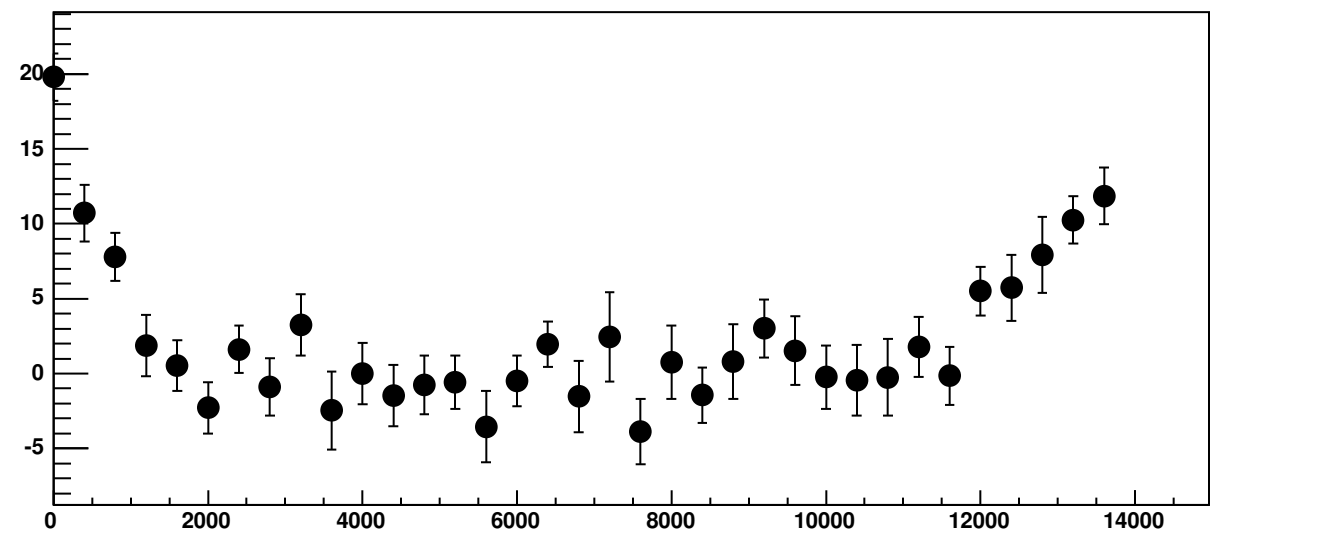
Chip 4, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



Chip 4, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

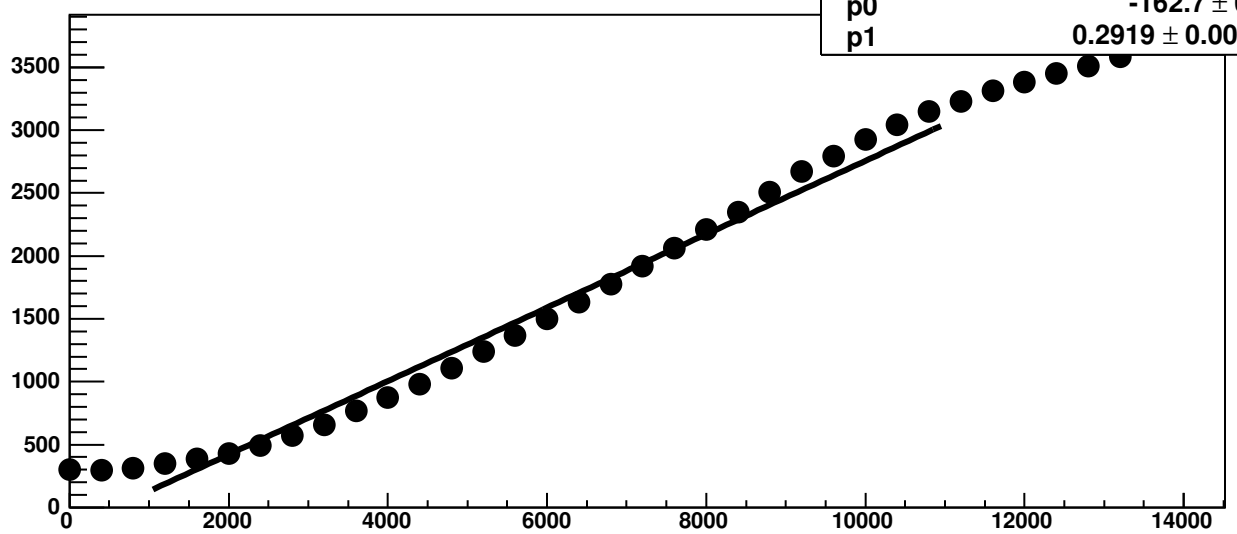


Chip 4, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

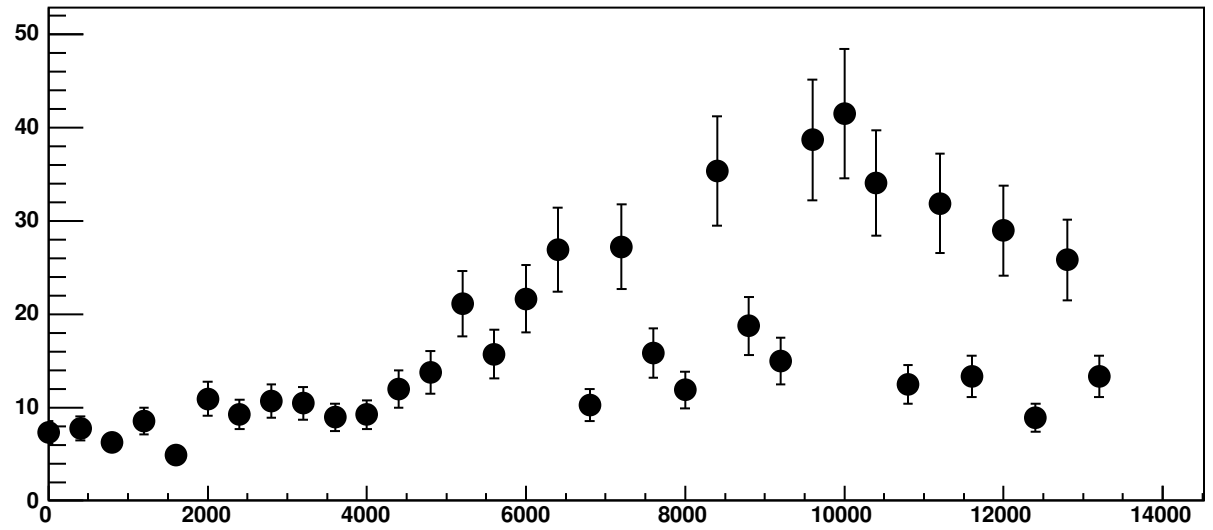


Chip 4, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

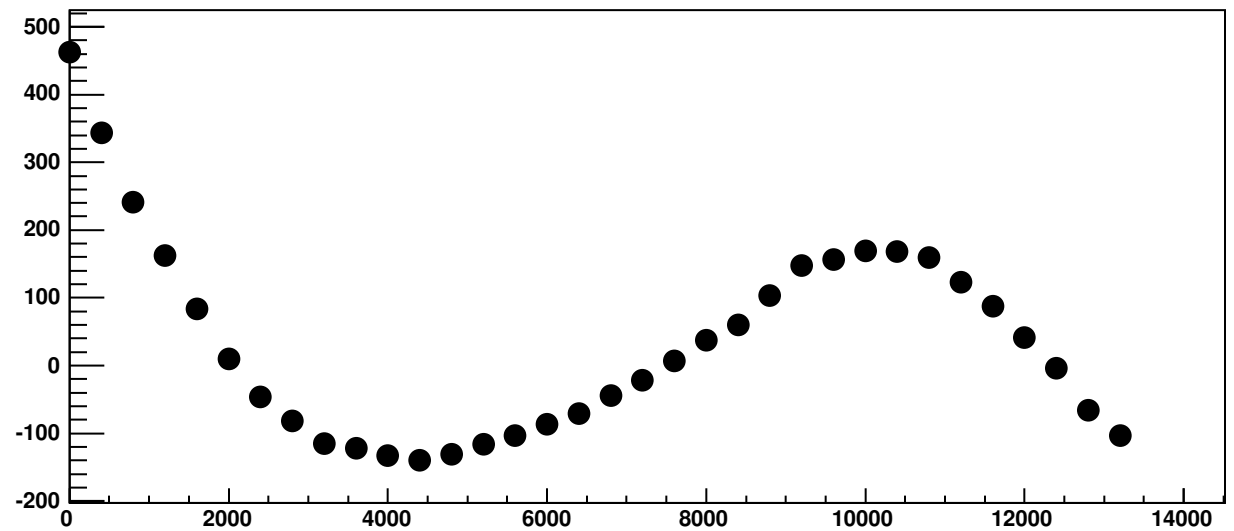
χ^2 / ndf	3.705e+04 / 23
p0	-162.7 $\pm$ 0.9711
p1	0.2919 $\pm$ 0.0002016

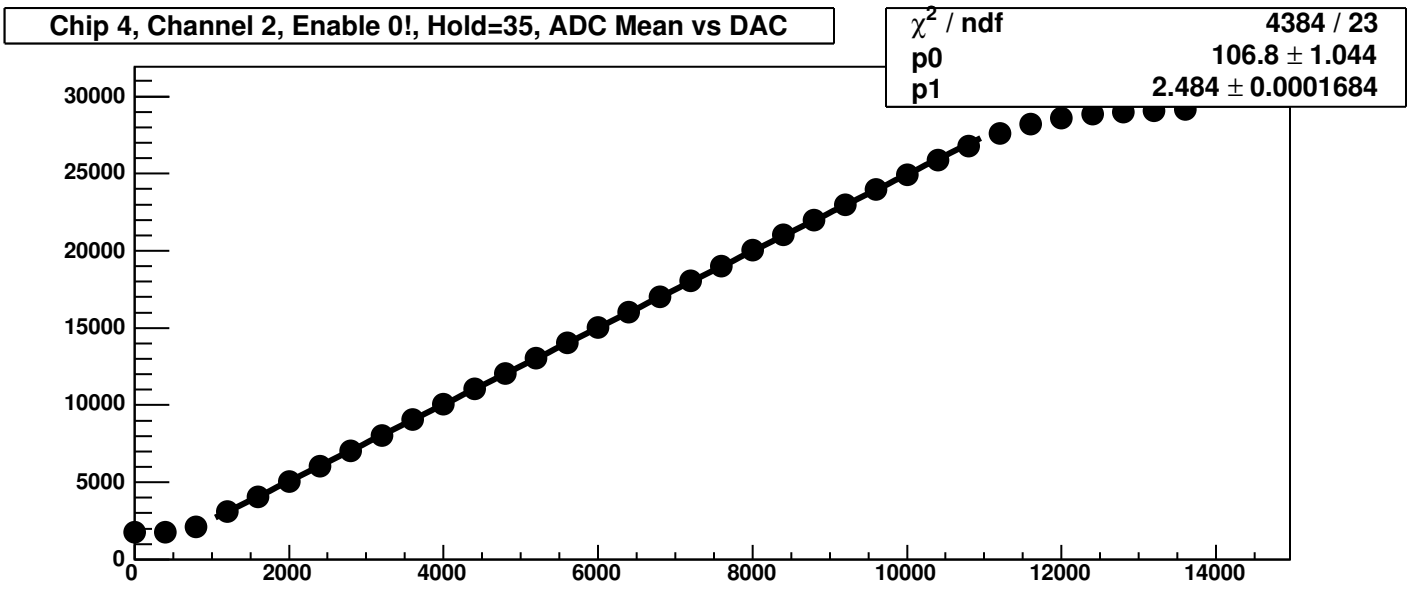


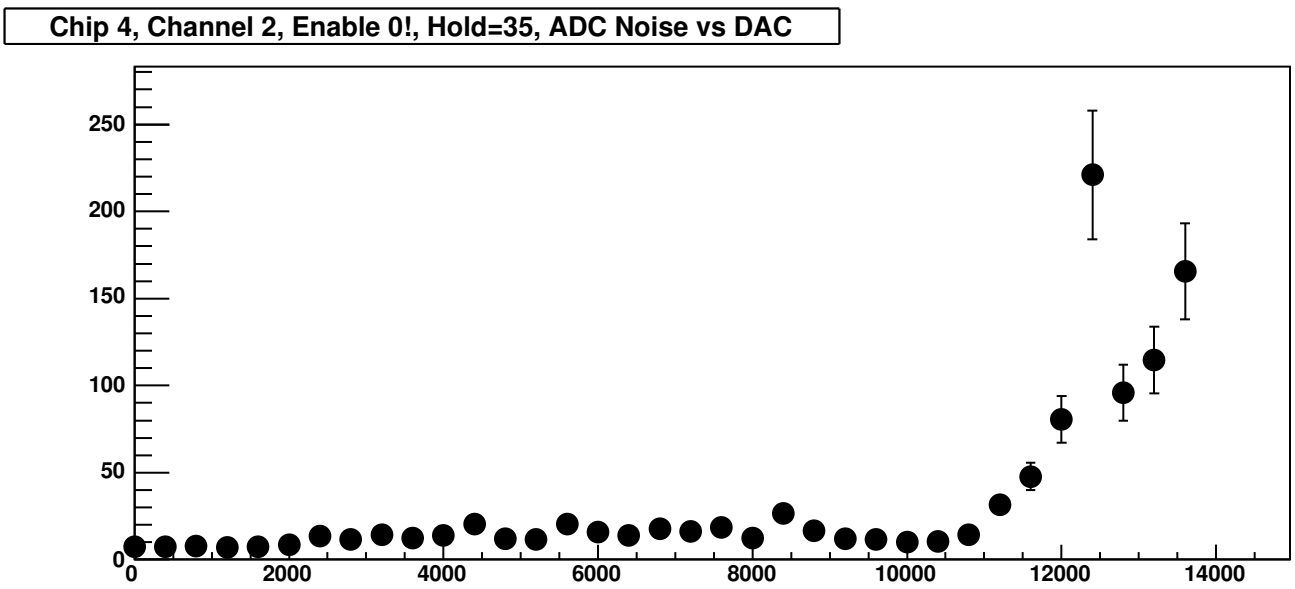
Chip 4, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC

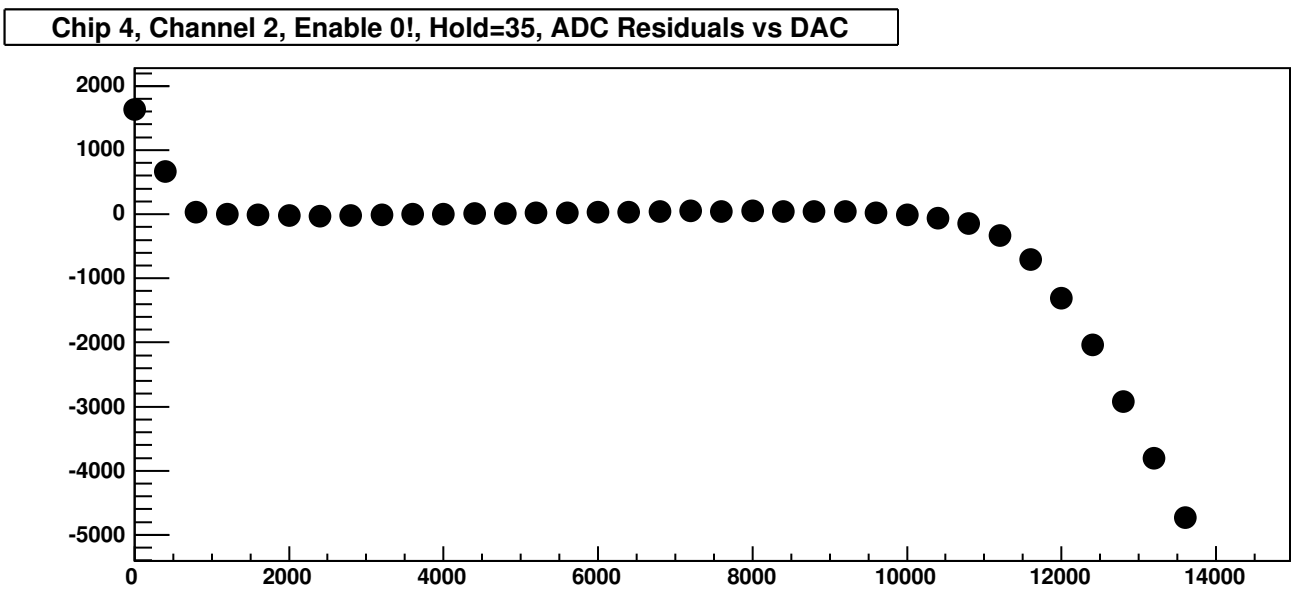


Chip 4, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

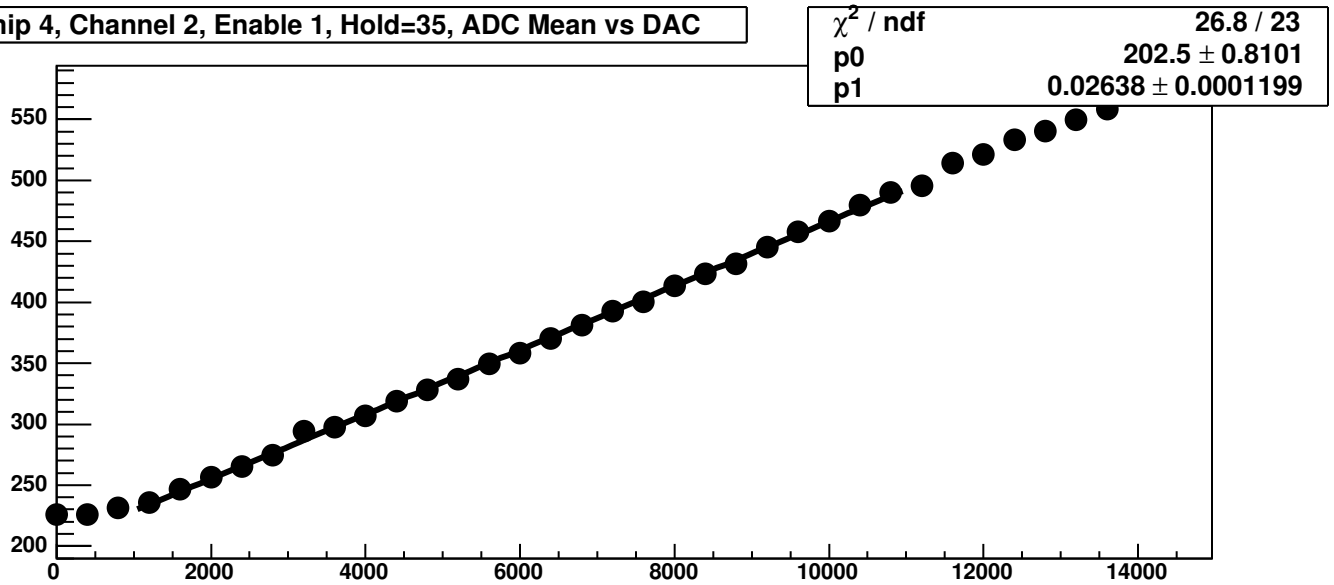



Chip 4, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 4384 / 23
p0 106.8 ± 1.044
p1 2.484 ± 0.0001684
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

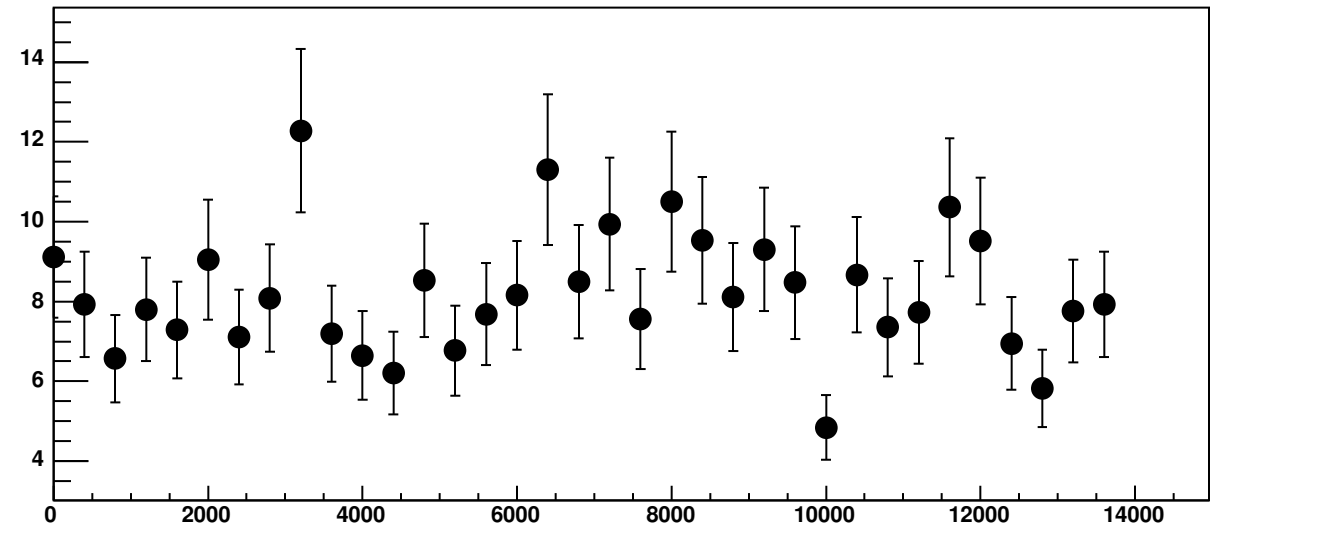

Chip 4, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000

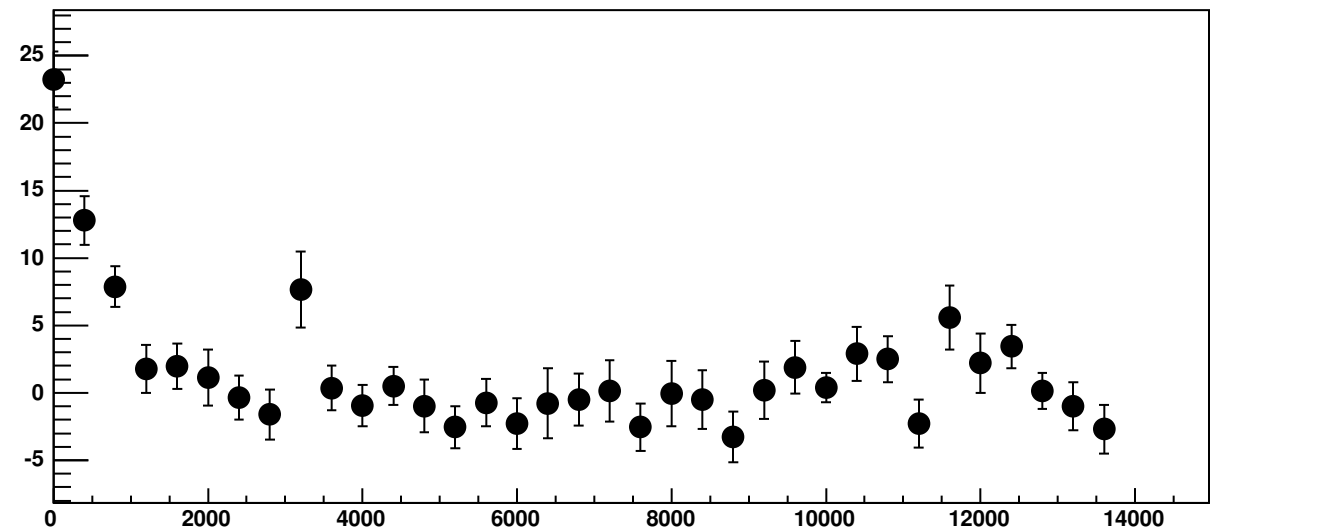
Chip 4, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



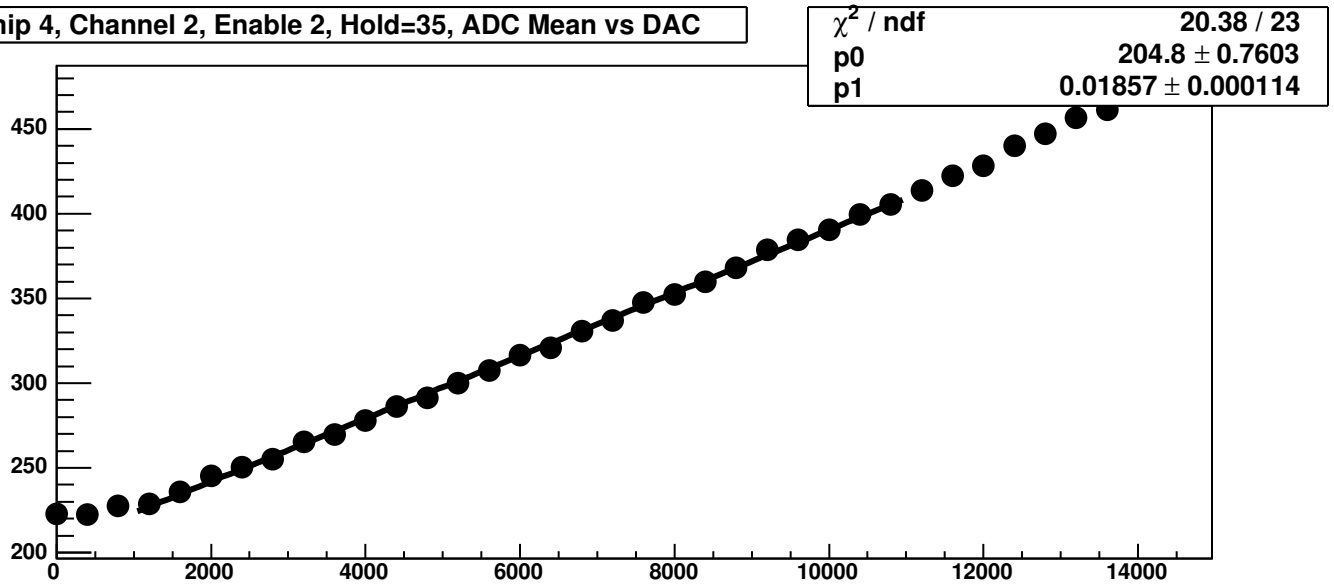
Chip 4, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



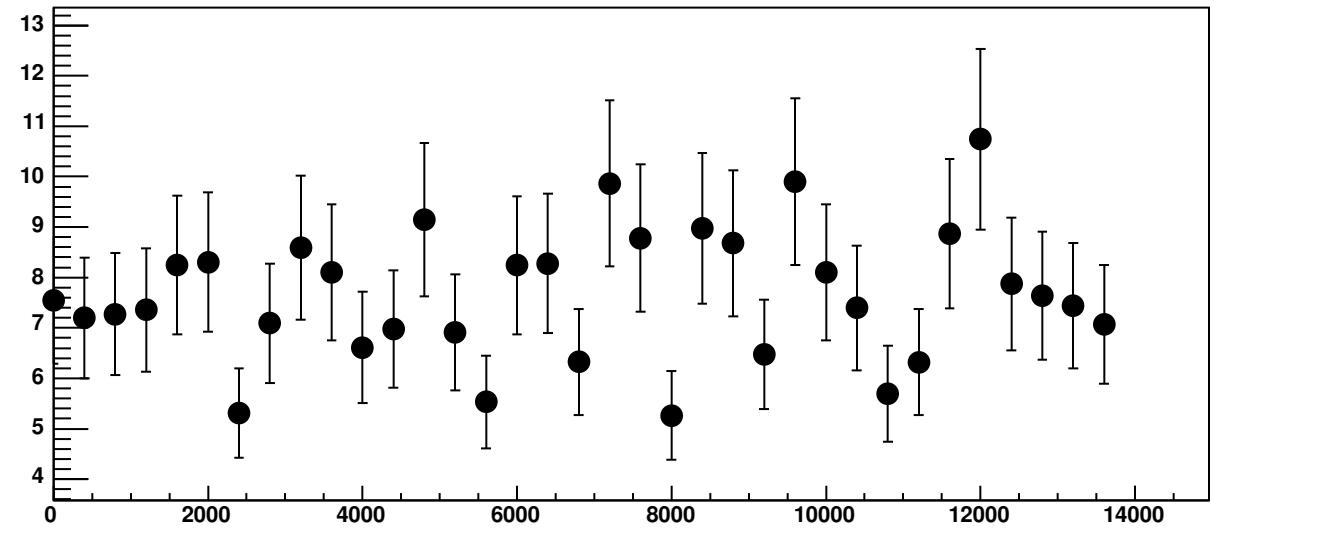
Chip 4, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



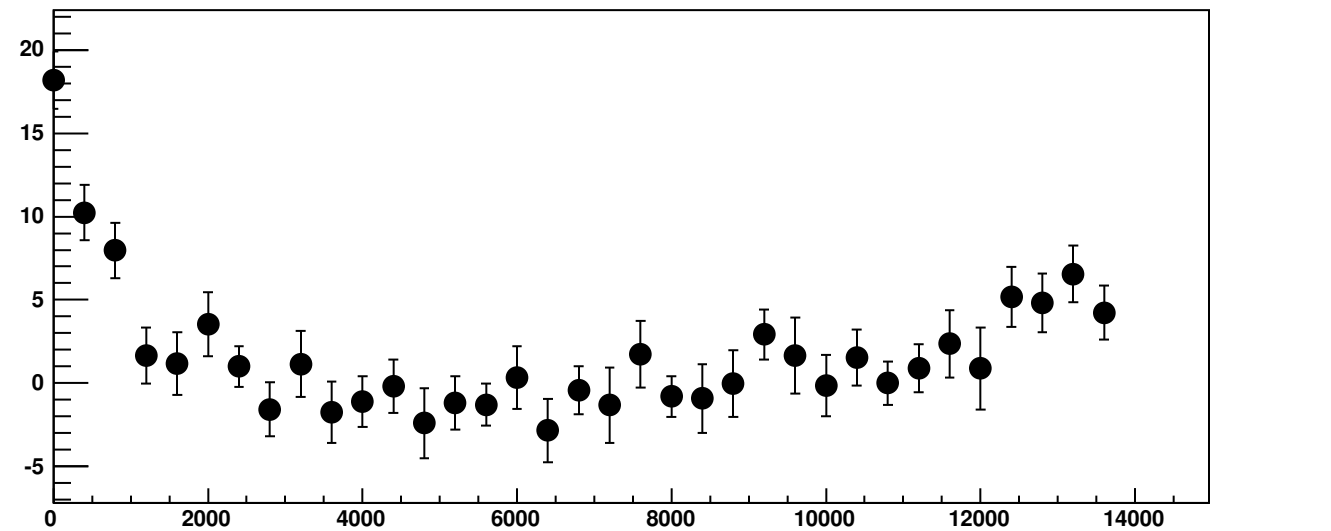
Chip 4, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



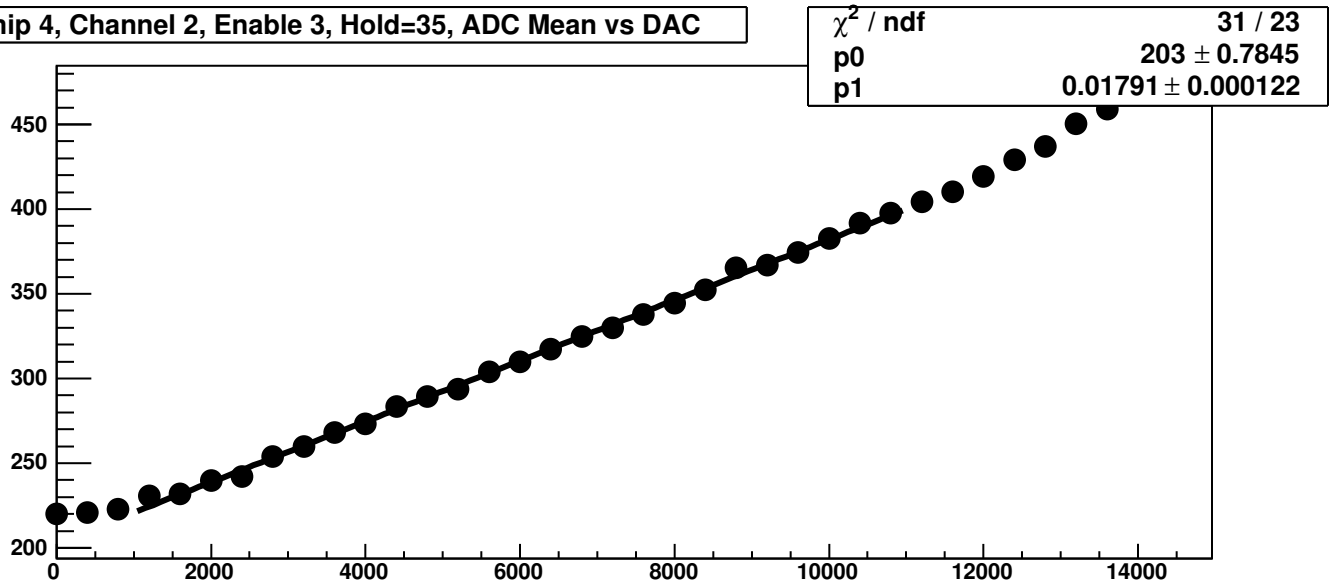
Chip 4, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



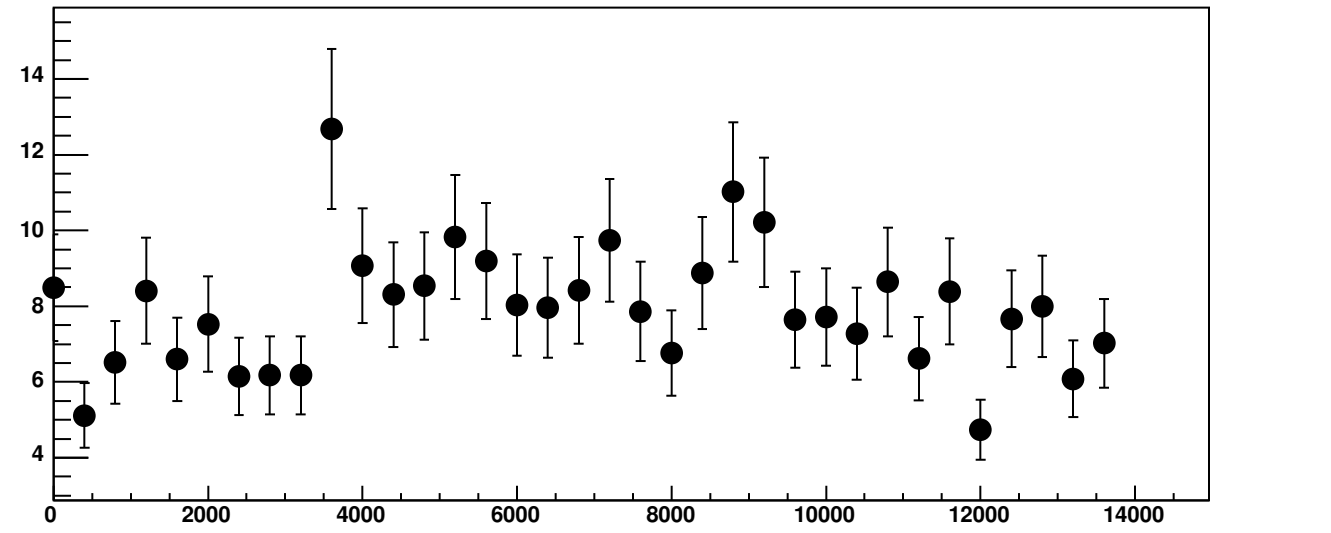
Chip 4, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



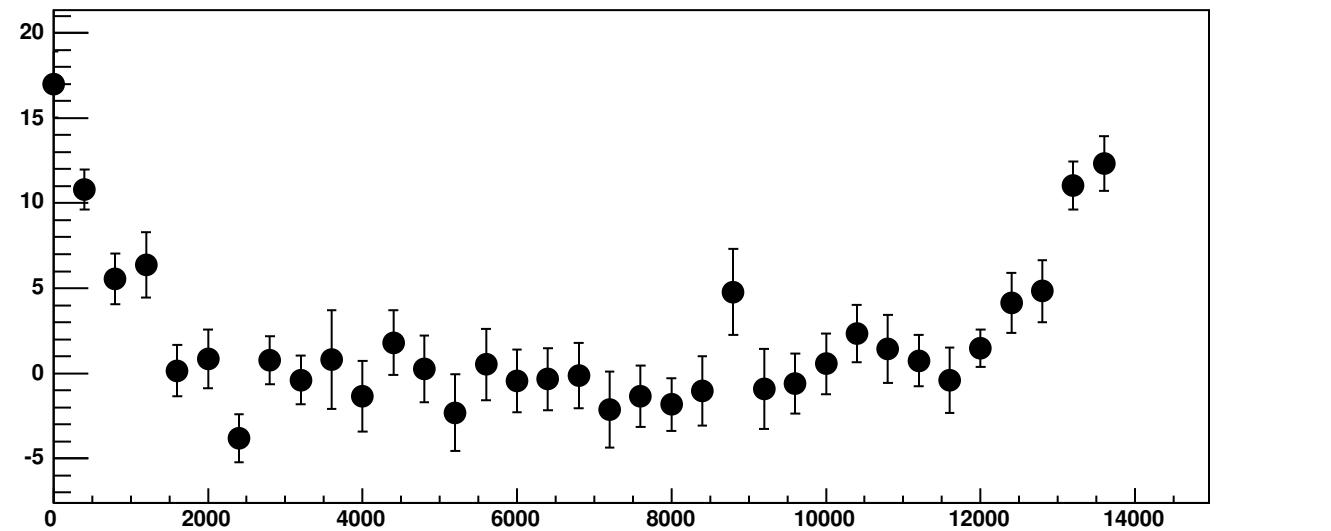
Chip 4, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

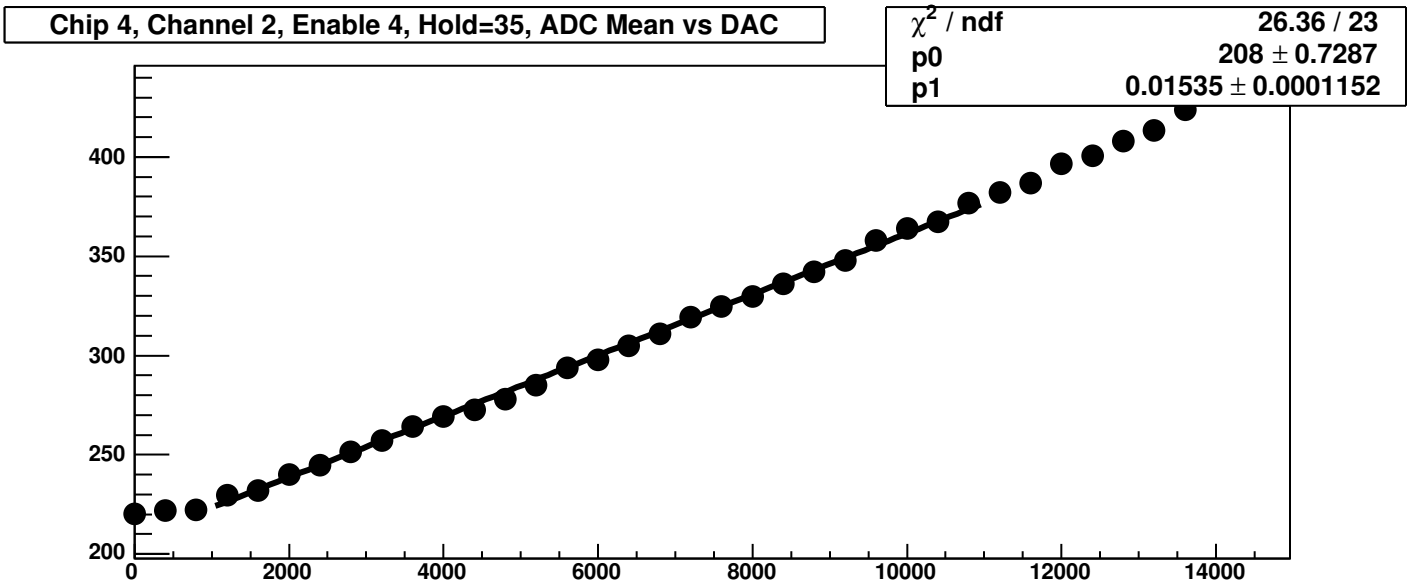


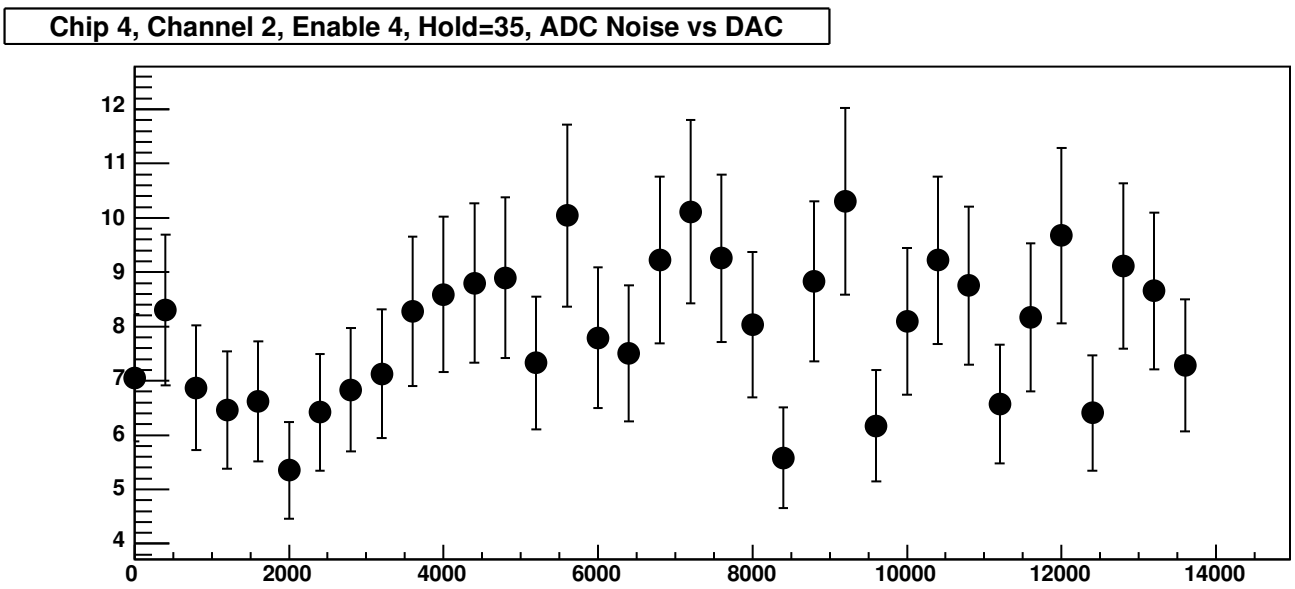
Chip 4, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC

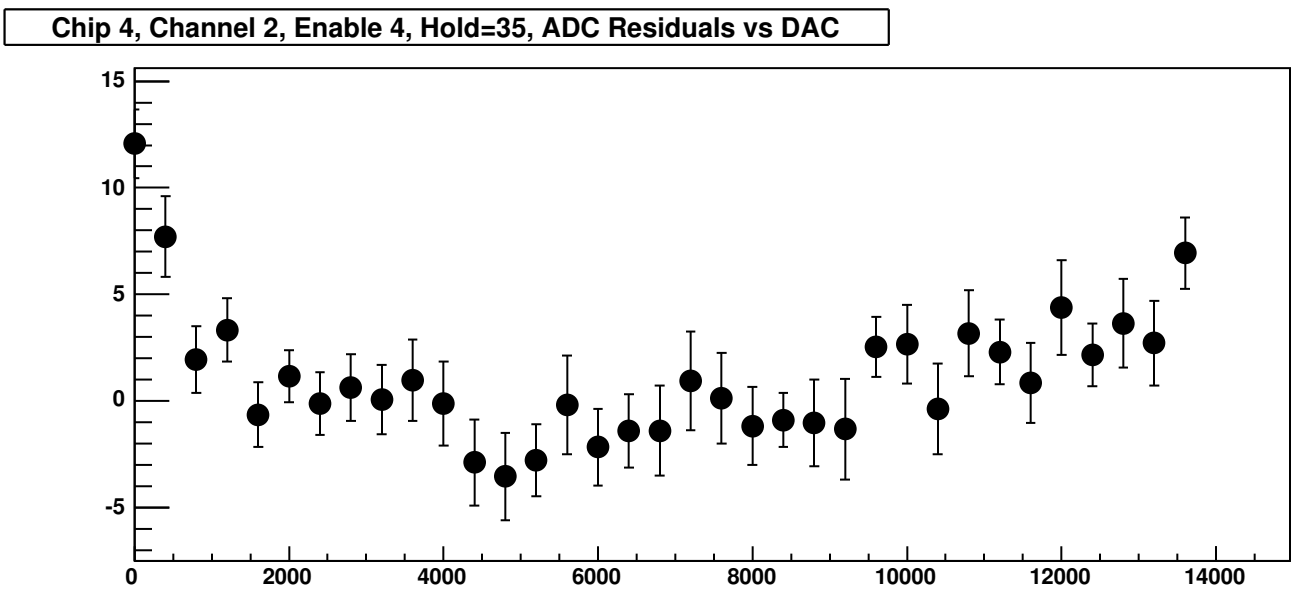


Chip 4, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

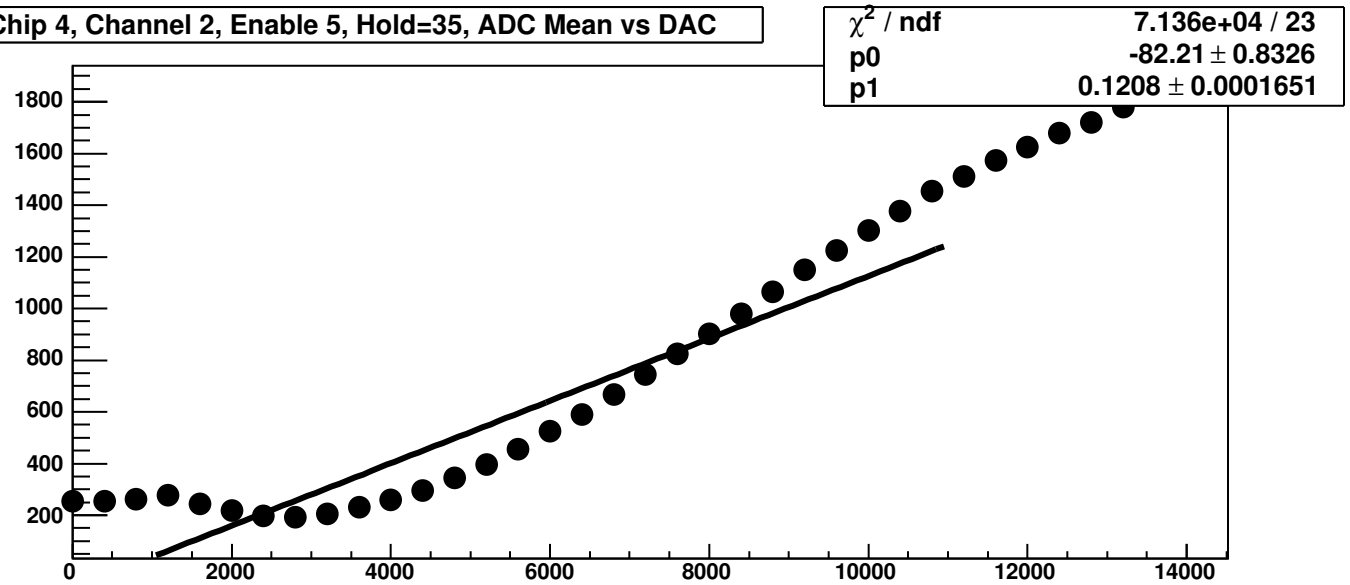



Chip 4, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.36 / 23
p0 208 ± 0.7287
p1 0.01535 ± 0.0001152
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

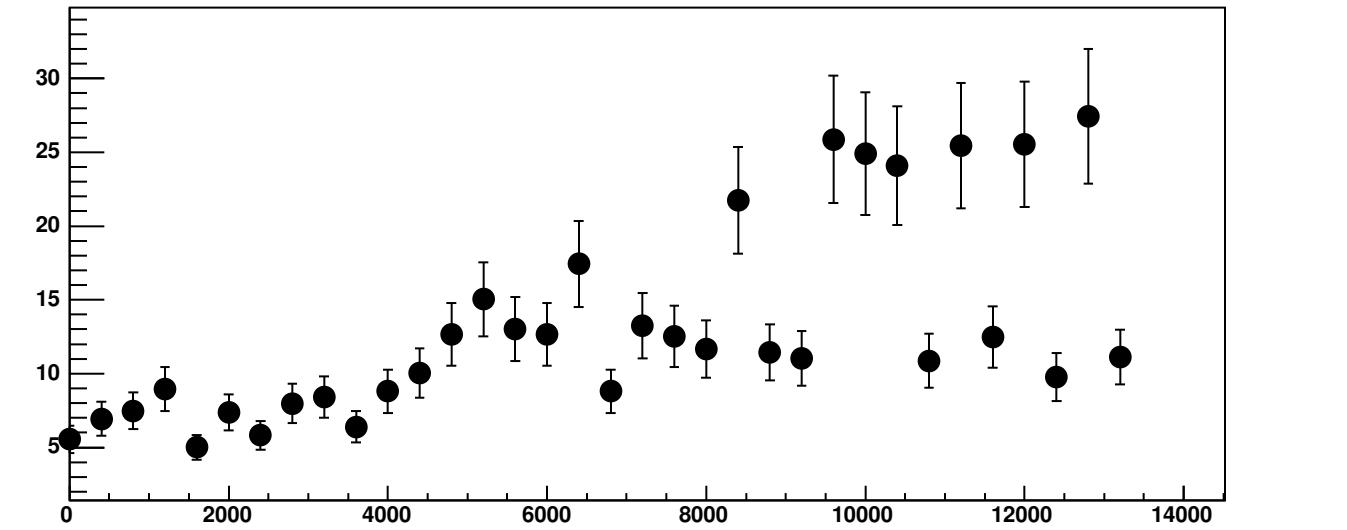

Chip 4, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

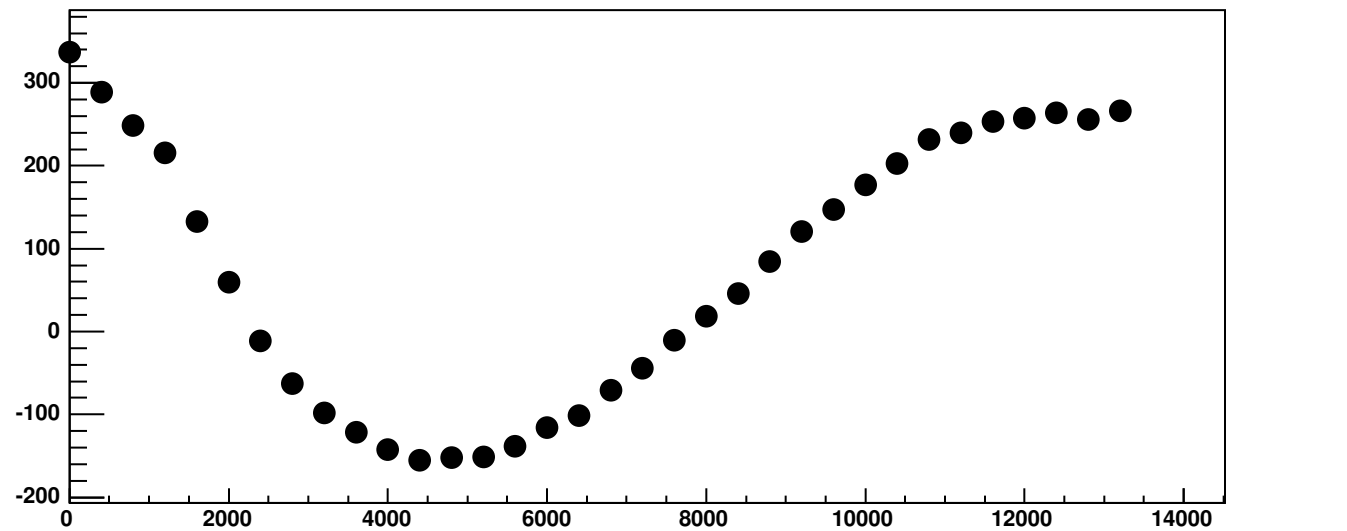
Chip 4, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

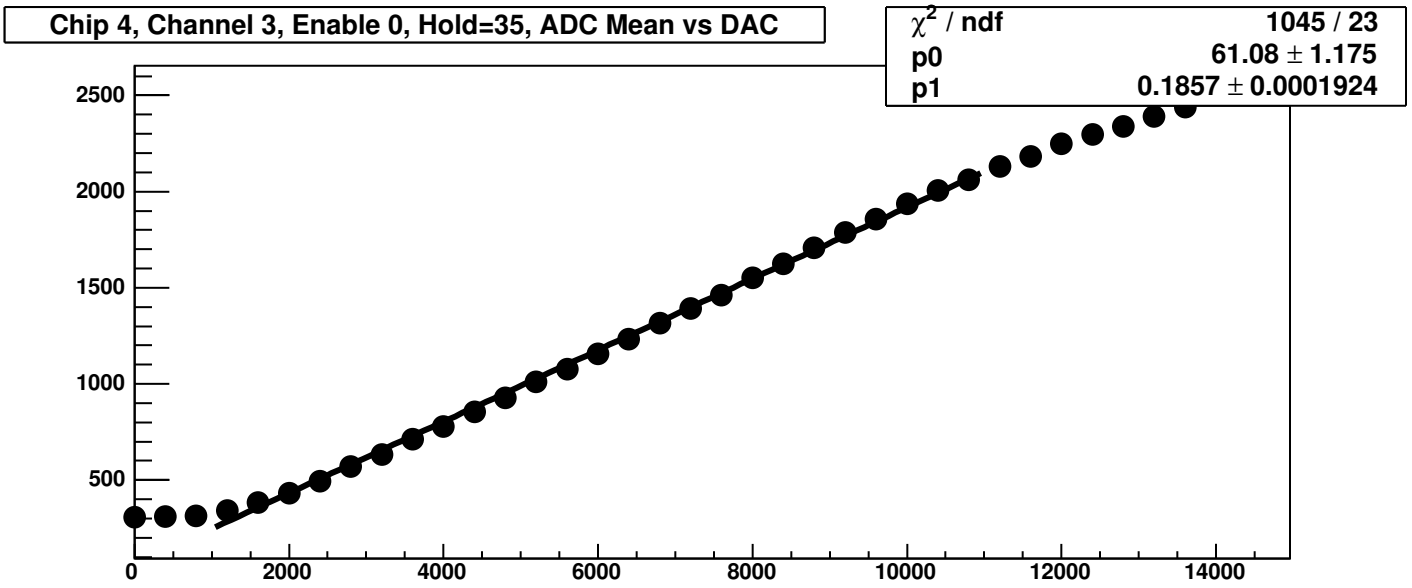


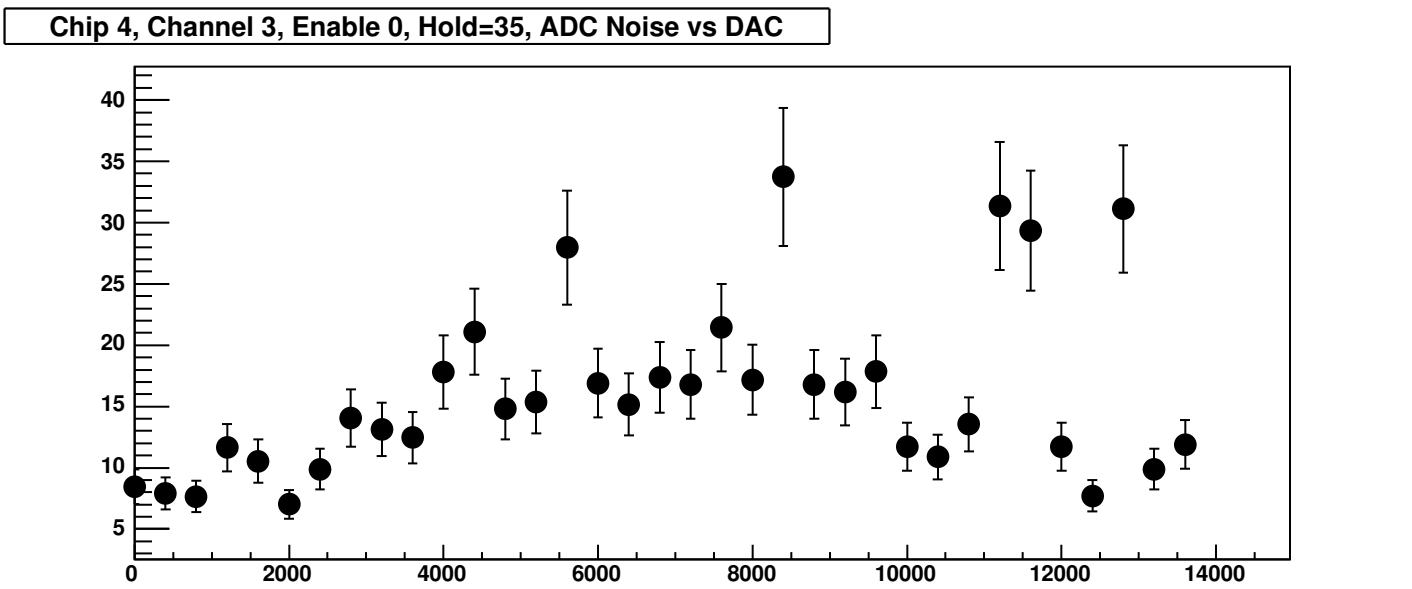
Chip 4, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC

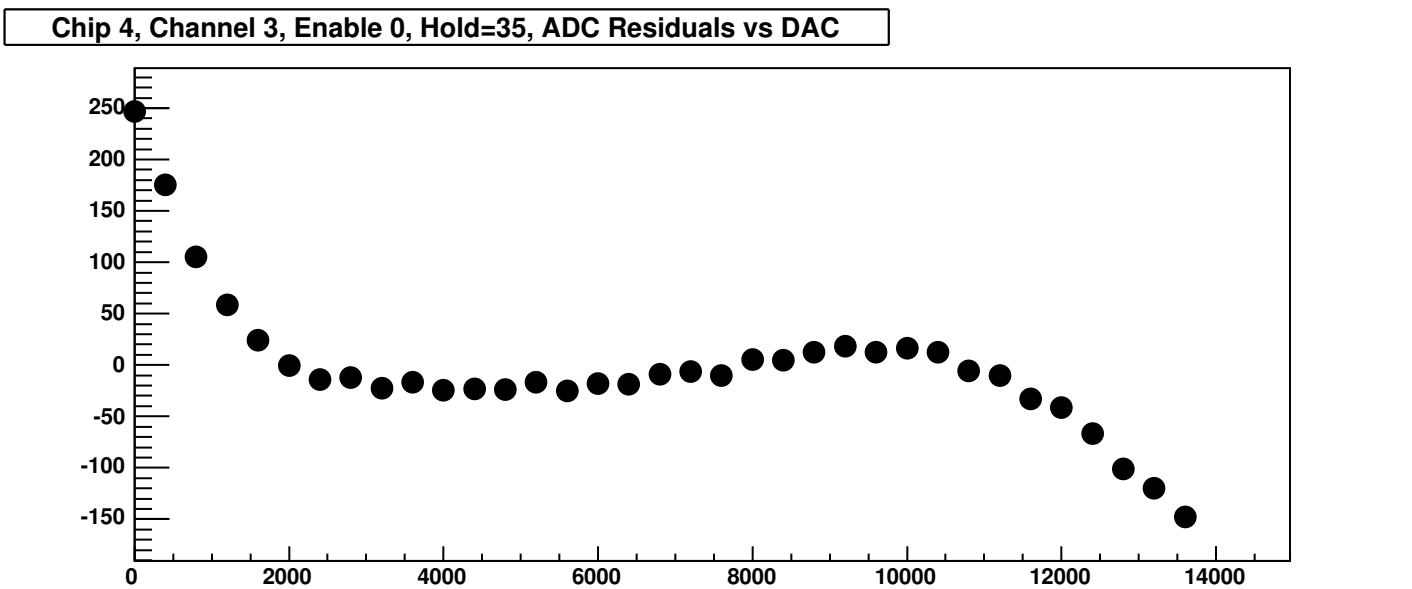


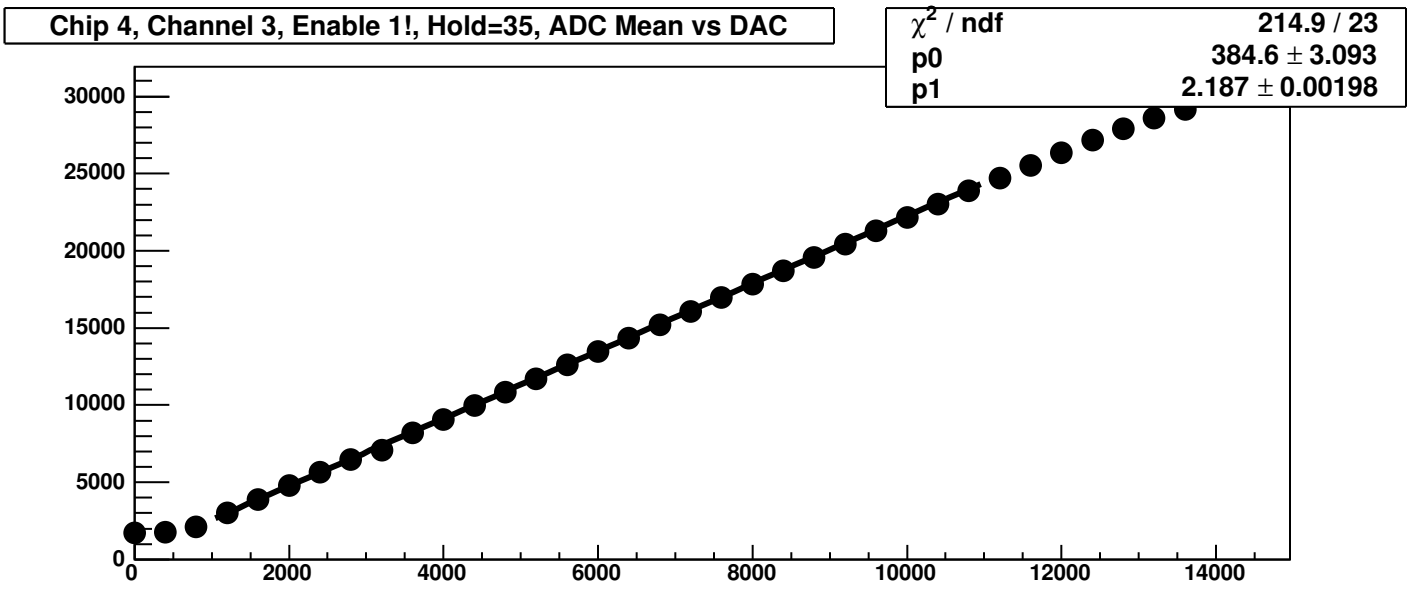
Chip 4, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

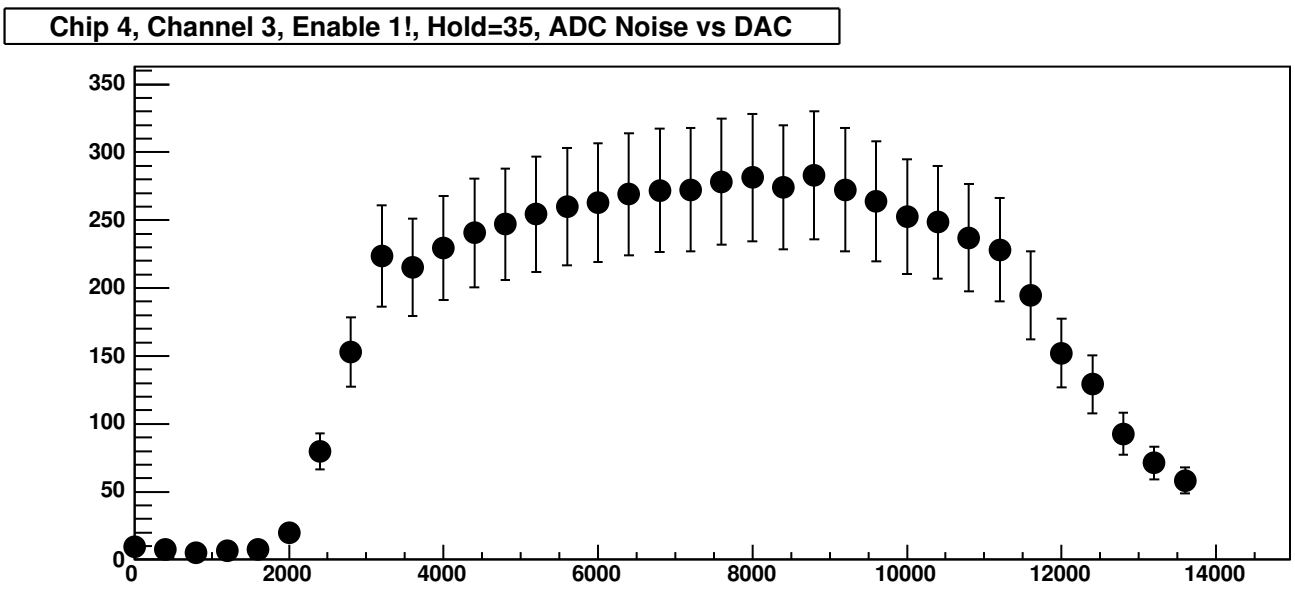


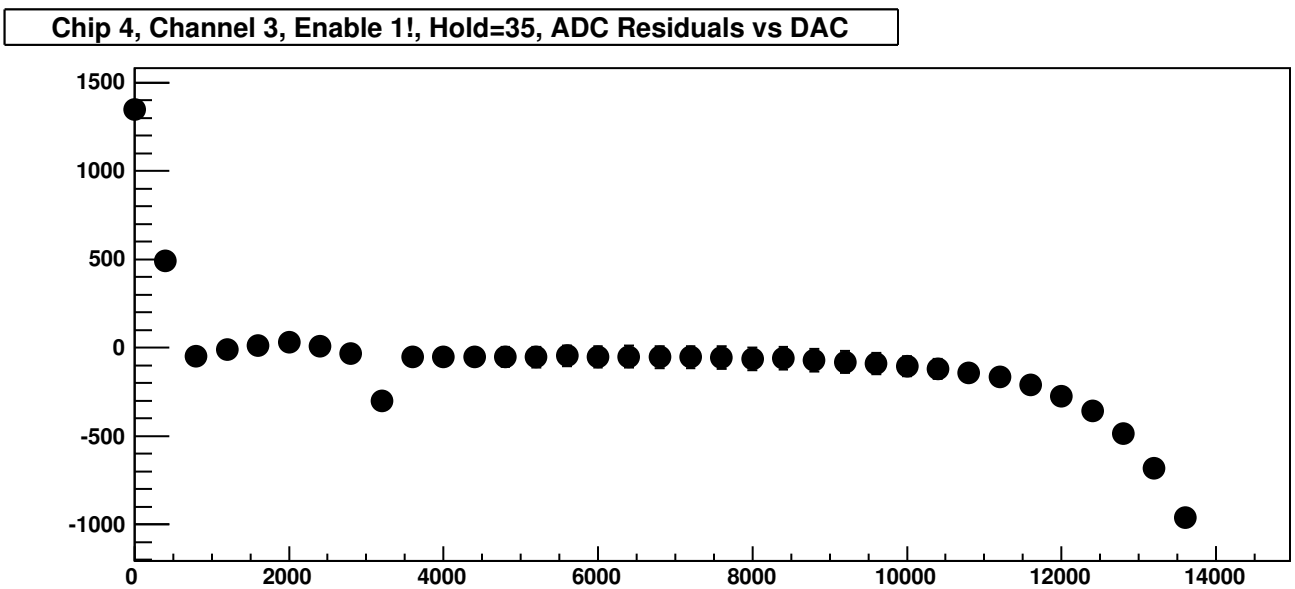

Chip 4, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1045 / 23
p0 61.08 ± 1.175
p1 0.1857 ± 0.0001924
2500
2000
1500
1000
500
0
2000
4000
6000
8000
10000
12000
14000

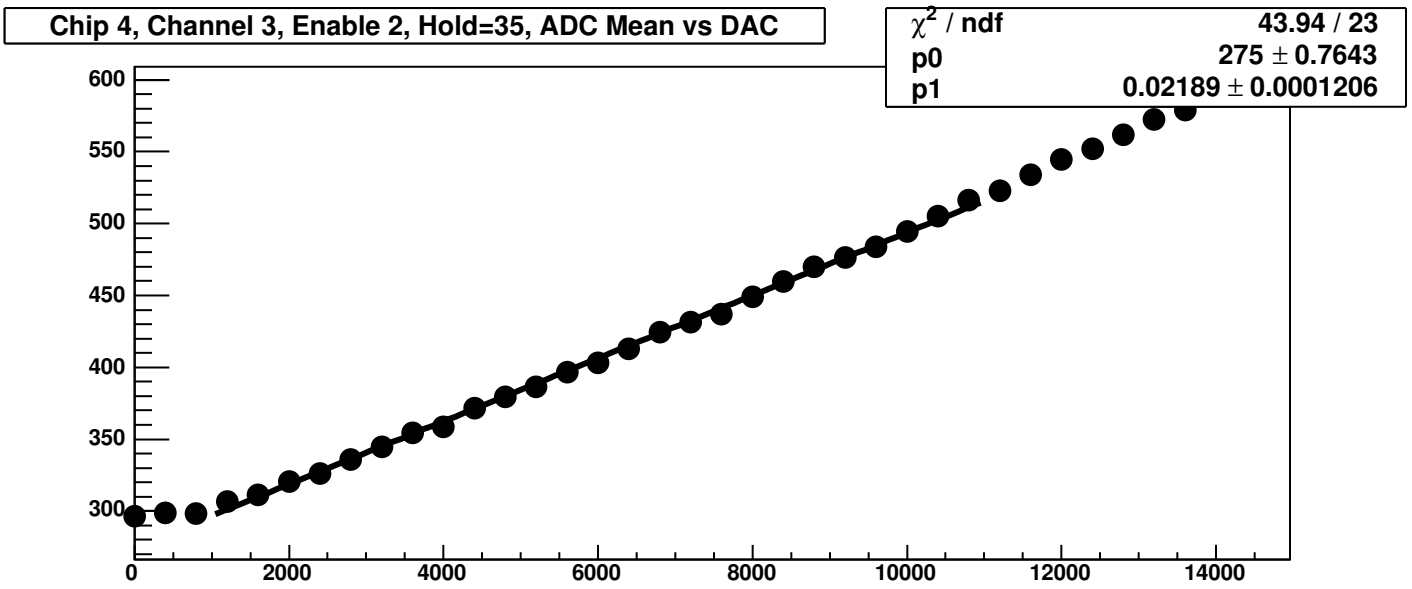

Chip 4, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

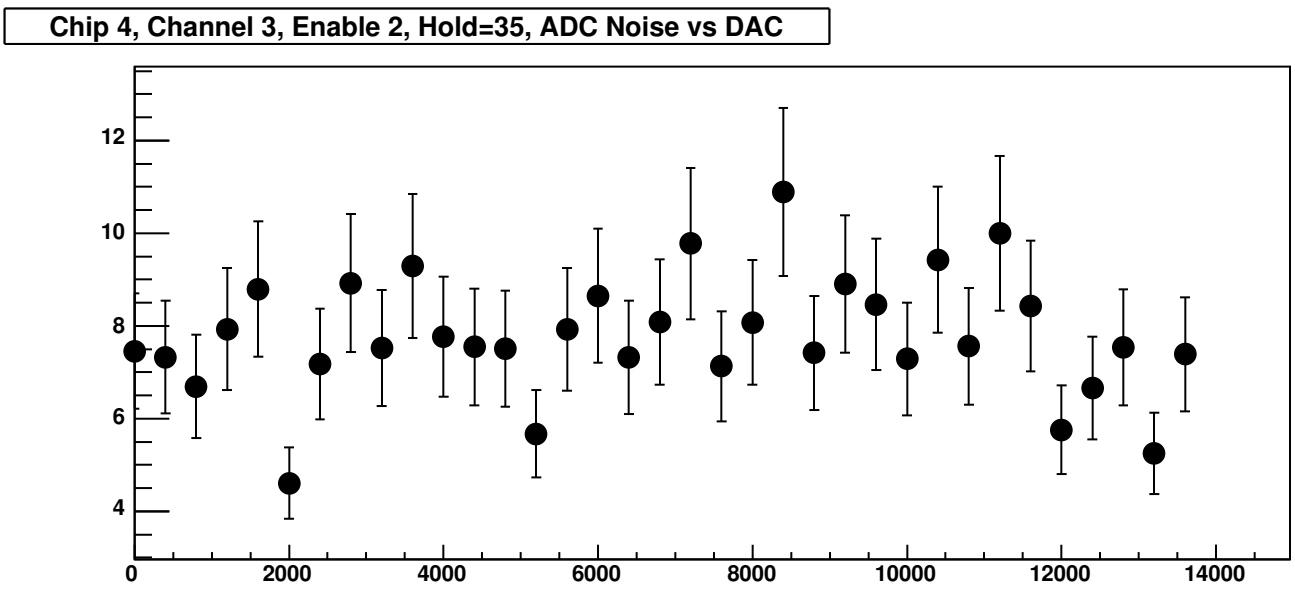

Chip 4, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

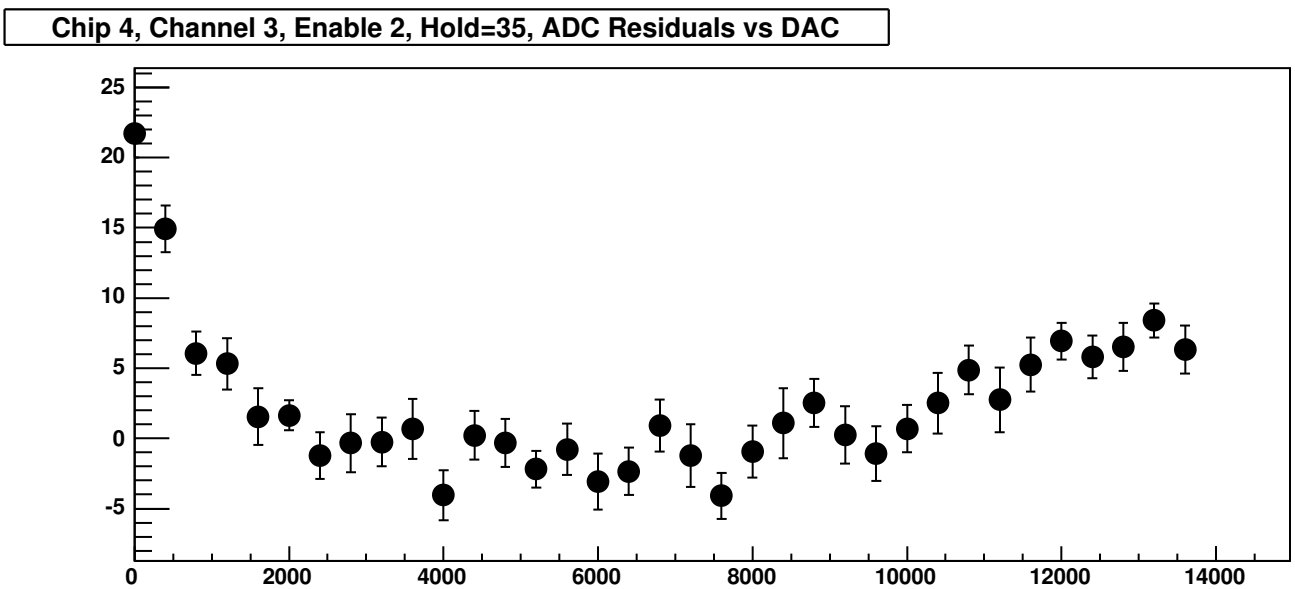

Chip 4, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 214.9 / 23
p0 384.6 ± 3.093
p1 2.187 ± 0.00198
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

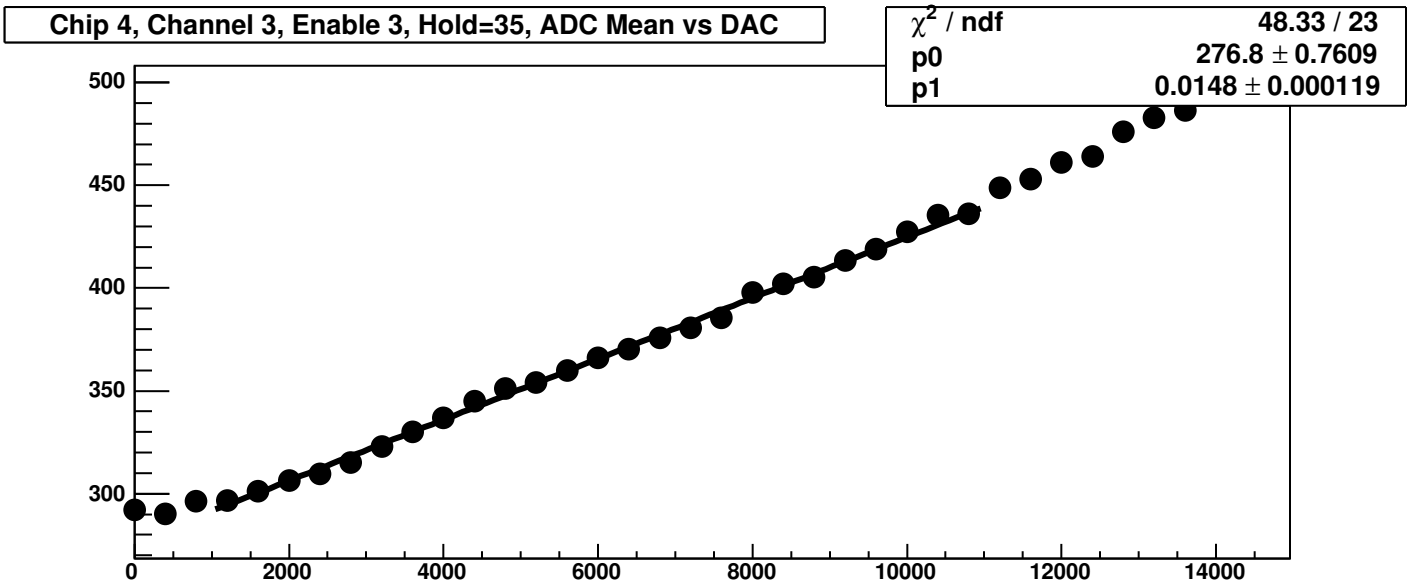

Chip 4, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

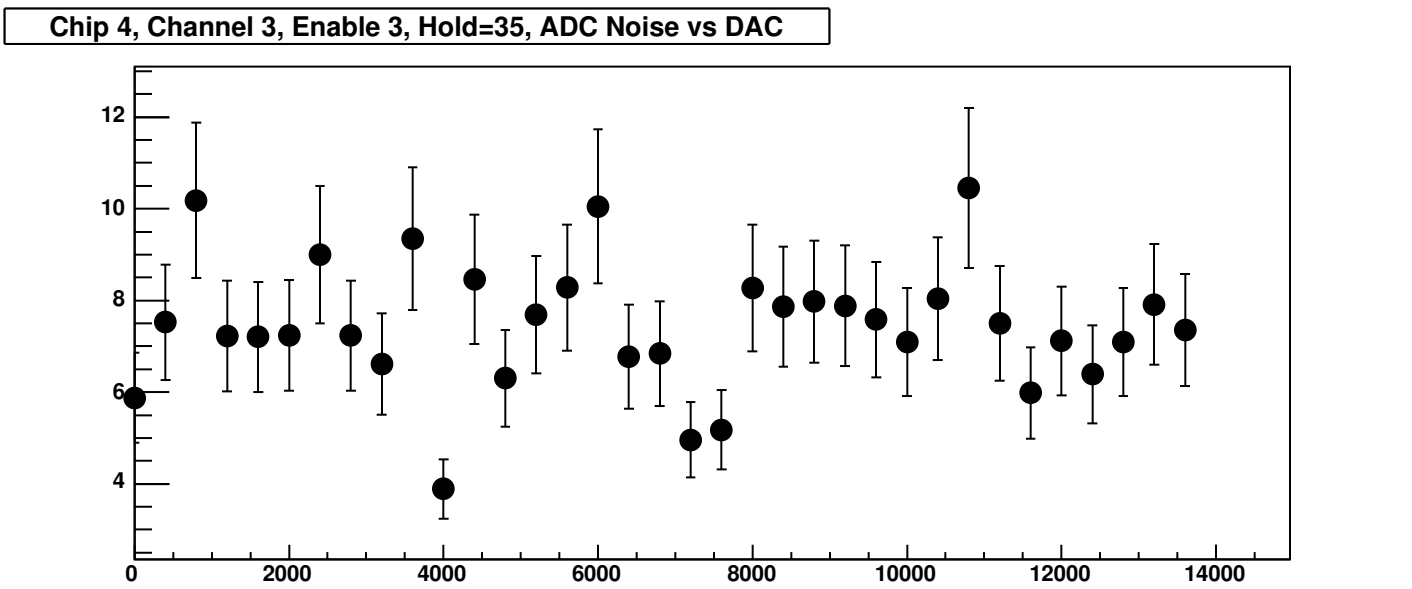

Chip 4, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

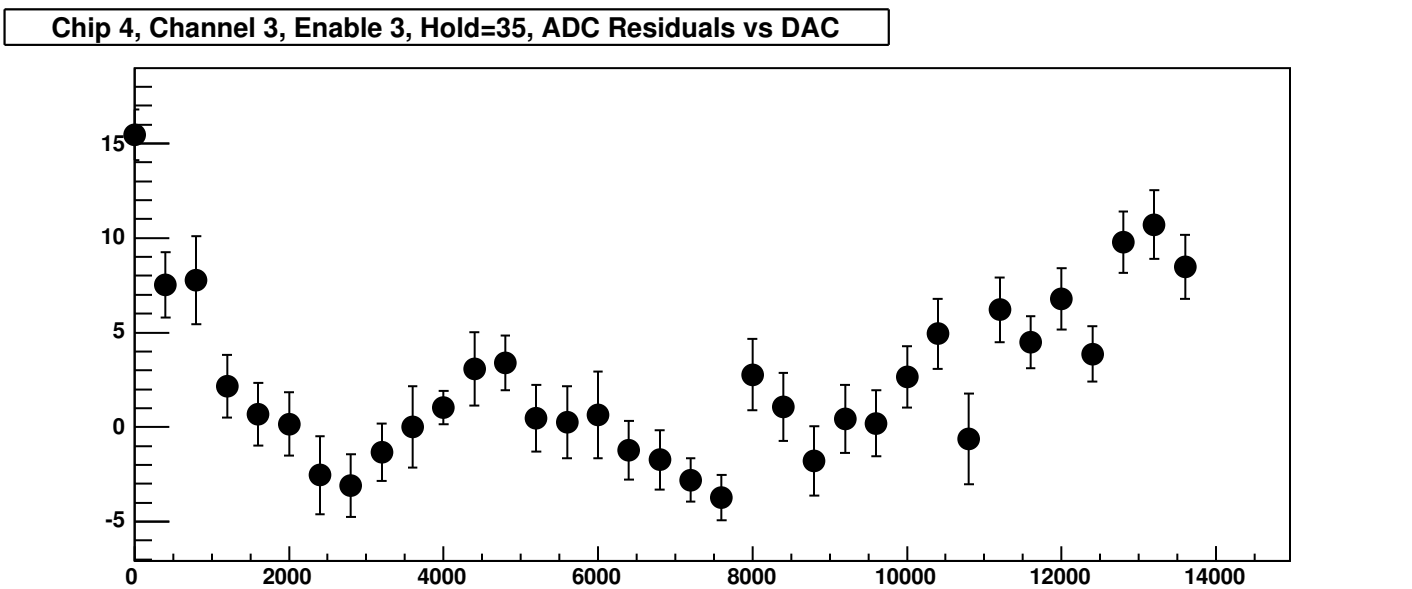

Chip 4, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 43.94 / 23
p0 275 ± 0.7643
p1 0.02189 ± 0.0001206
600
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

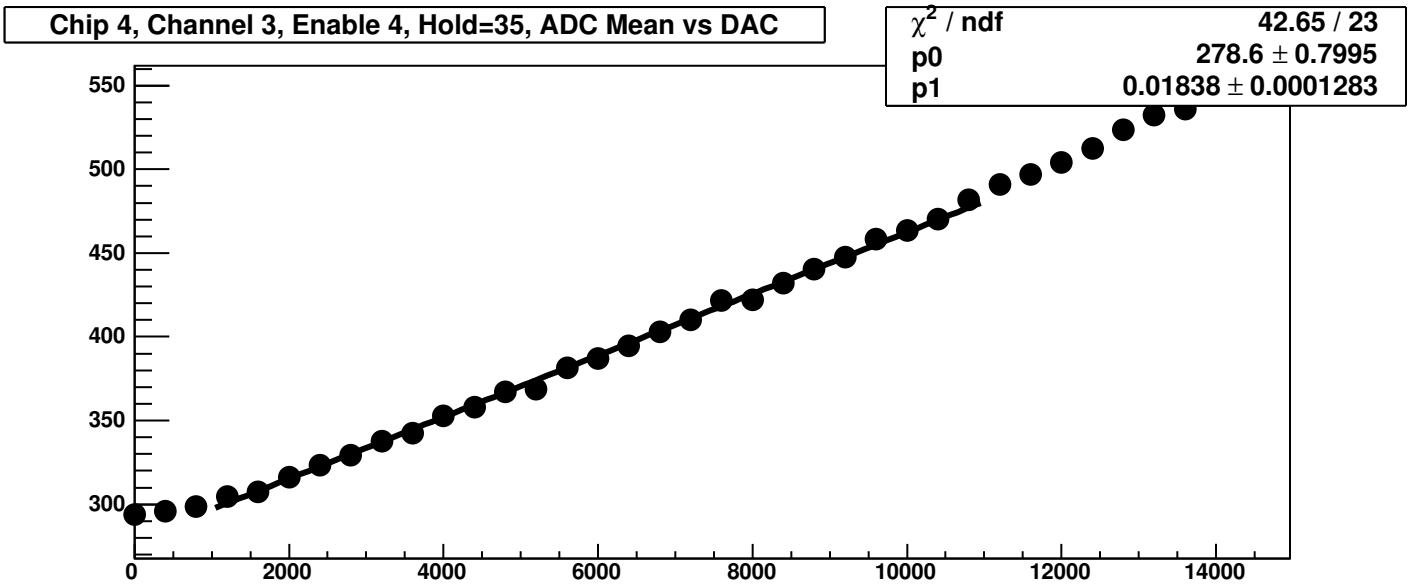

Chip 4, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

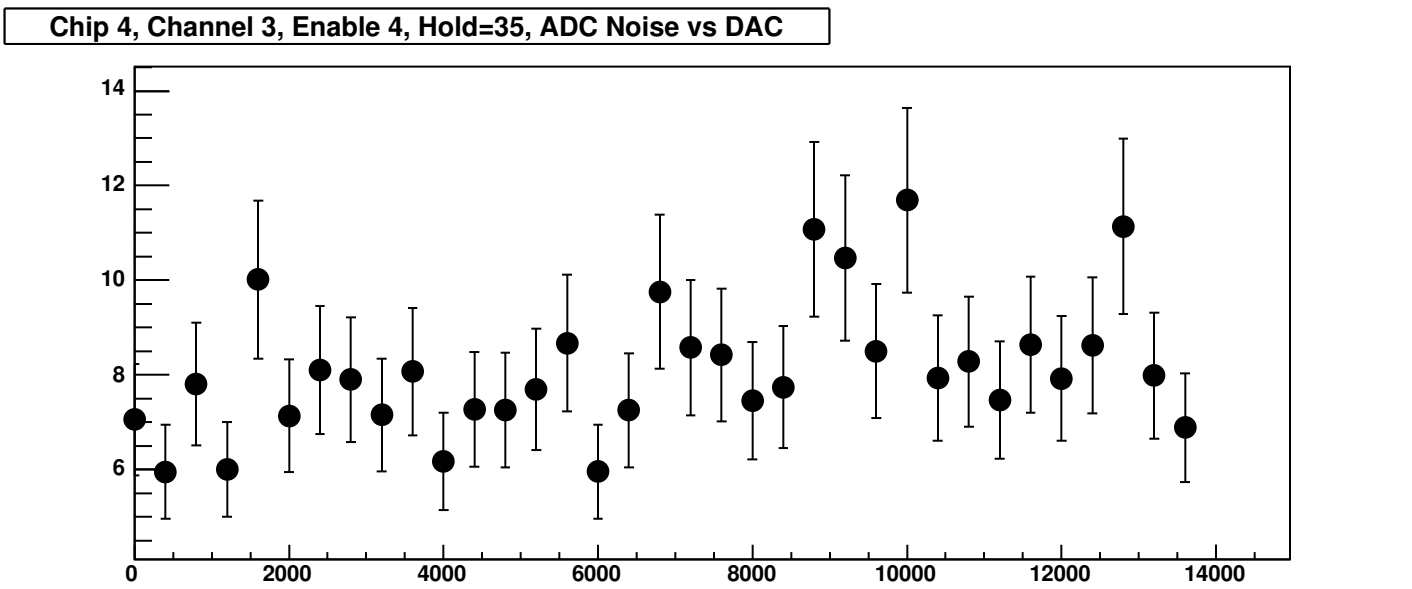

Chip 4, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

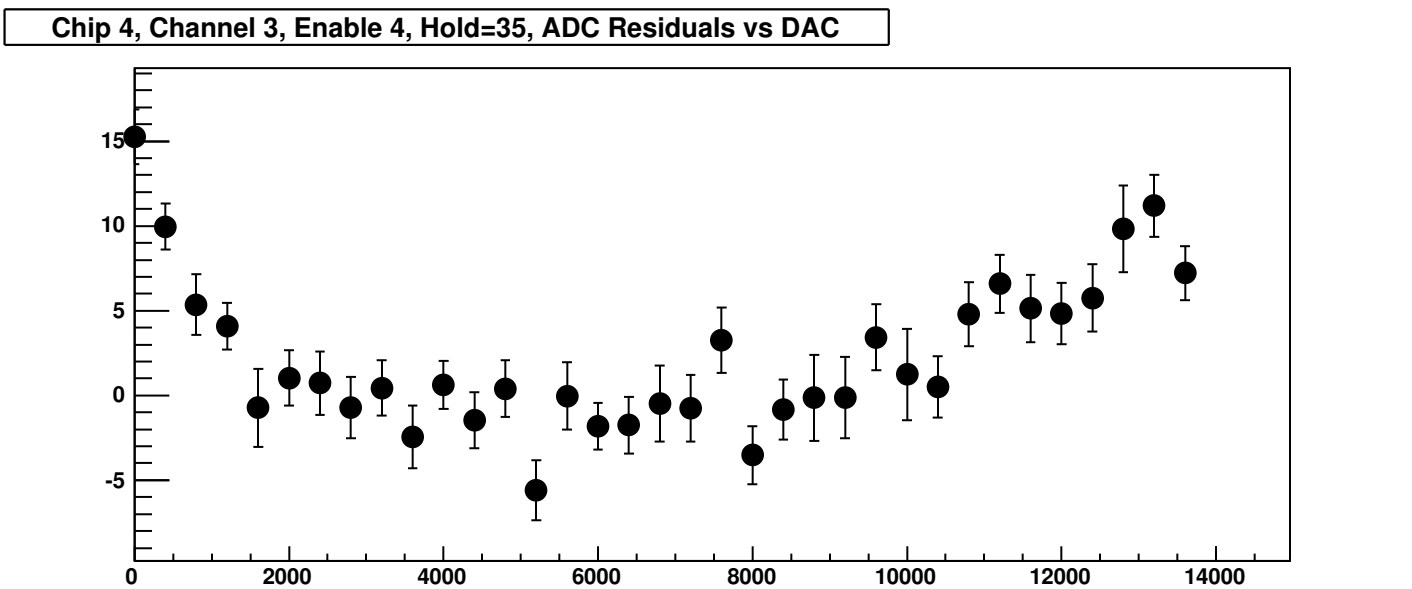

Chip 4, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 48.33 / 23
p0 276.8 ± 0.7609
p1 0.0148 ± 0.000119
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

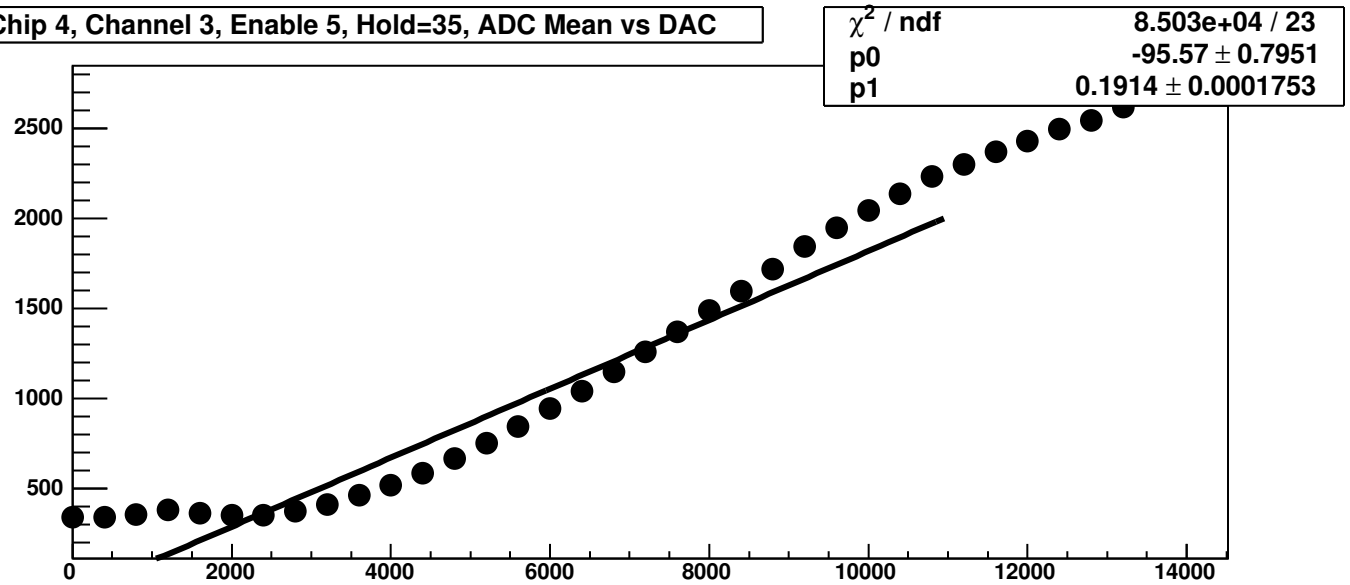

Chip 4, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 42.65 / 23
p0 278.6 ± 0.7995
p1 0.01838 ± 0.0001283
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

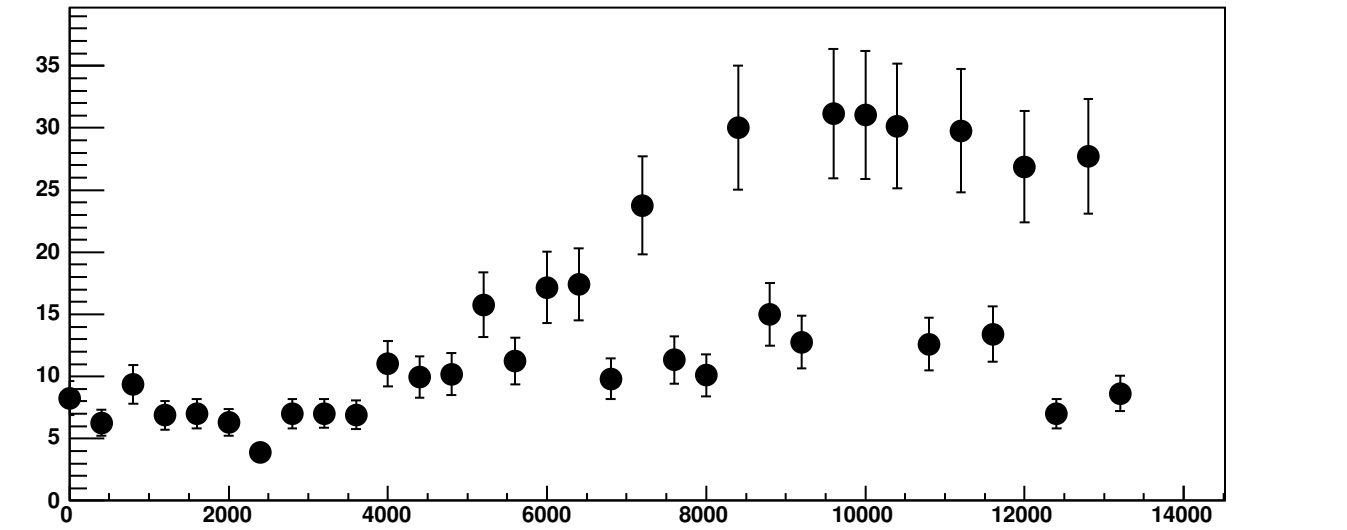

Chip 4, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

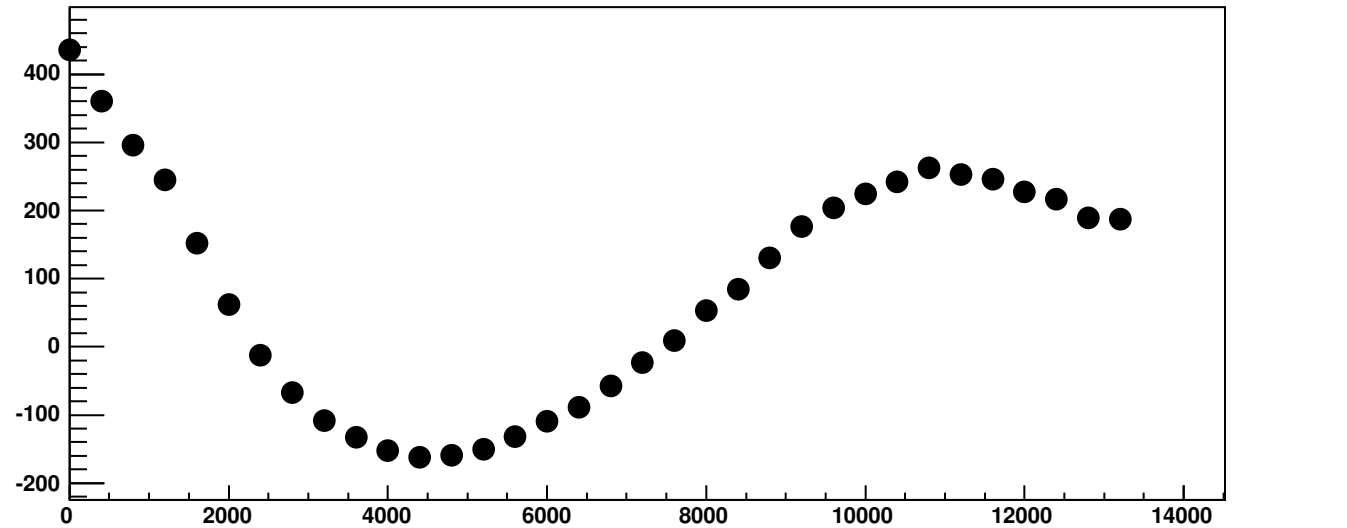
Chip 4, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

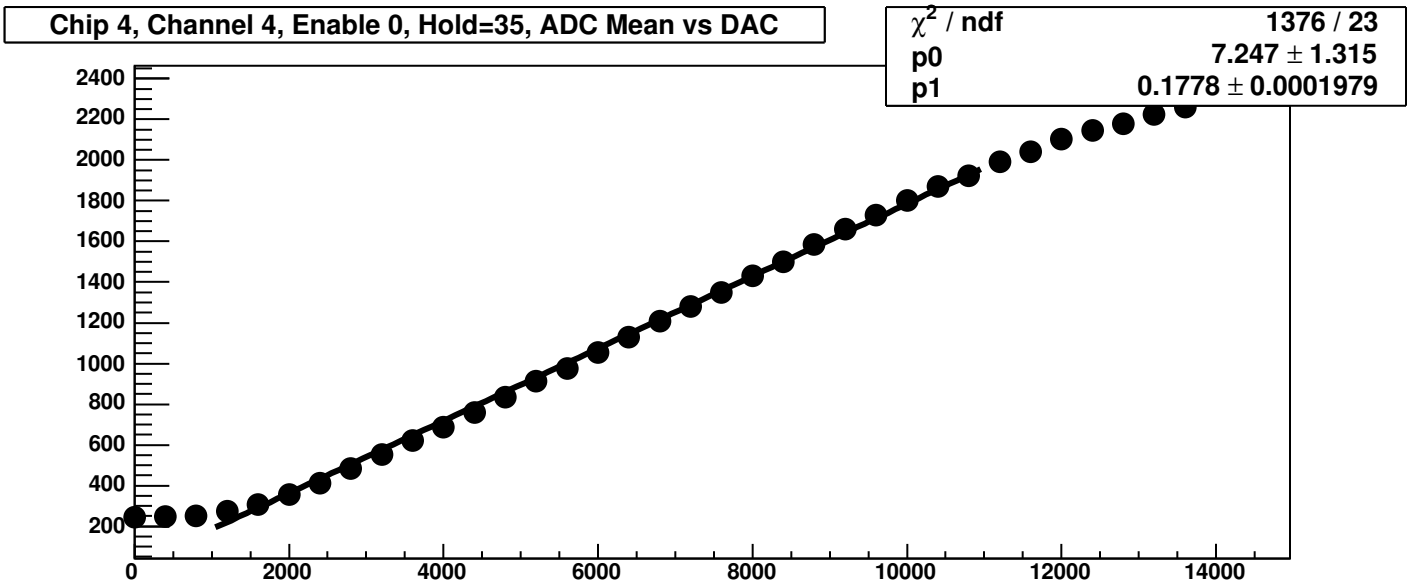


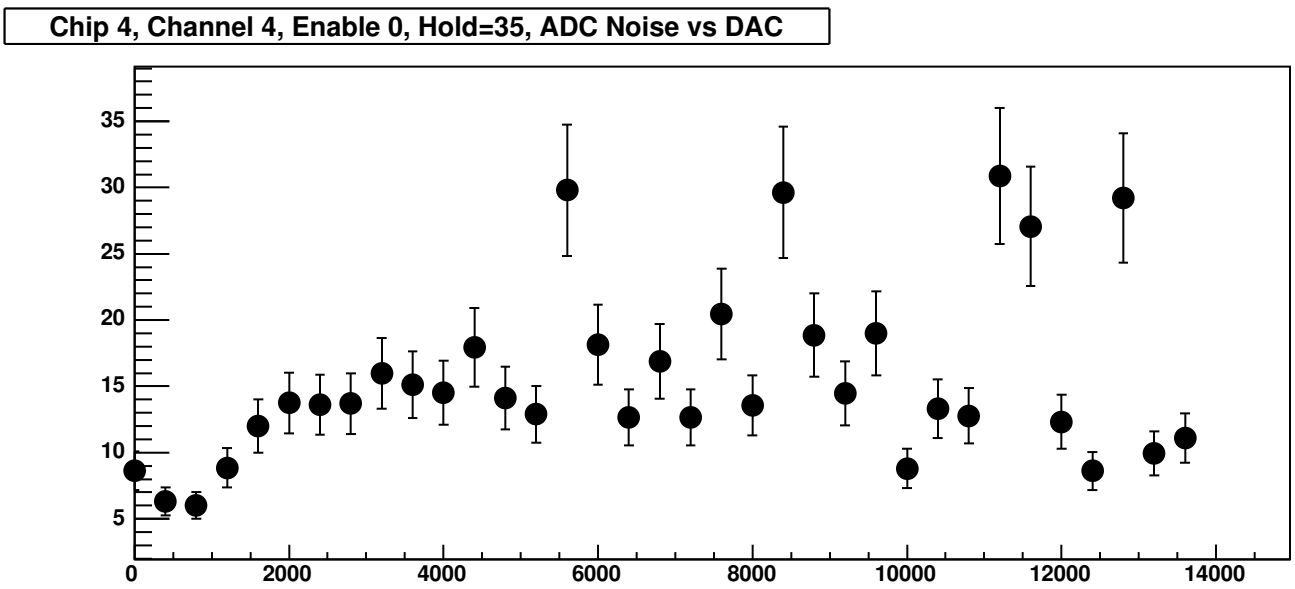
Chip 4, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC

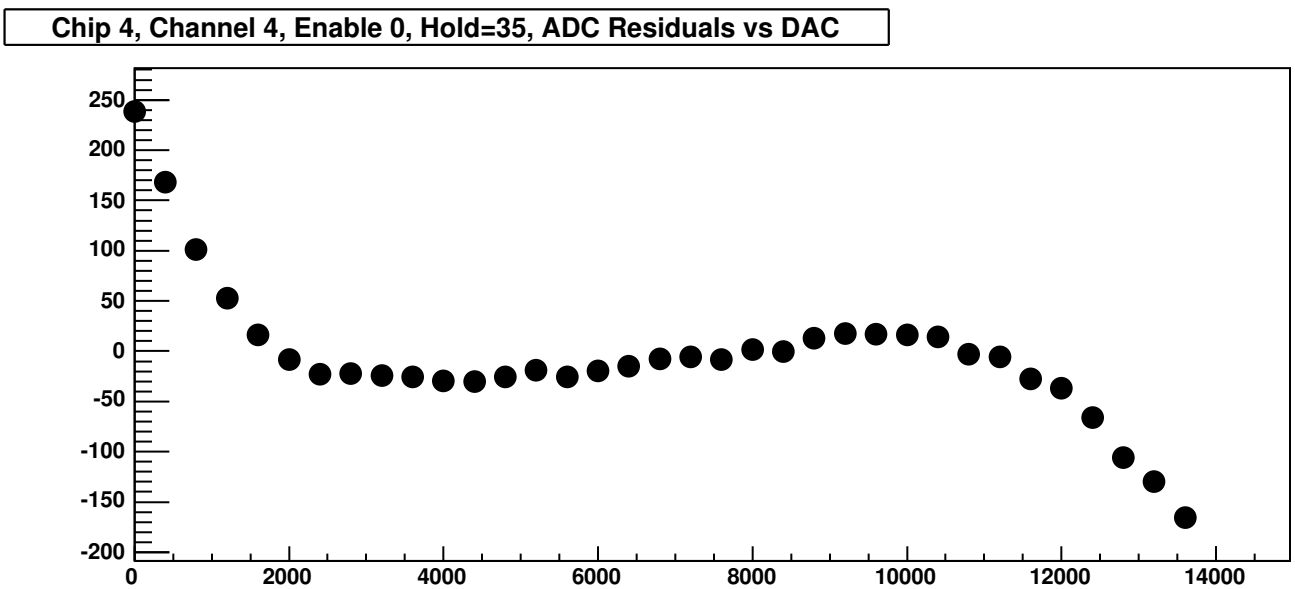


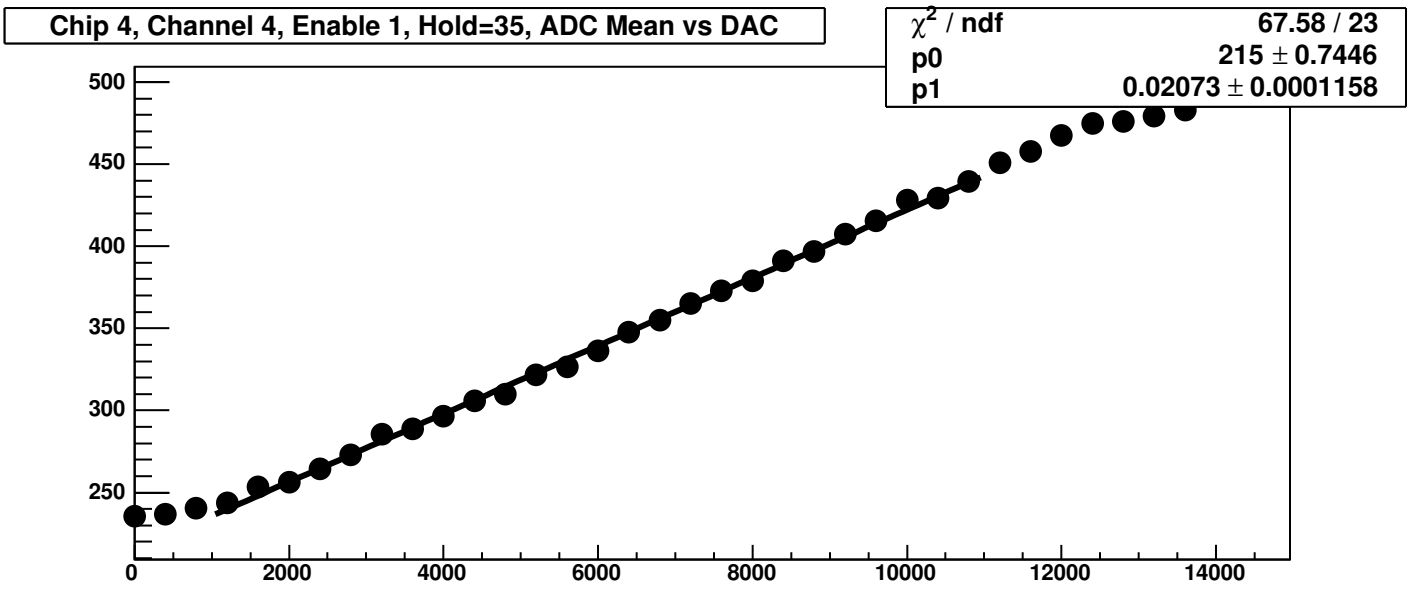
Chip 4, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

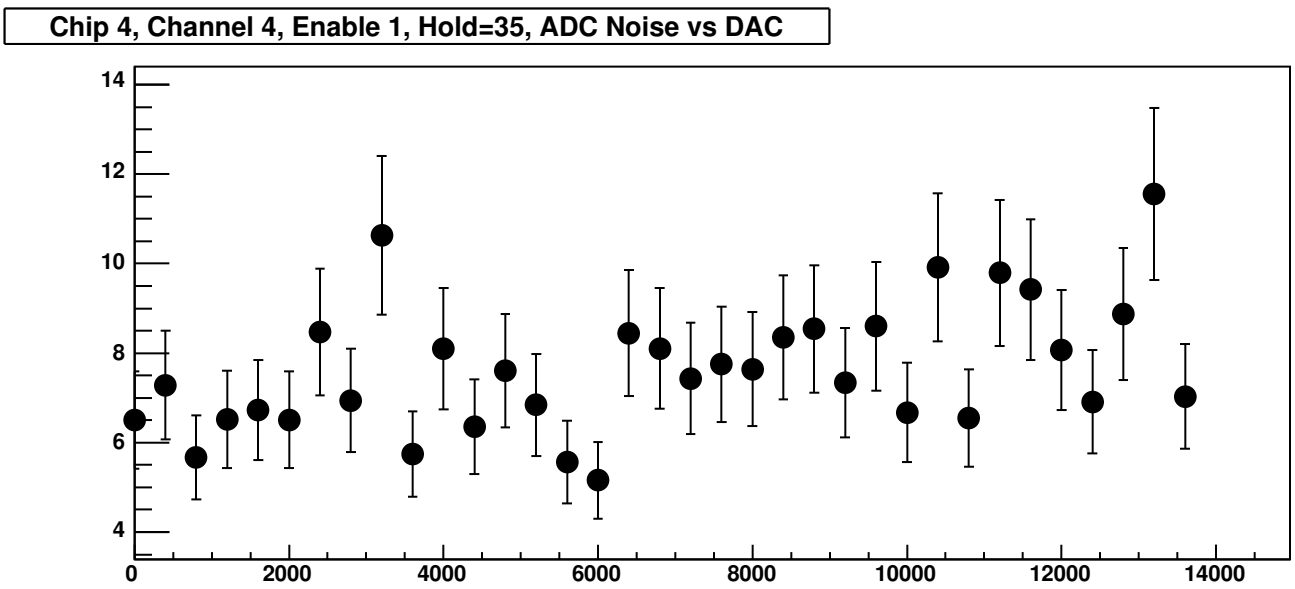


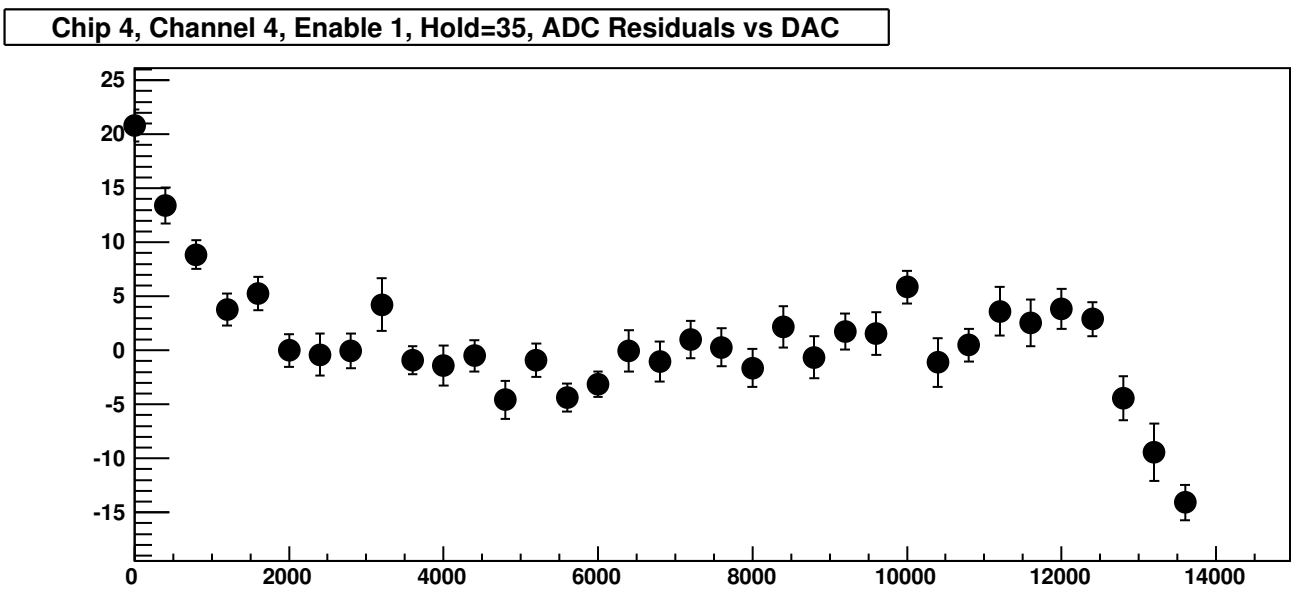

Chip 4, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1376 / 23
p0 7.247 ± 1.315
p1 0.1778 ± 0.0001979
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

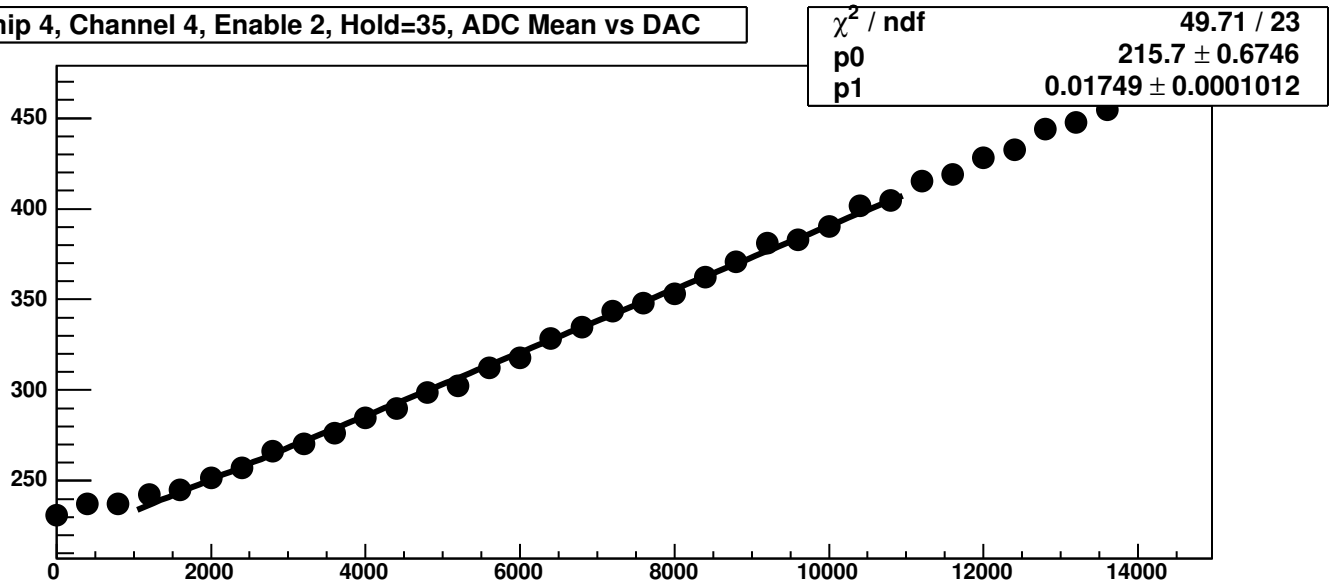

Chip 4, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 67.58 / 23
p0 215 ± 0.7446
p1 0.02073 ± 0.0001158
500
450
400
350
300
250
0
2000
4000
6000
8000
10000
12000
14000

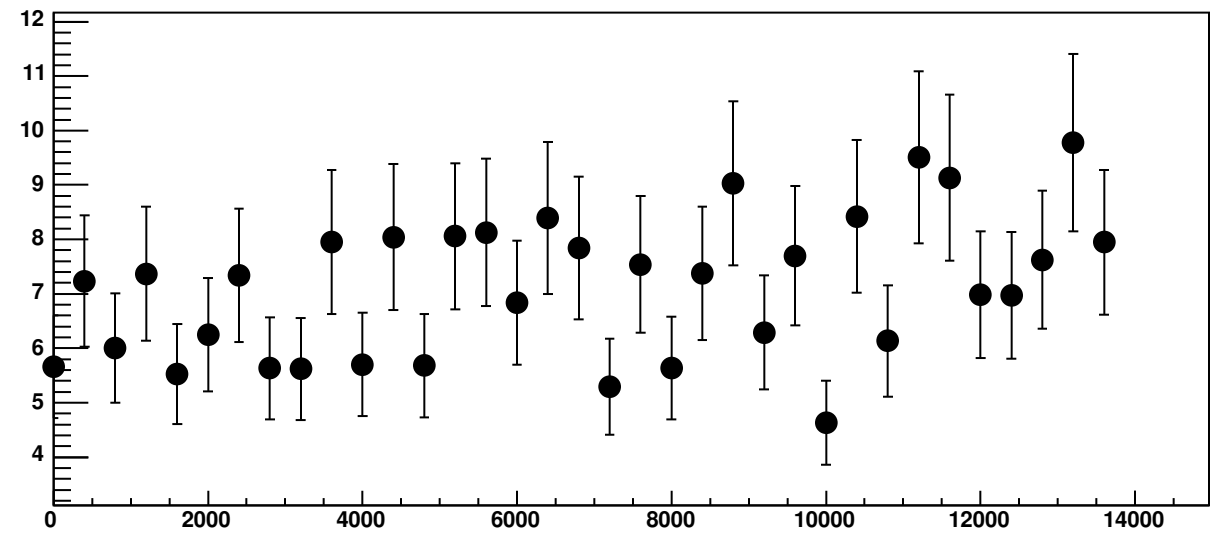

Chip 4, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

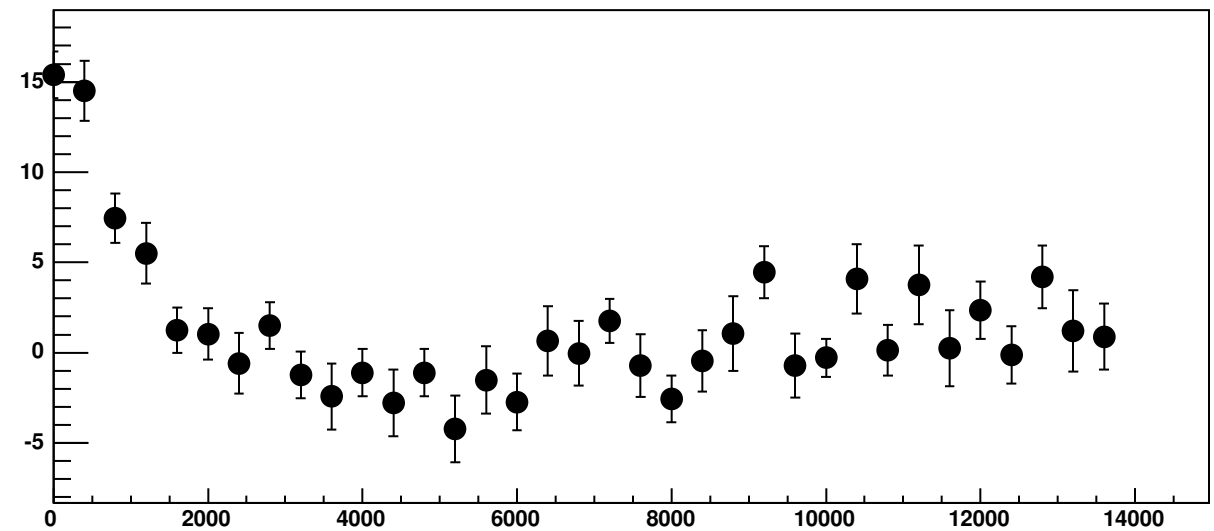
Chip 4, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

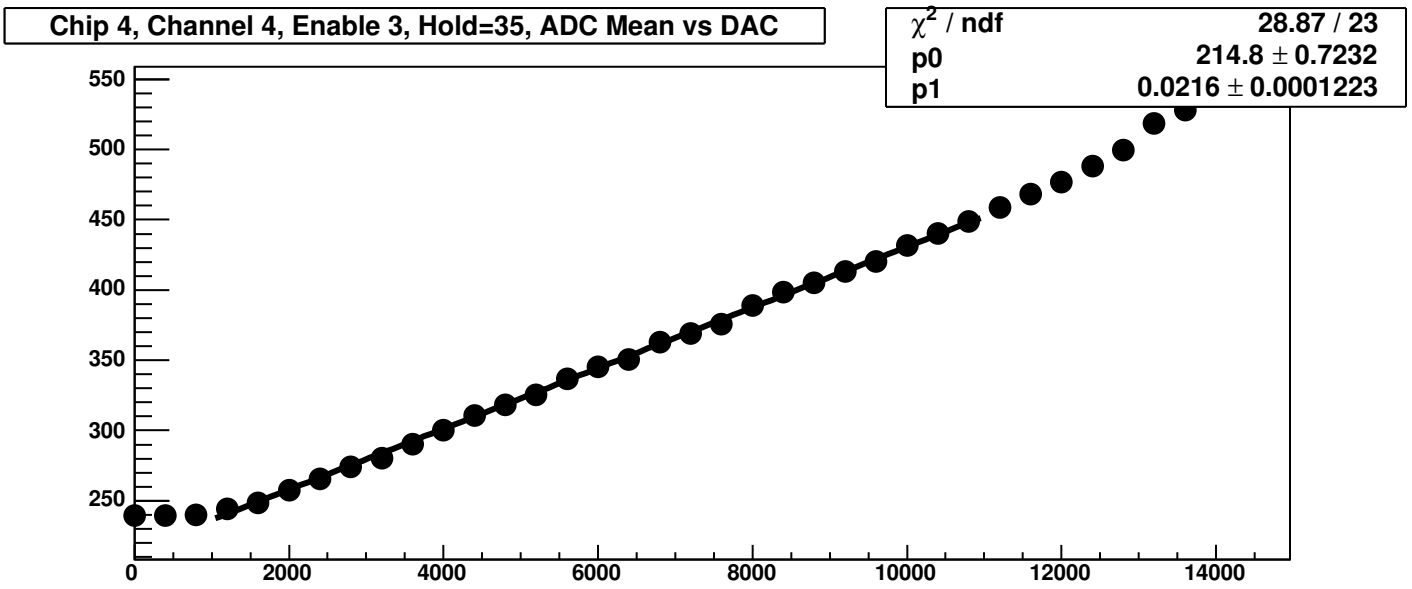


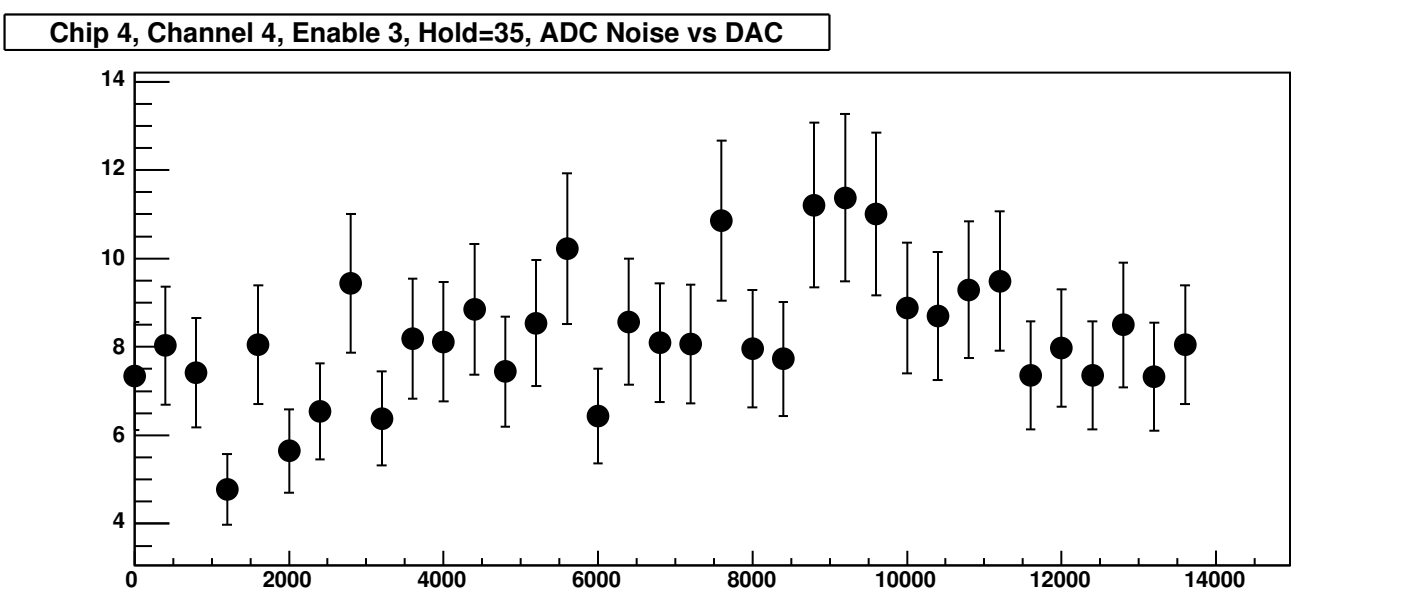
Chip 4, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC

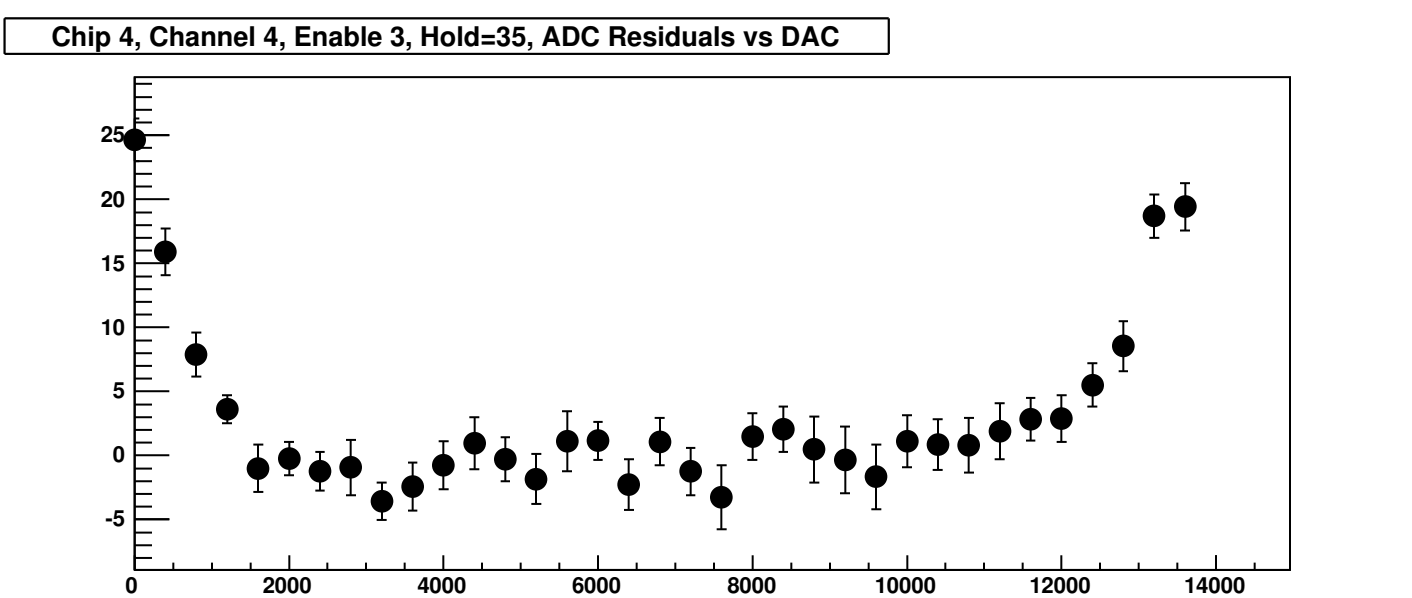


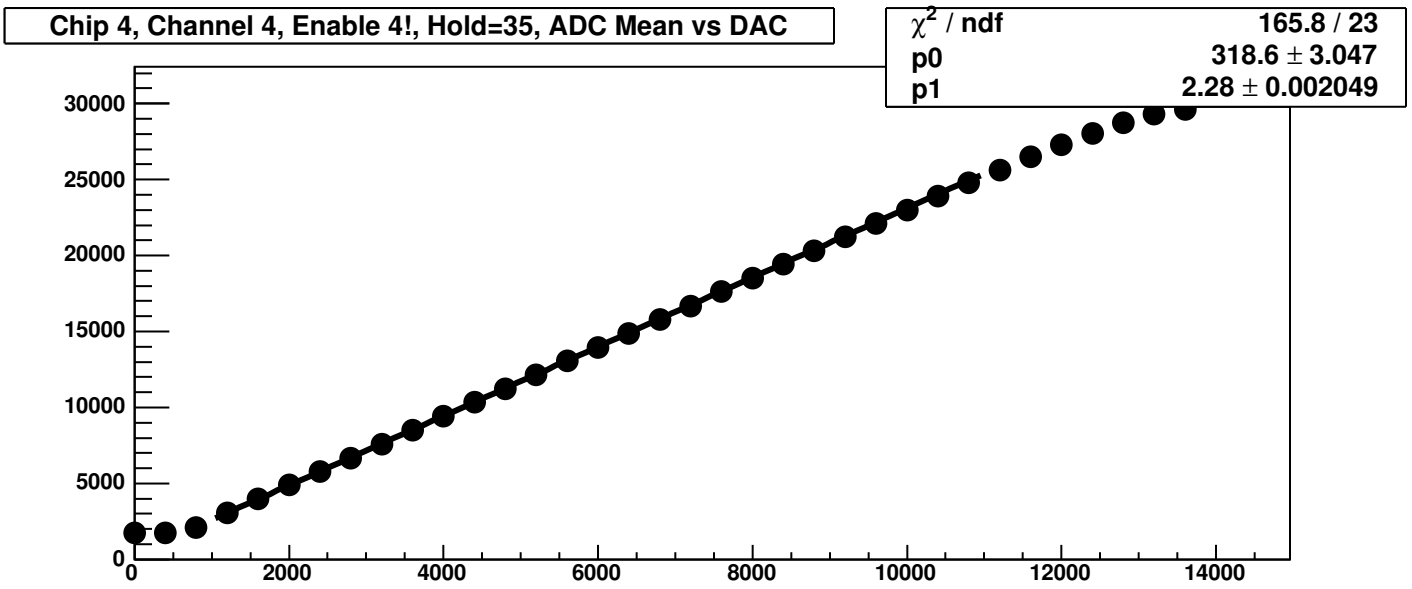
Chip 4, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

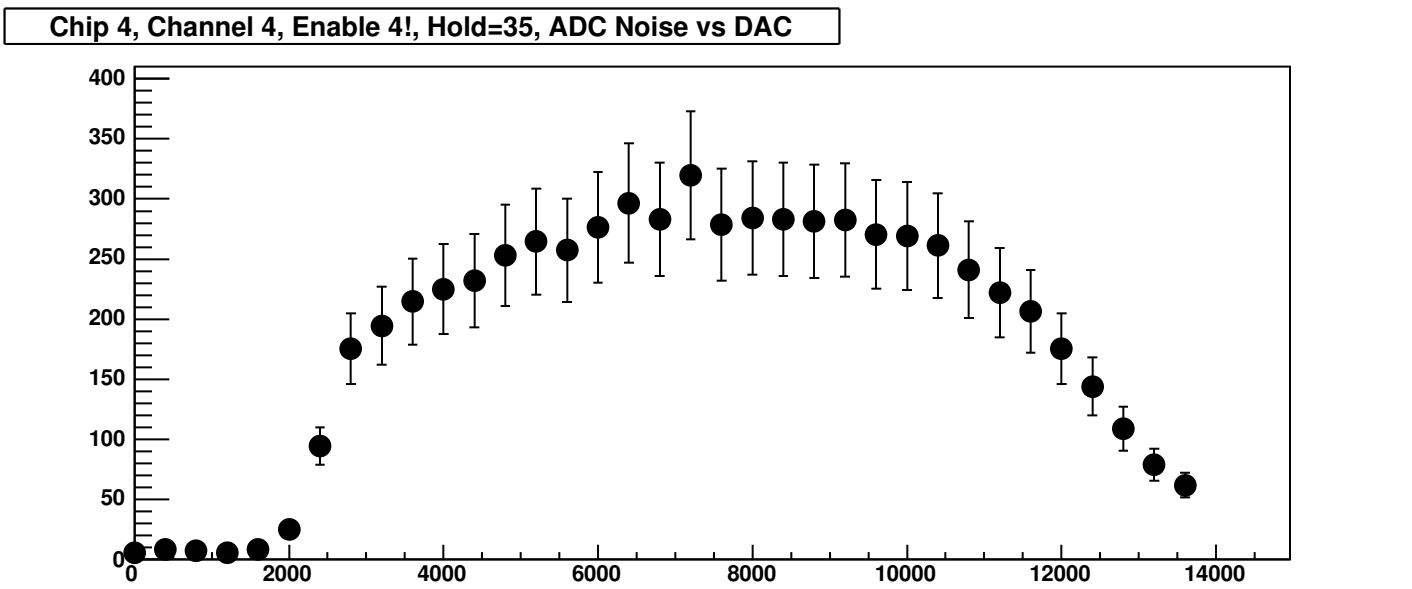


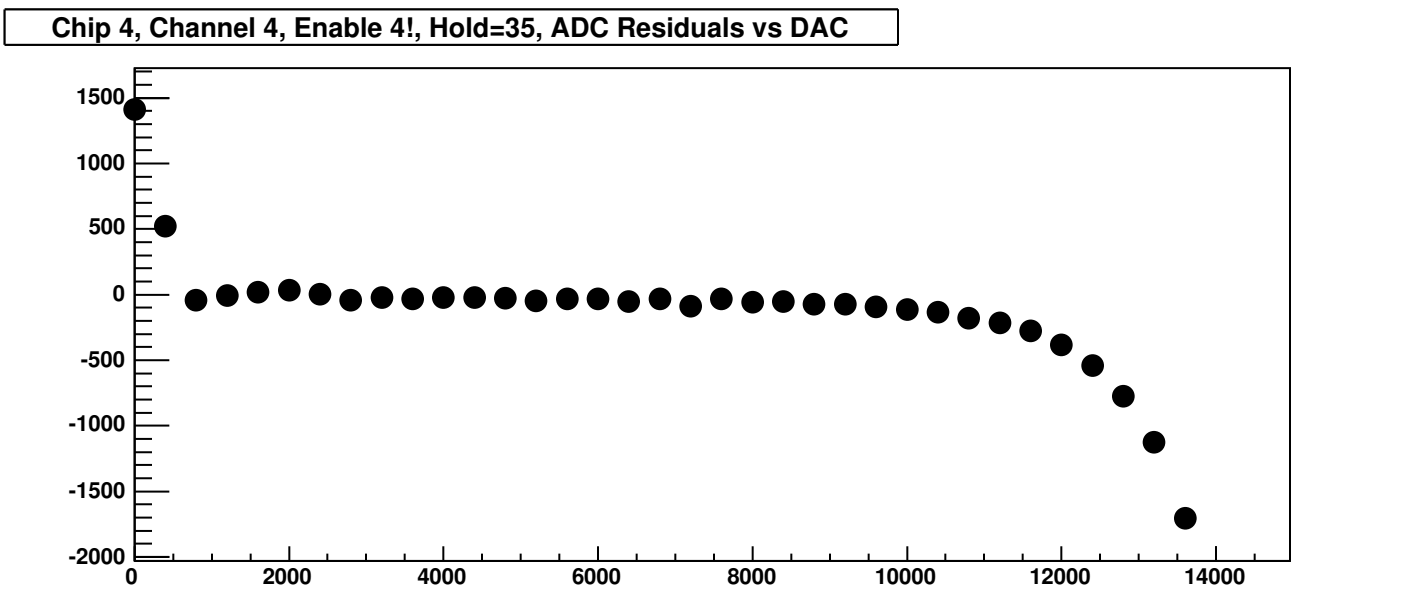

Chip 4, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.87 / 23
p0 214.8 ± 0.7232
p1 0.0216 ± 0.0001223
550
500
450
400
350
300
250
0
2000
4000
6000
8000
10000
12000
14000

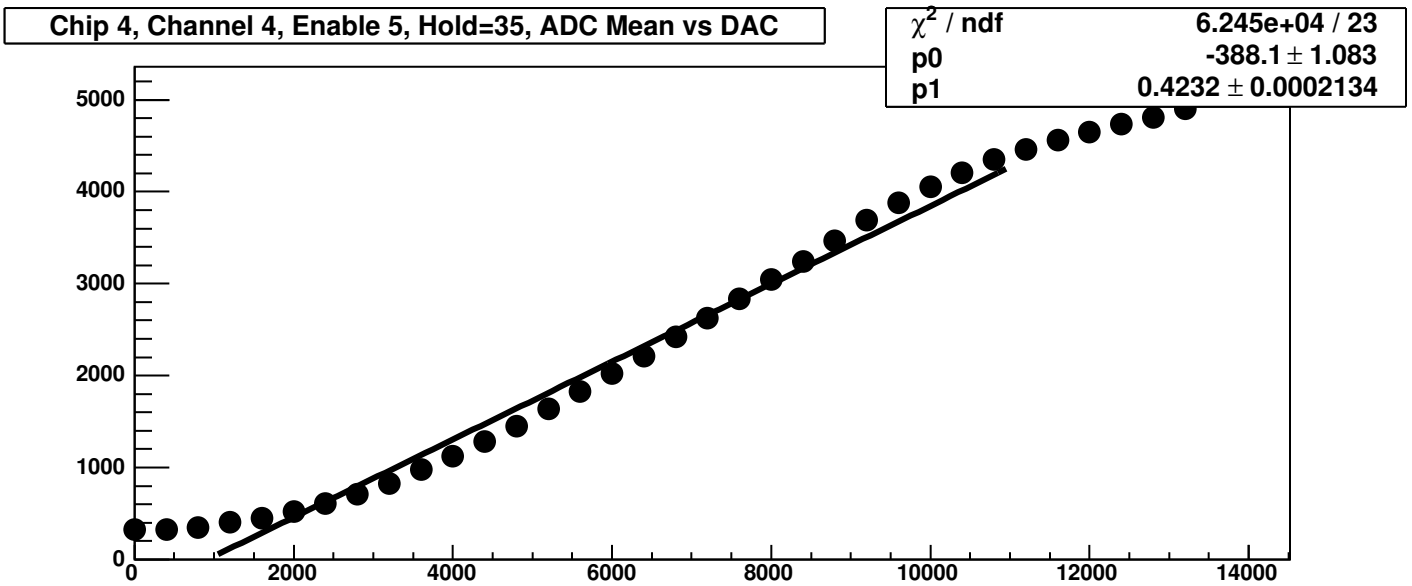

Chip 4, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

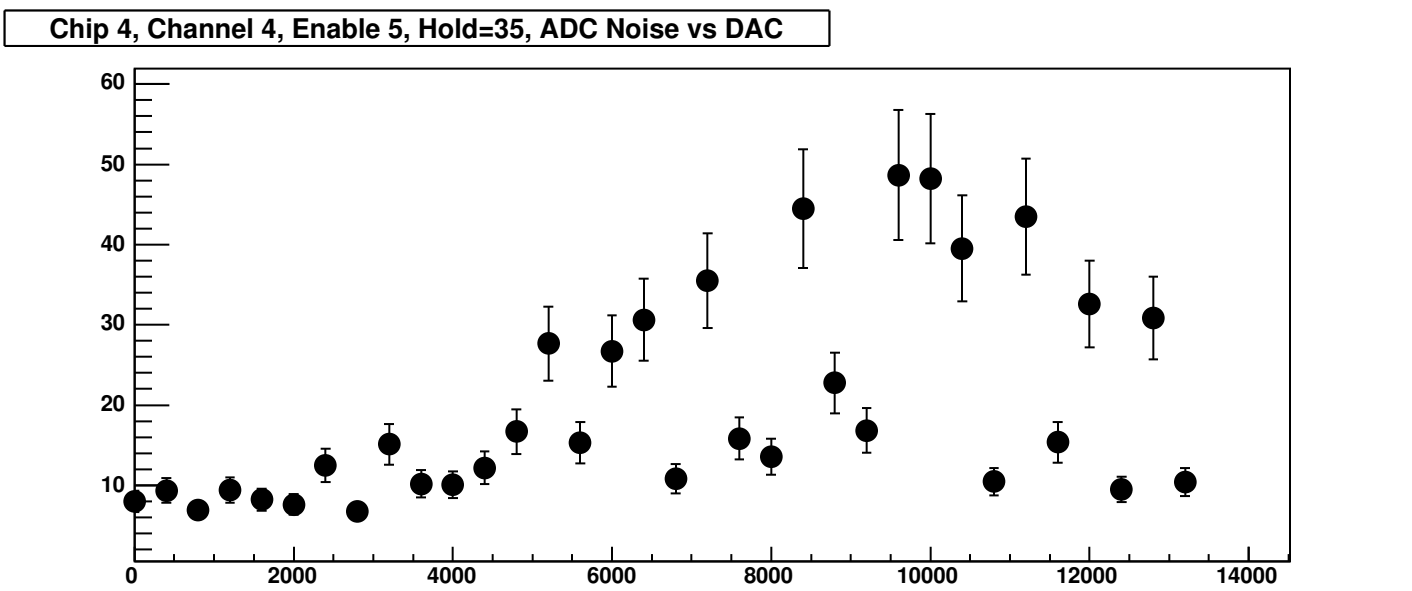

Chip 4, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

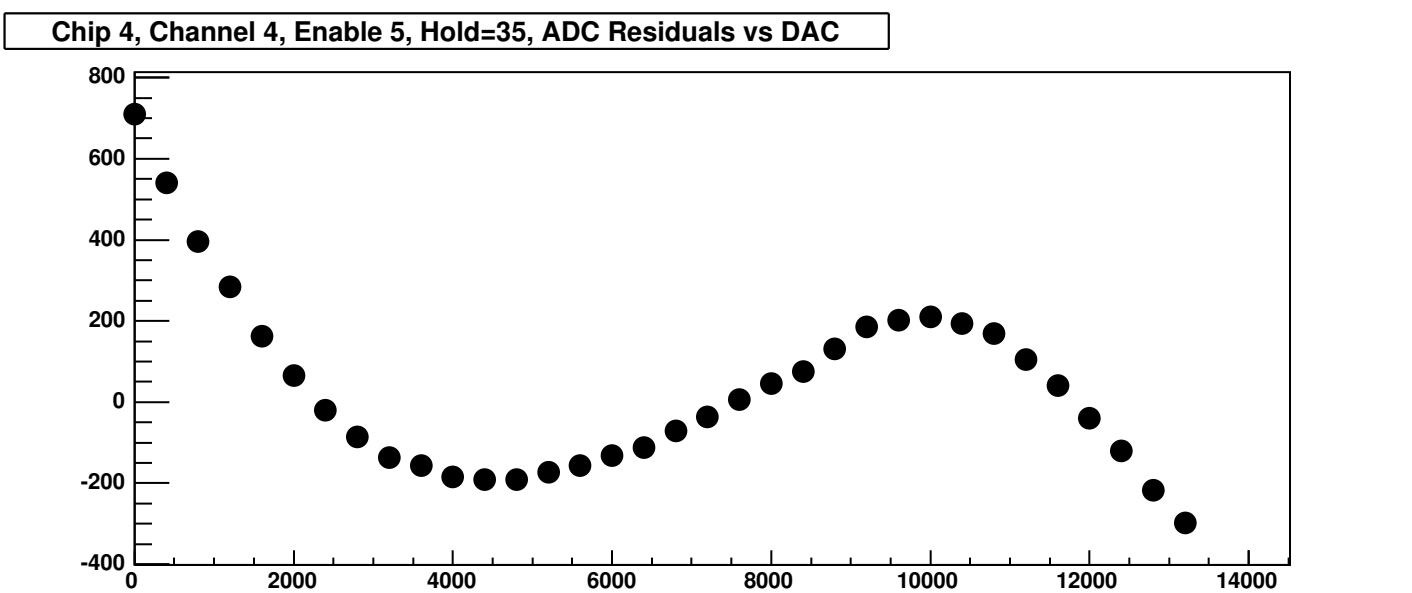

Chip 4, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 165.8 / 23
p0 318.6 ± 3.047
p1 2.28 ± 0.002049
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

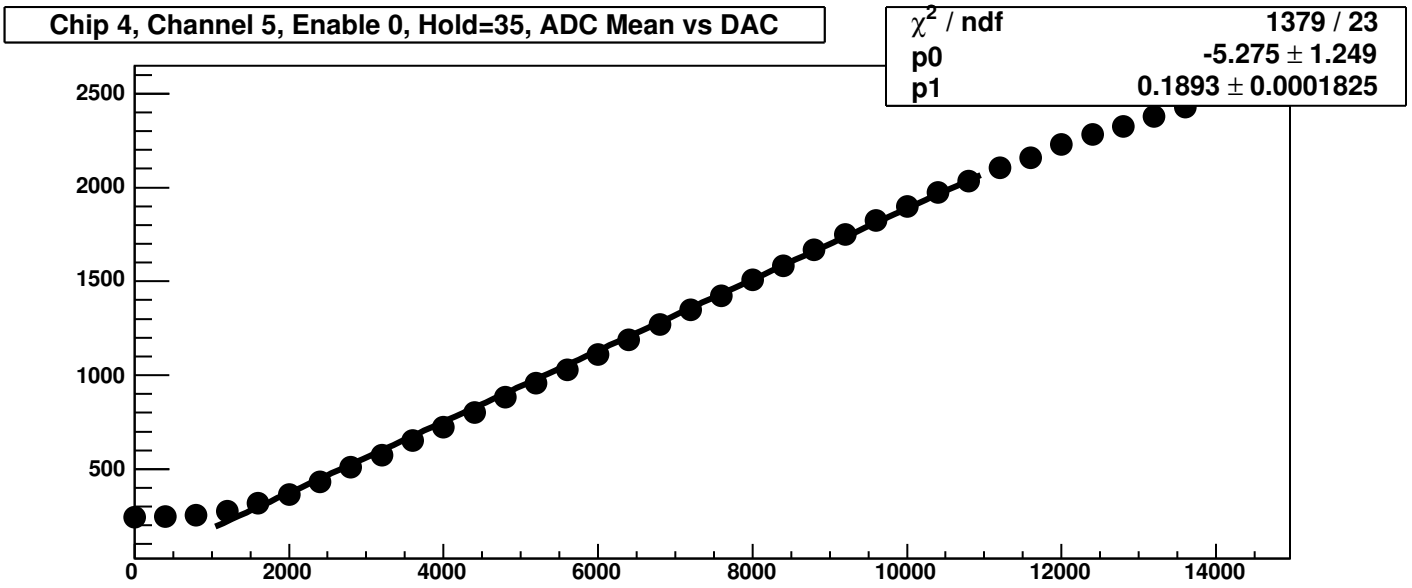

Chip 4, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

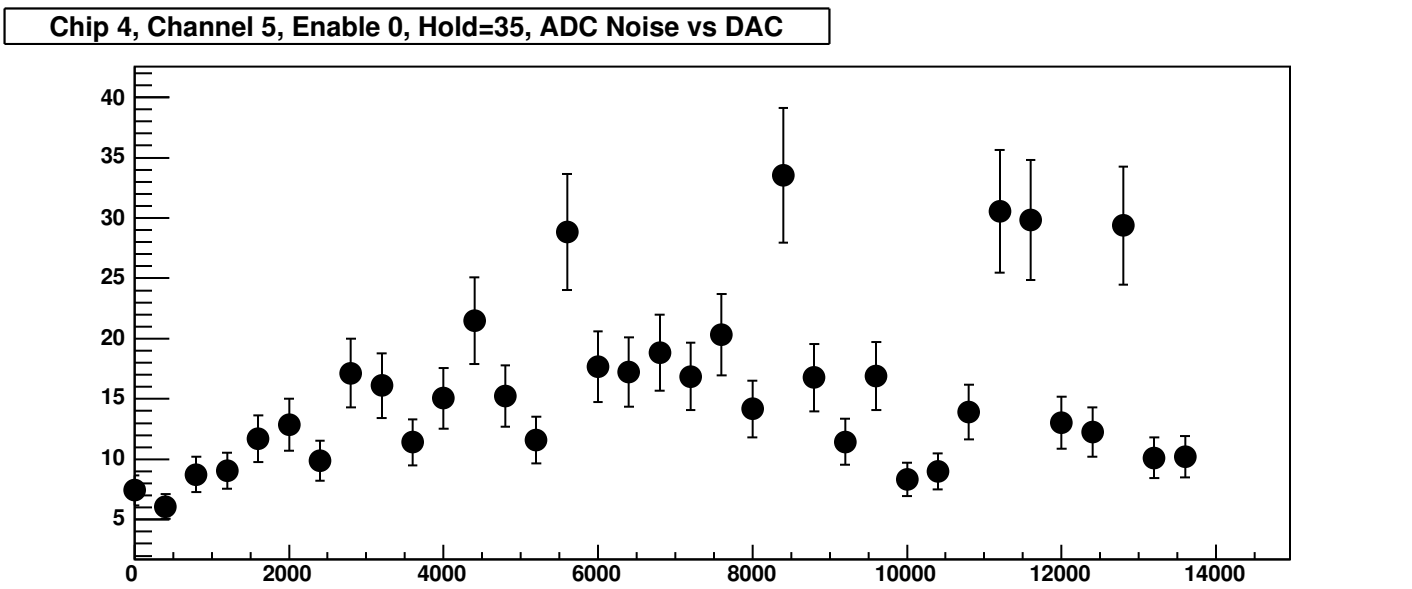

Chip 4, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

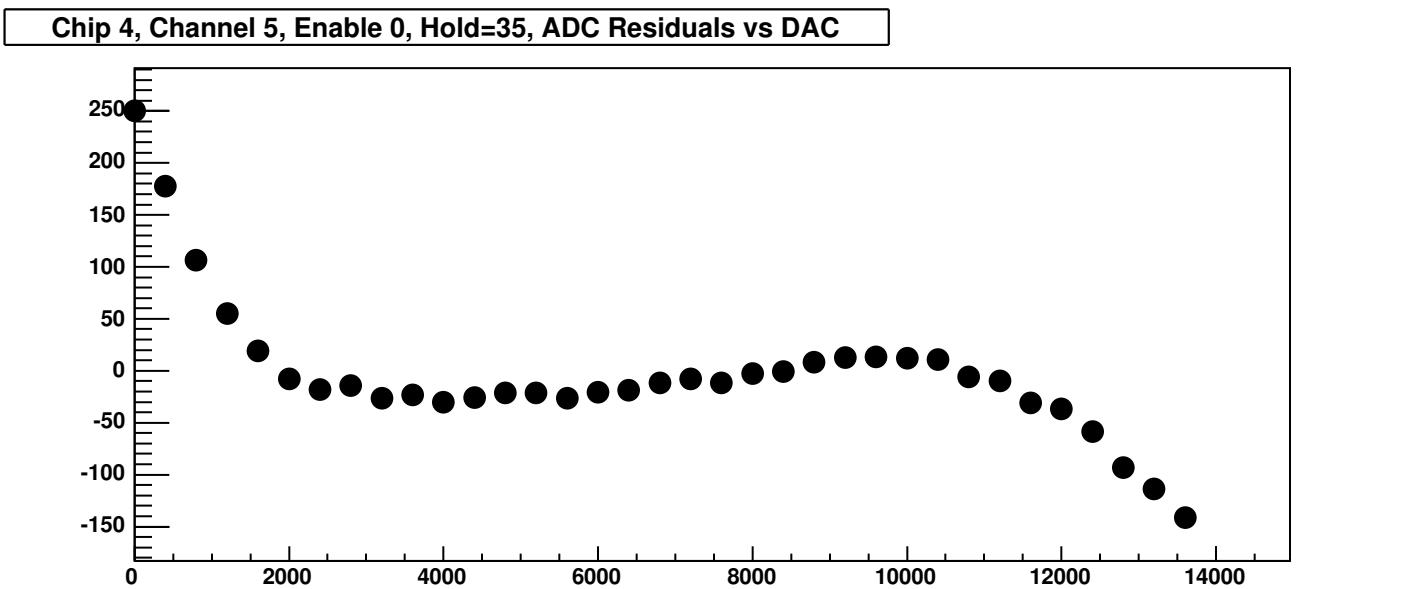

Chip 4, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 6.245e+04 / 23
p0 -388.1 ± 1.083
p1 0.4232 ± 0.0002134
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

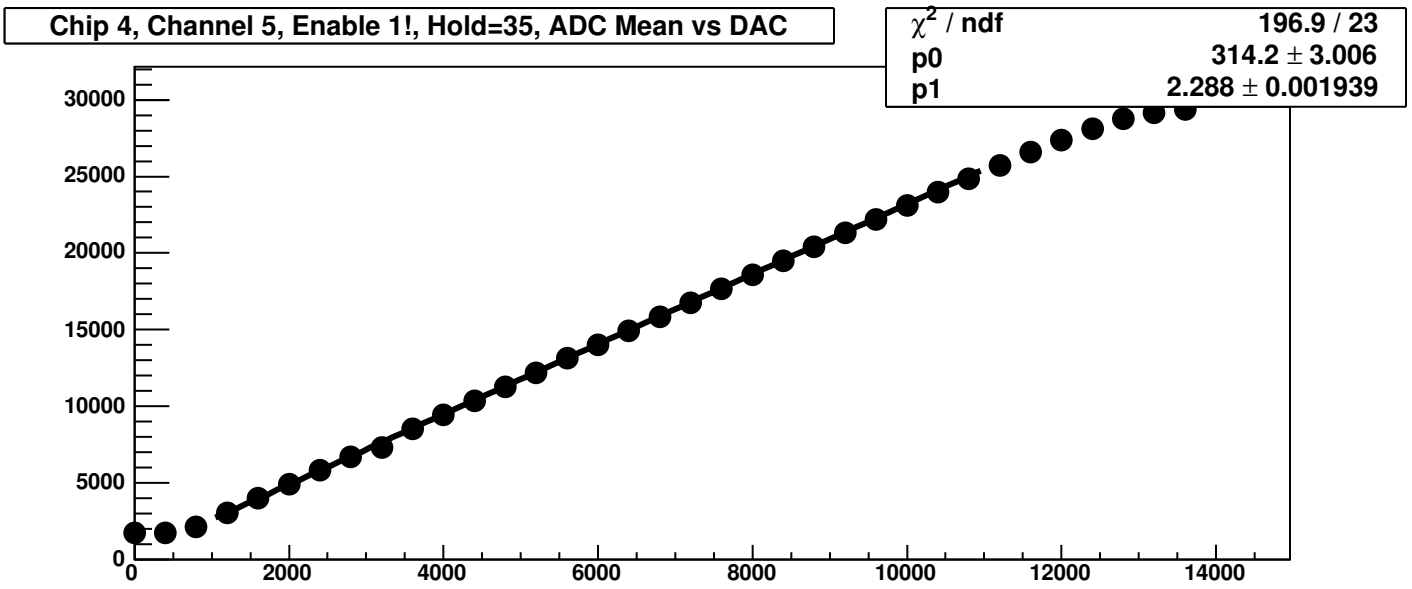

Chip 4, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

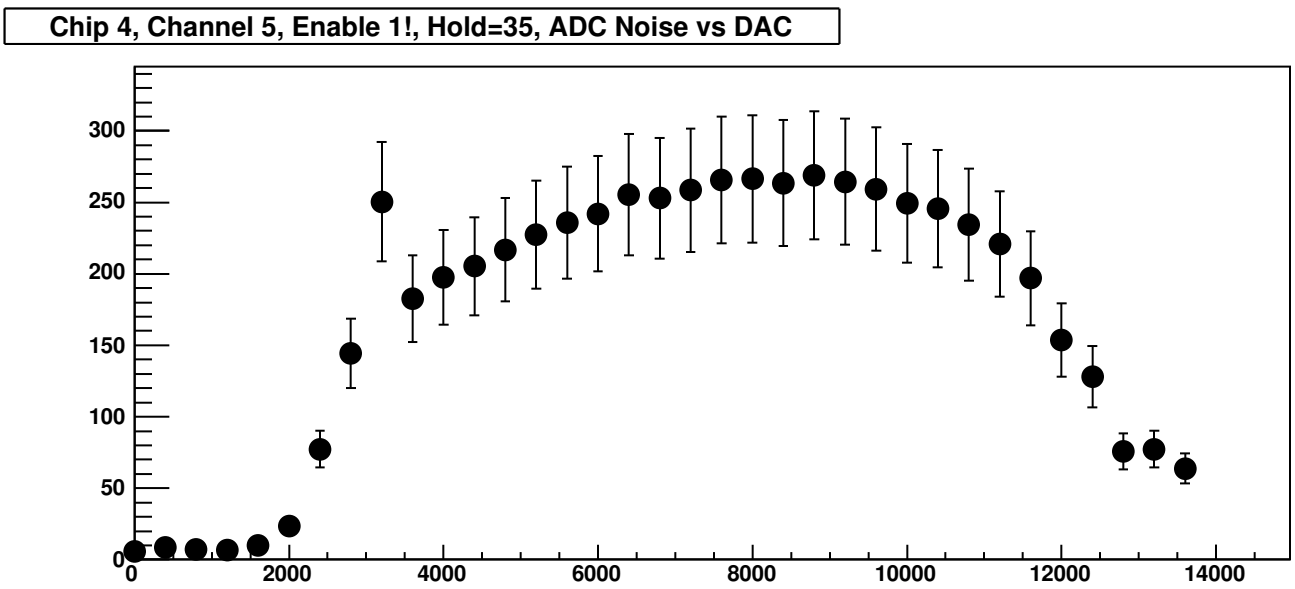

Chip 4, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

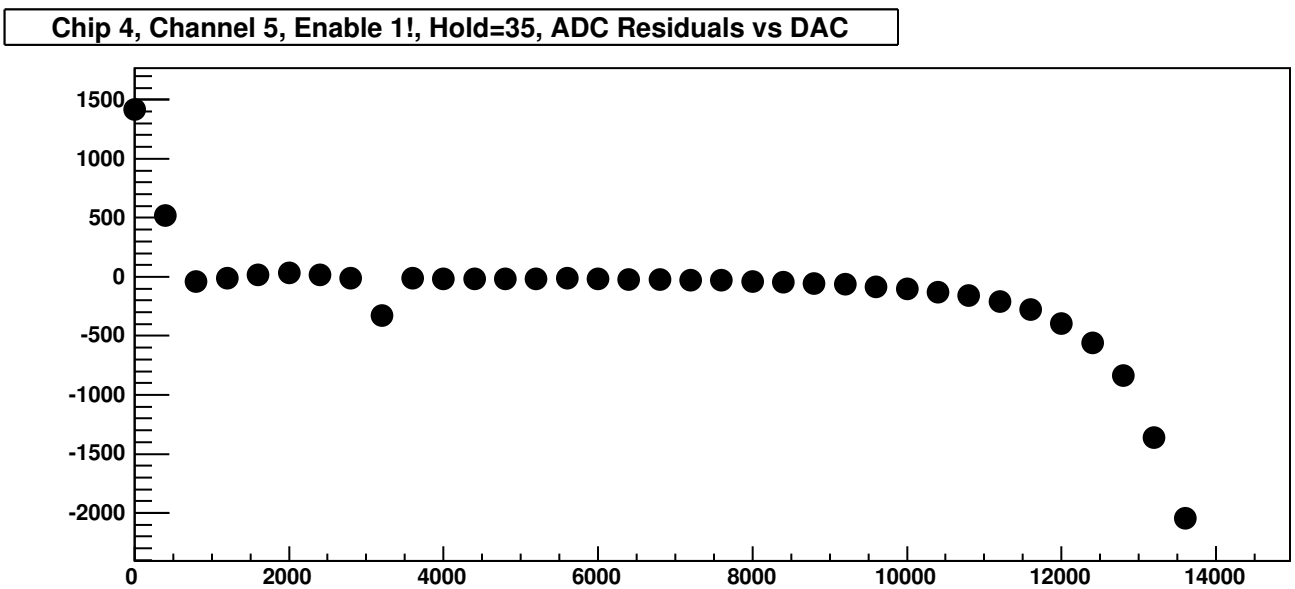

Chip 4, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1379 / 23
p0 -5.275 ± 1.249
p1 0.1893 ± 0.0001825
2500
2000
1500
1000
500
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

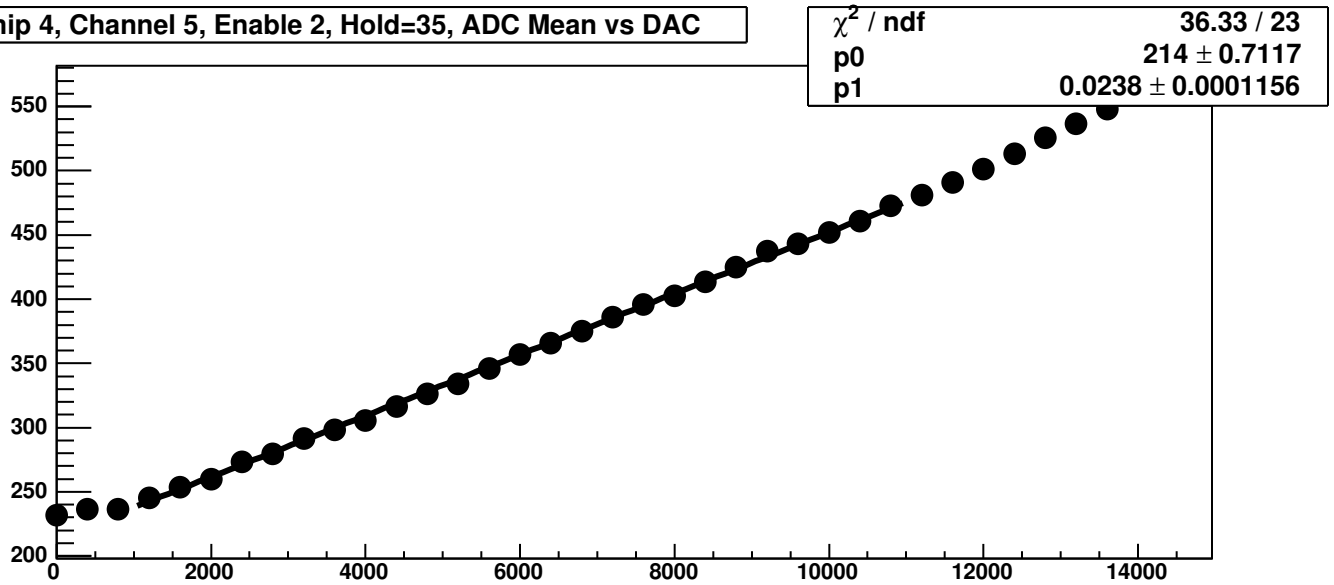

Chip 4, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 196.9 / 23
p0 314.2 ± 3.006
p1 2.288 ± 0.001939
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

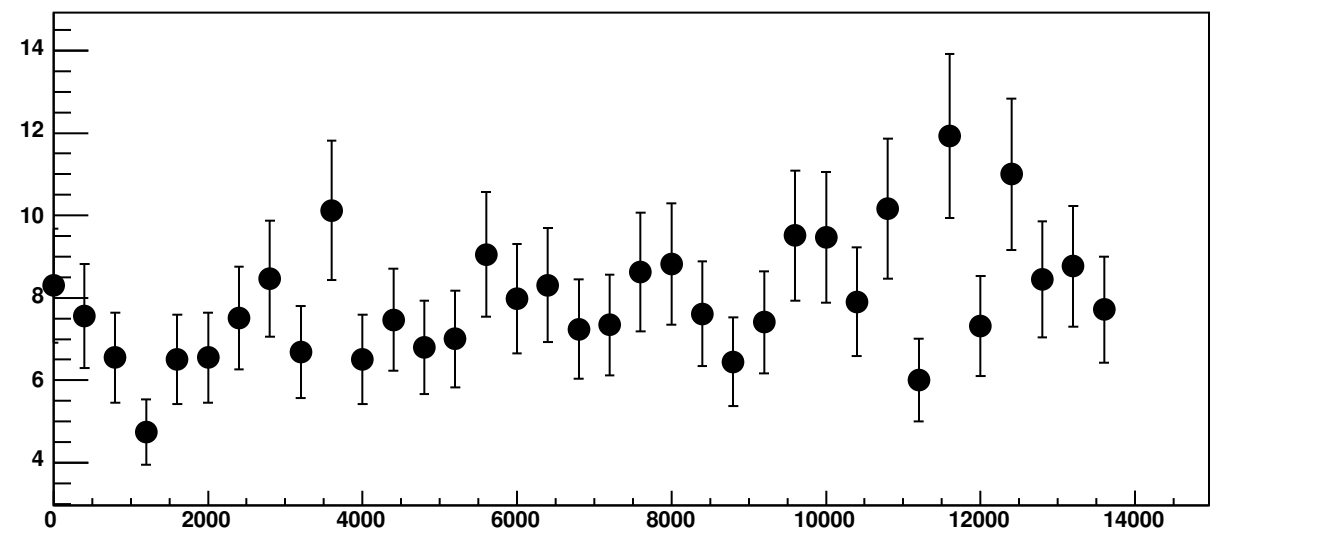

Chip 4, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

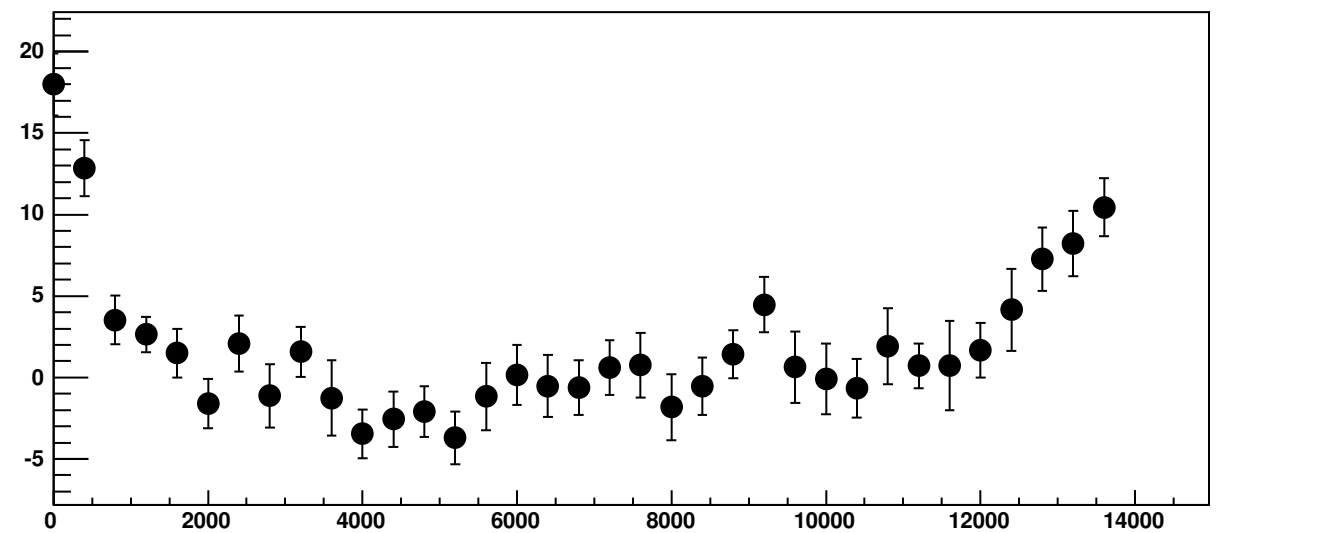
Chip 4, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

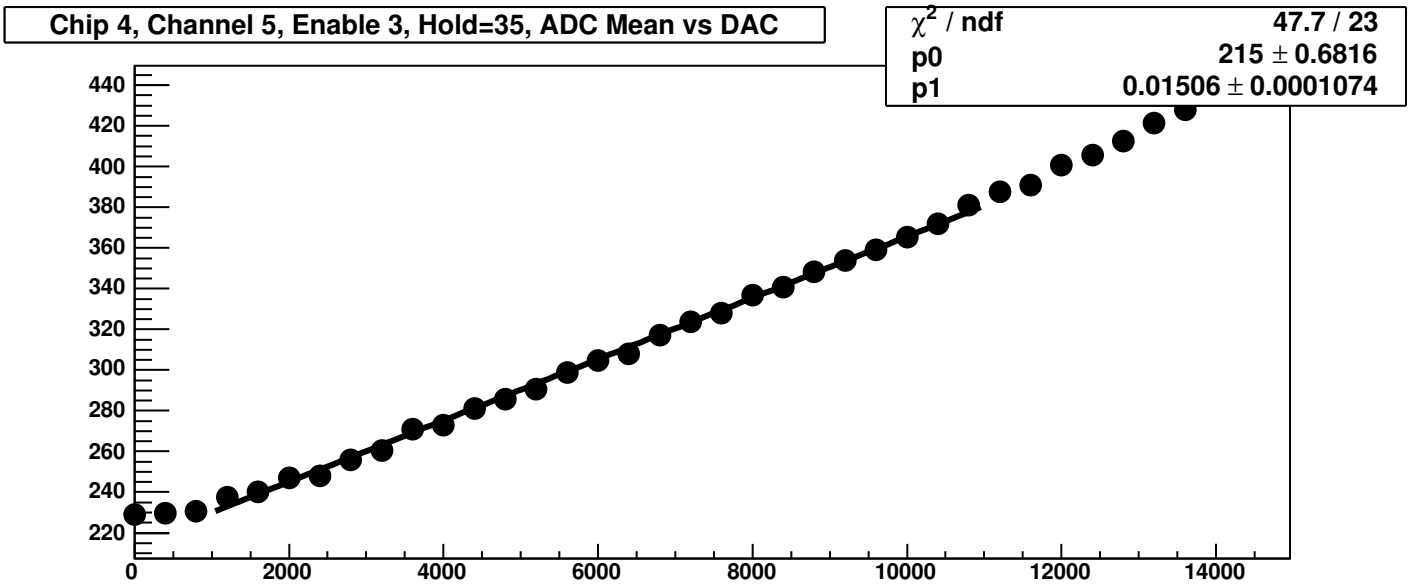


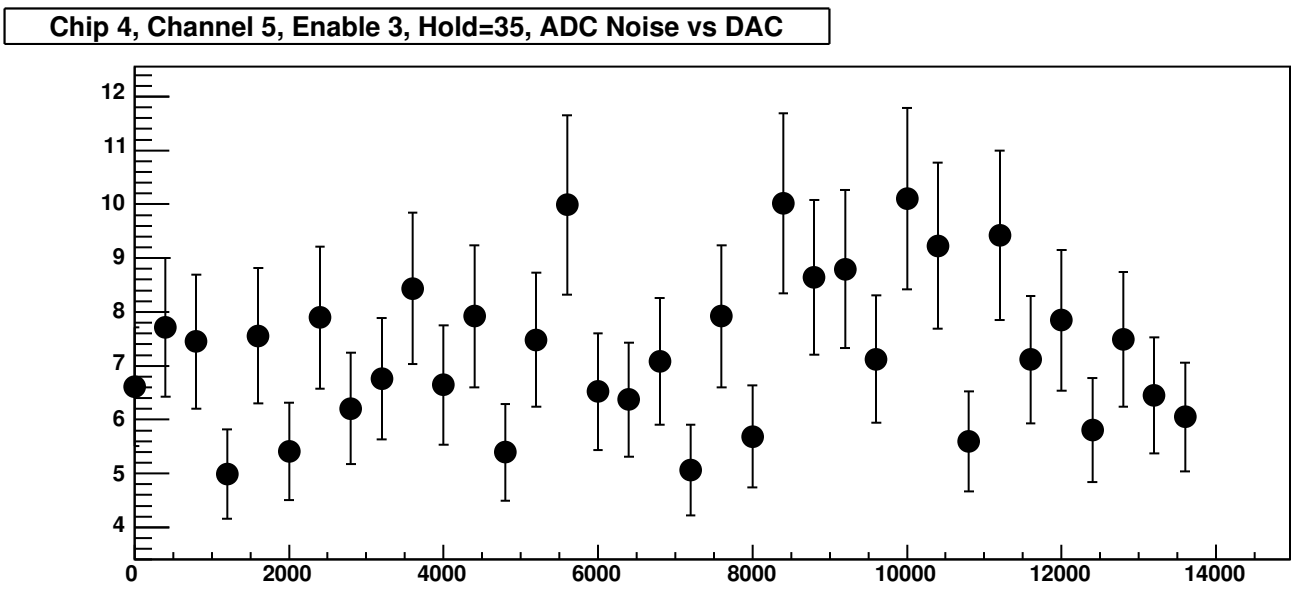
Chip 4, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC

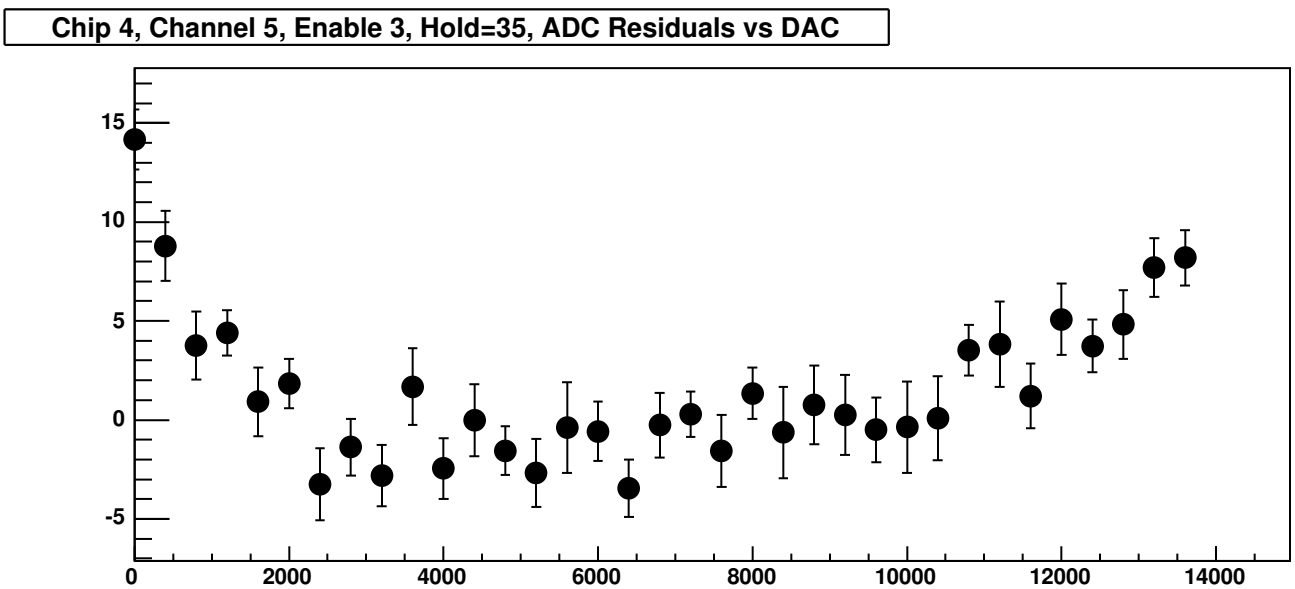


Chip 4, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

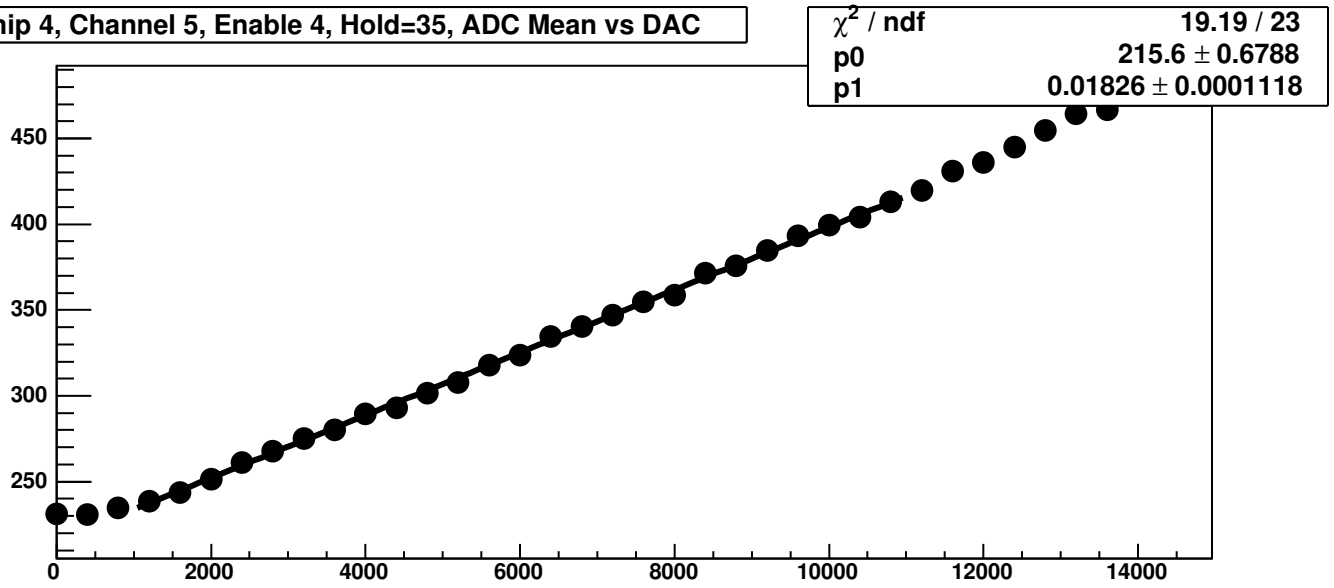



Chip 4, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 47.7 / 23
p0 215 ± 0.6816
p1 0.01506 ± 0.0001074
440
420
400
380
360
340
320
300
280
260
240
220
0
2000
4000
6000
8000
10000
12000
14000

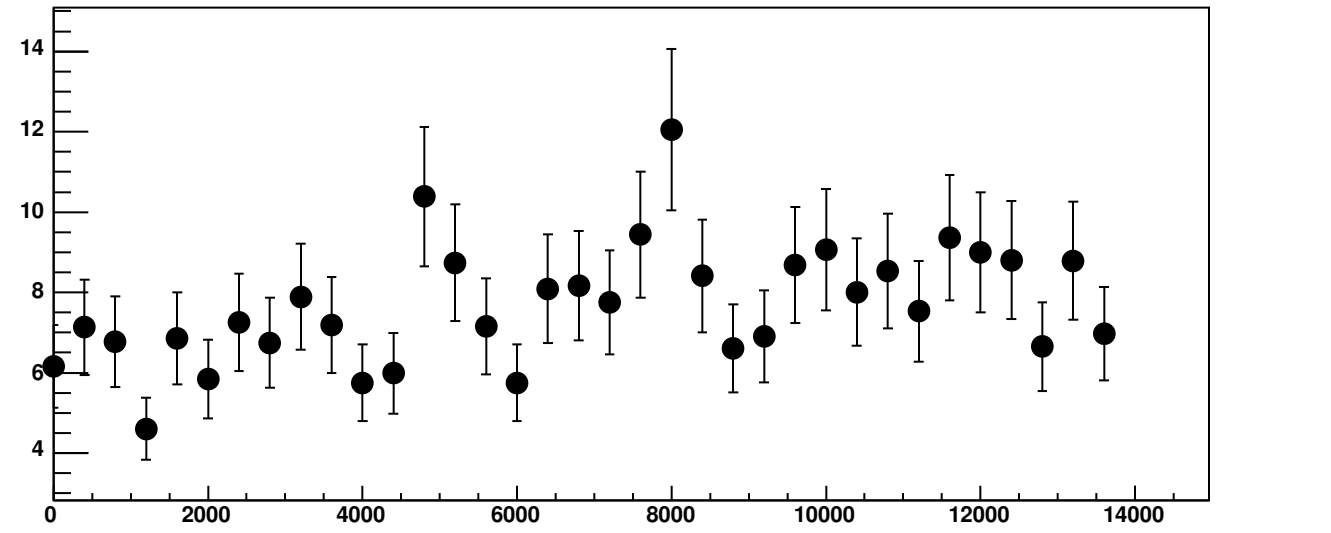

Chip 4, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

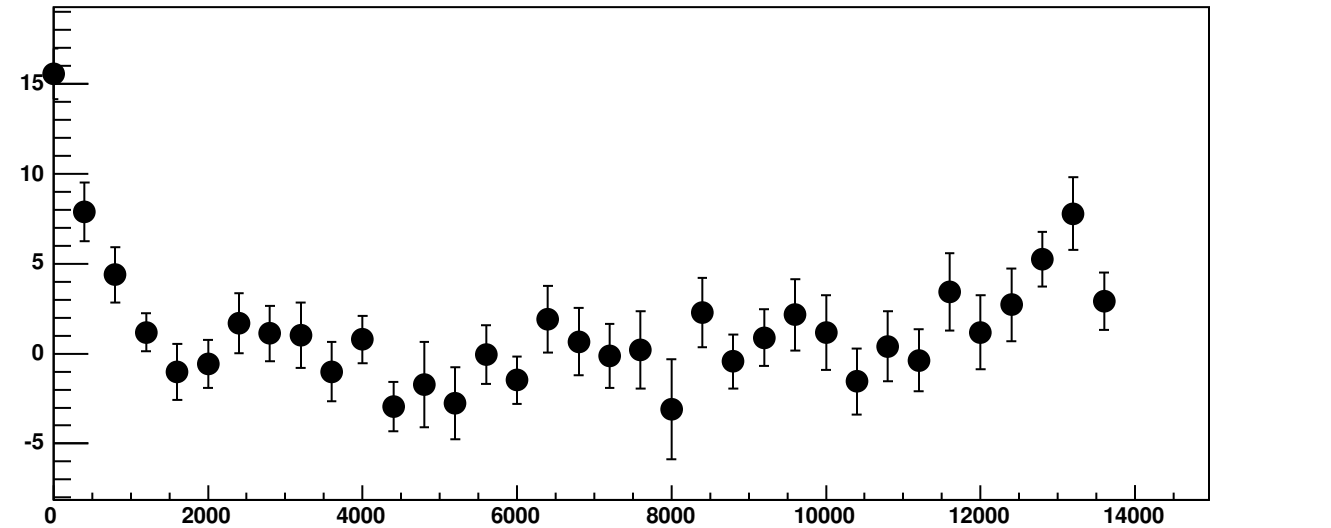
Chip 4, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

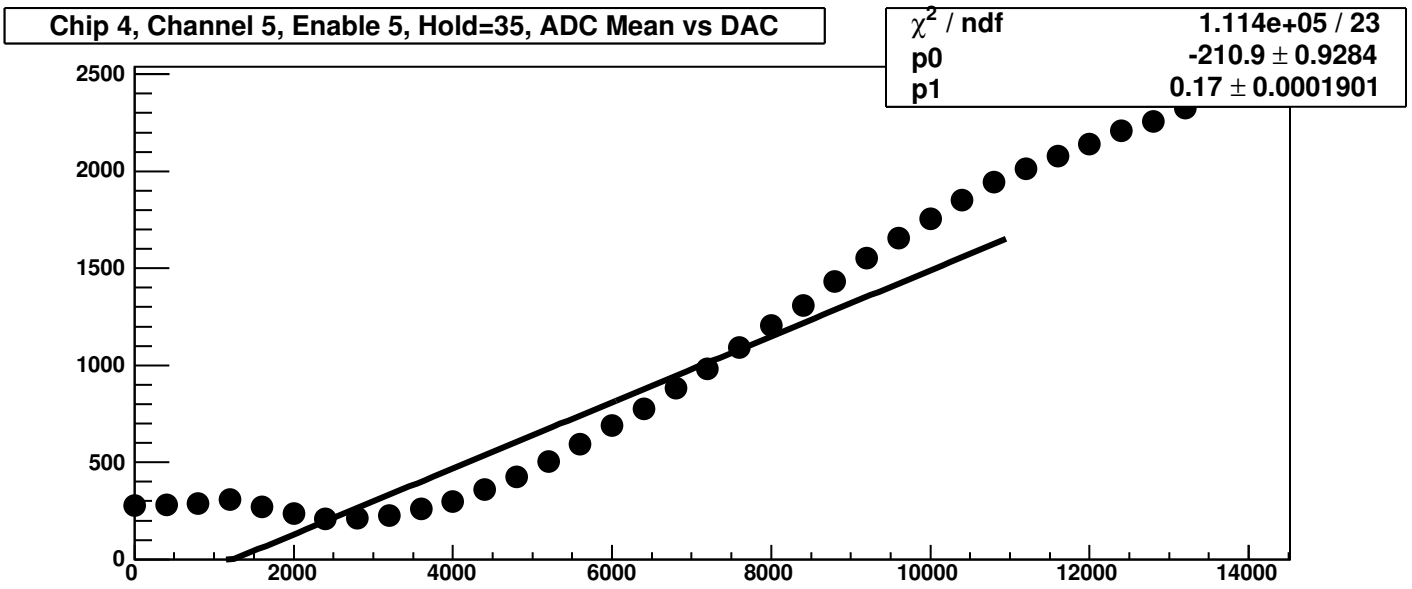


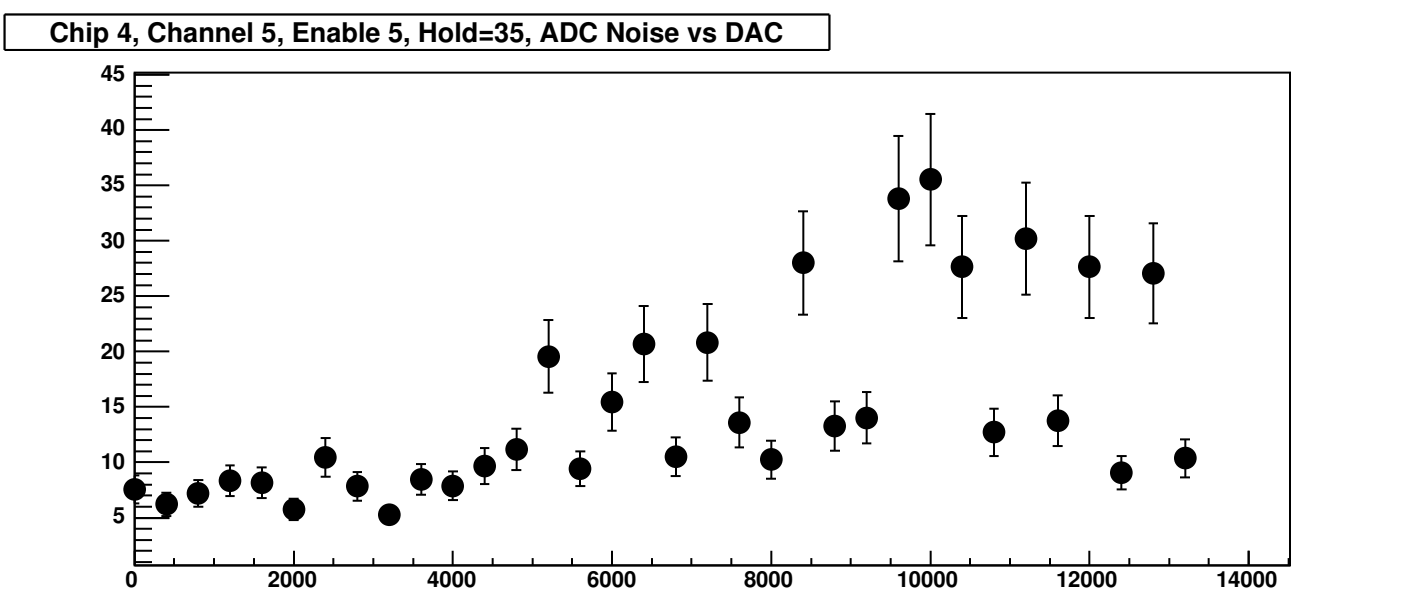
Chip 4, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC

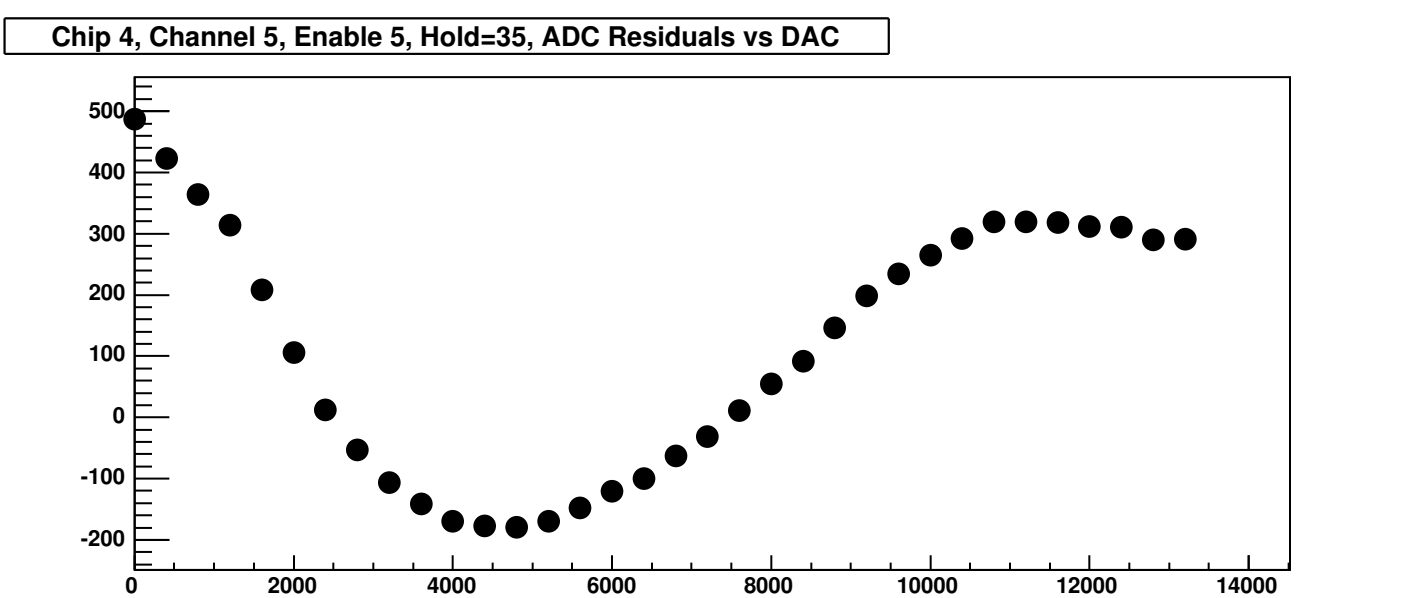


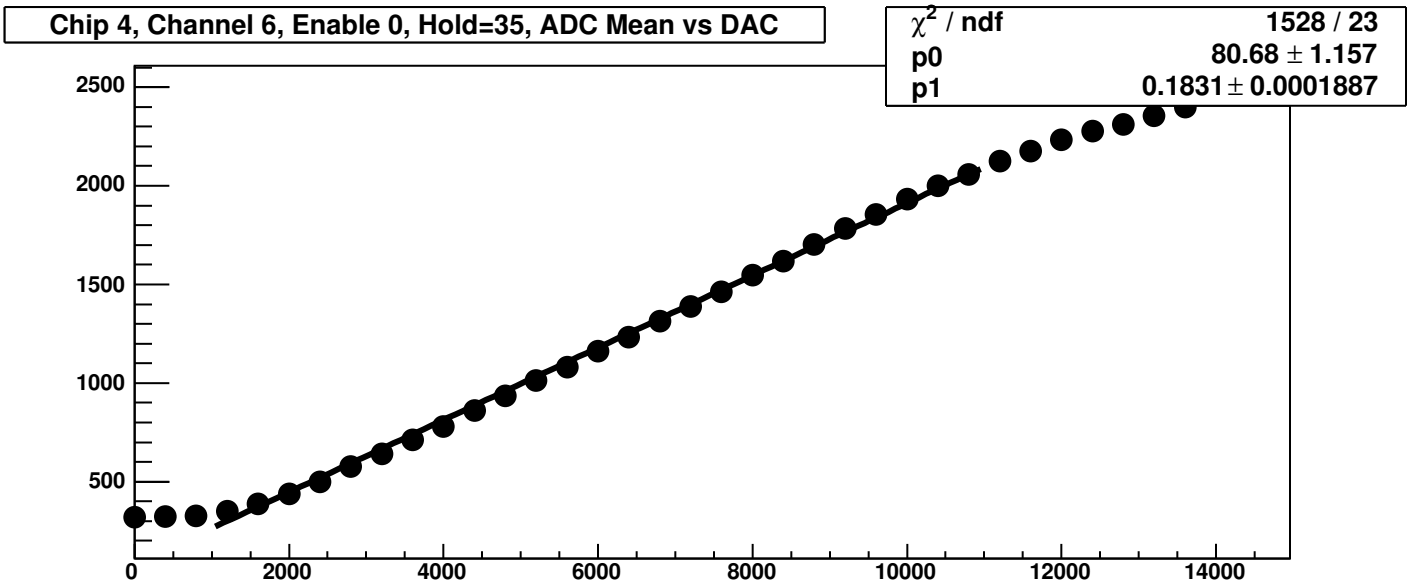
Chip 4, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC

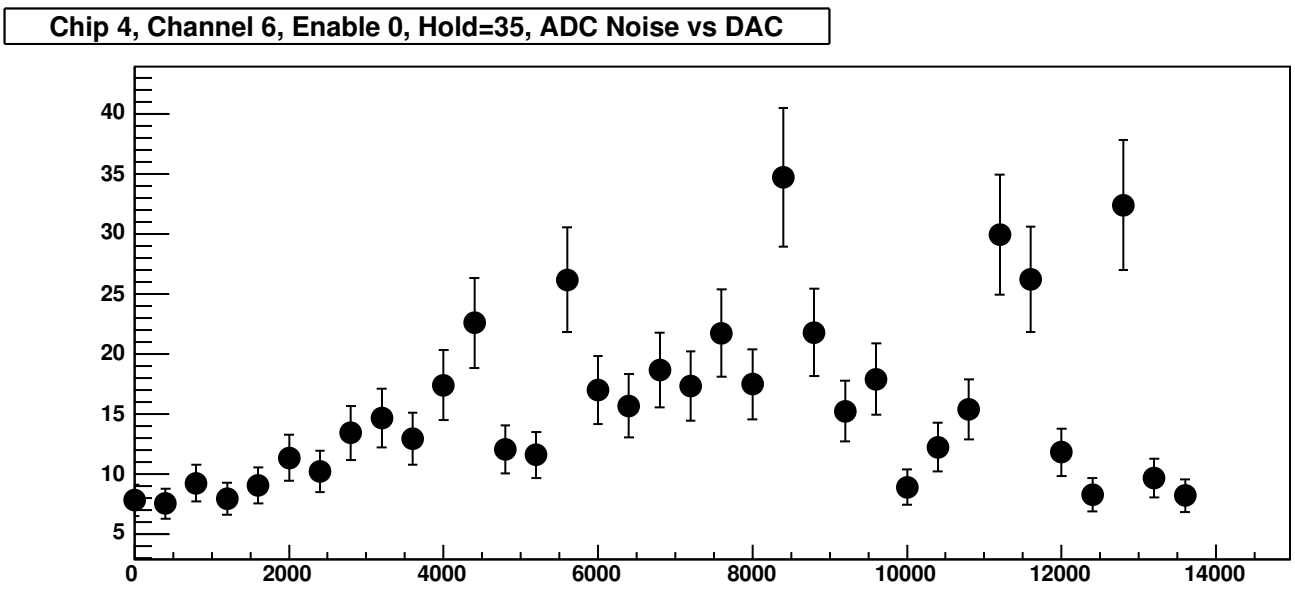


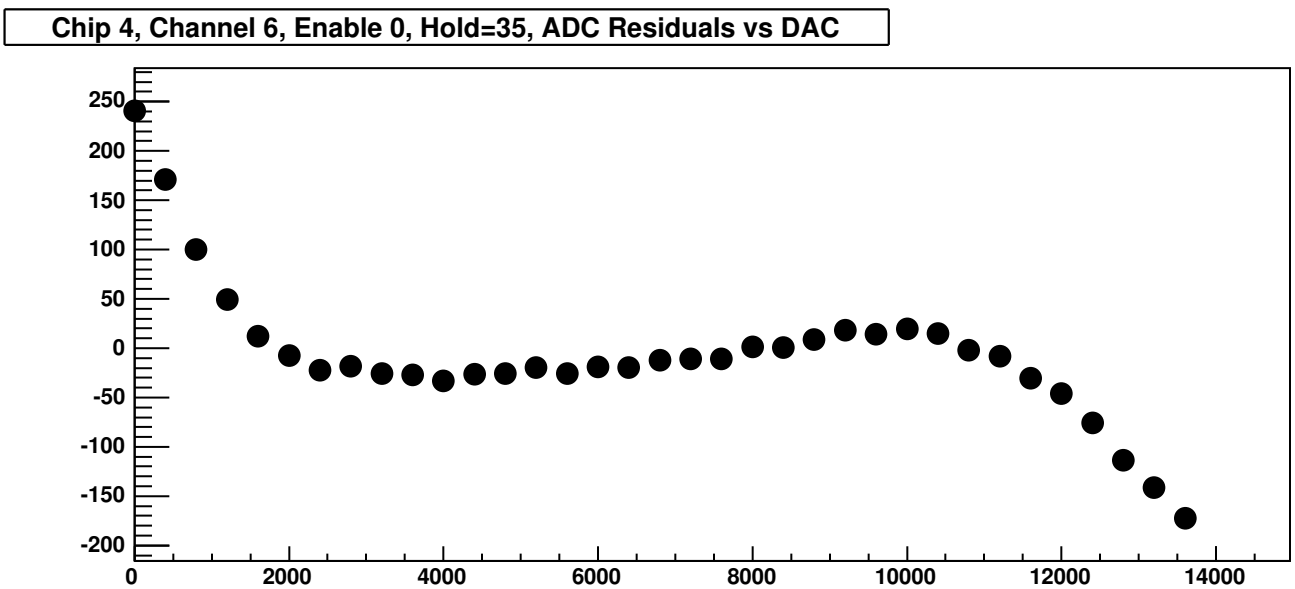

Chip 4, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.114e+05 / 23
p0 -210.9 ± 0.9284
p1 0.17 ± 0.0001901
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

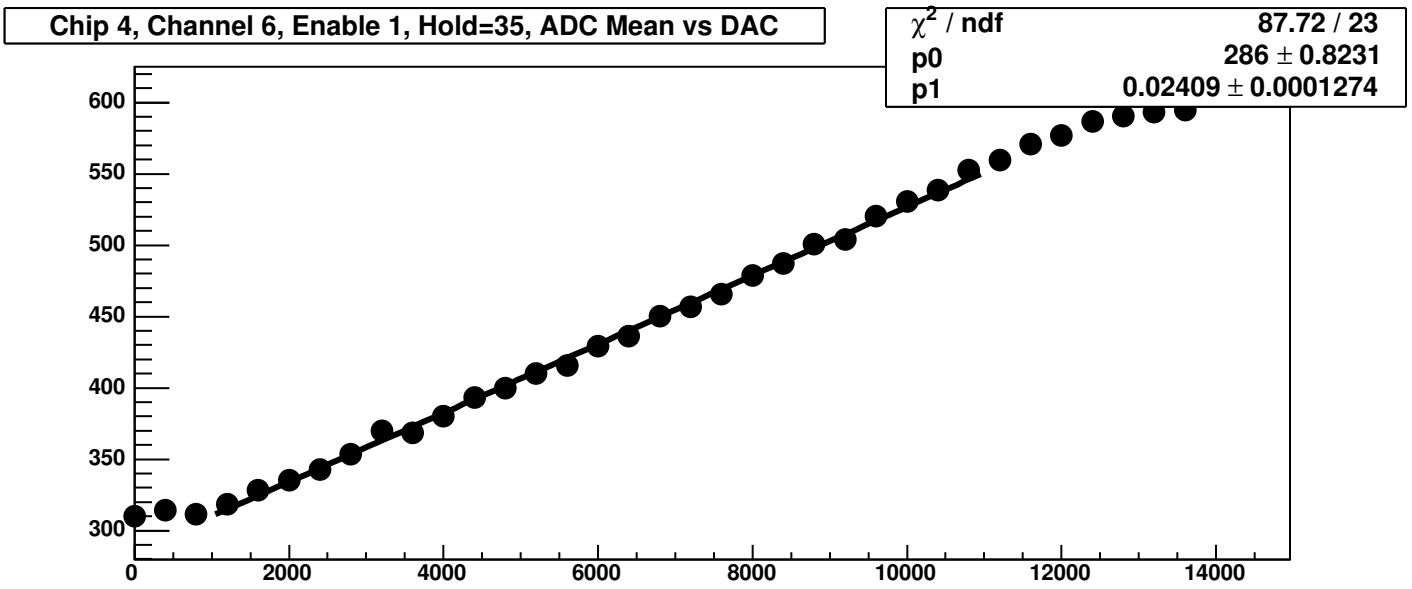

Chip 4, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

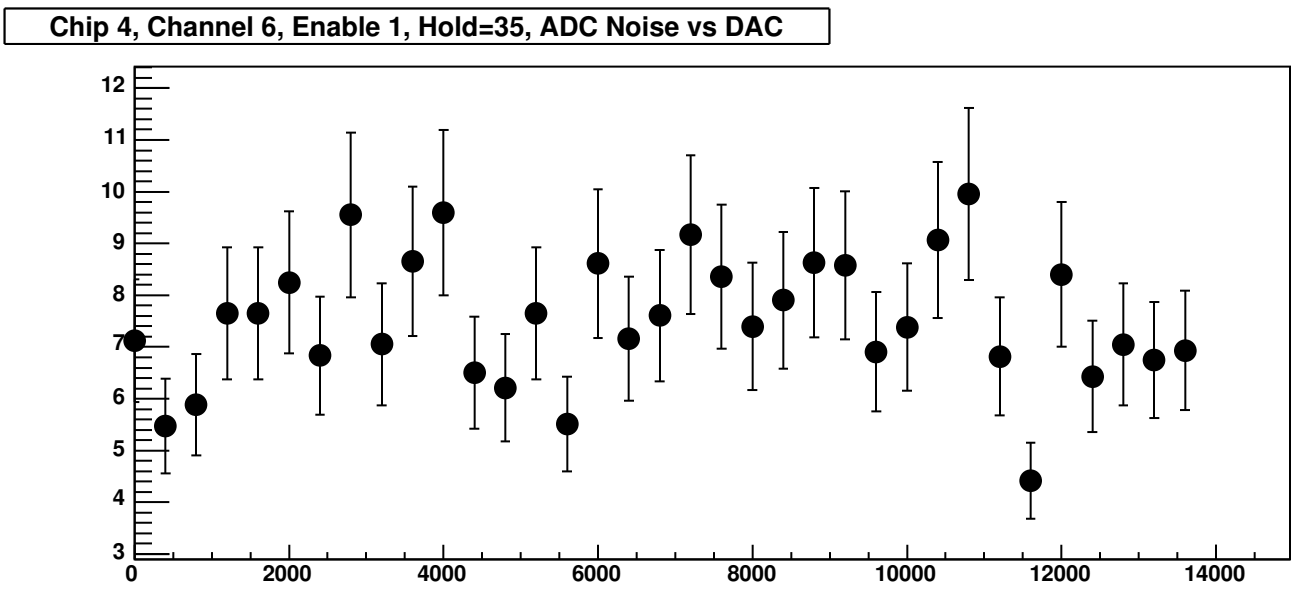

Chip 4, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

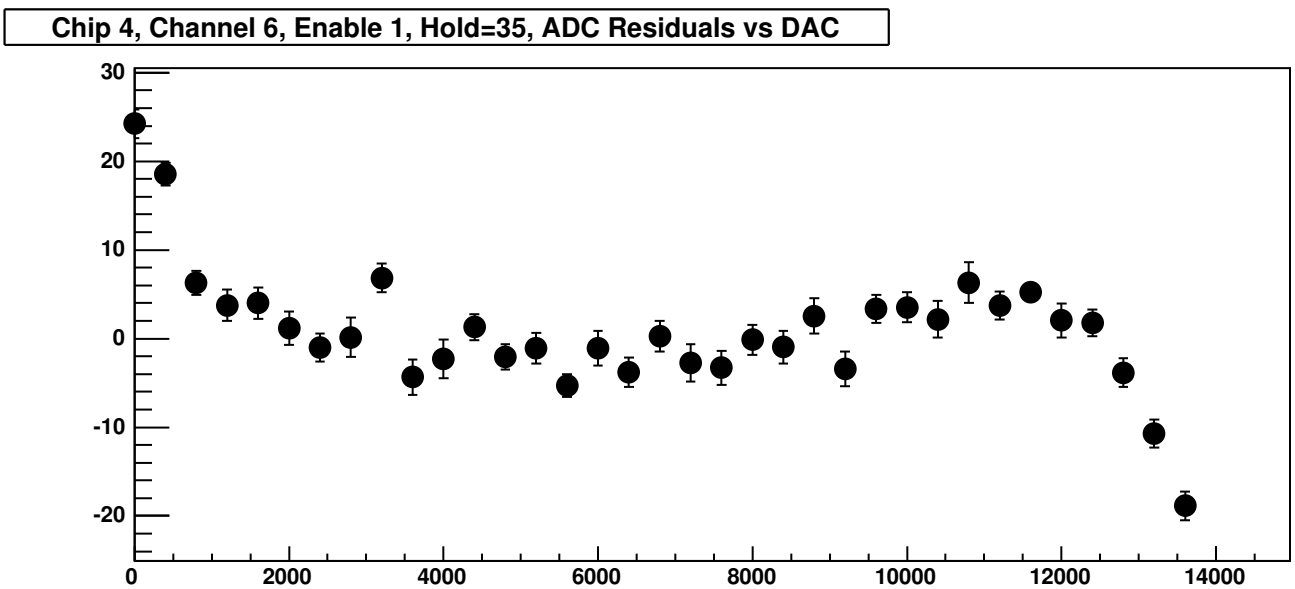

Chip 4, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1528 / 23
p0 80.68 ± 1.157
p1 0.1831 ± 0.0001887
2500
2000
1500
1000
500
0
2000
4000
6000
8000
10000
12000
14000

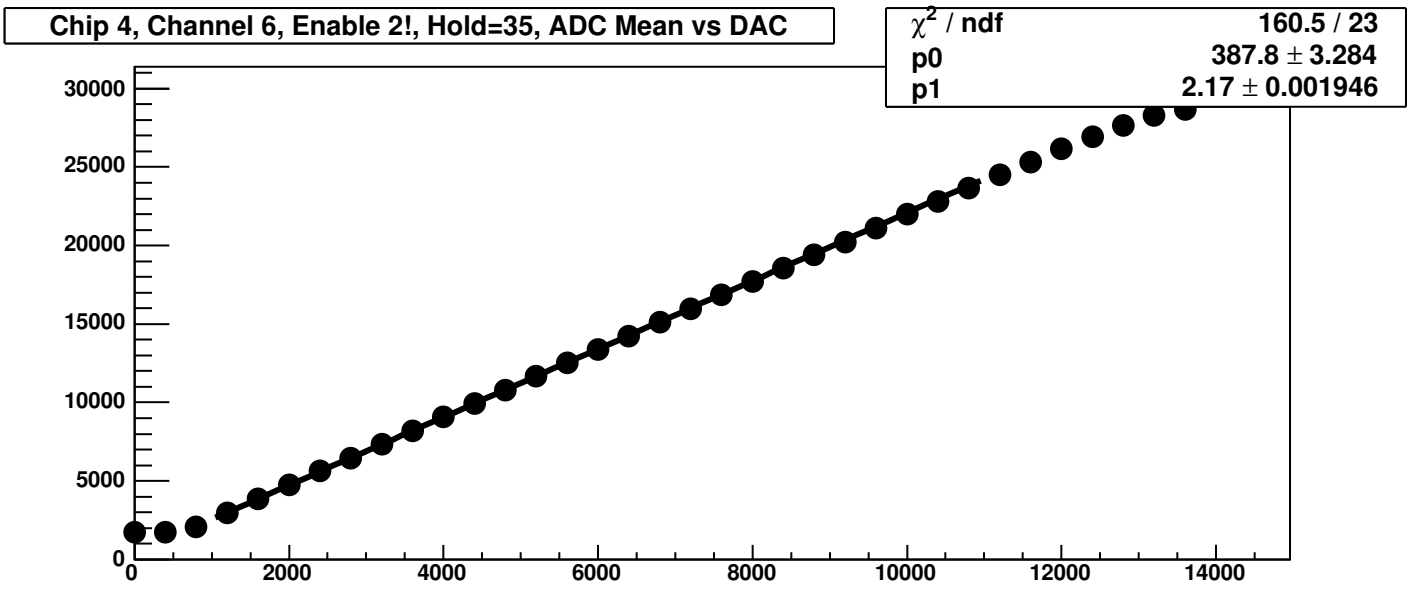

Chip 4, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

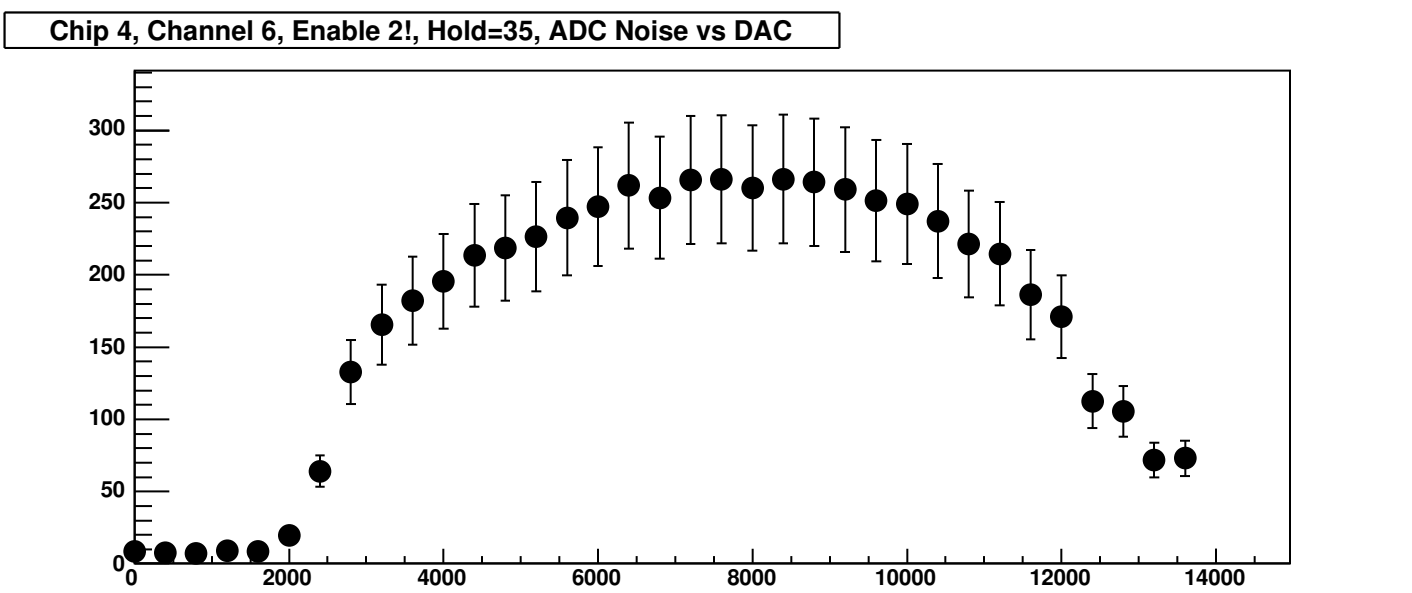

Chip 4, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

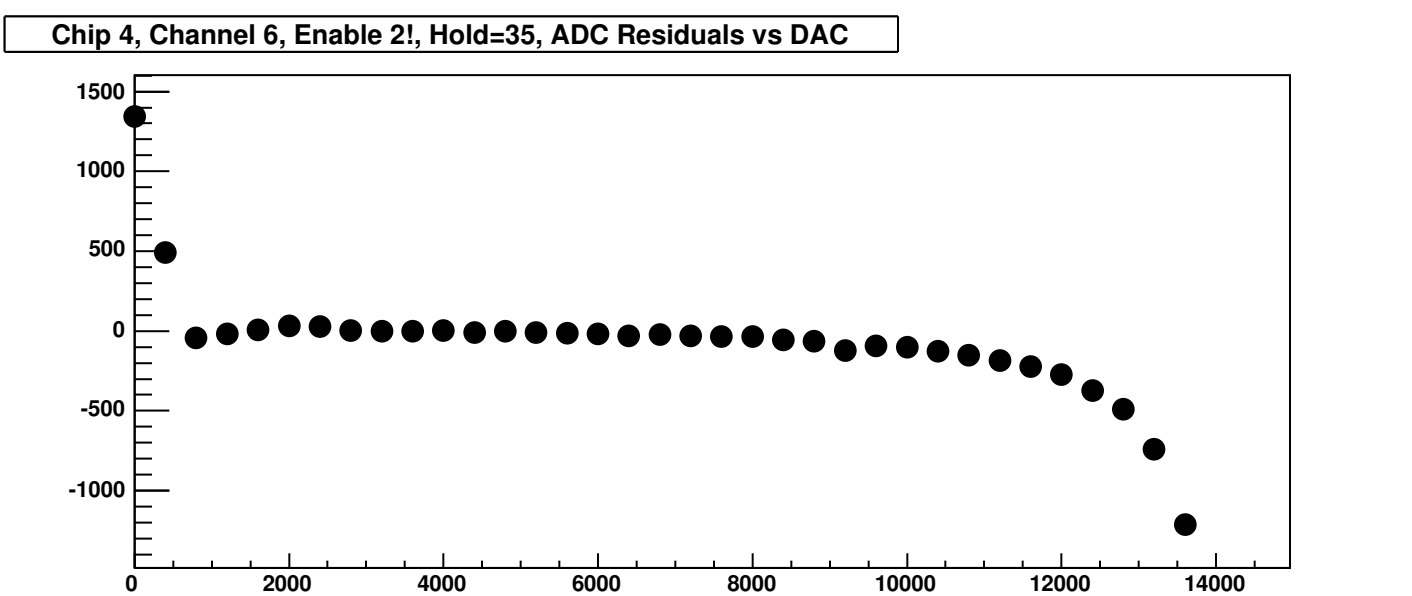

Chip 4, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 87.72 / 23
p0 286 ± 0.8231
p1 0.02409 ± 0.0001274
600
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

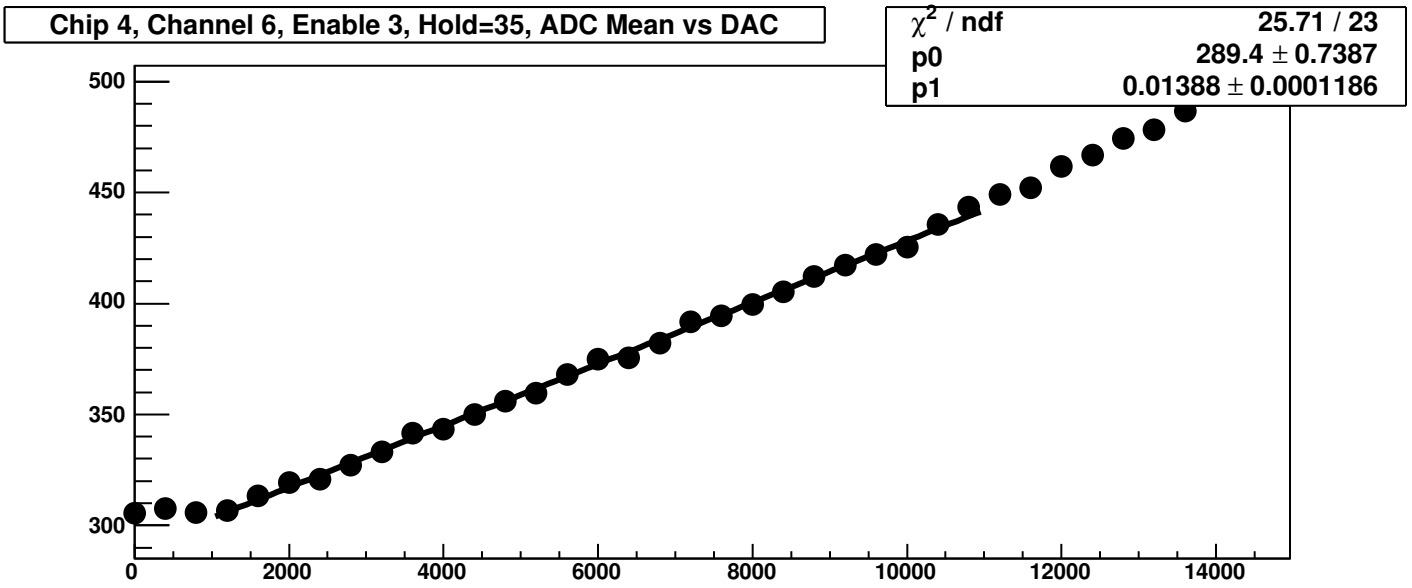

Chip 4, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

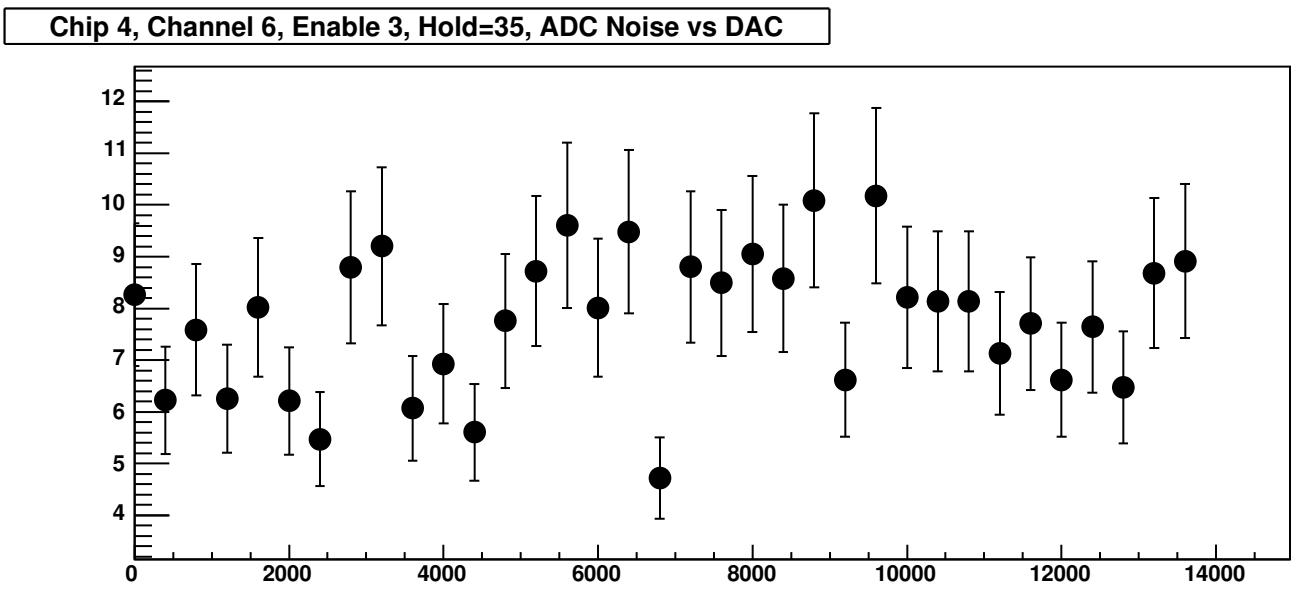

Chip 4, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

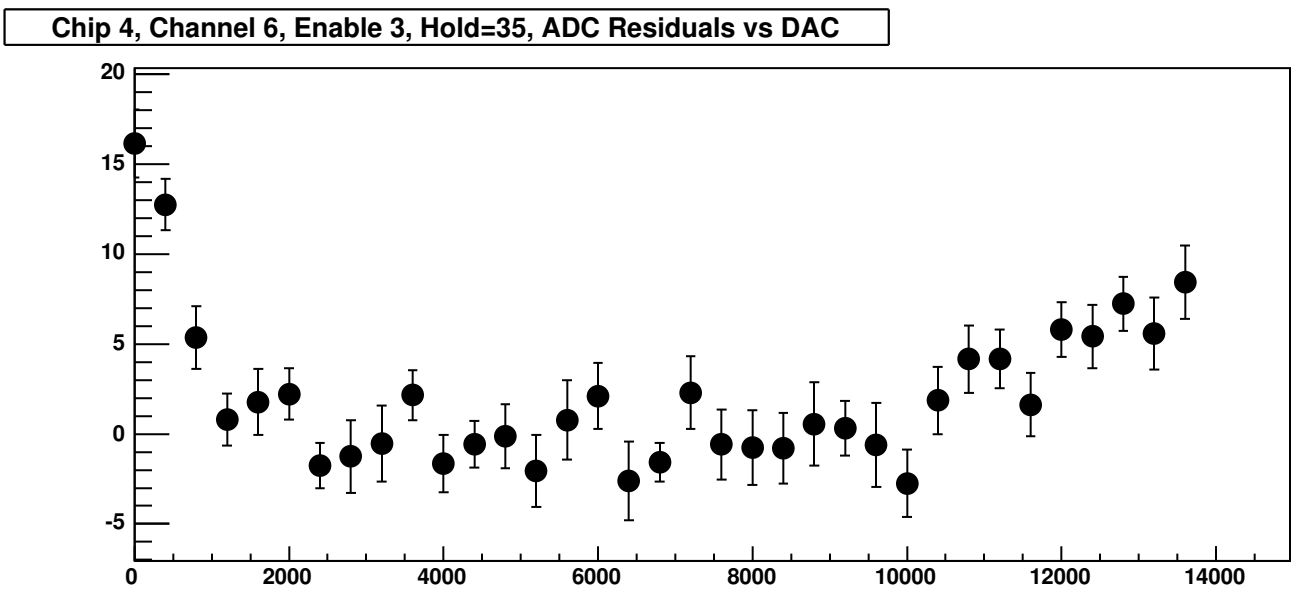

Chip 4, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 160.5 / 23
p0 387.8 ± 3.284
p1 2.17 ± 0.001946
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

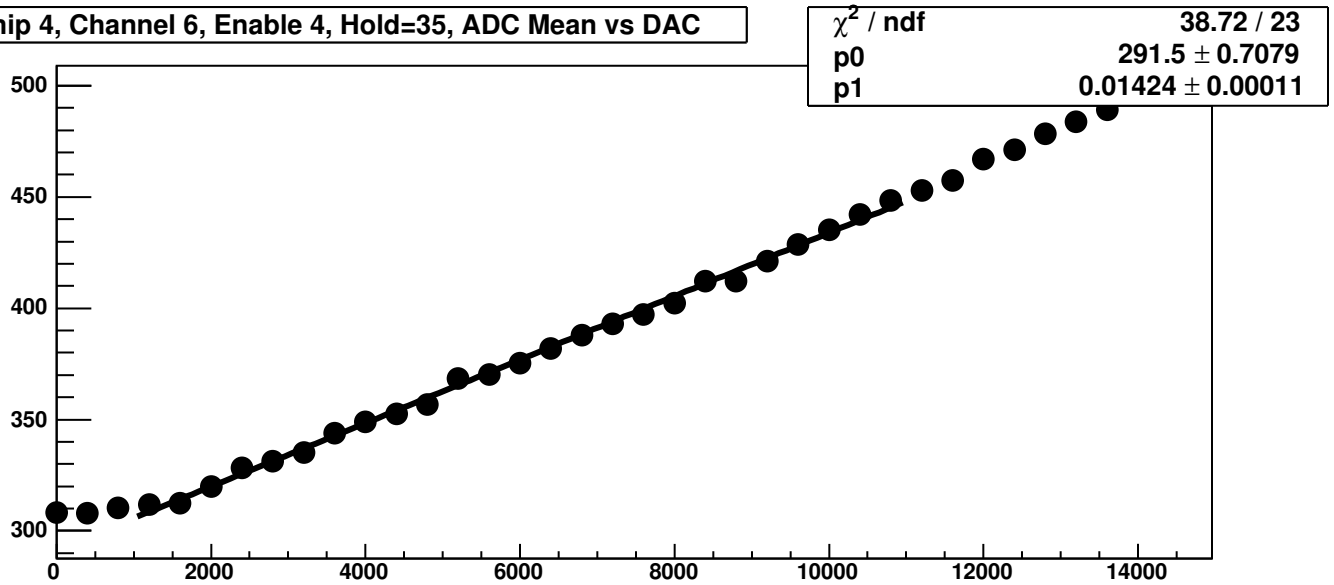

Chip 4, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.71 / 23
p0 289.4 ± 0.7387
p1 0.01388 ± 0.0001186
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

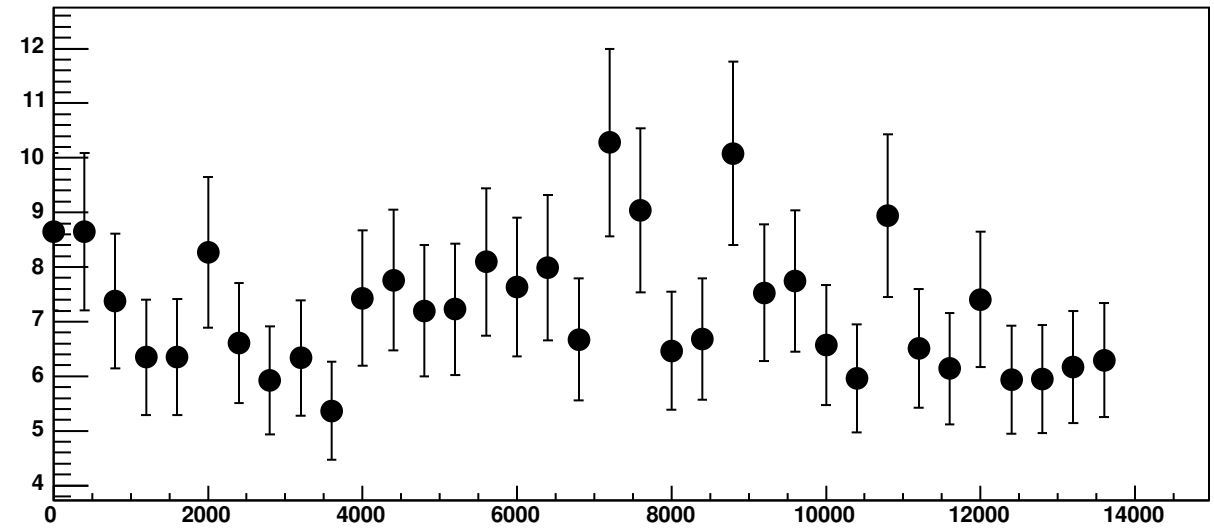

Chip 4, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

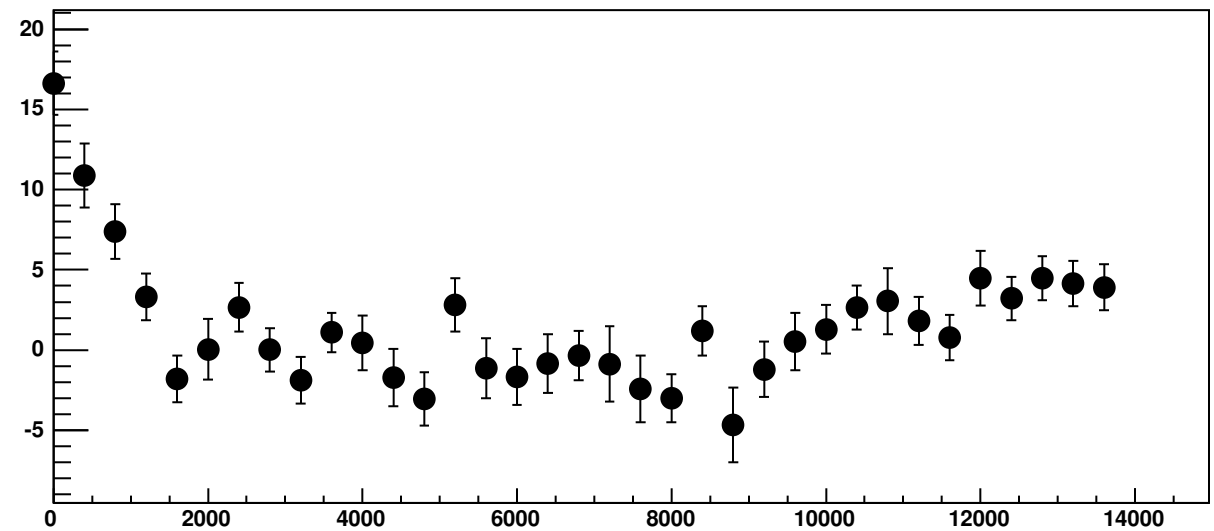
Chip 4, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



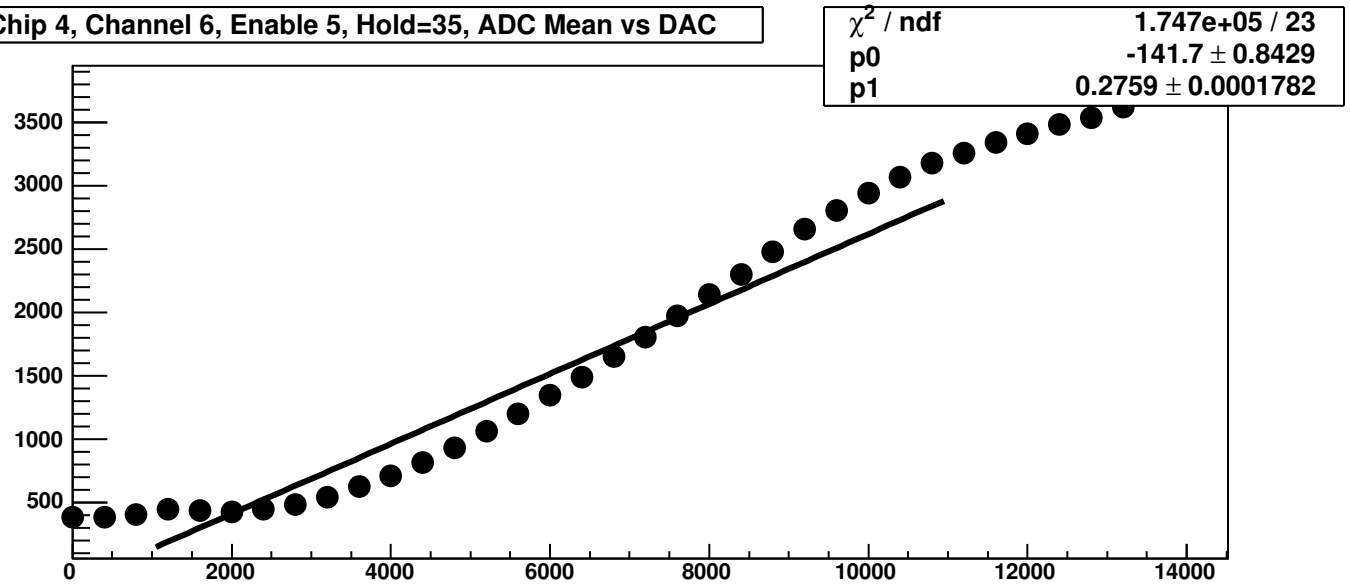
Chip 4, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



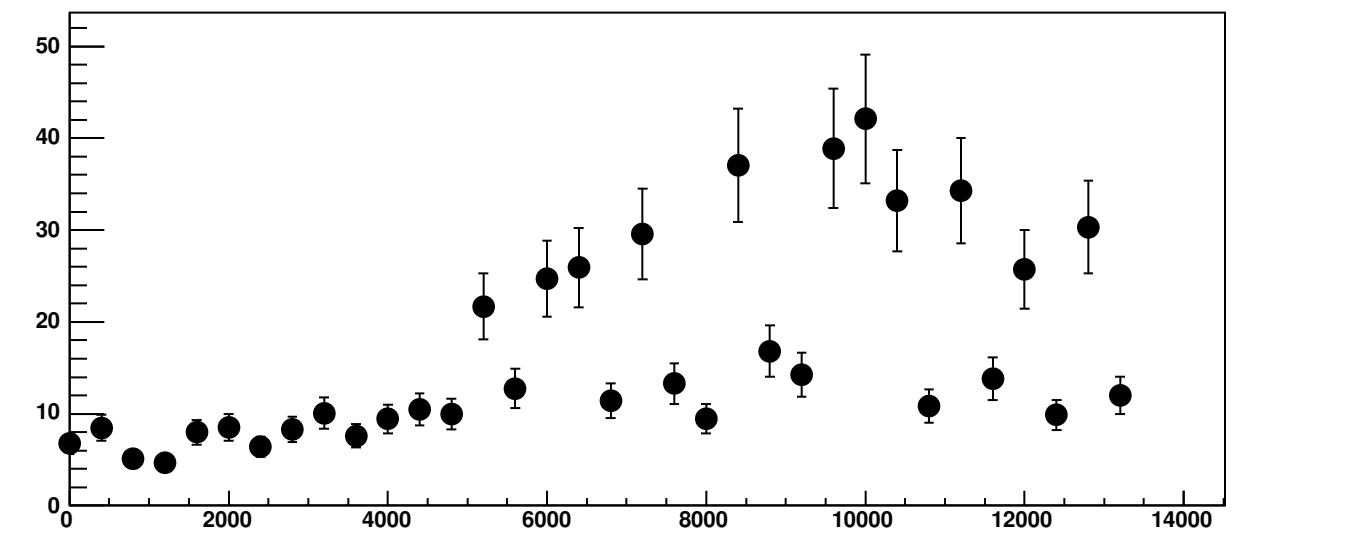
Chip 4, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



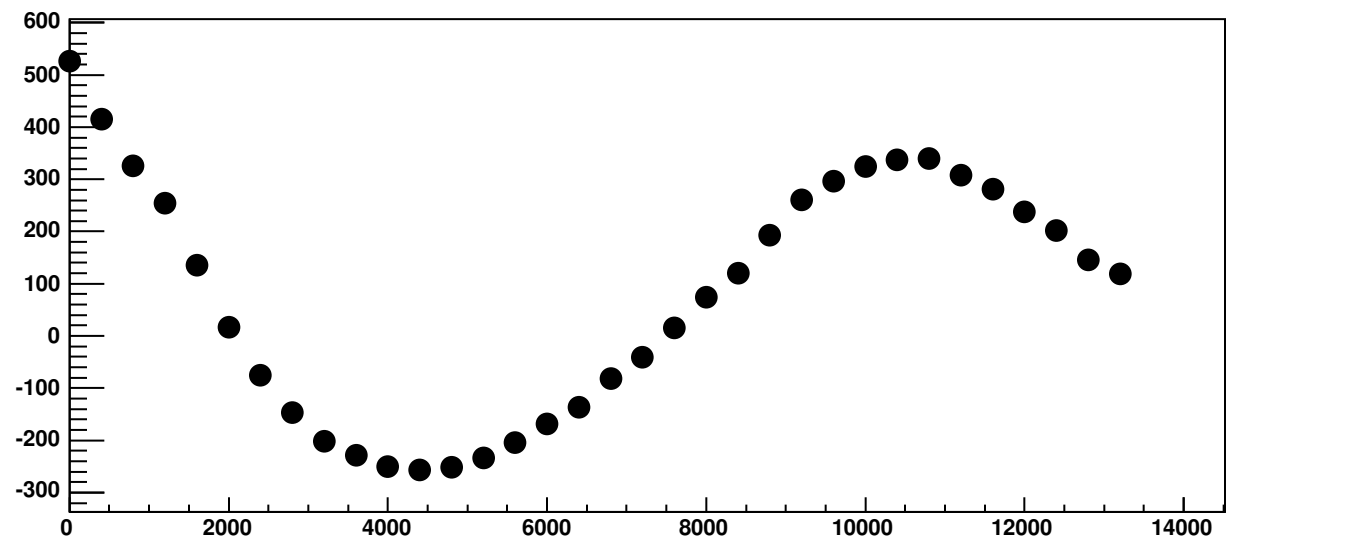
Chip 4, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC



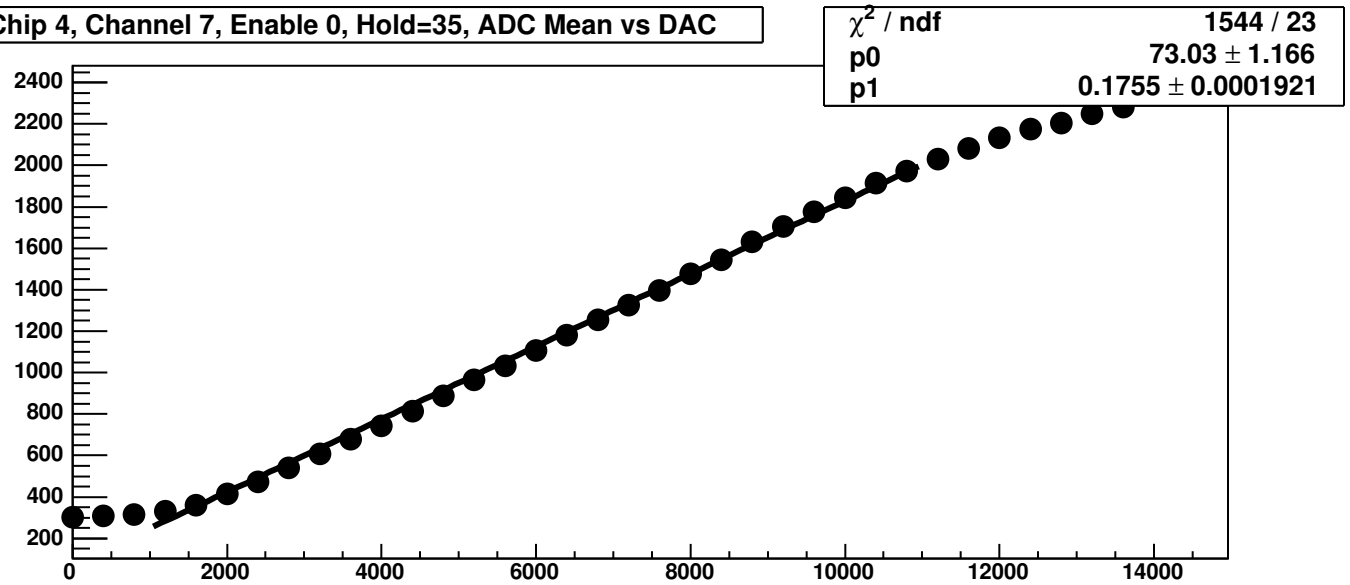
Chip 4, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



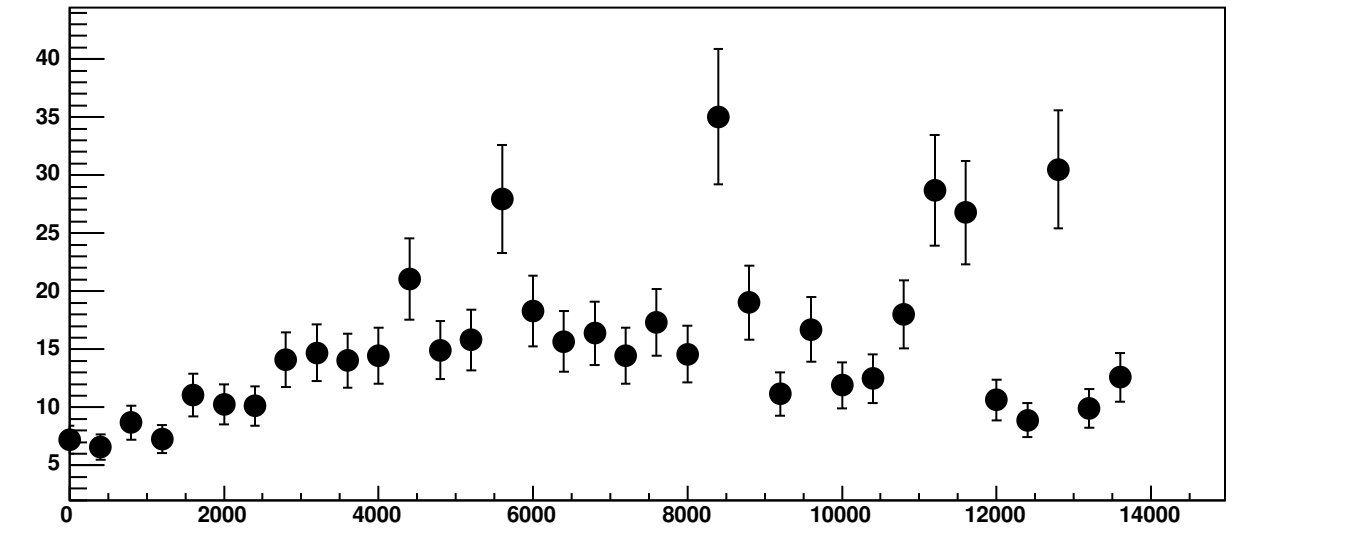
Chip 4, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC



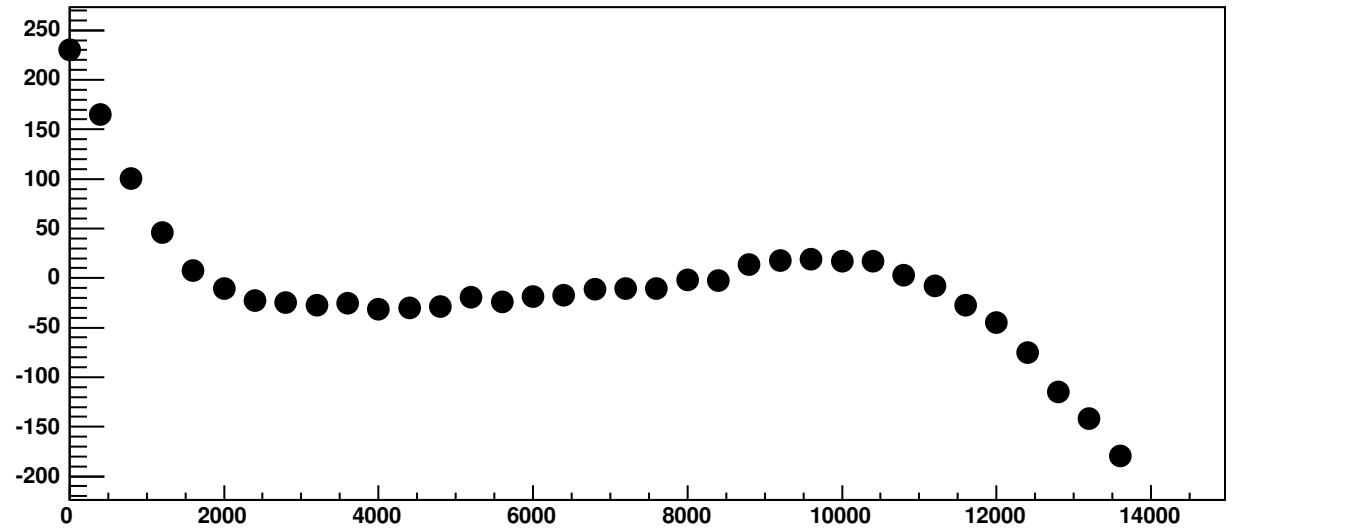
Chip 4, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

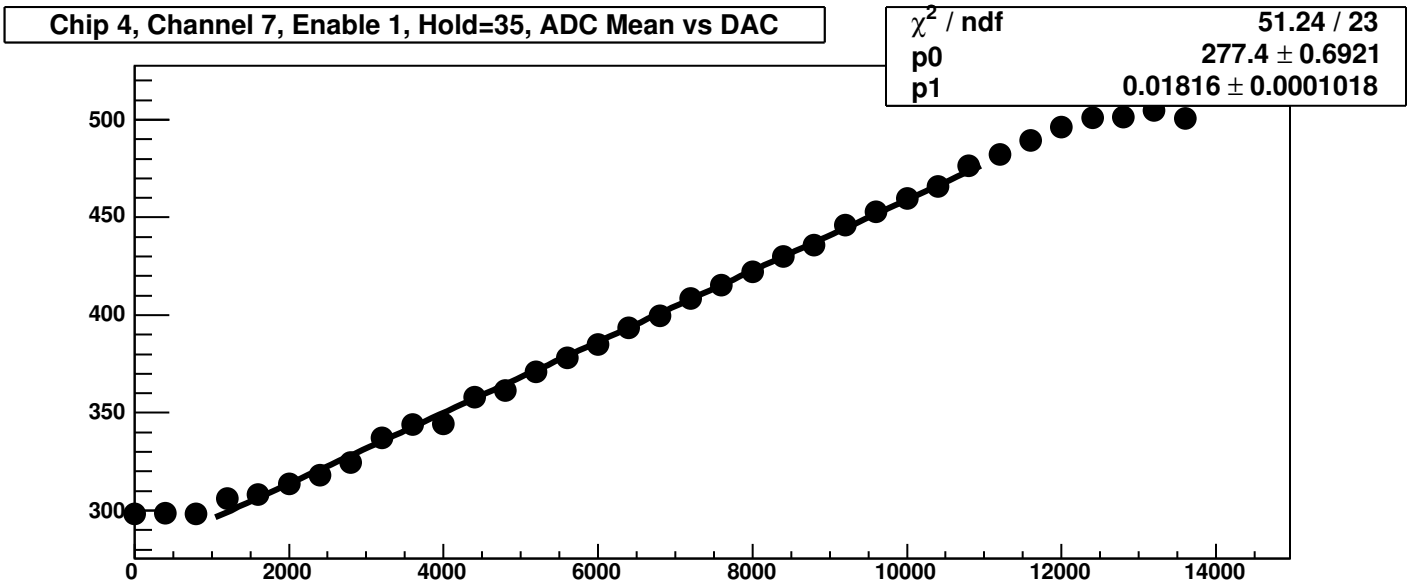


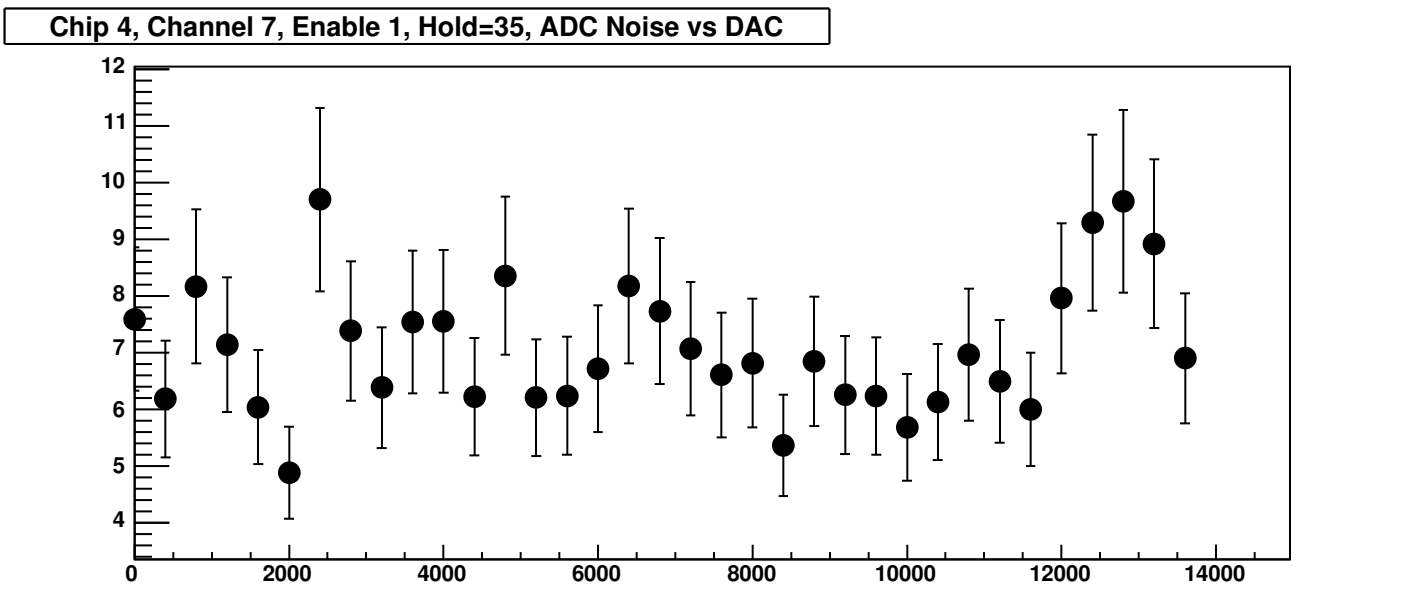
Chip 4, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC

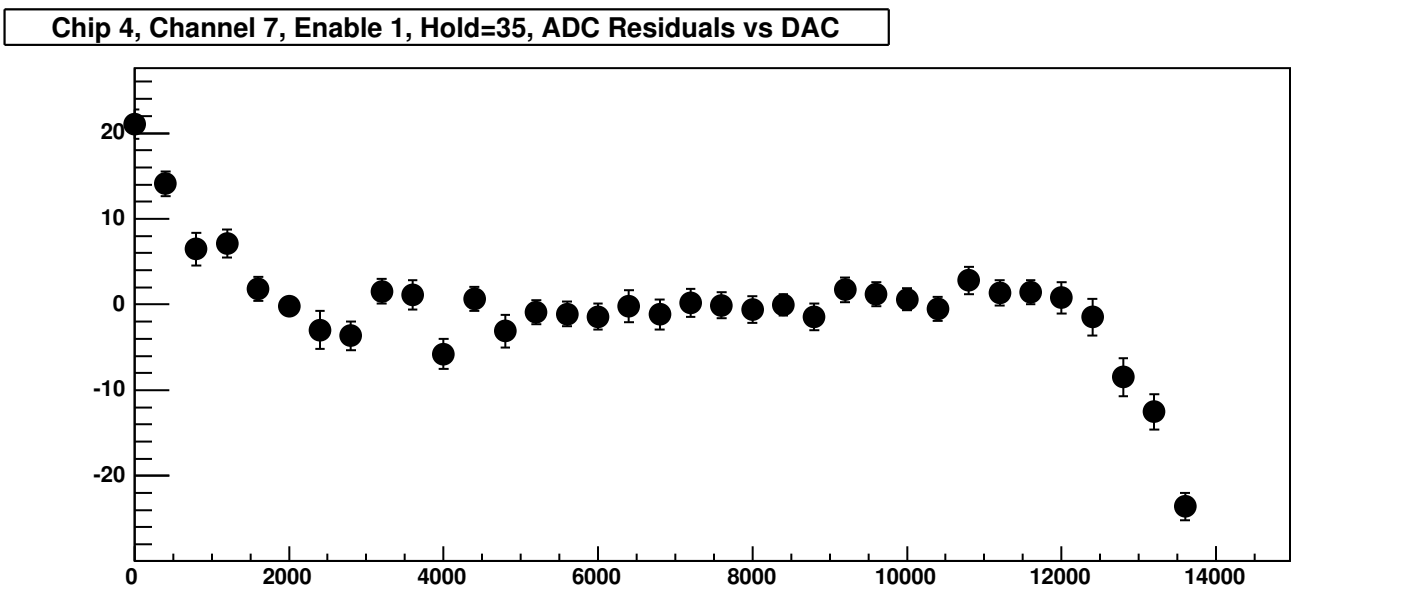


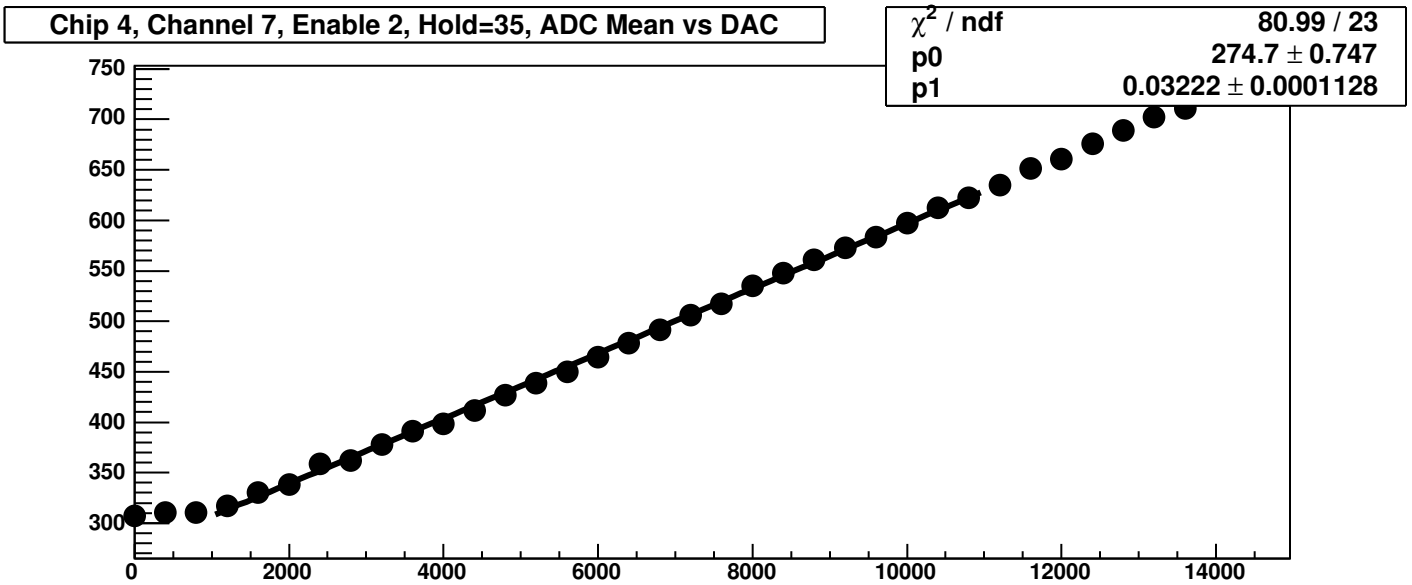
Chip 4, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

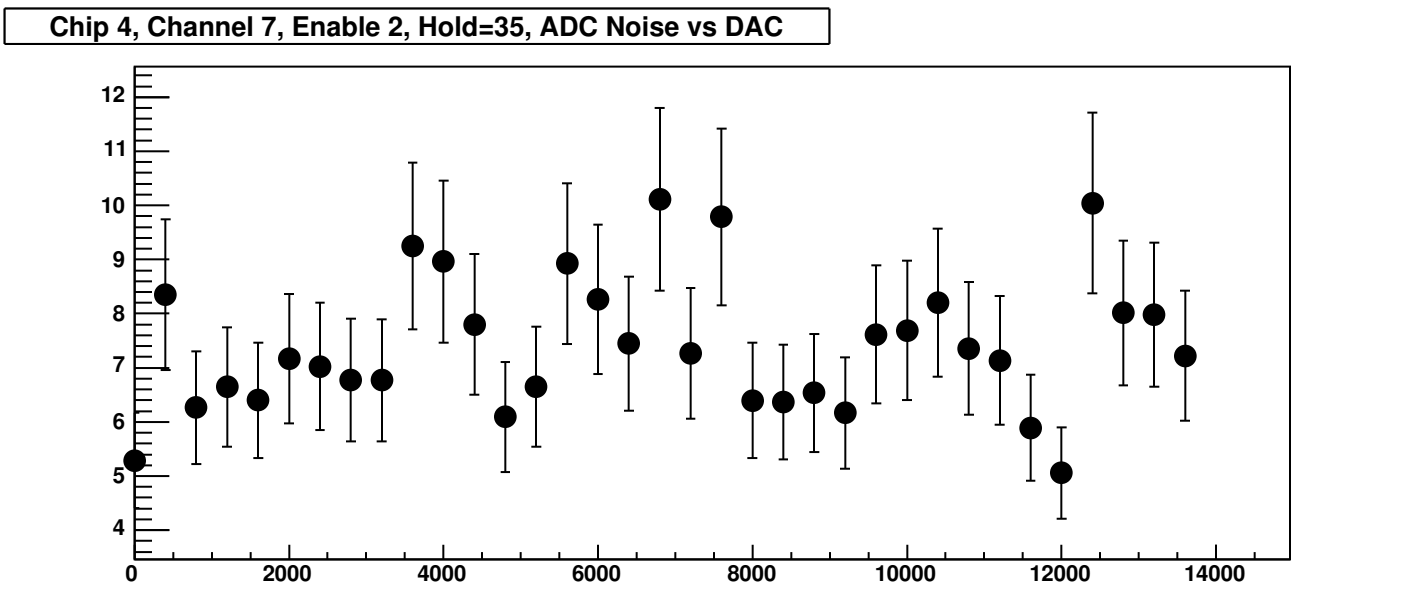


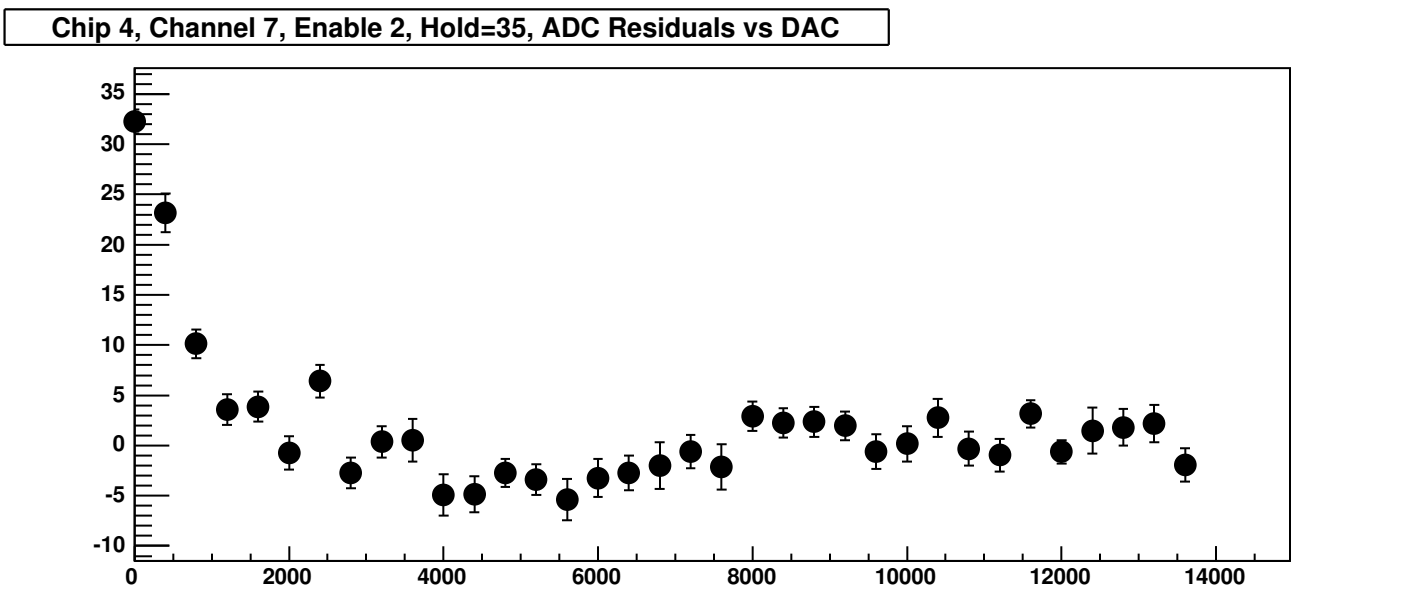

Chip 4, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 51.24 / 23
p0 277.4 ± 0.6921
p1 0.01816 ± 0.0001018
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

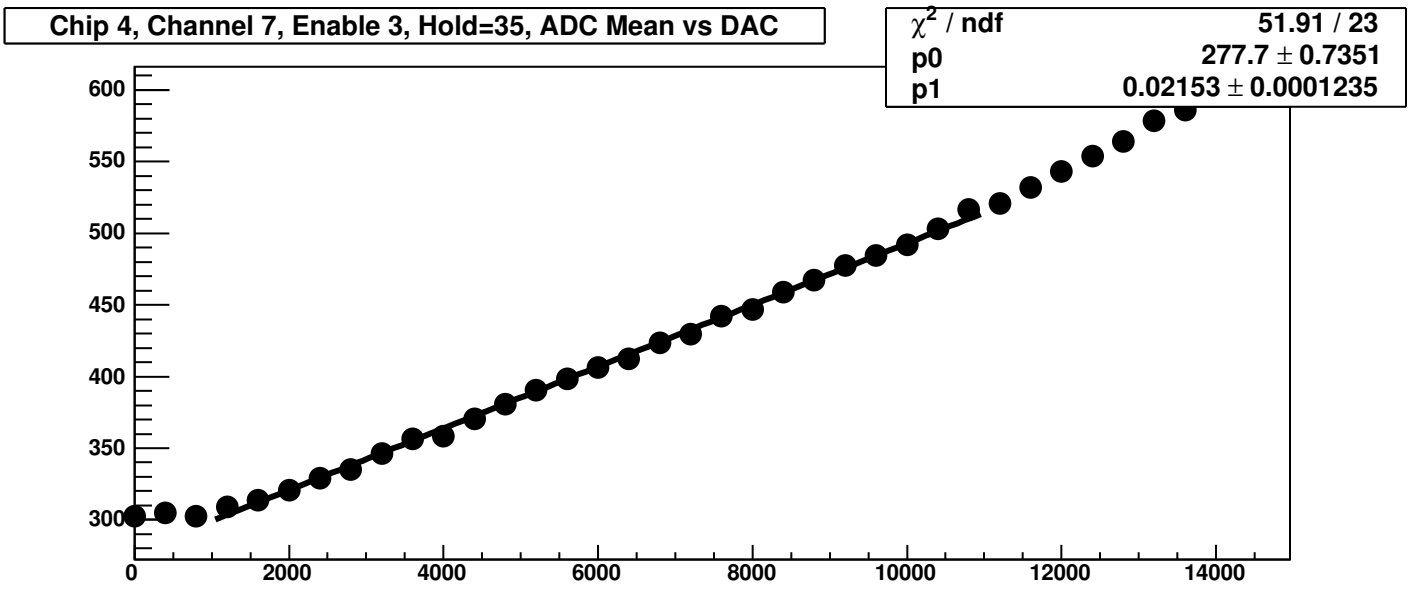

Chip 4, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

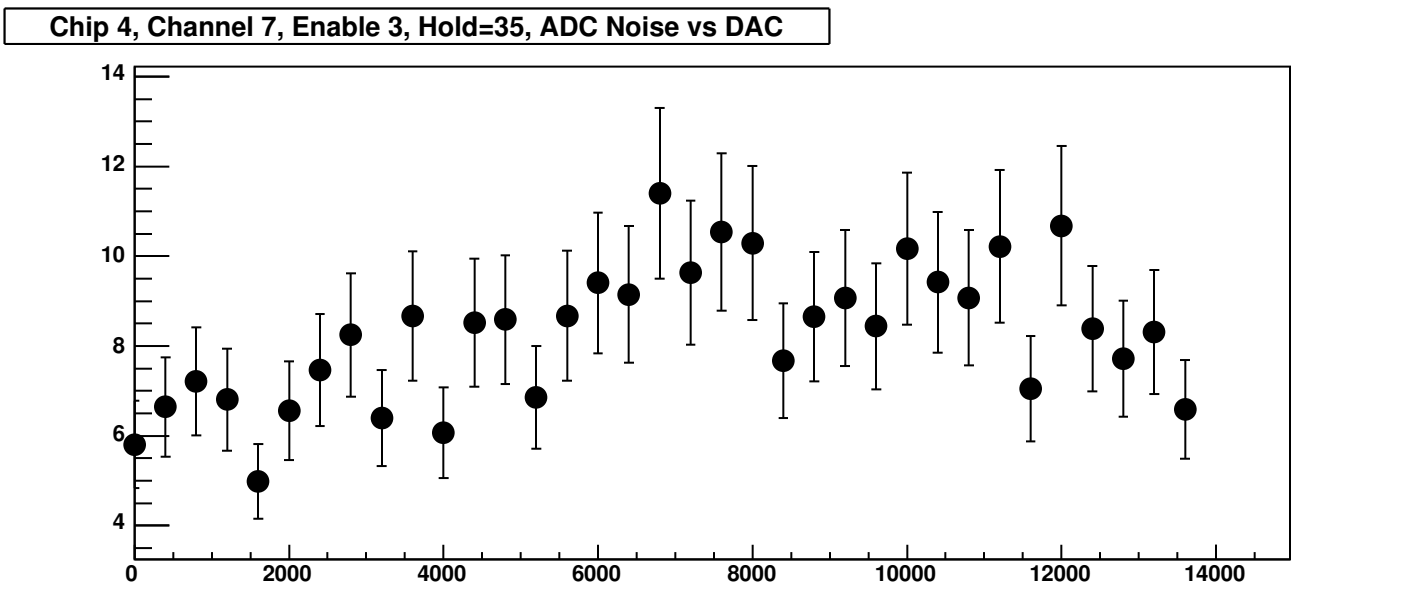

Chip 4, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

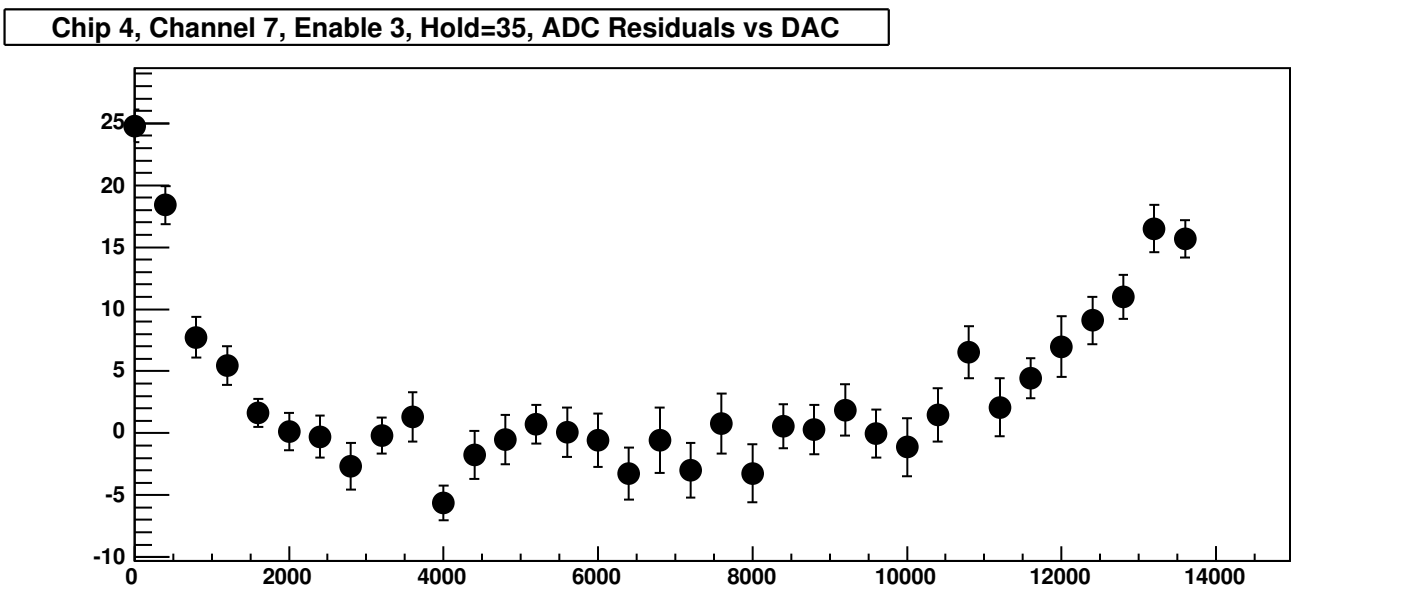

Chip 4, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 80.99 / 23
p0 274.7 ± 0.747
p1 0.03222 ± 0.0001128
750
700
650
600
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

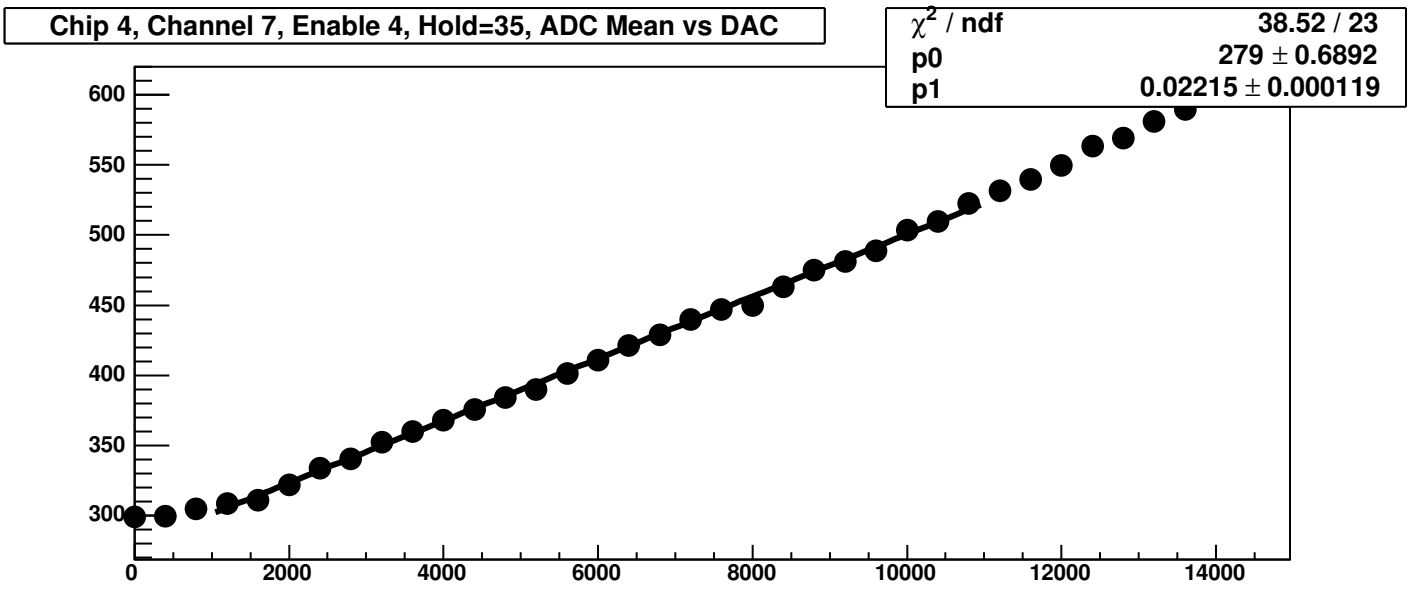

Chip 4, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

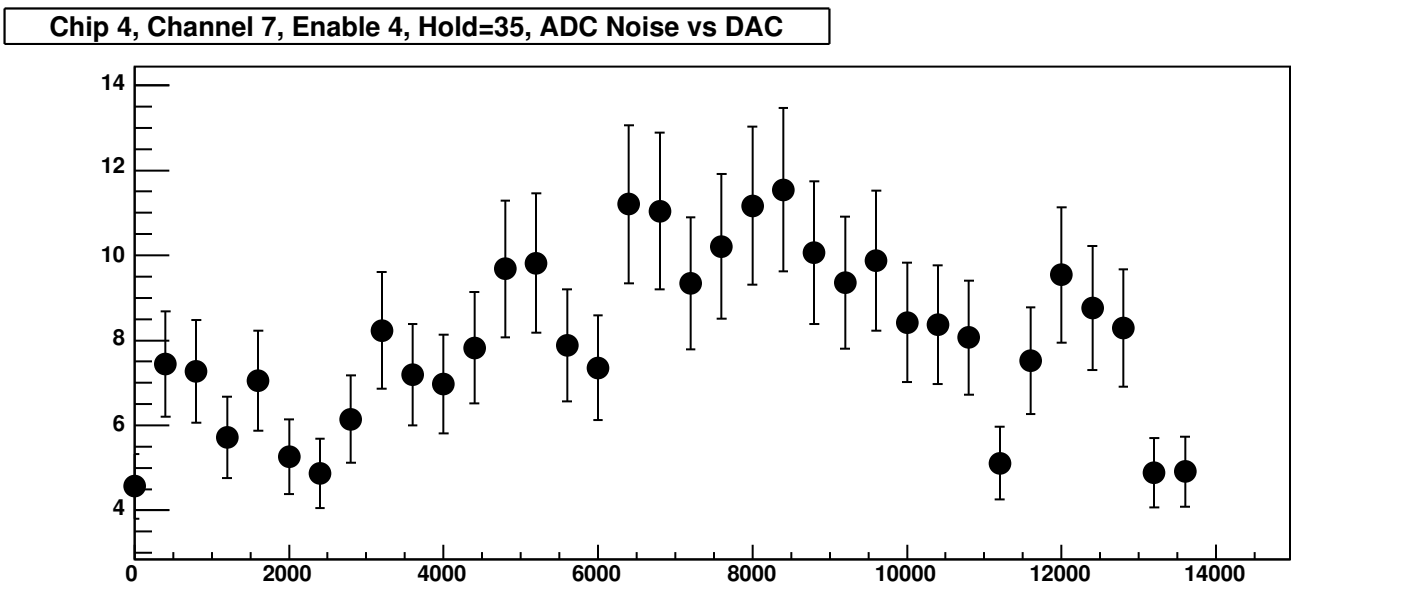

Chip 4, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

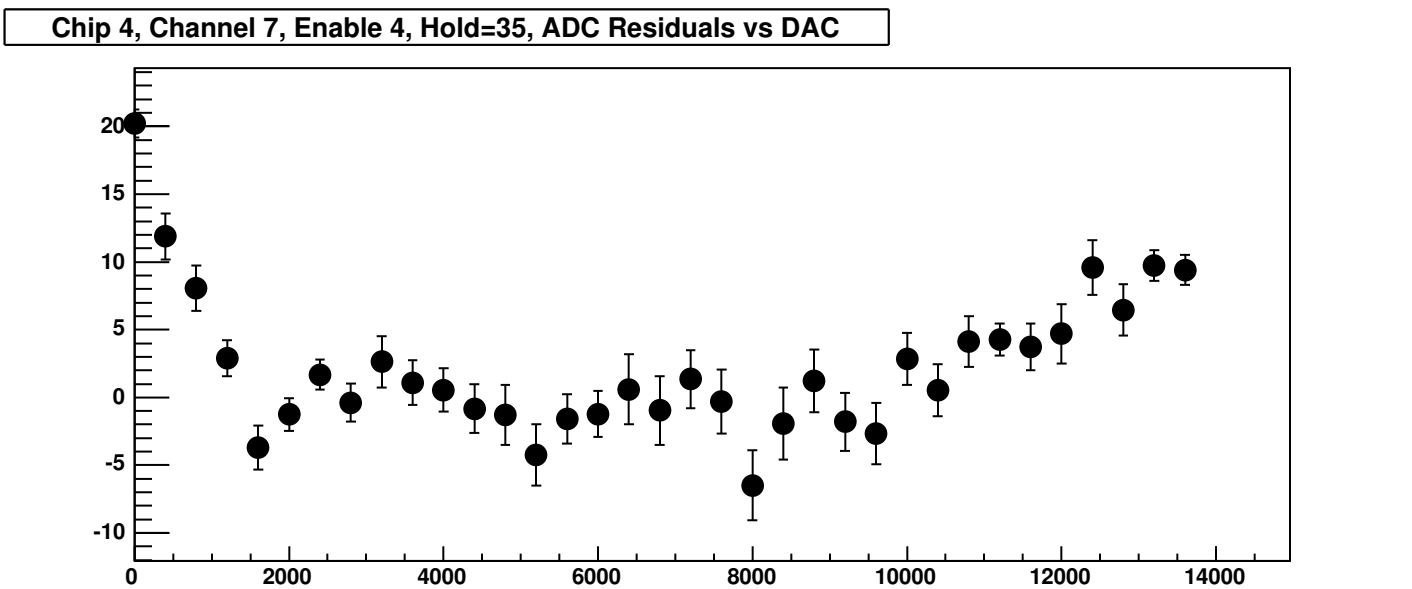

Chip 4, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 51.91 / 23
p0 277.7 ± 0.7351
p1 0.02153 ± 0.0001235
600
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

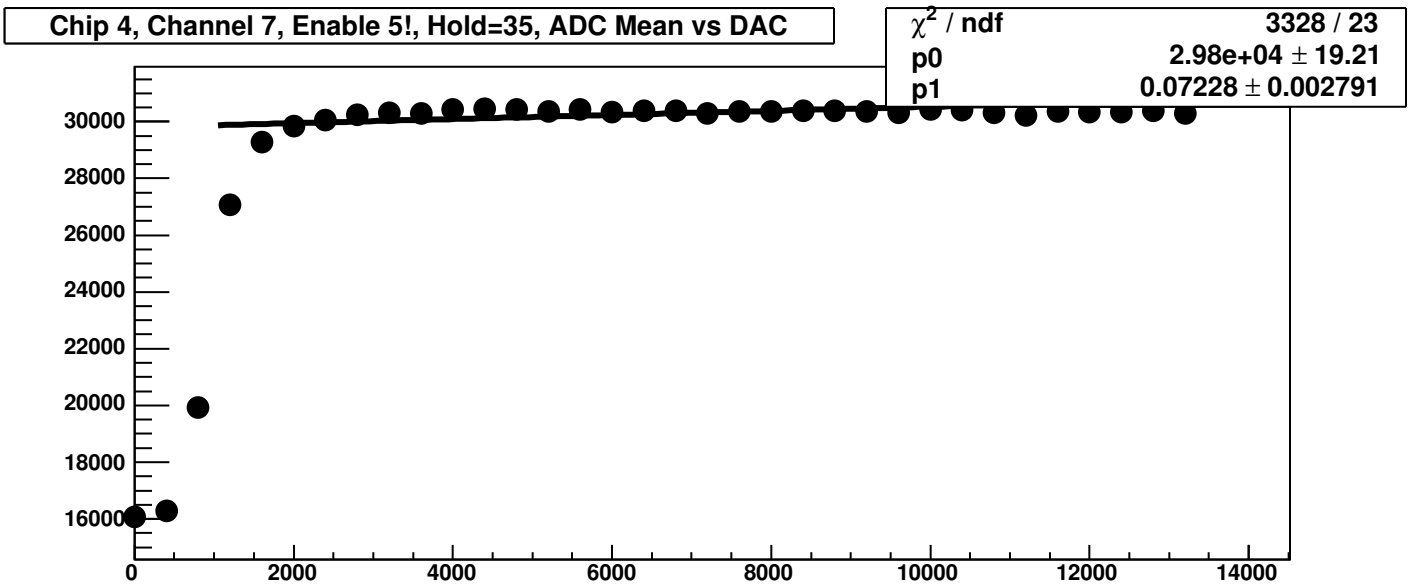

Chip 4, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

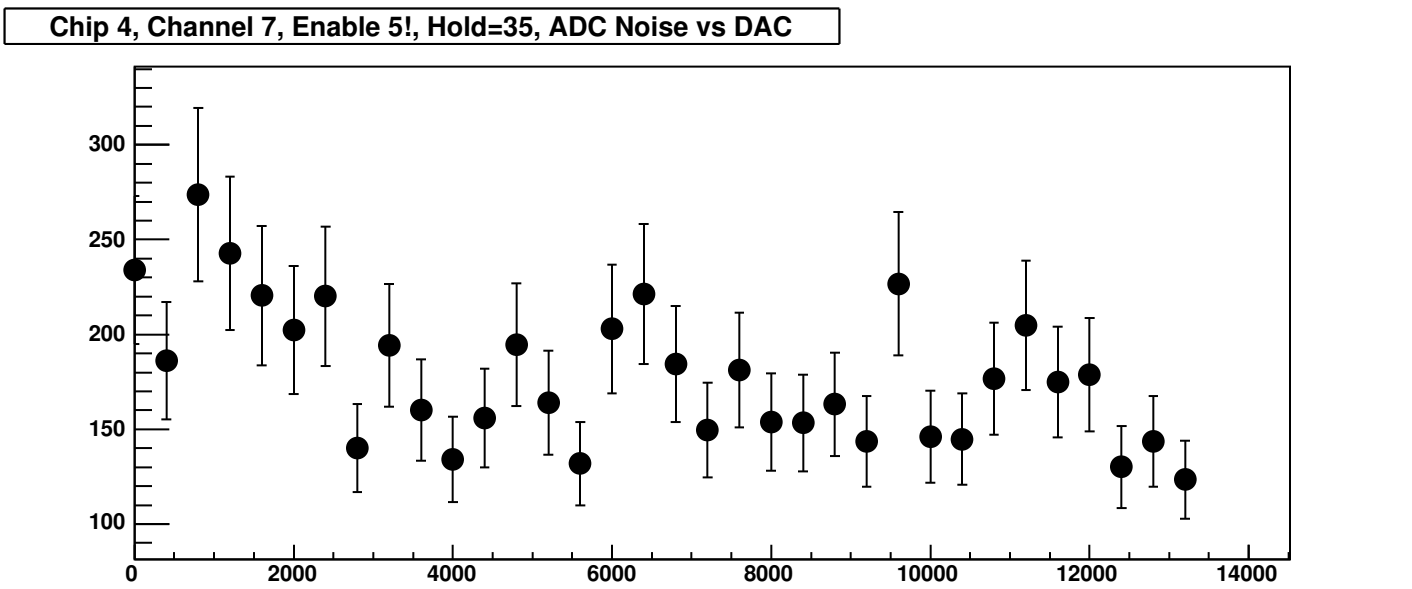

Chip 4, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

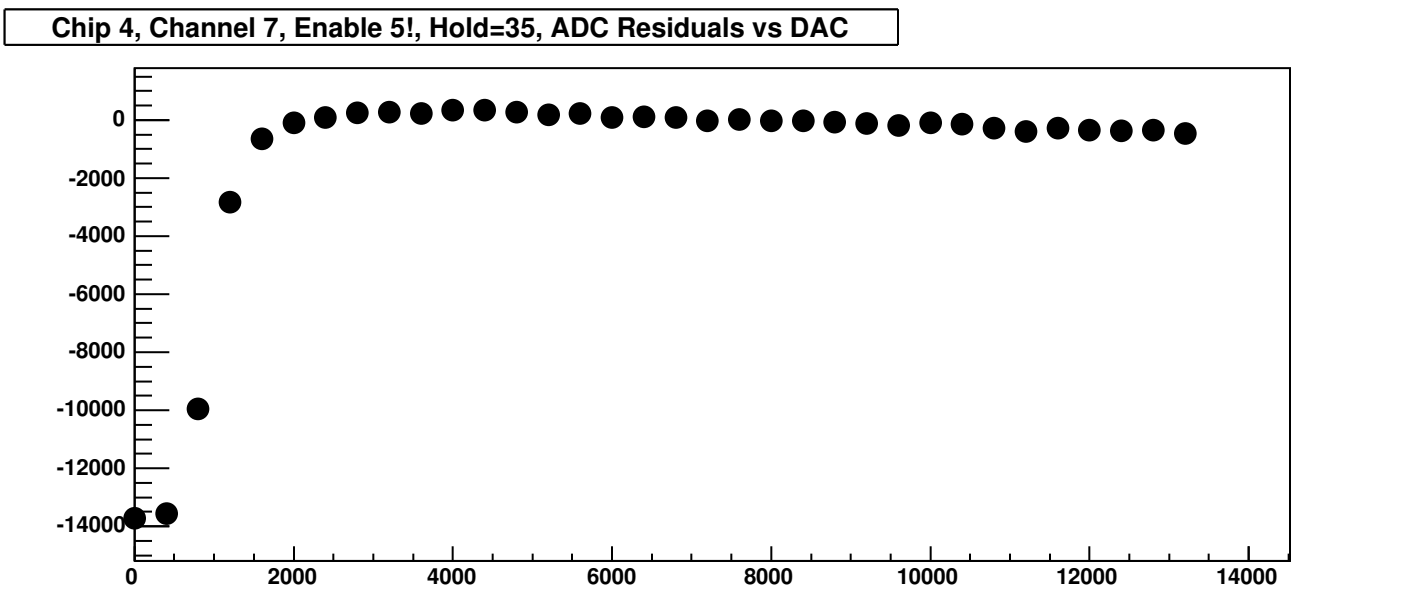

Chip 4, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 38.52 / 23
p0 279 ± 0.6892
p1 0.02215 ± 0.000119
600
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

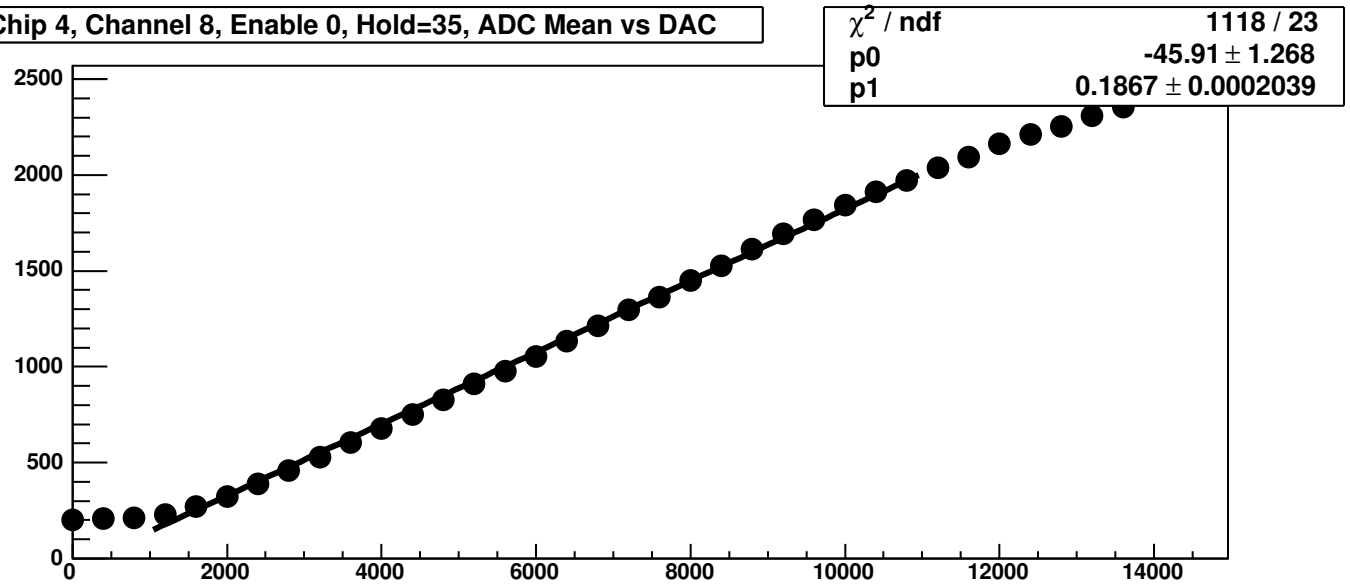

Chip 4, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ^2 / ndf 3328 / 23
p0 2.98e+04 ± 19.21
p1 0.07228 ± 0.002791
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

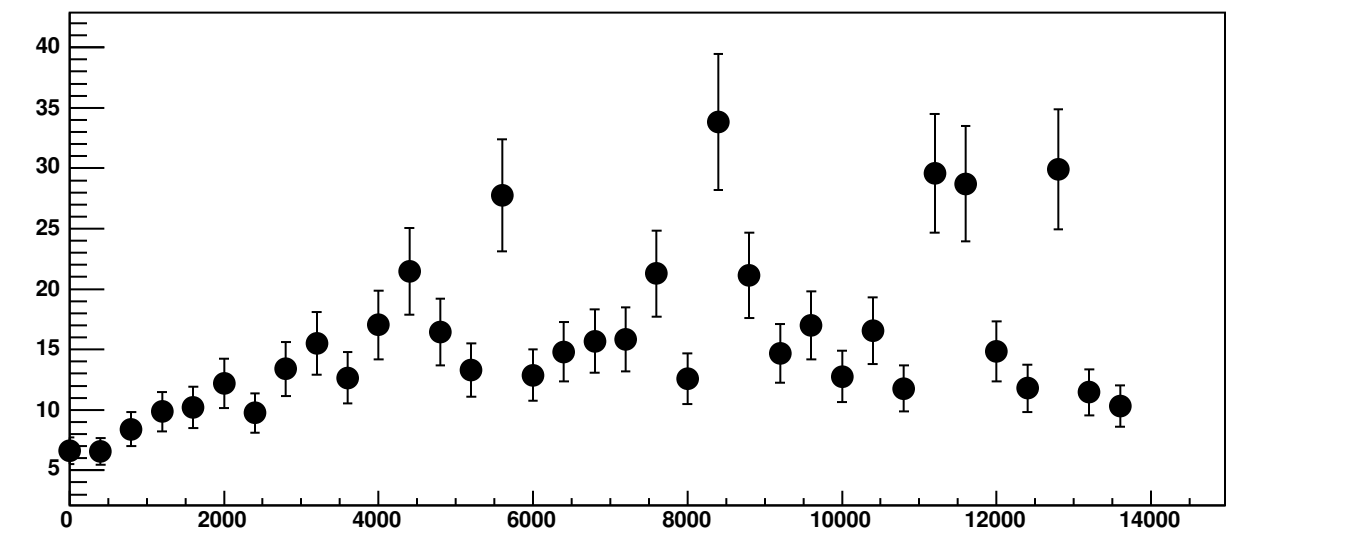

Chip 4, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

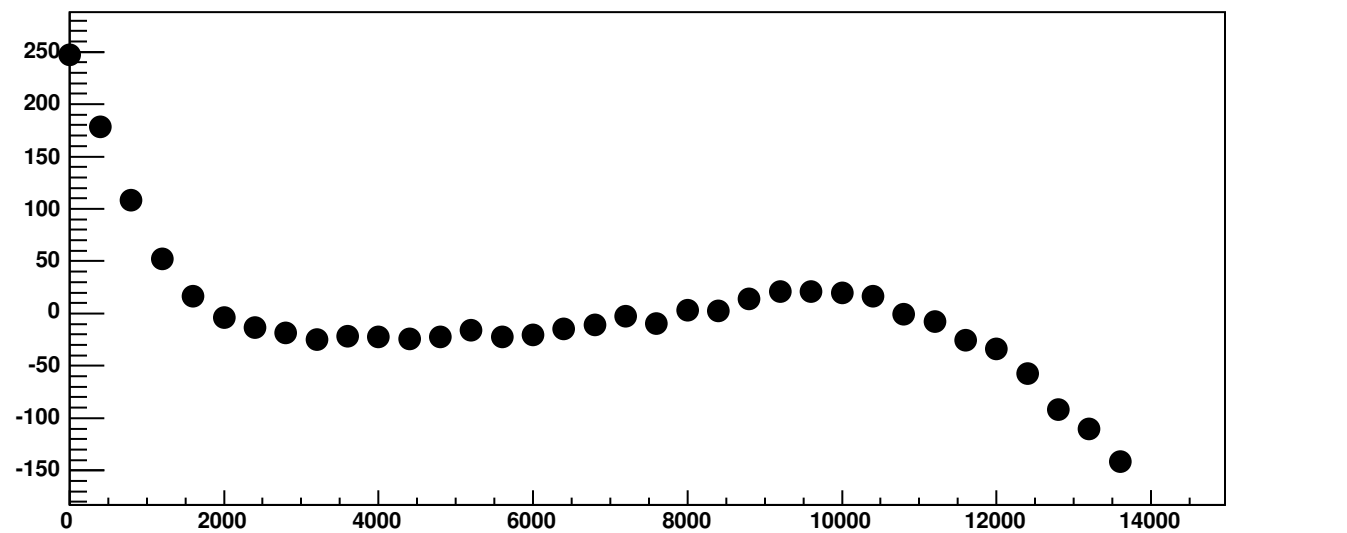
Chip 4, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



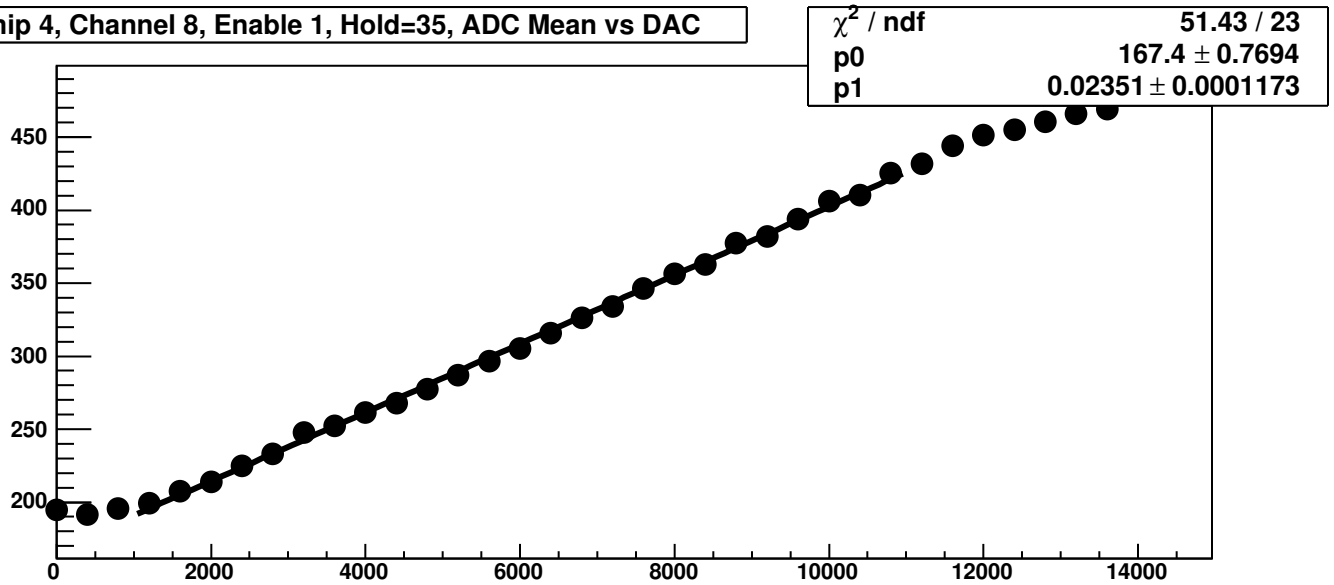
Chip 4, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



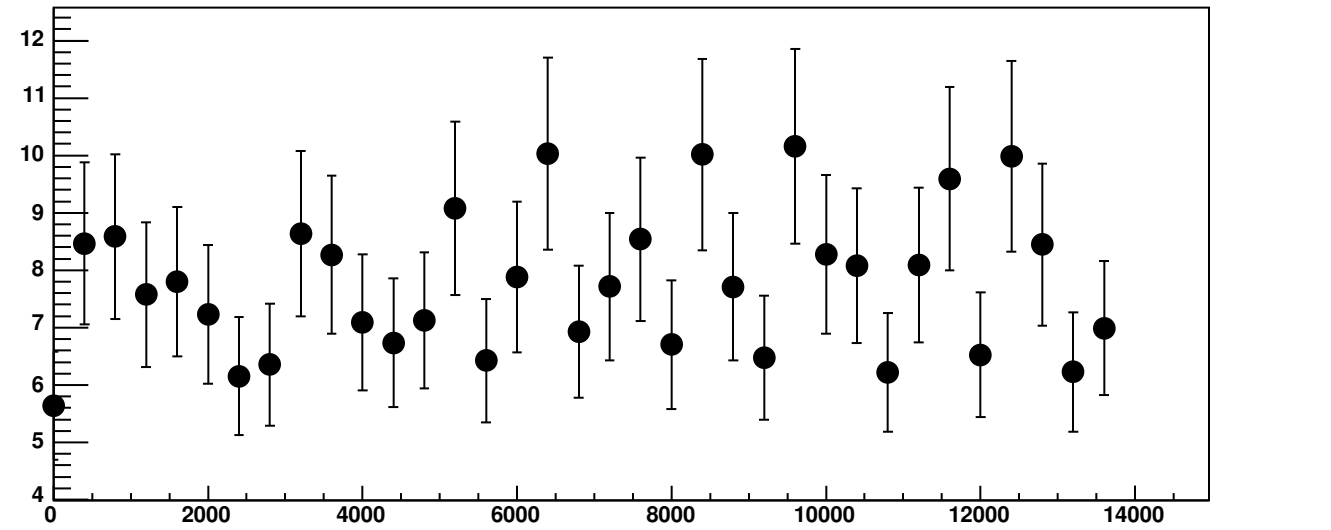
Chip 4, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



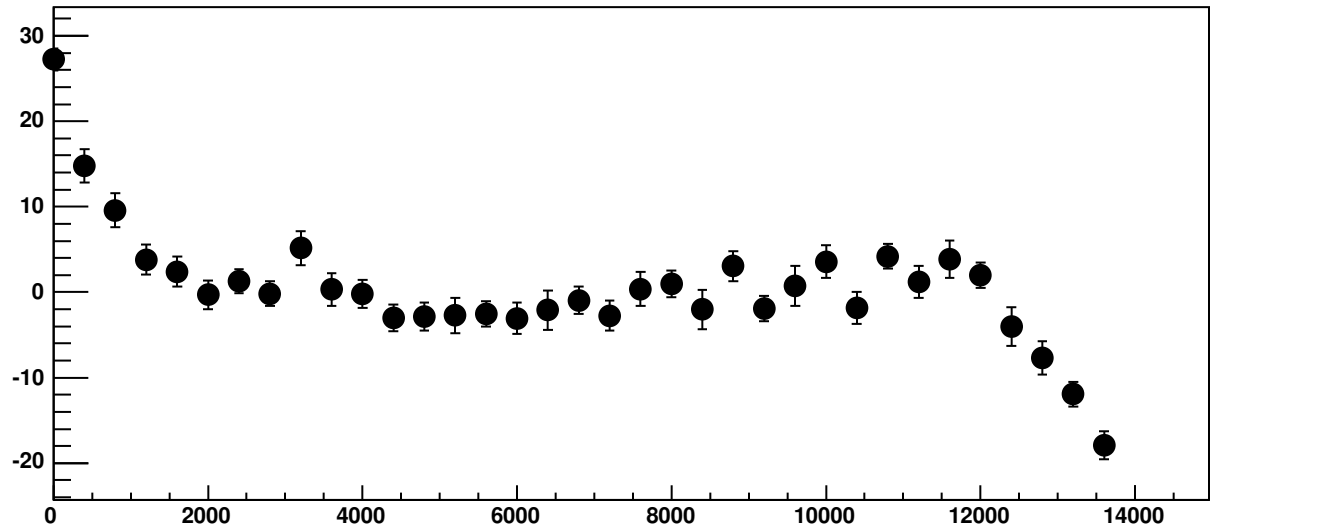
Chip 4, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

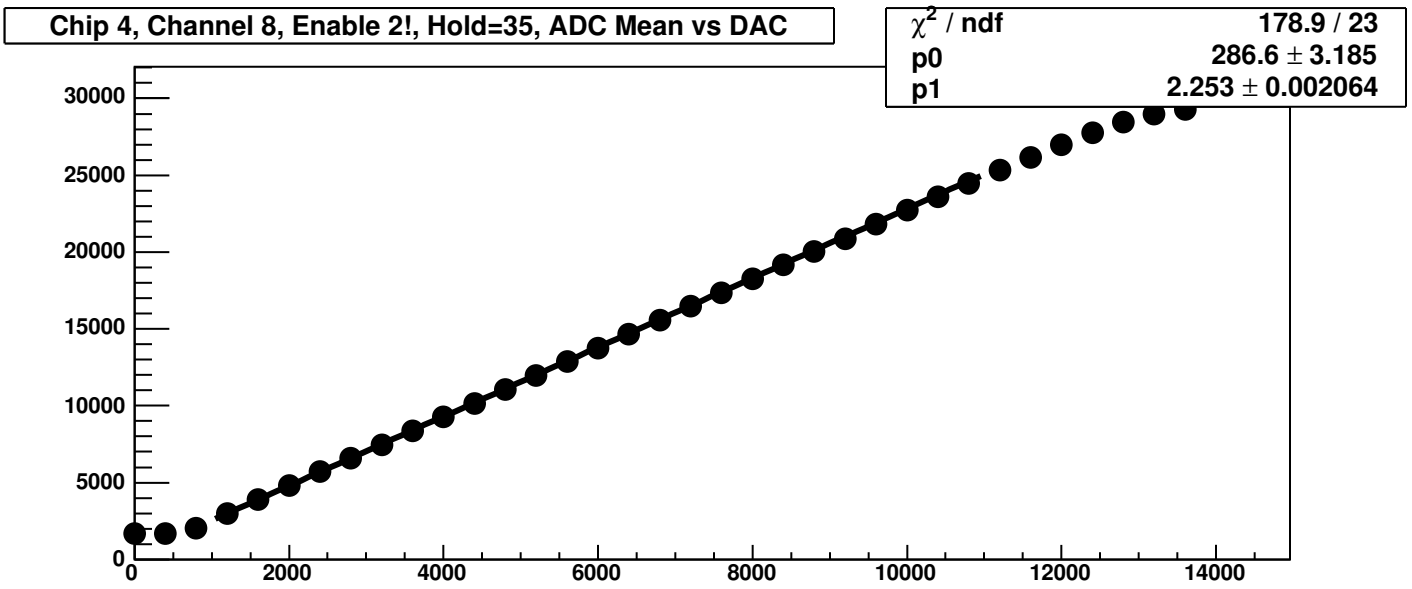


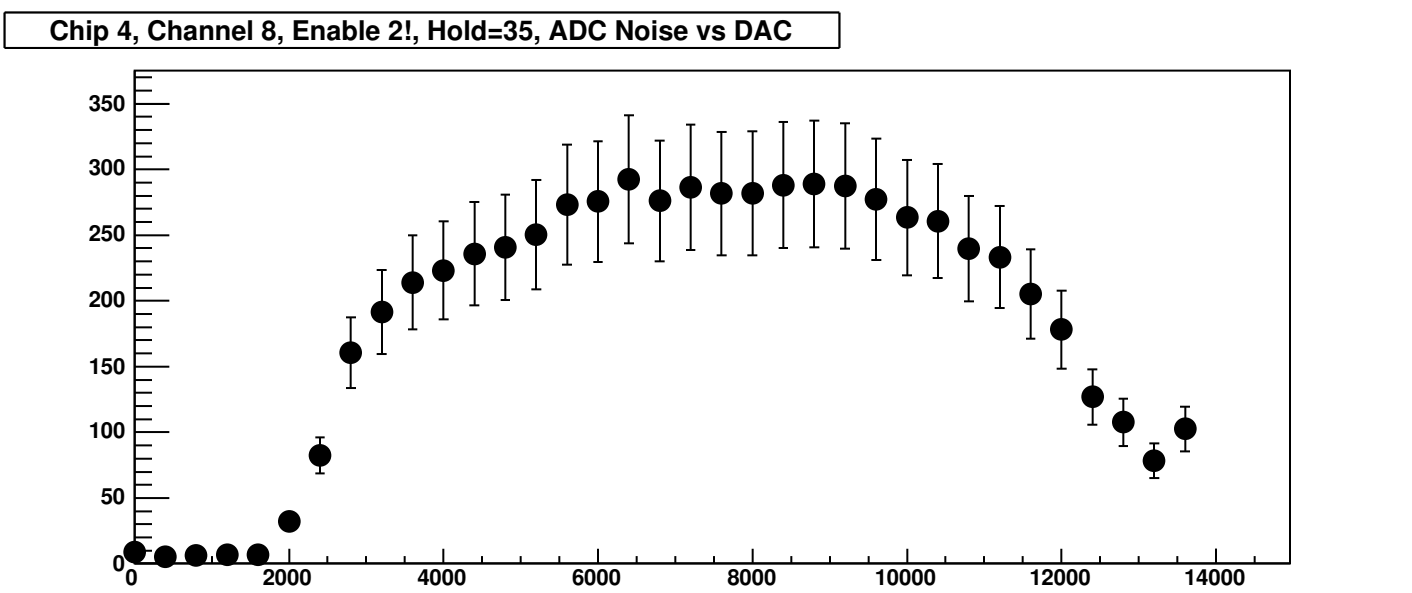
Chip 4, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC

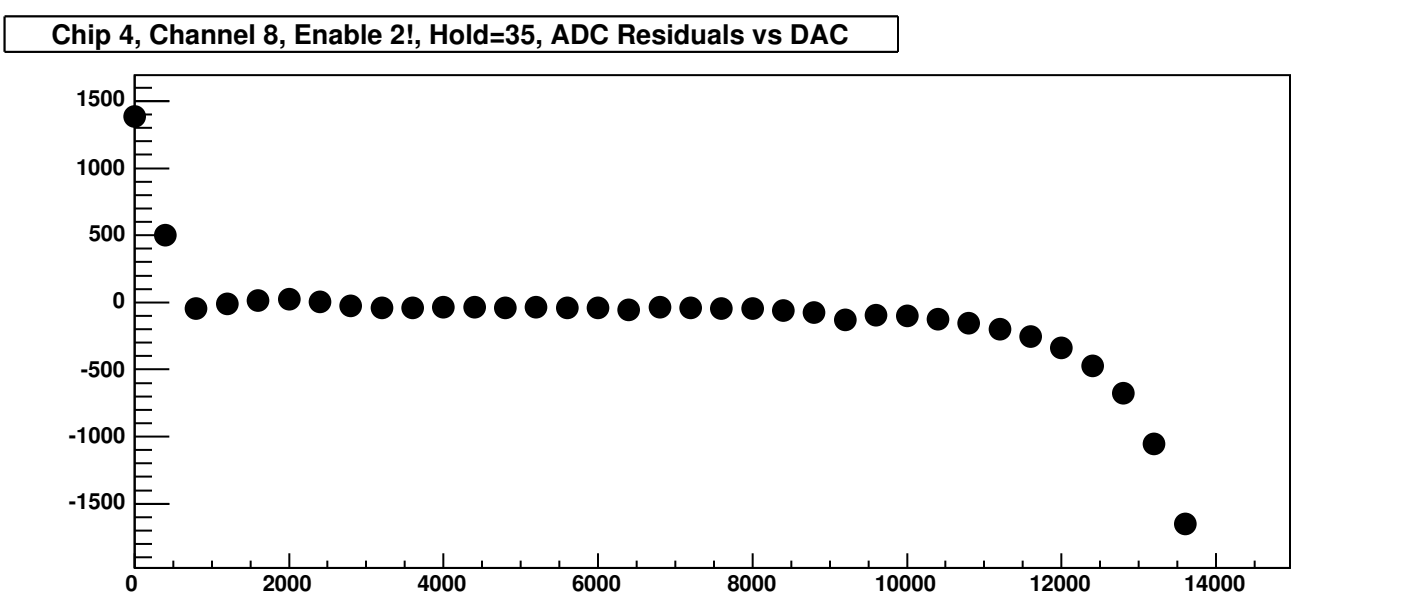


Chip 4, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

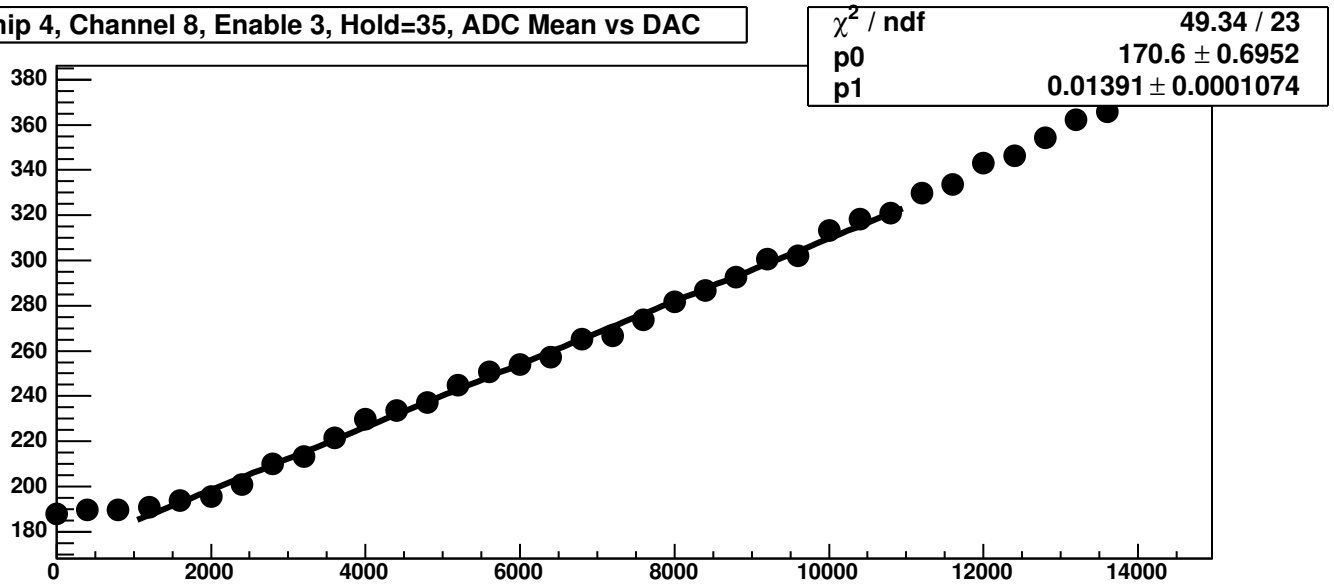



Chip 4, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 178.9 / 23
p0 286.6 ± 3.185
p1 2.253 ± 0.002064
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

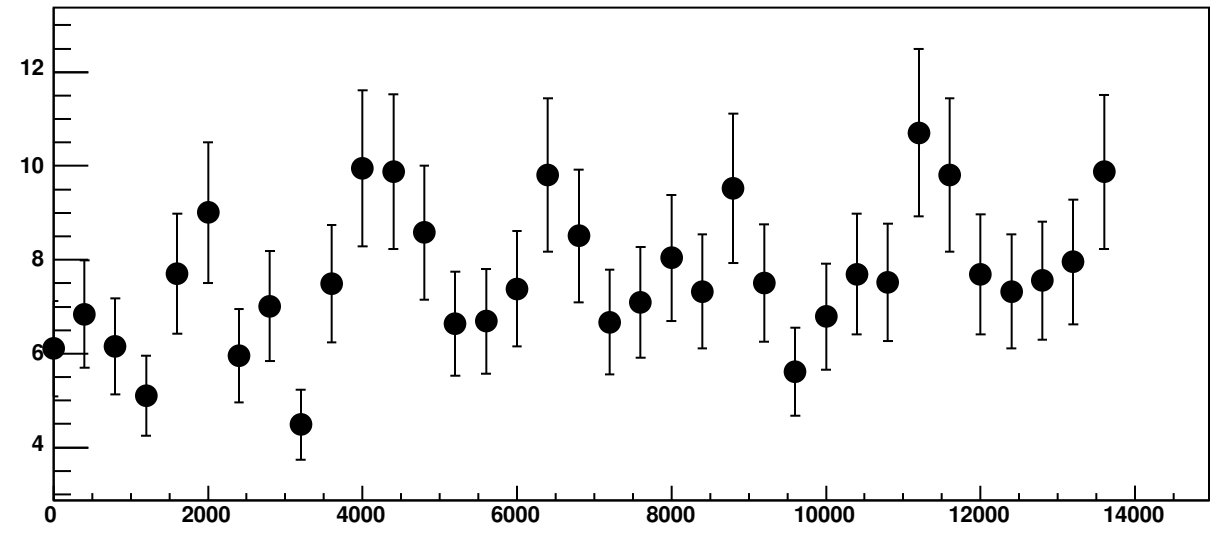

Chip 4, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

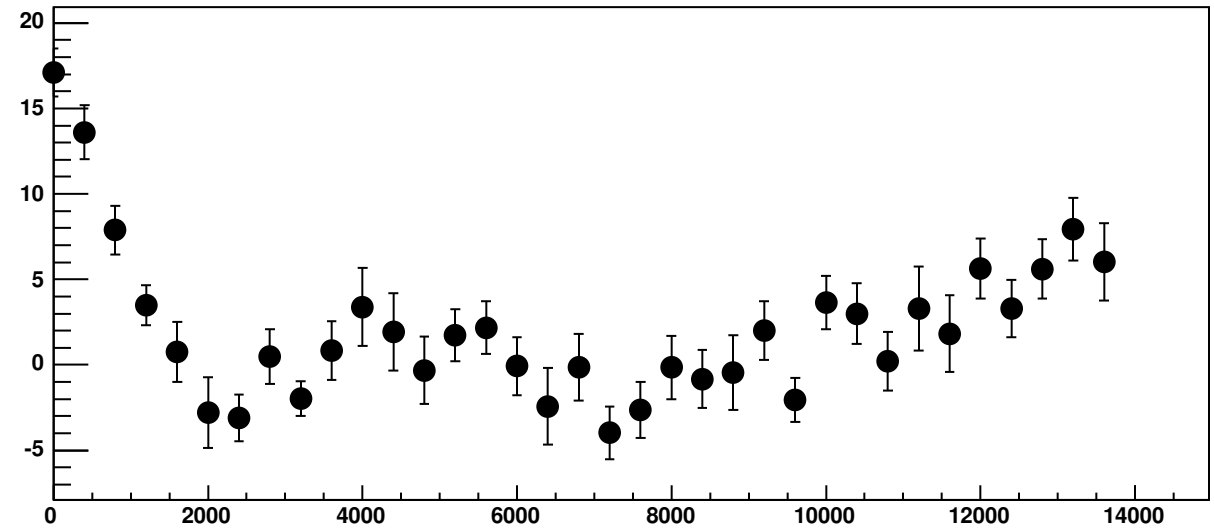
Chip 4, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



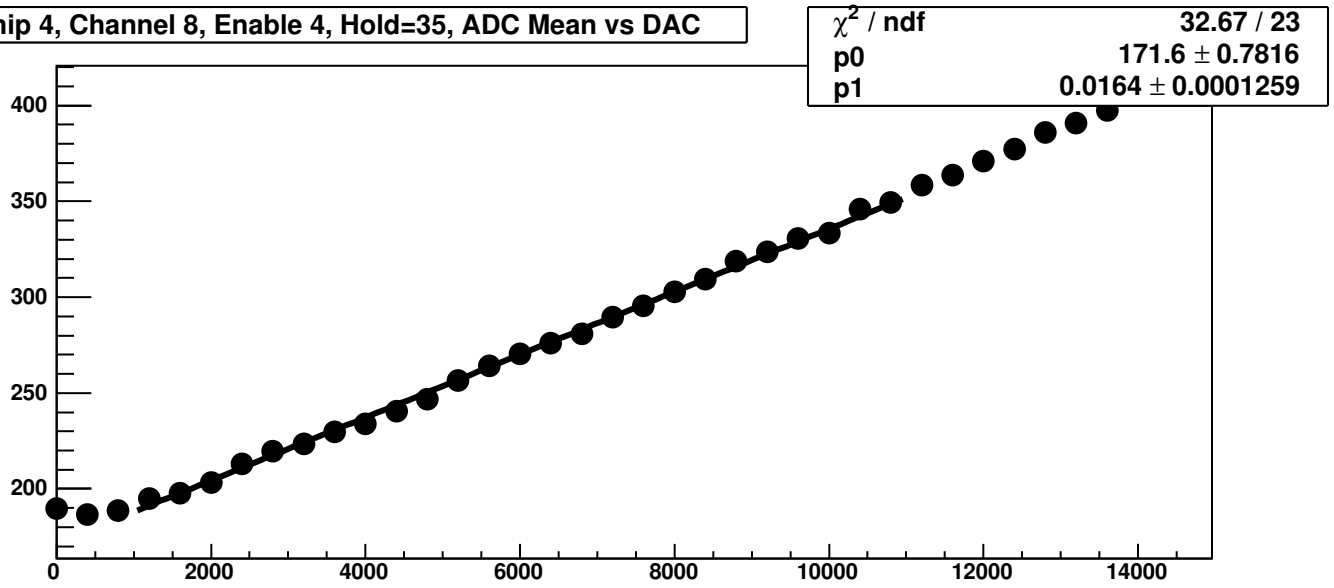
Chip 4, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



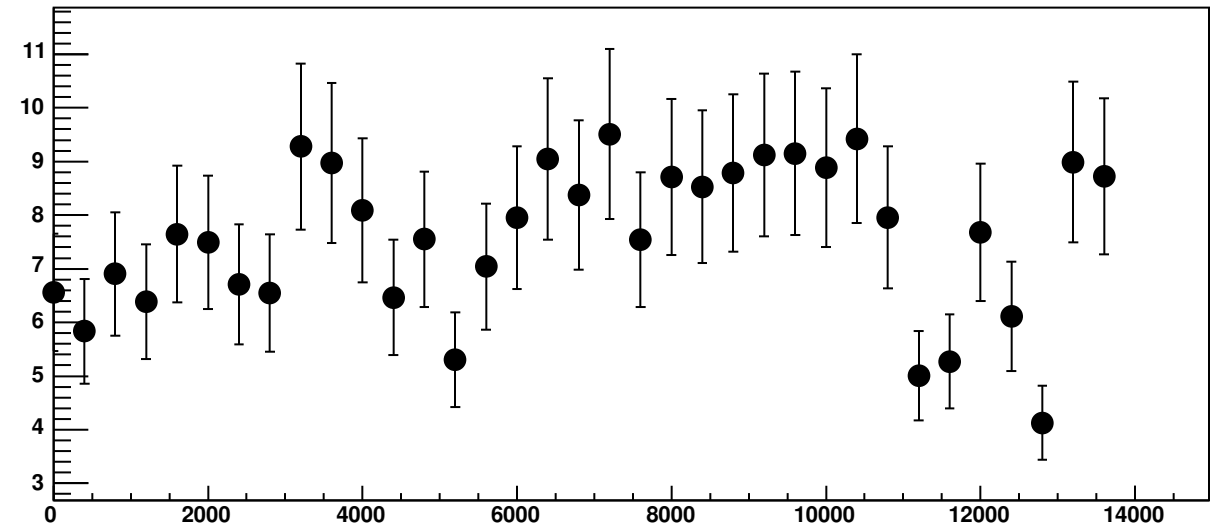
Chip 4, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



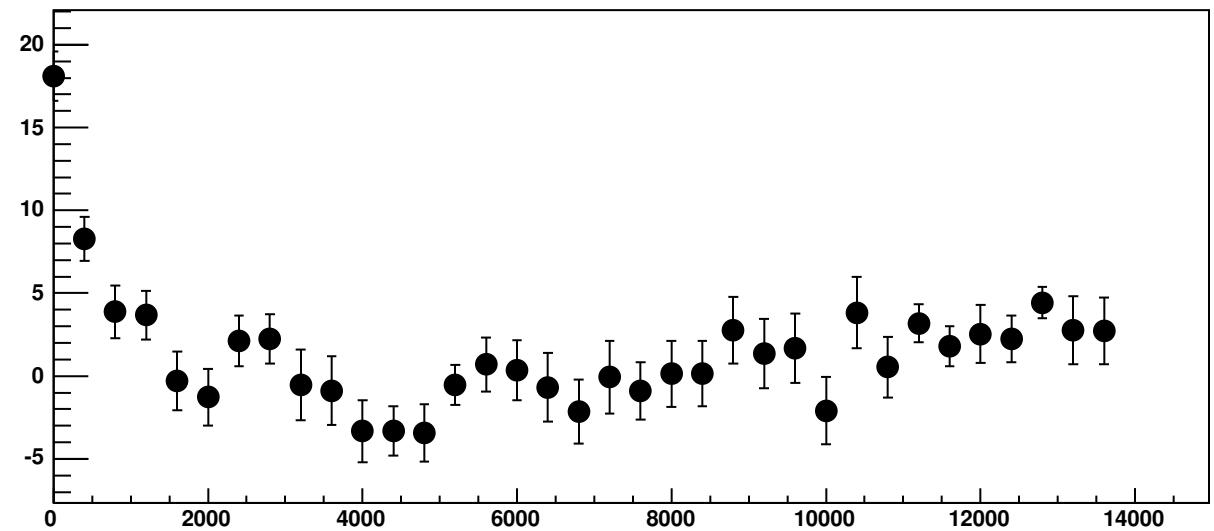
Chip 4, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



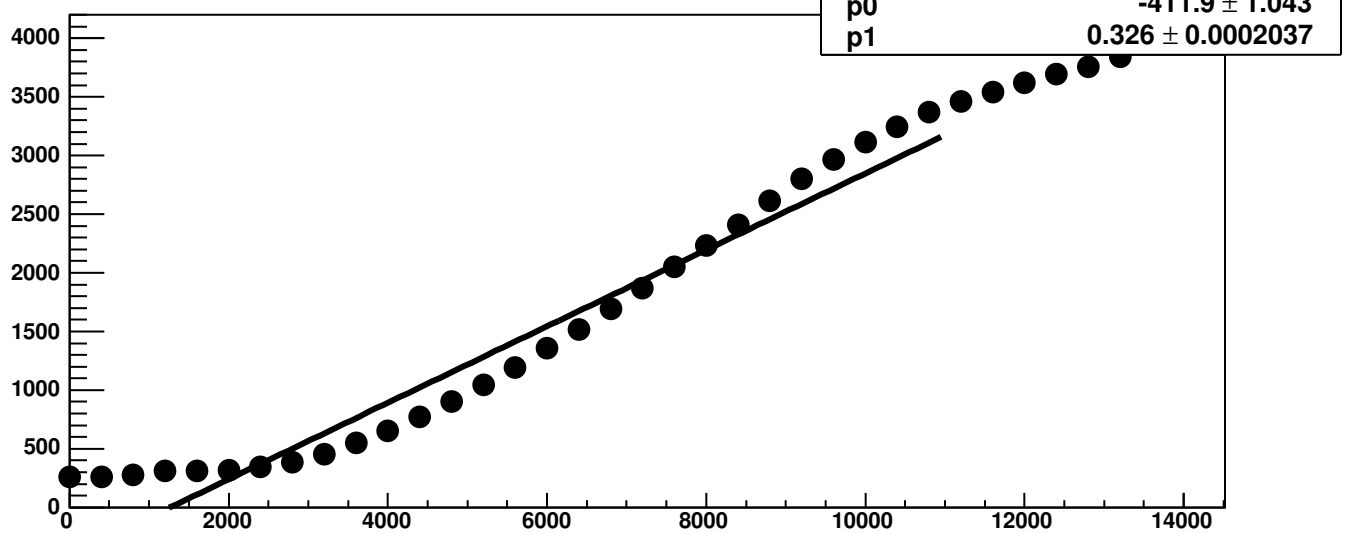
Chip 4, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



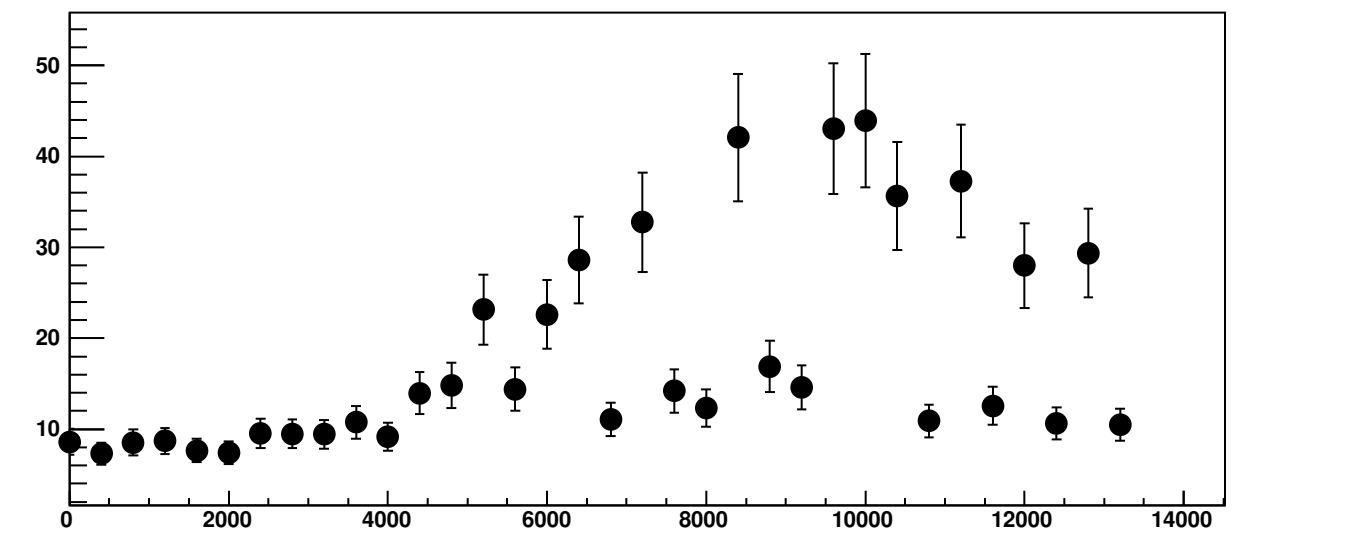
Chip 4, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



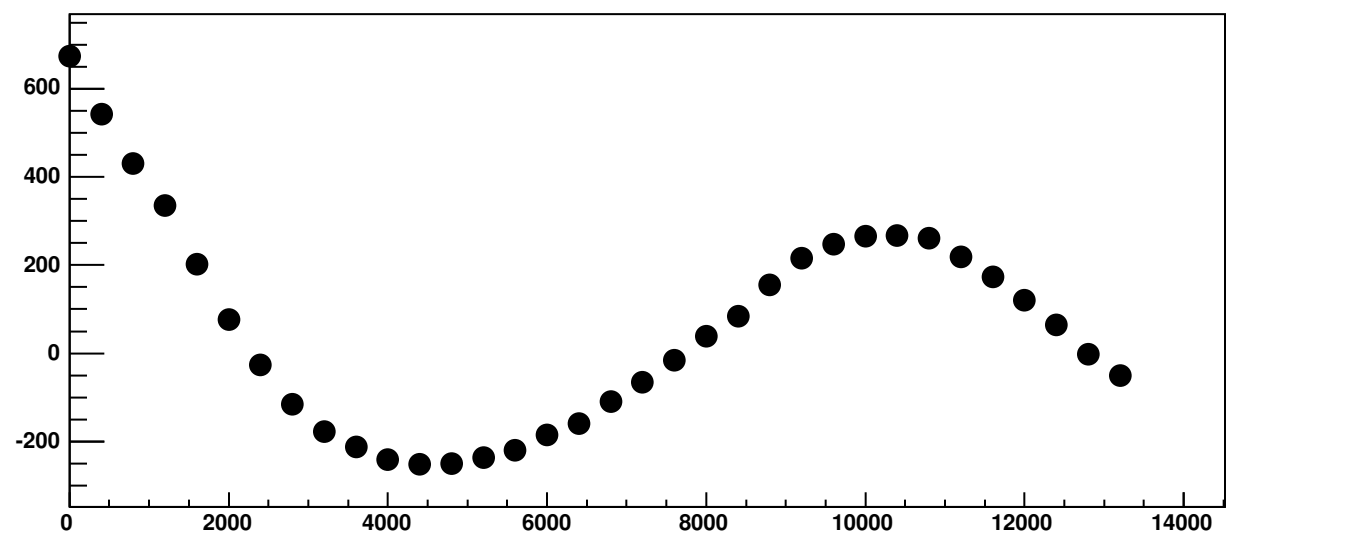
Chip 4, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC

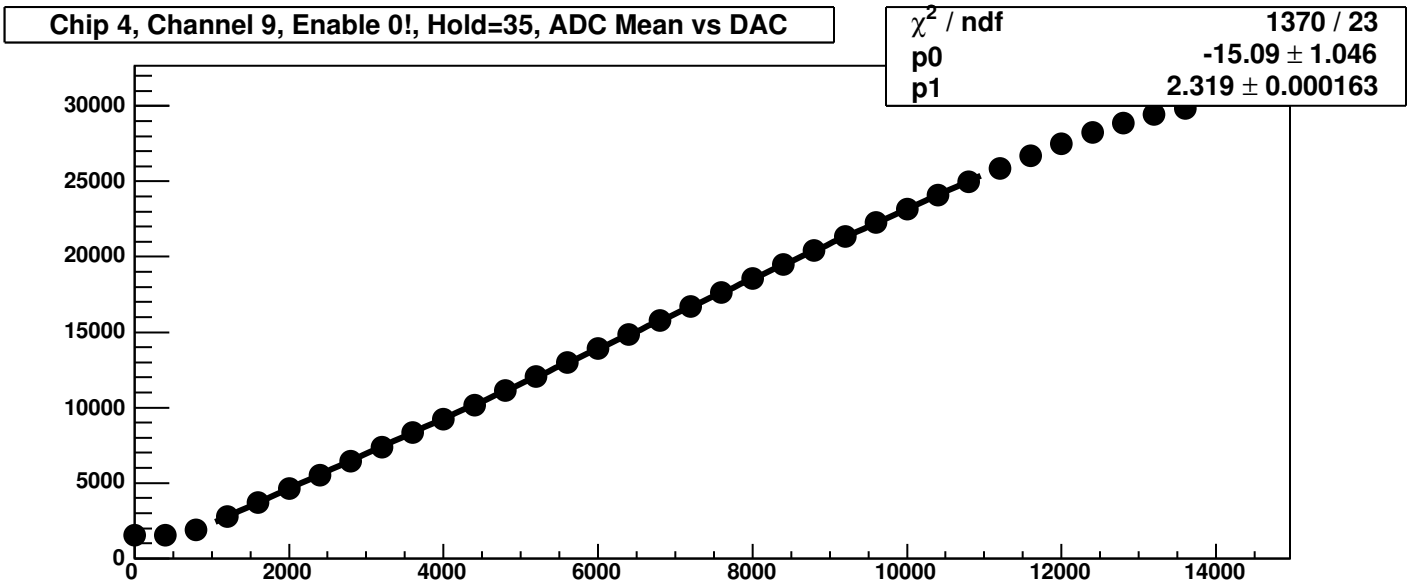


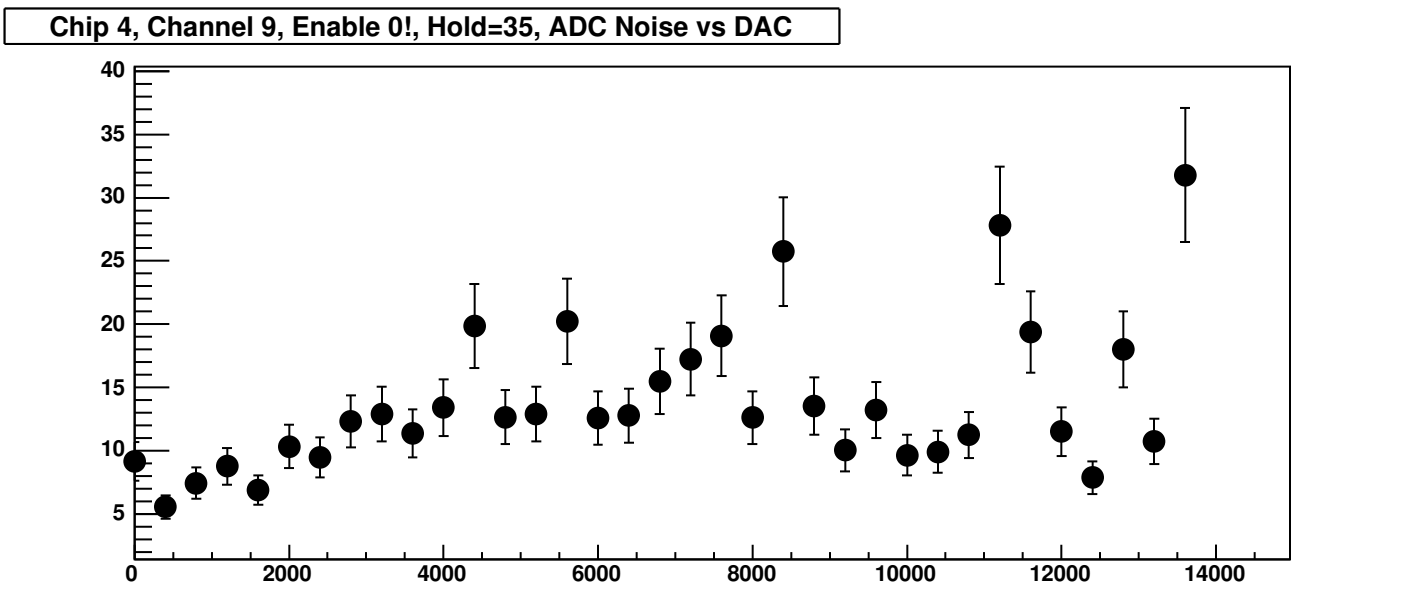
Chip 4, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC

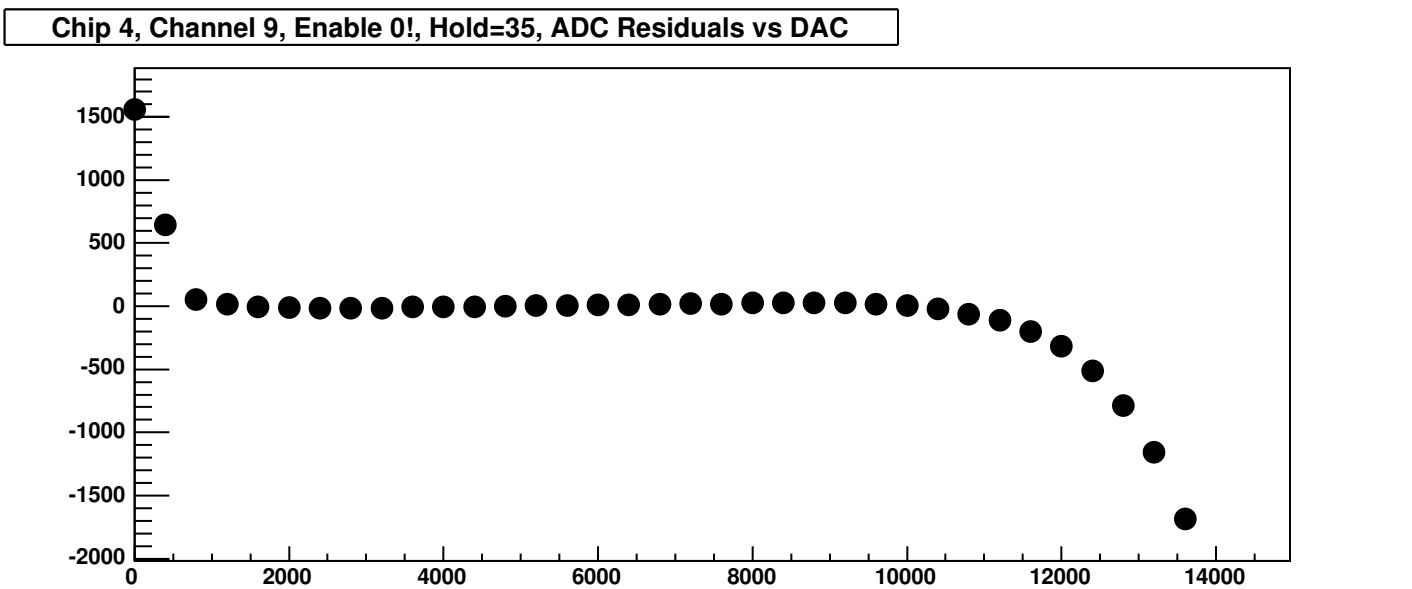


Chip 4, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC

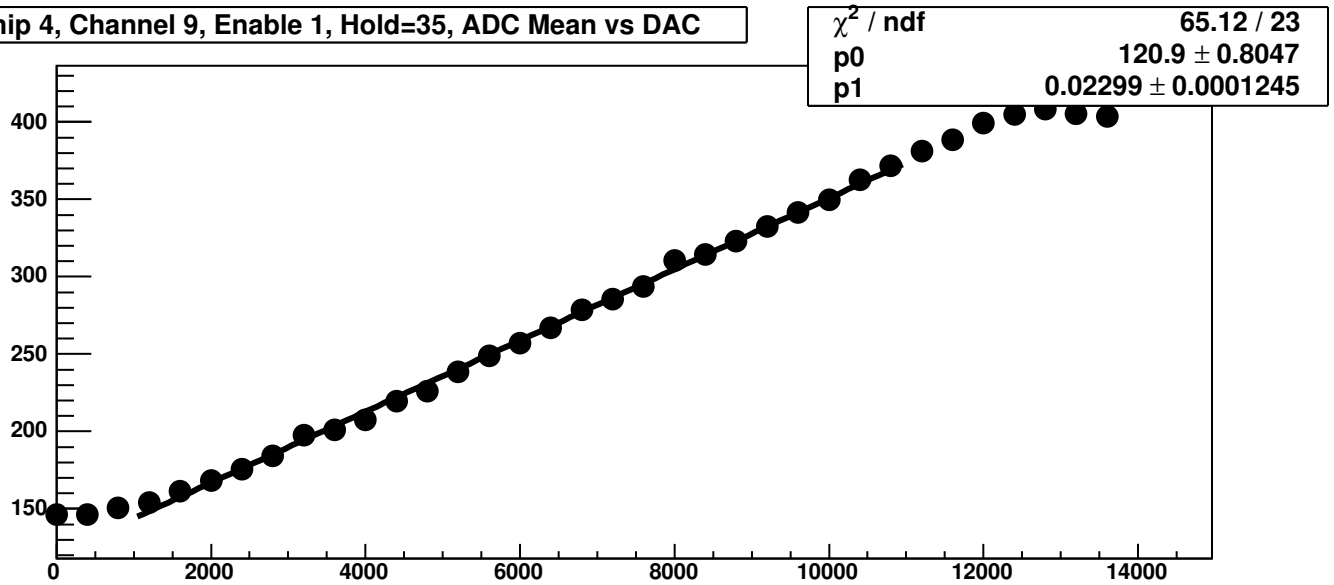



Chip 4, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1370 / 23
p0 -15.09 ± 1.046
p1 2.319 ± 0.000163
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

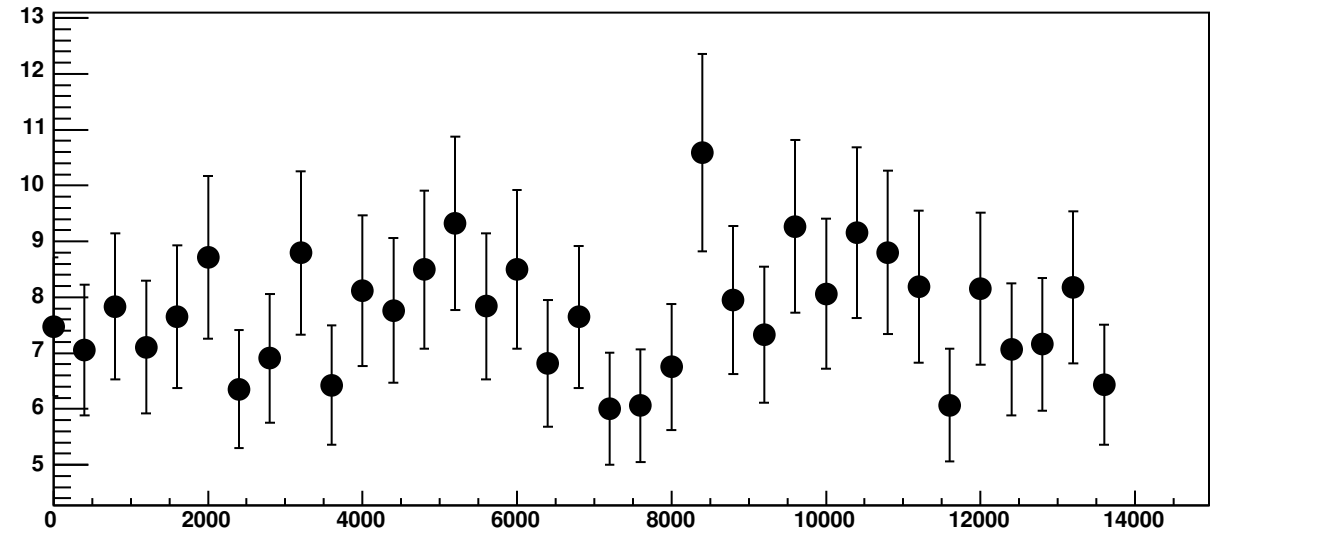

Chip 4, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

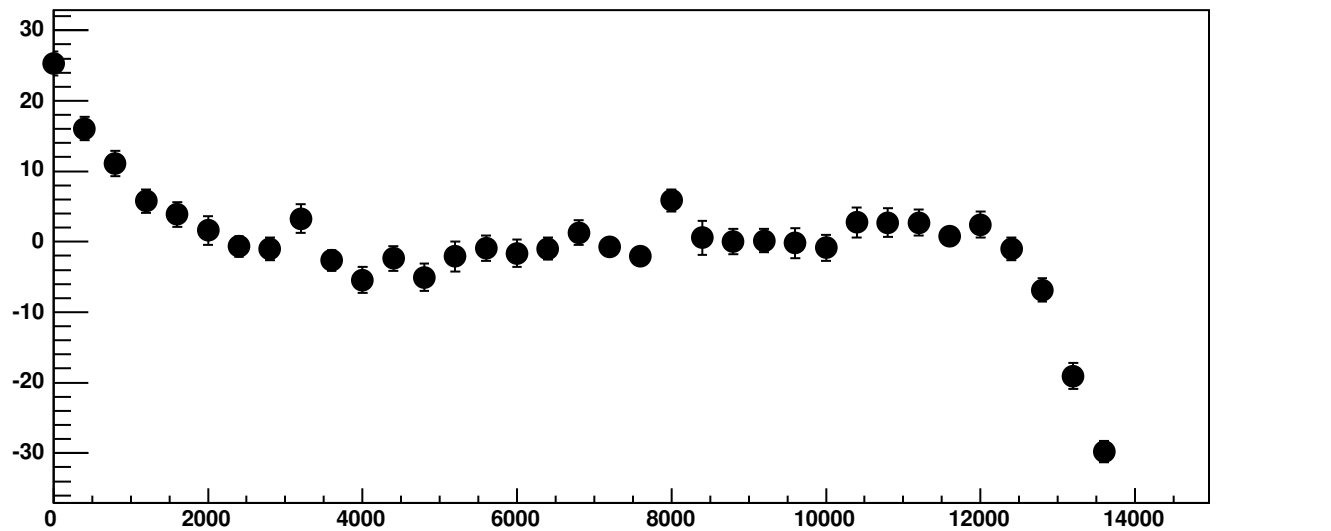
Chip 4, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



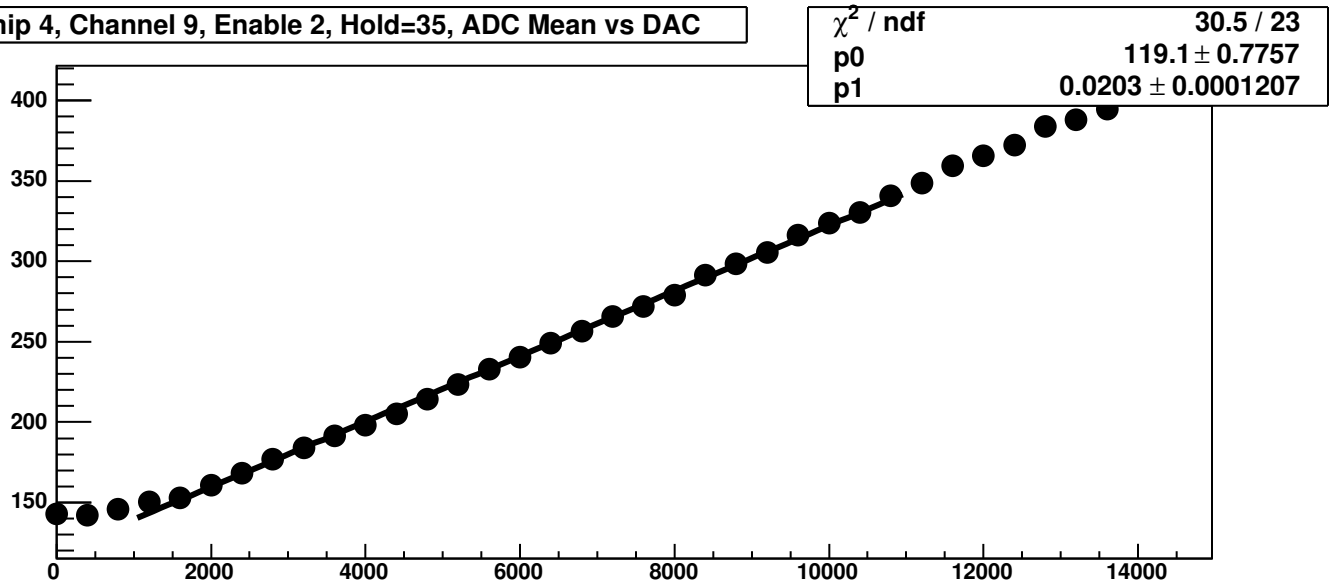
Chip 4, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



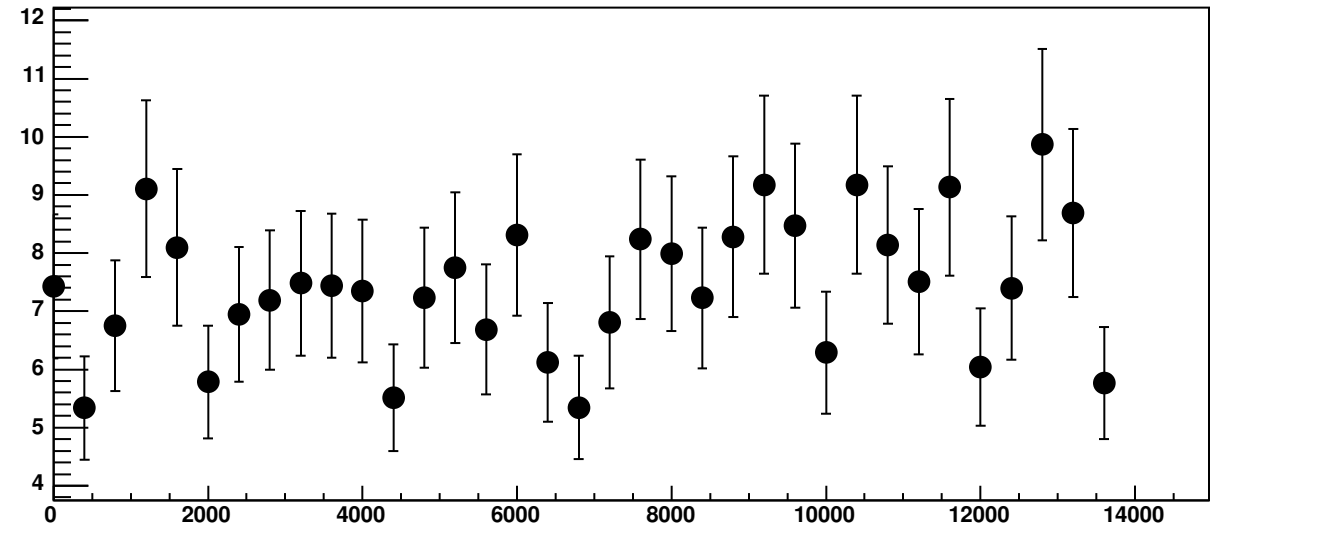
Chip 4, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



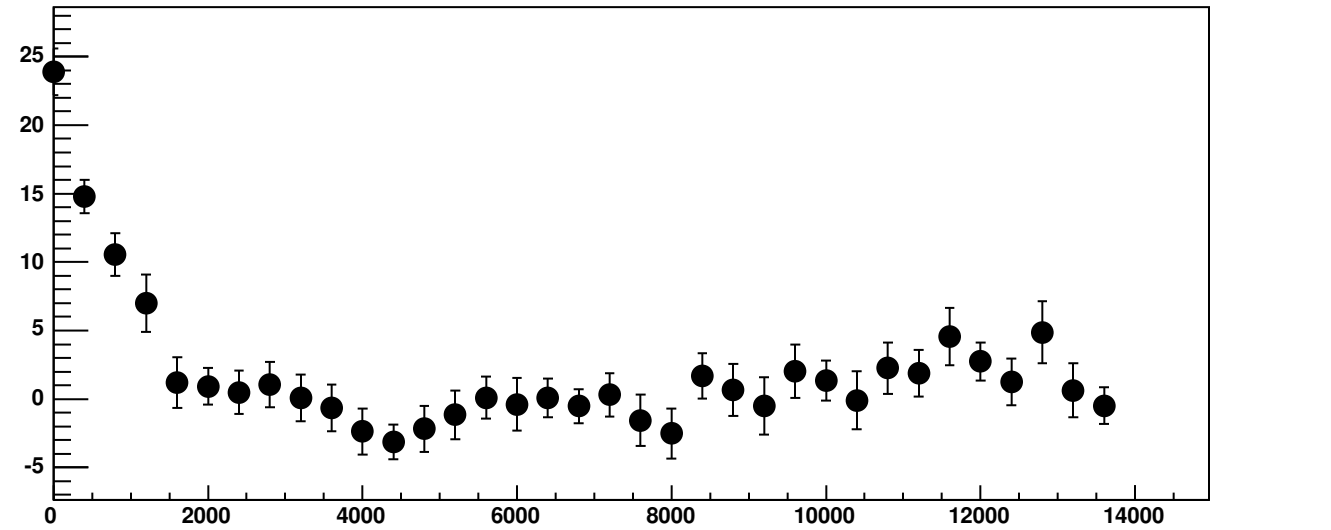
Chip 4, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

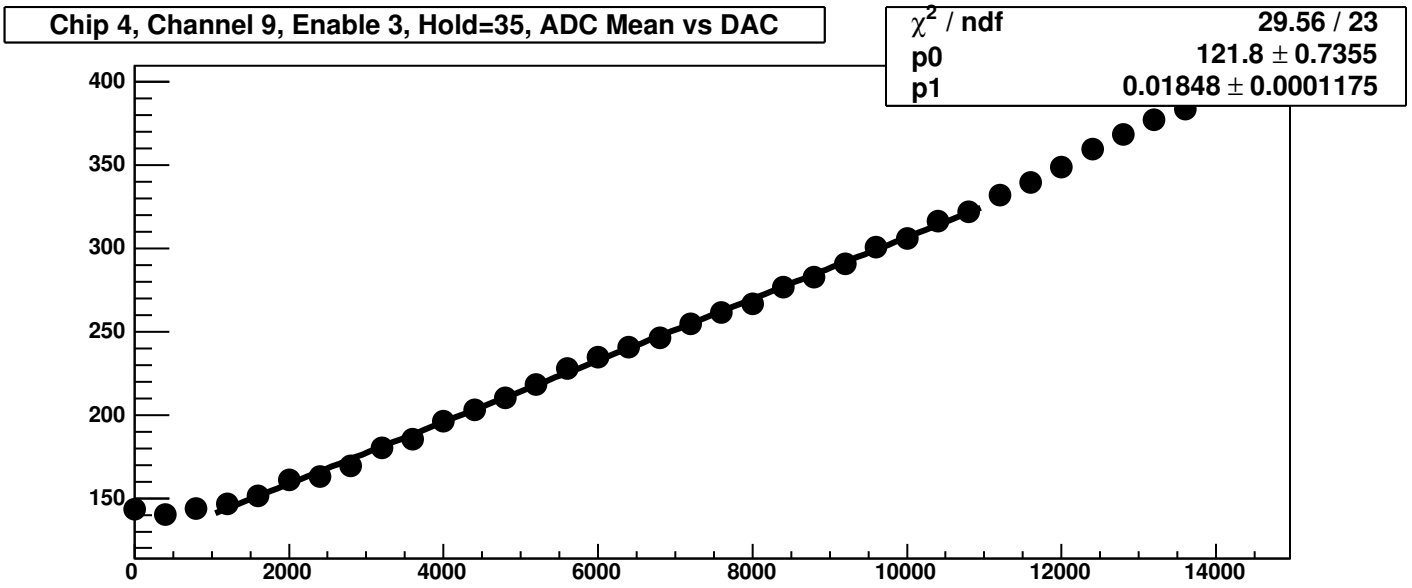


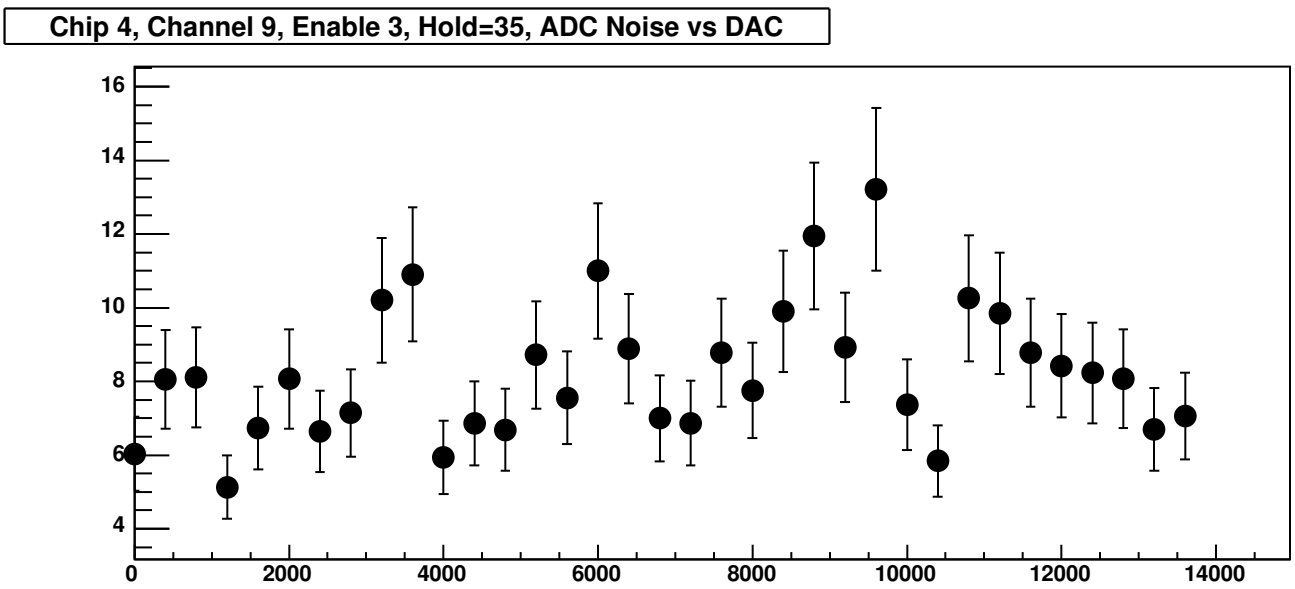
Chip 4, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC

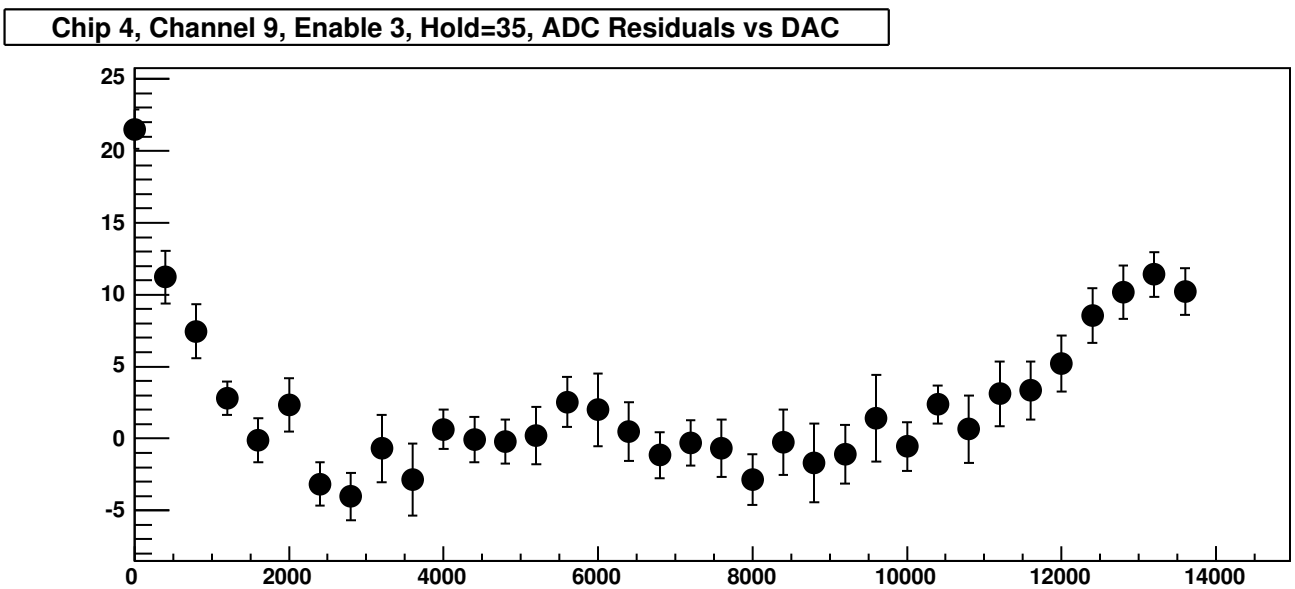


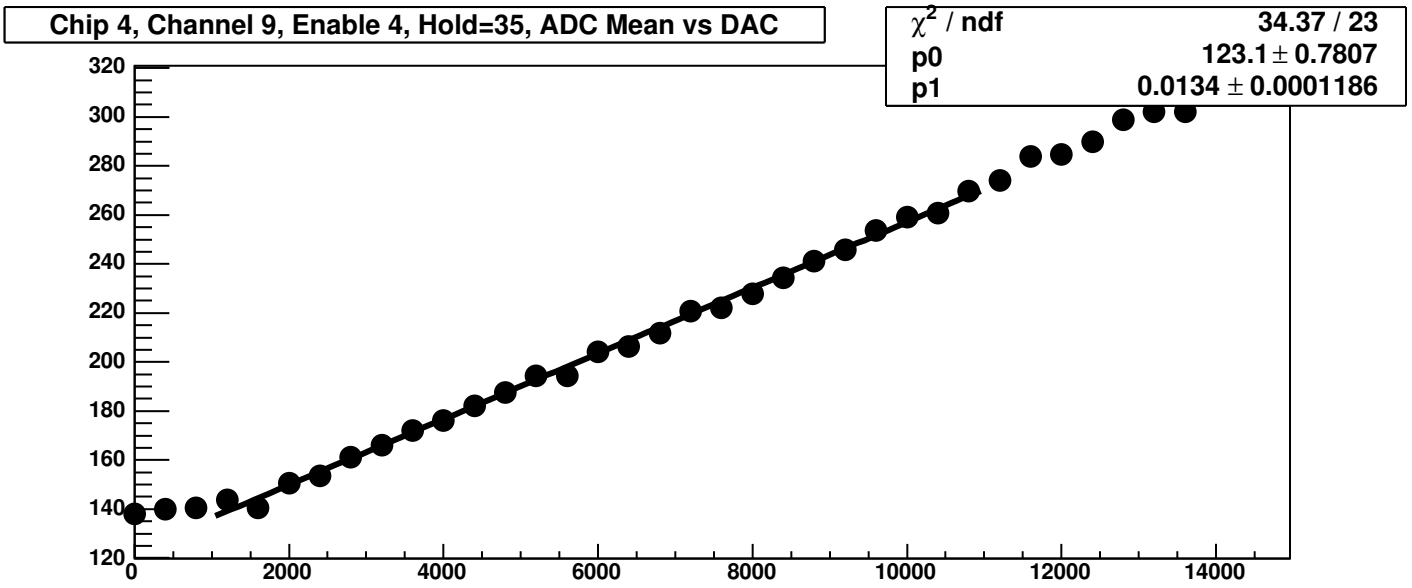
Chip 4, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

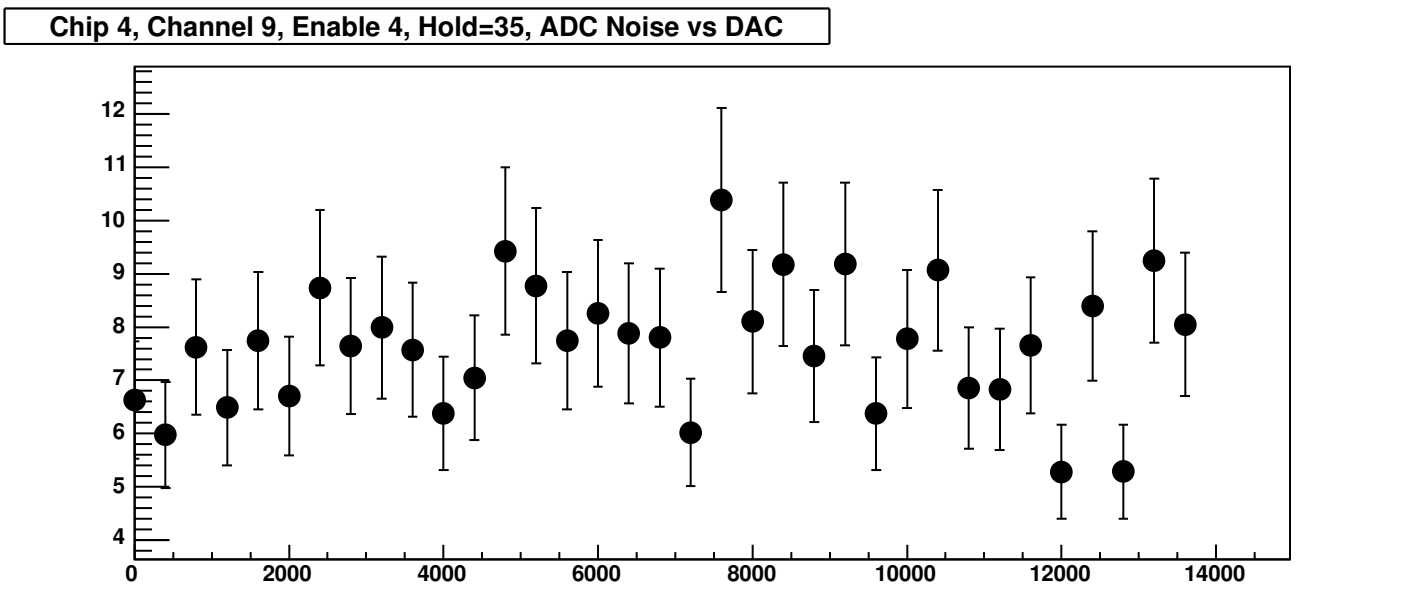


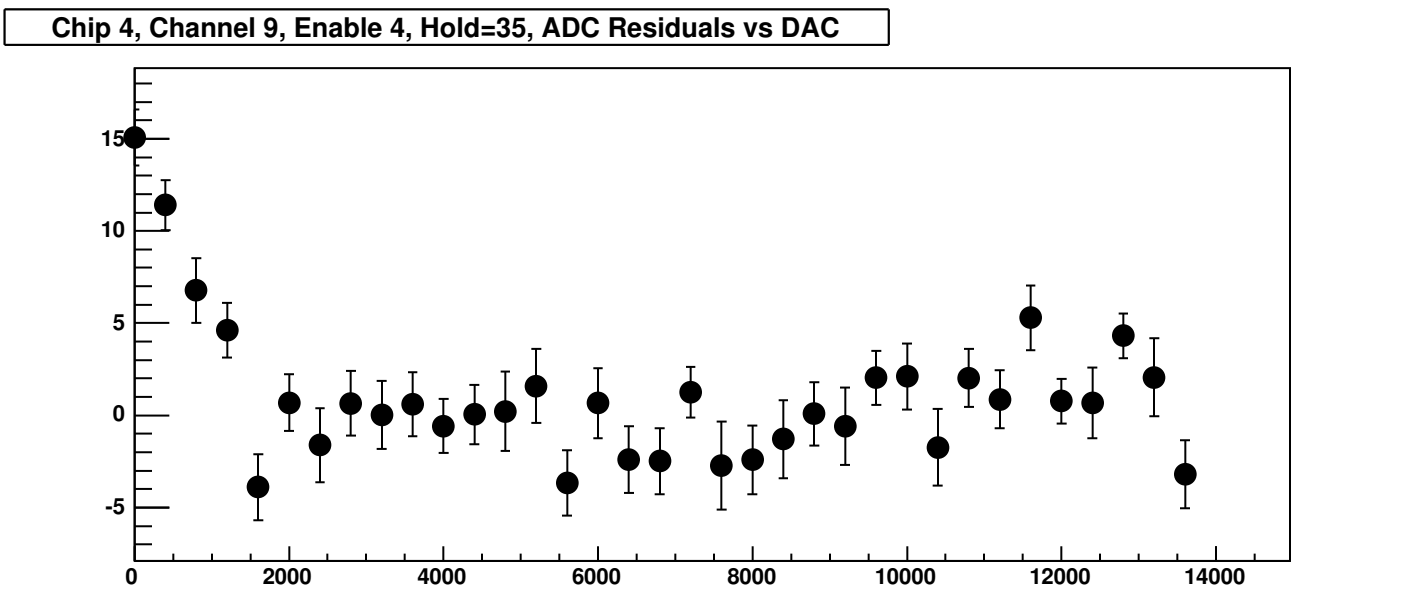

Chip 4, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 29.56 / 23
p0 121.8 ± 0.7355
p1 0.01848 ± 0.0001175
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

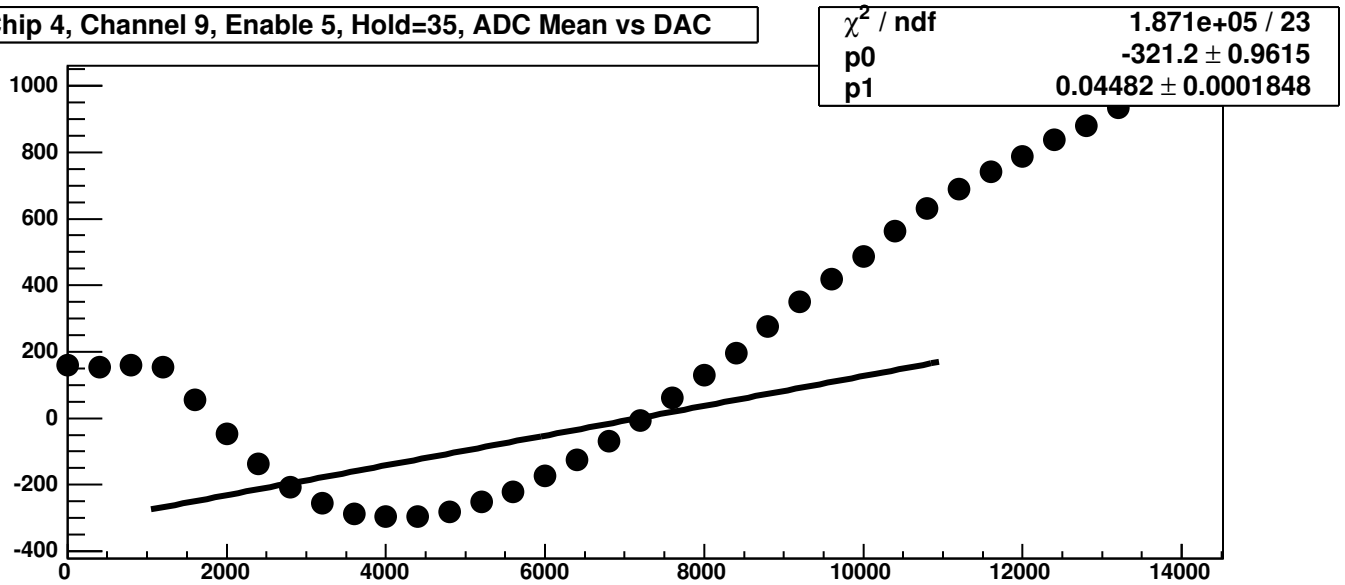

Chip 4, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 34.37 / 23
p0 123.1 ± 0.7807
p1 0.0134 ± 0.0001186
320
300
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

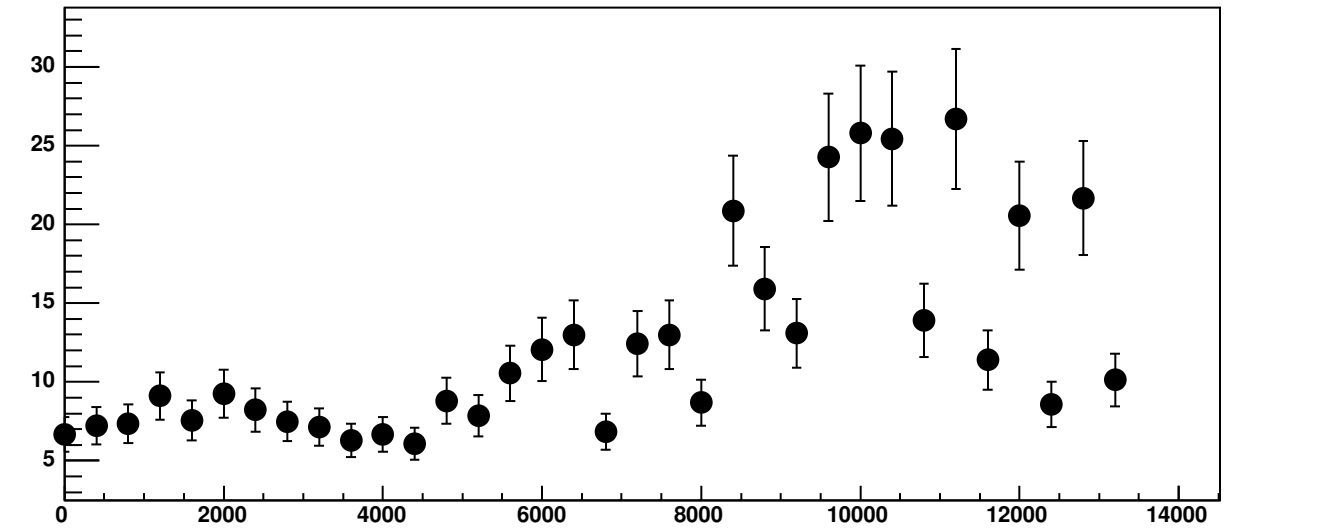

Chip 4, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

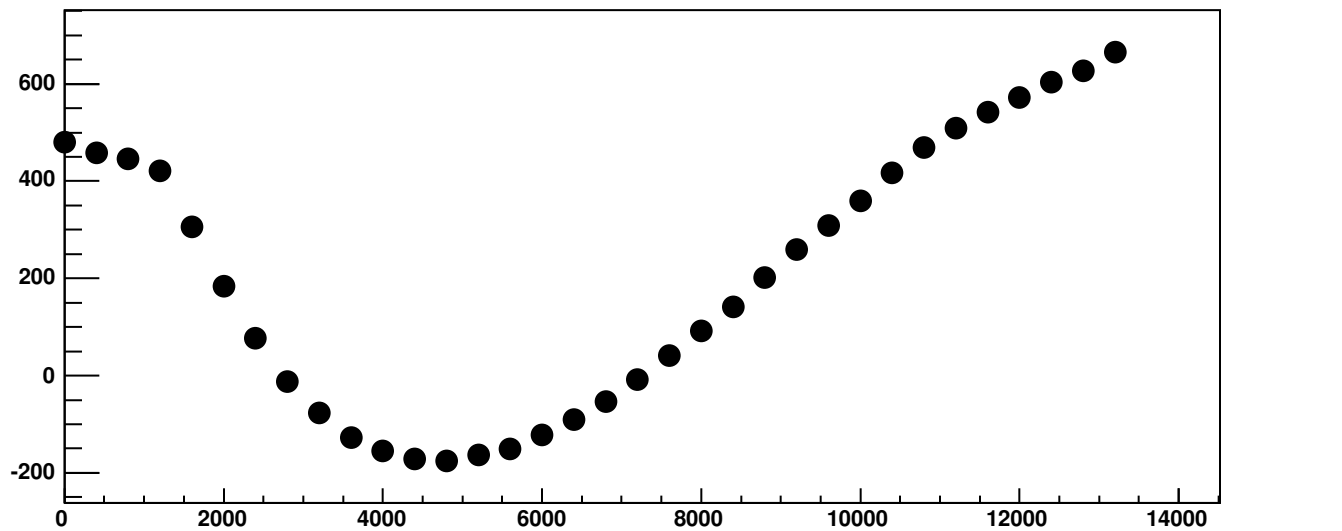
Chip 4, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

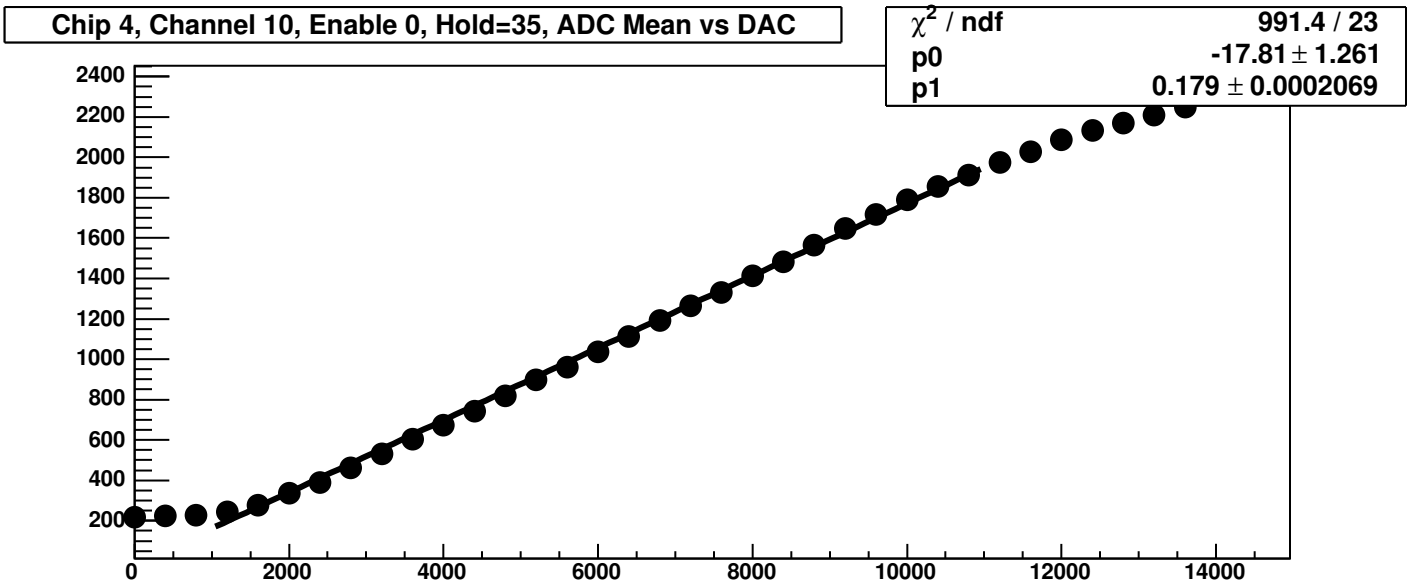


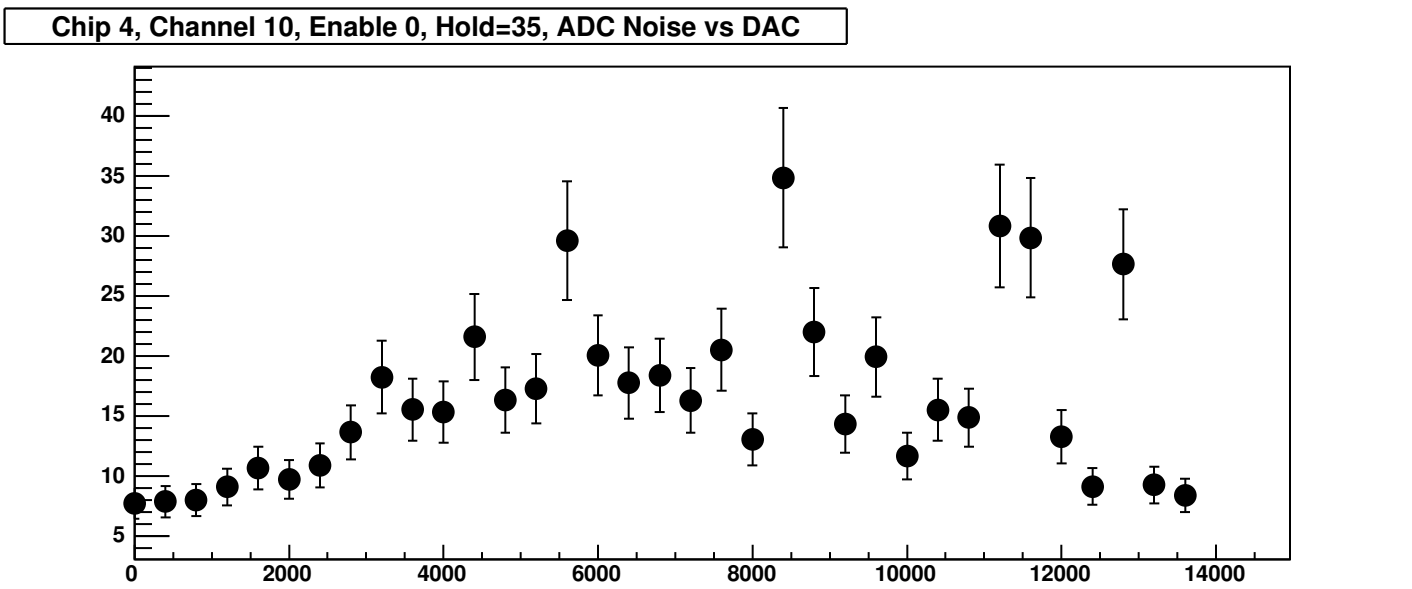
Chip 4, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

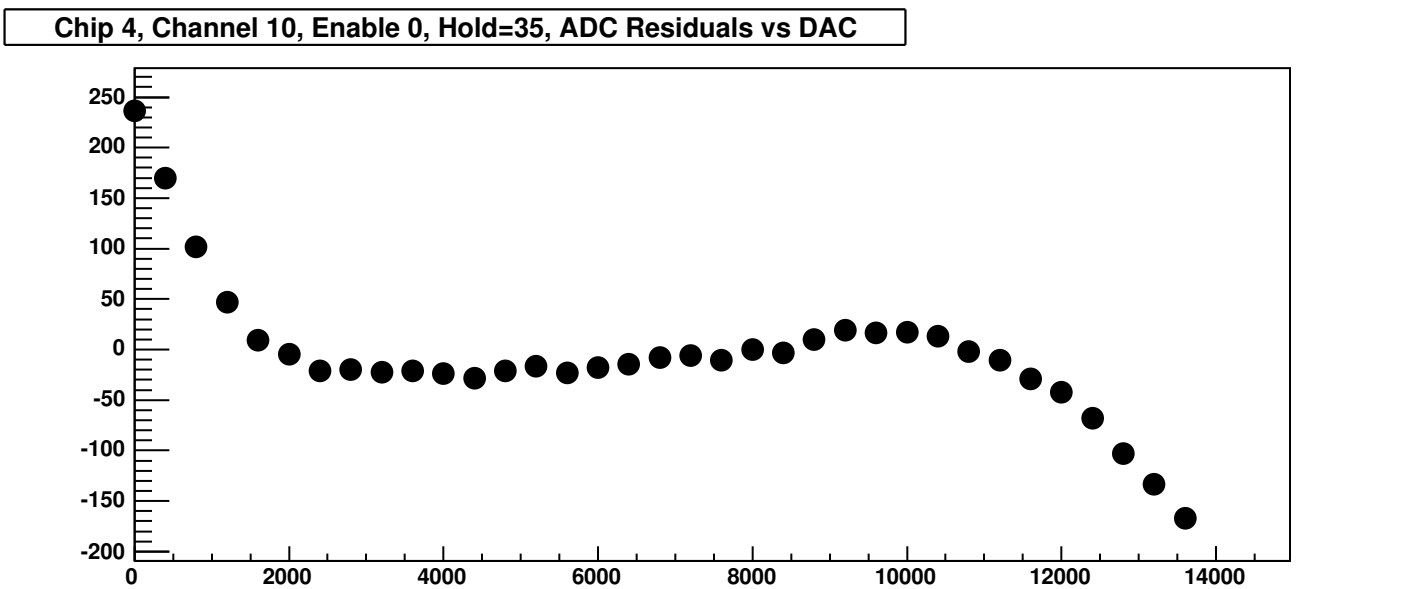


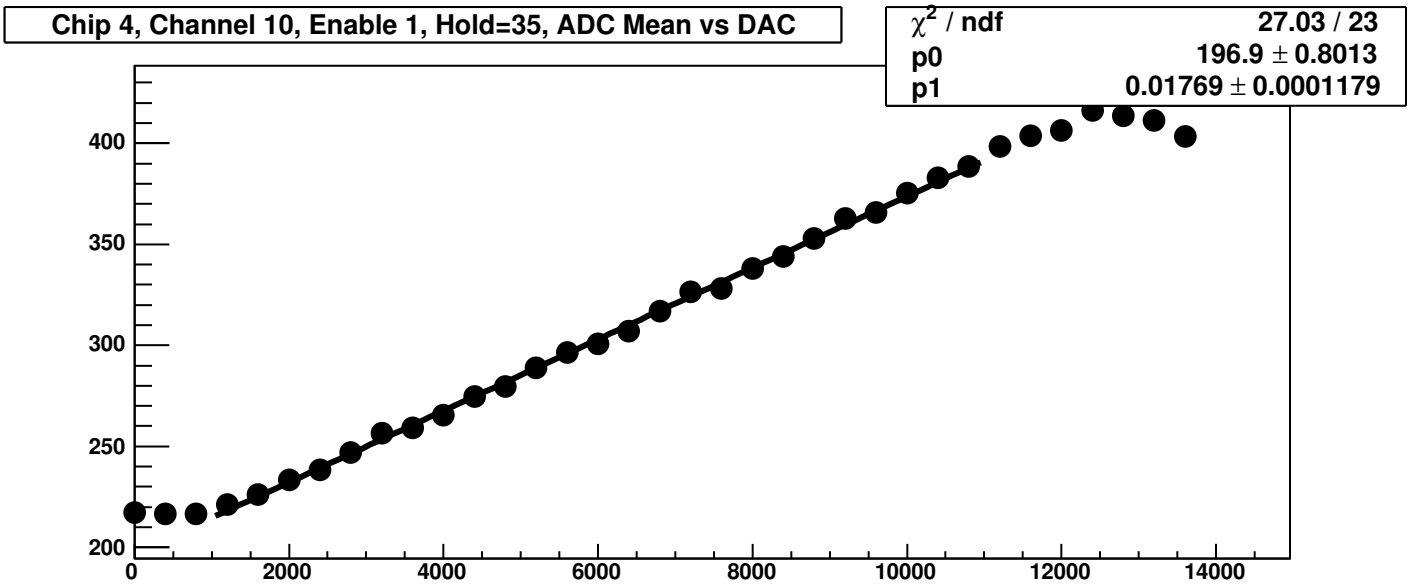
Chip 4, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

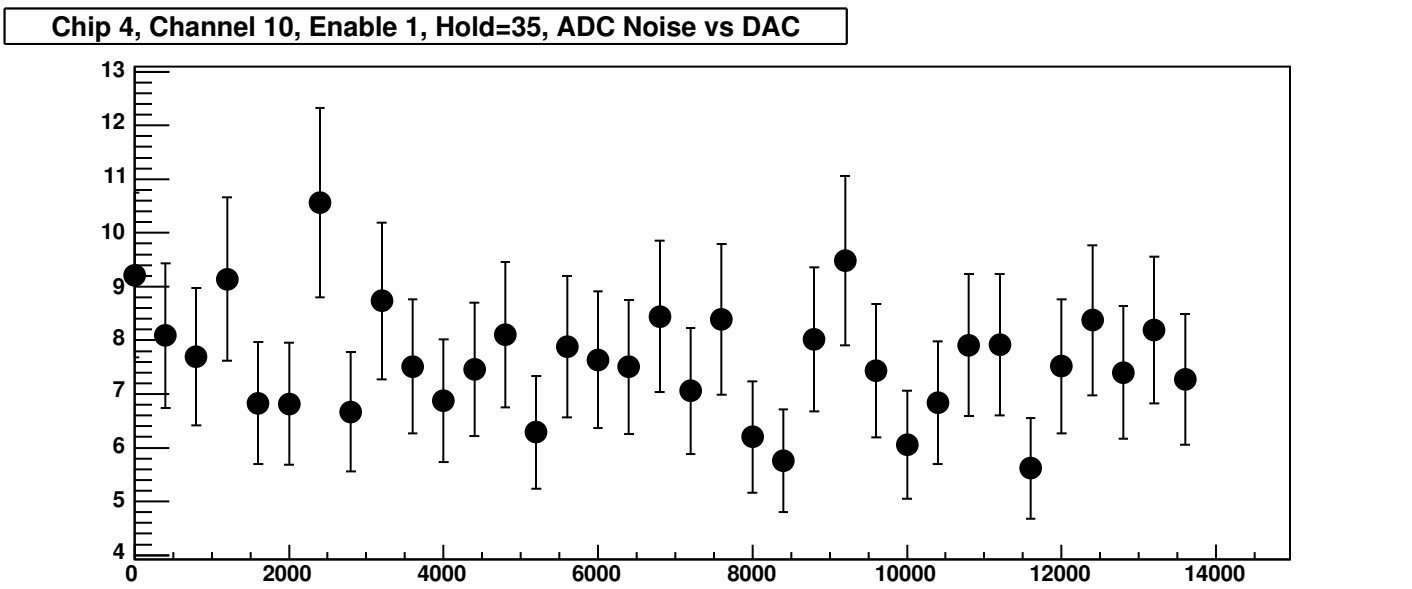


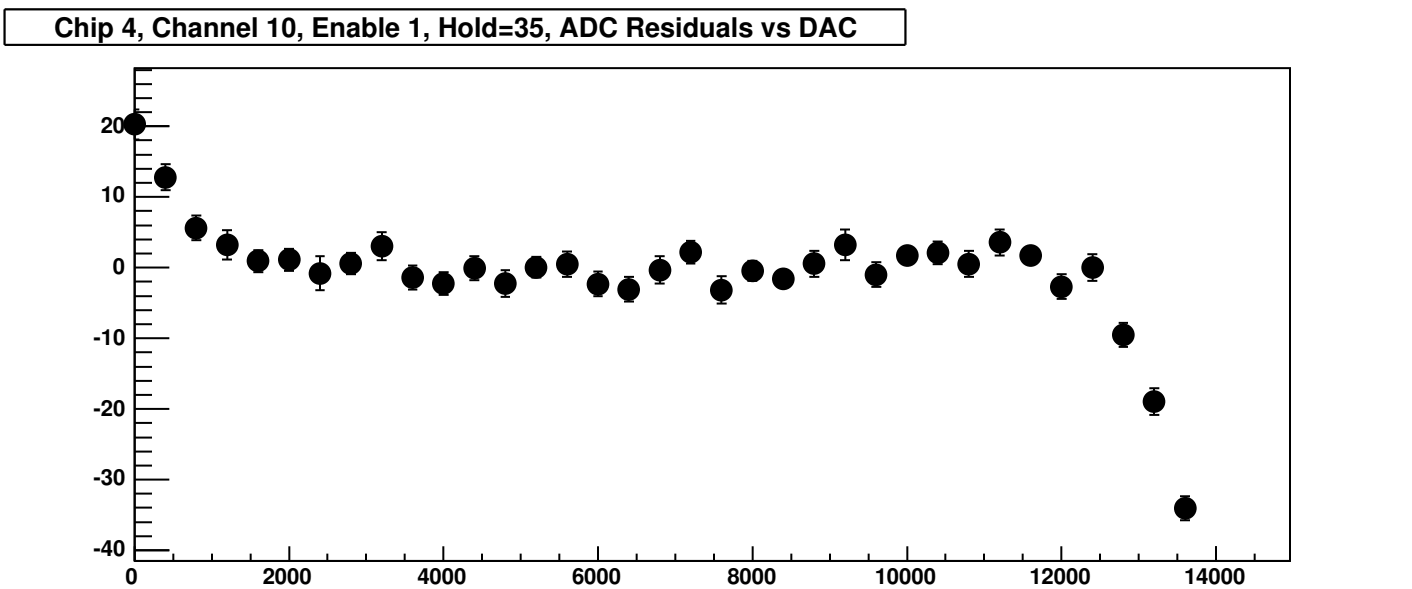

Chip 4, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 991.4 / 23
p0 -17.81 ± 1.261
p1 0.179 ± 0.0002069
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

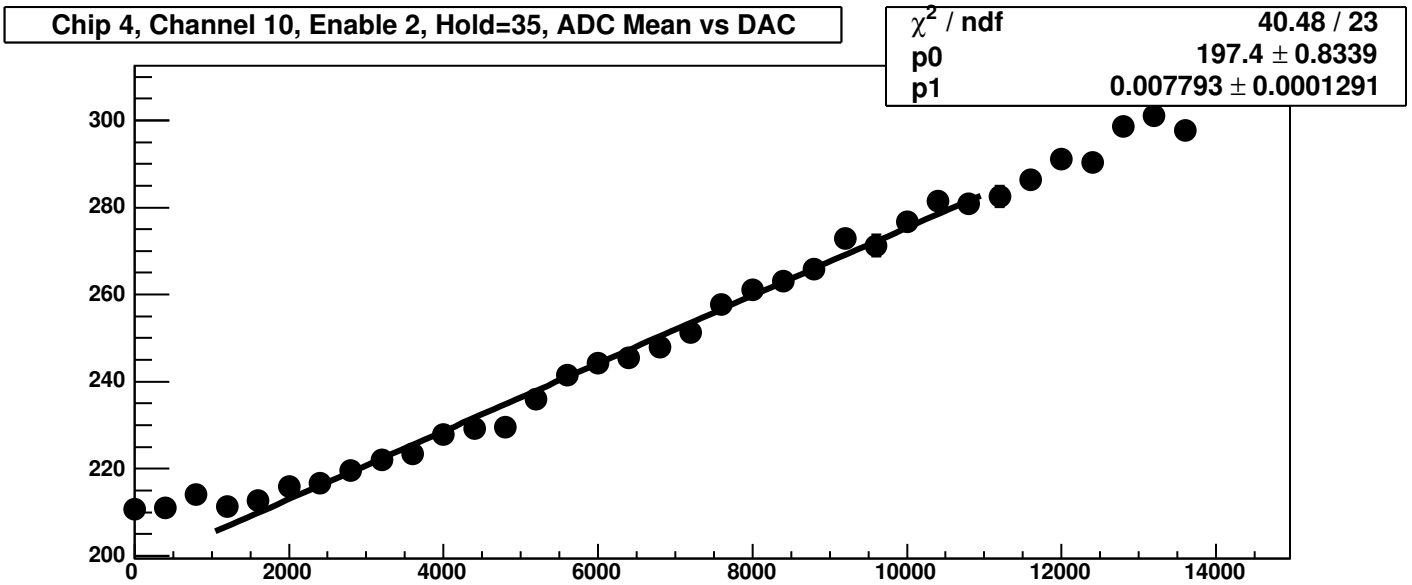

Chip 4, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

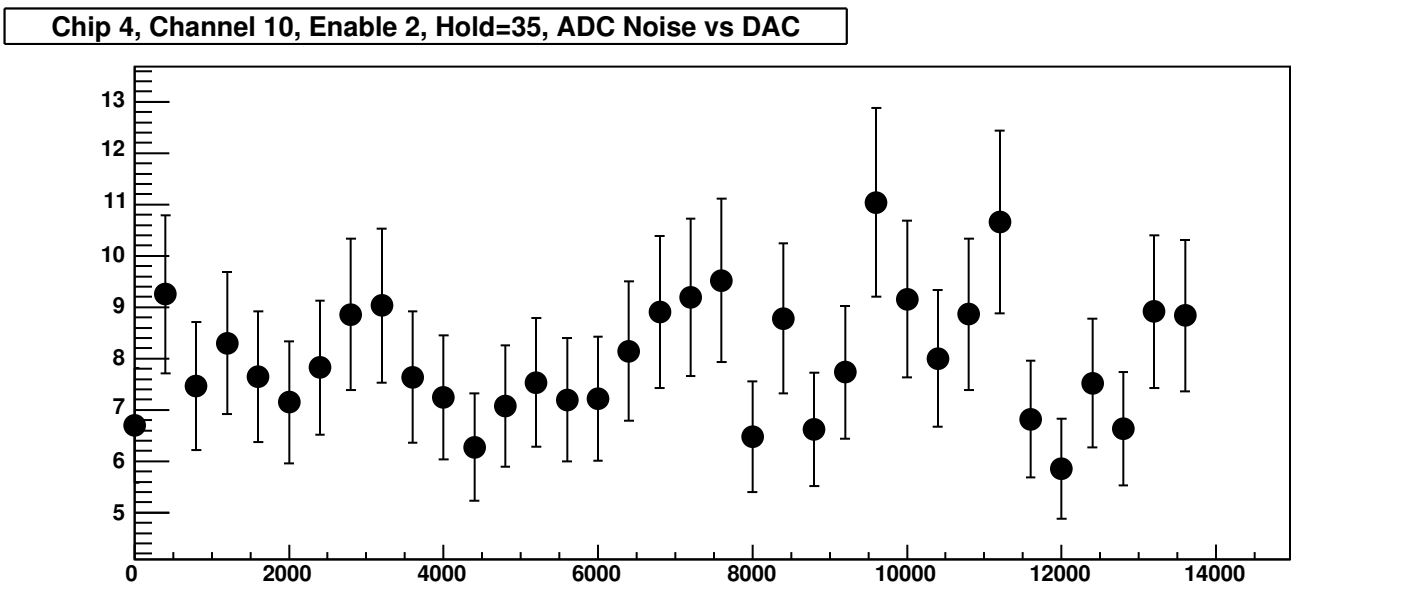

Chip 4, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

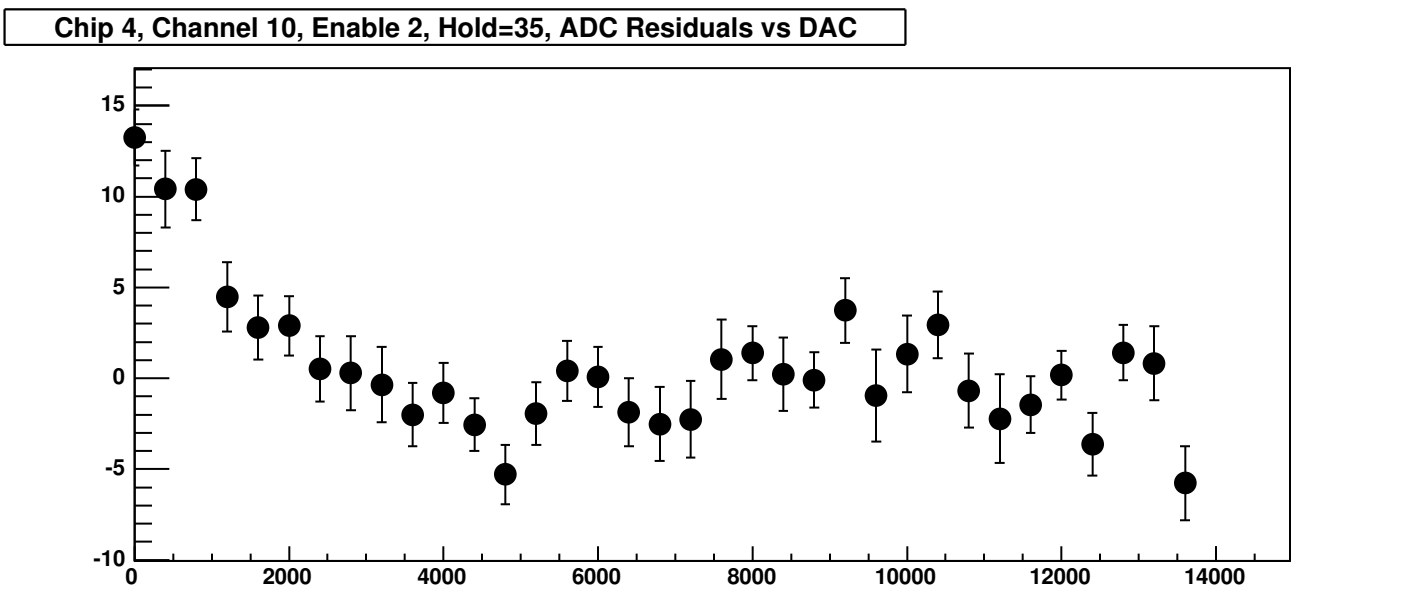

Chip 4, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 27.03 / 23
p0 196.9 ± 0.8013
p1 0.01769 ± 0.0001179
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

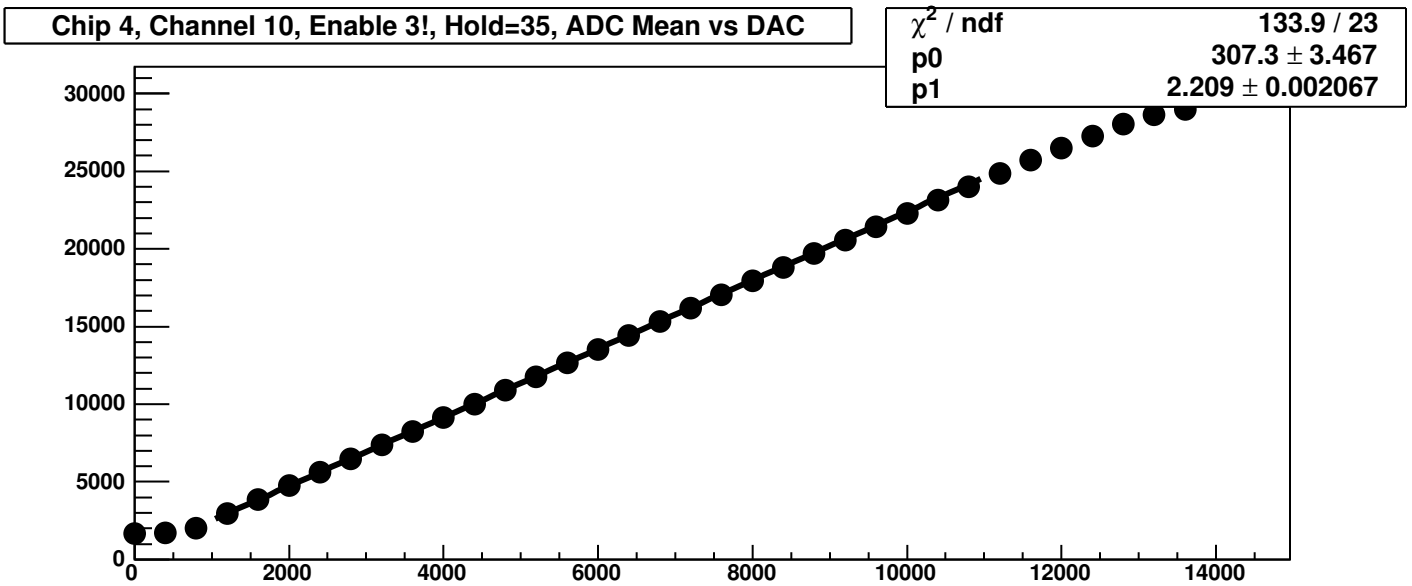

Chip 4, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

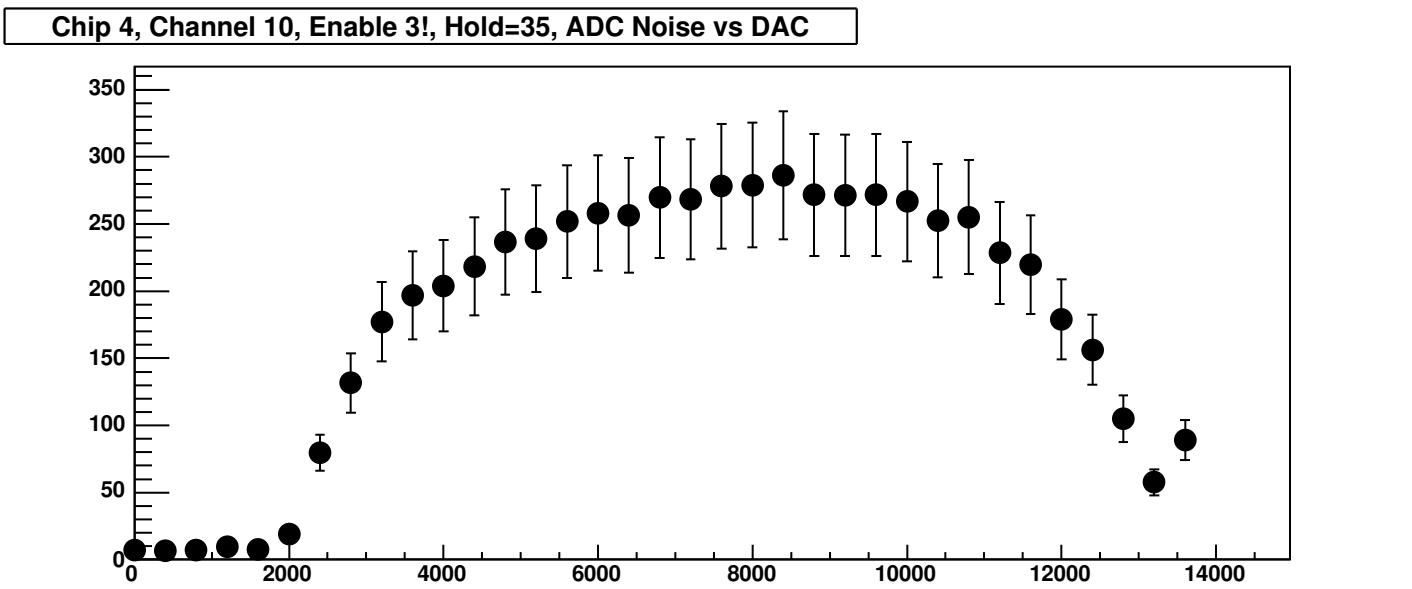

Chip 4, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

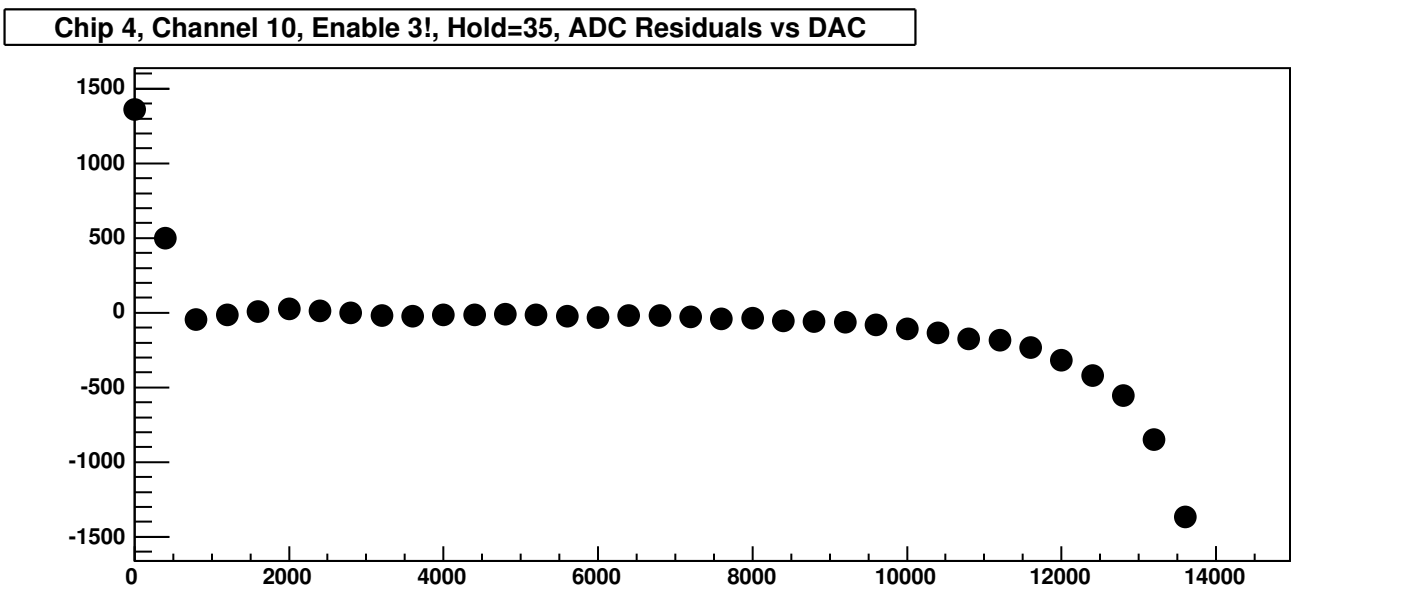

Chip 4, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 40.48 / 23
p0 197.4 ± 0.8339
p1 0.007793 ± 0.0001291
300
280
260
240
220
200
0
2000
4000
6000
8000
10000
12000
14000

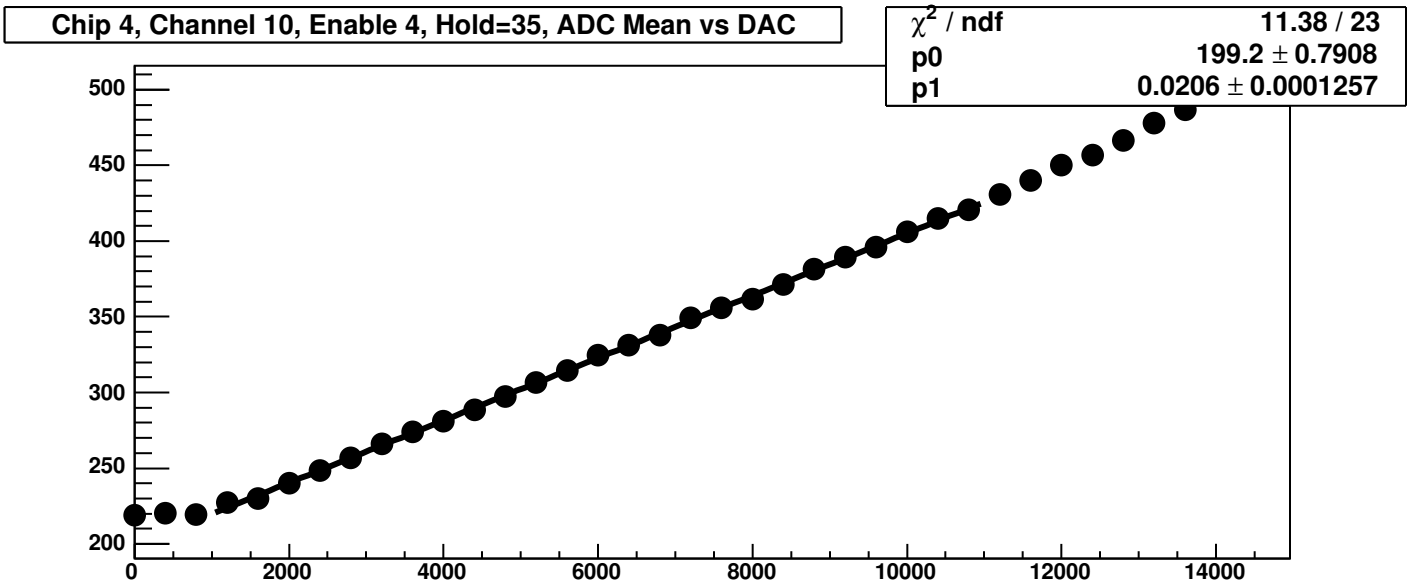

Chip 4, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

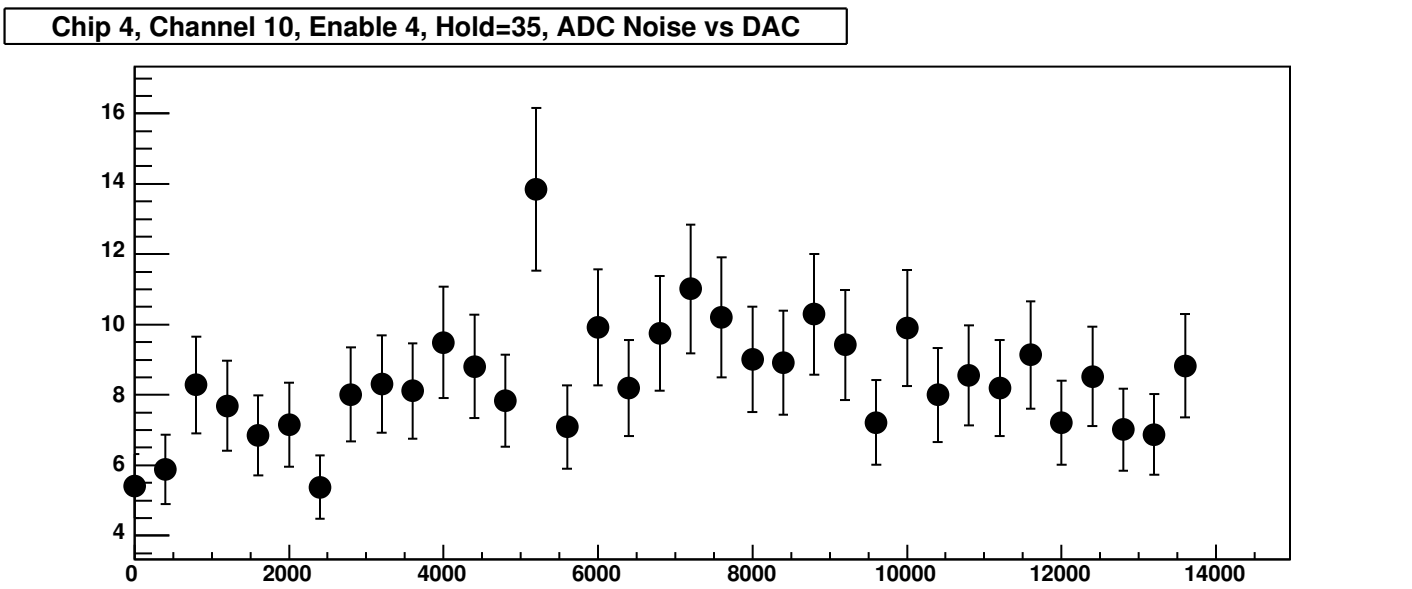

Chip 4, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

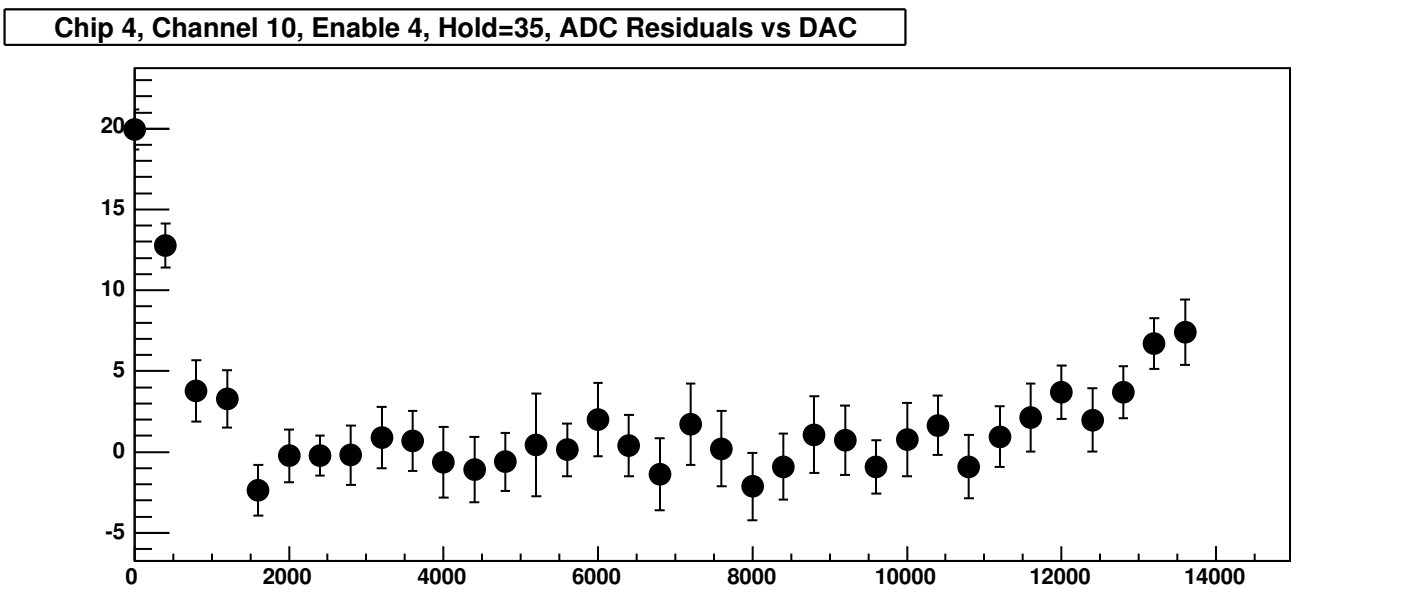

Chip 4, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 133.9 / 23
p0 307.3 ± 3.467
p1 2.209 ± 0.002067
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

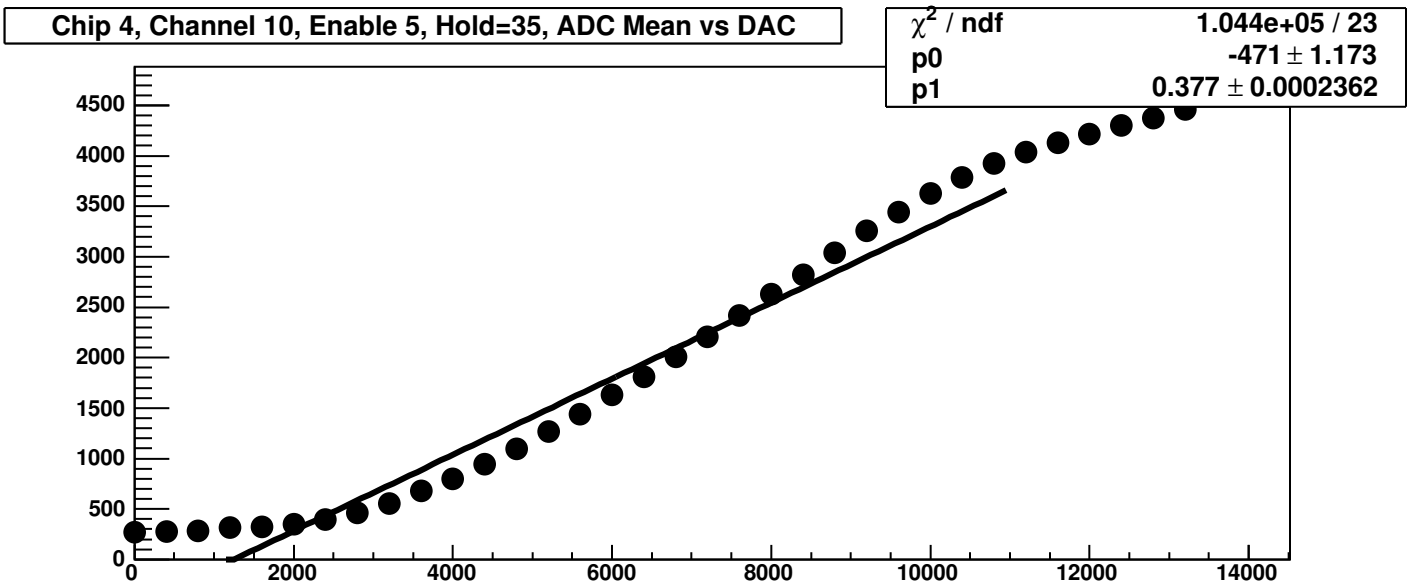

Chip 4, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

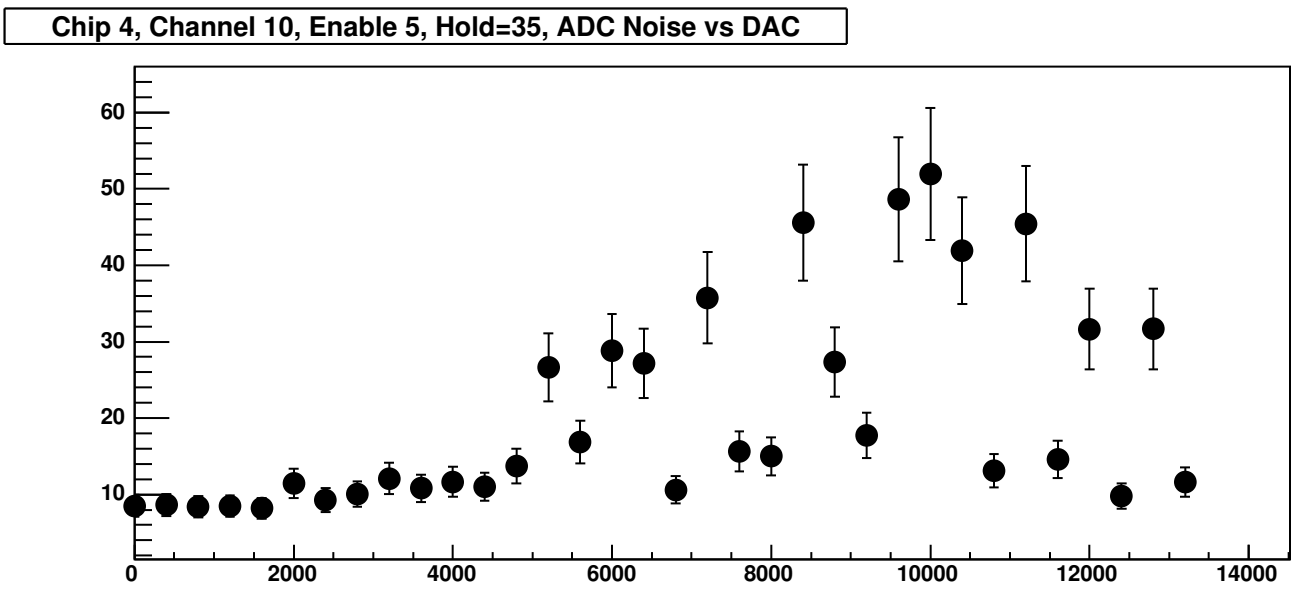

Chip 4, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

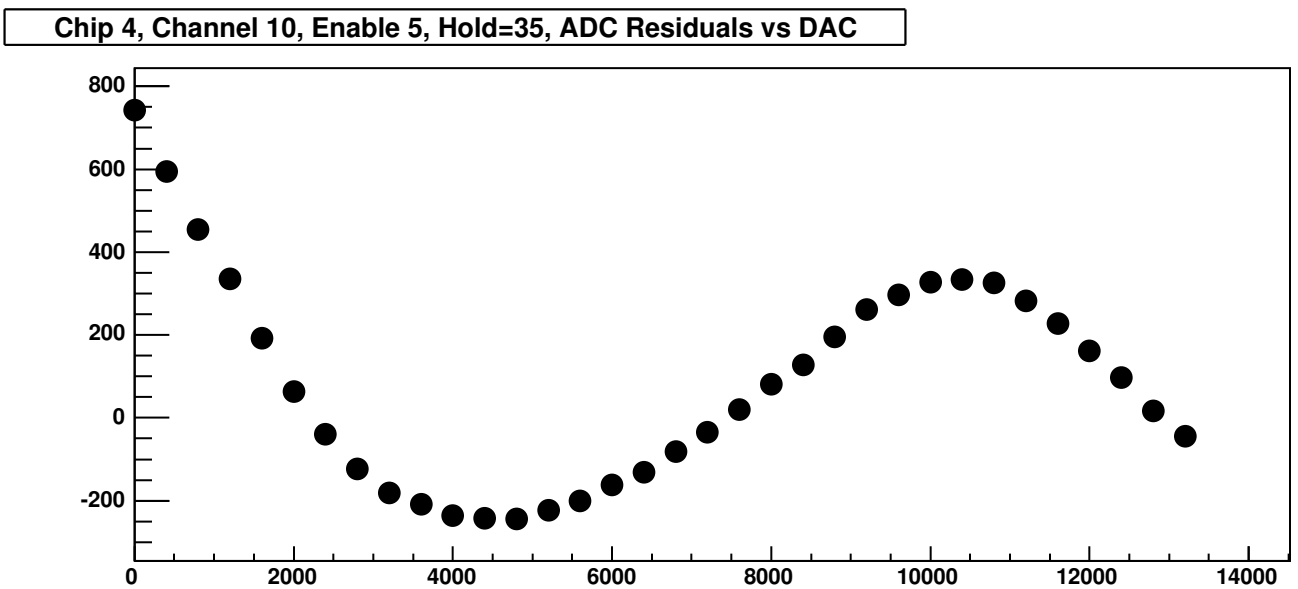

Chip 4, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 11.38 / 23
p0 199.2 ± 0.7908
p1 0.0206 ± 0.0001257
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

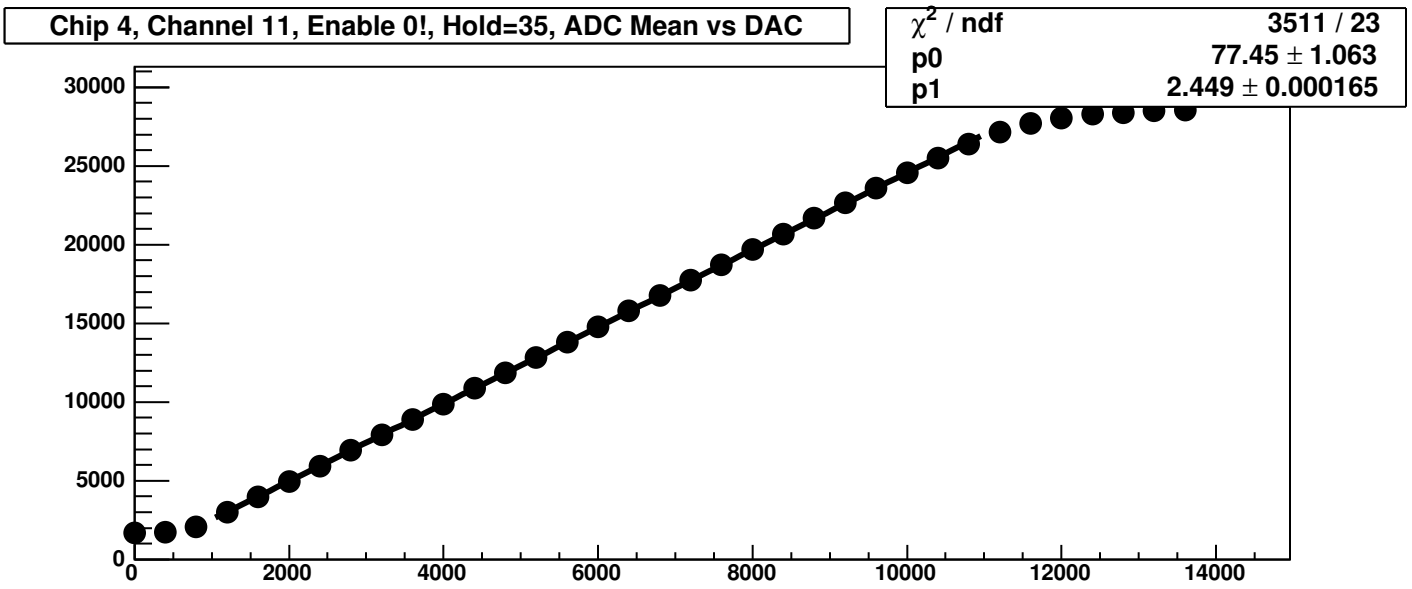

Chip 4, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

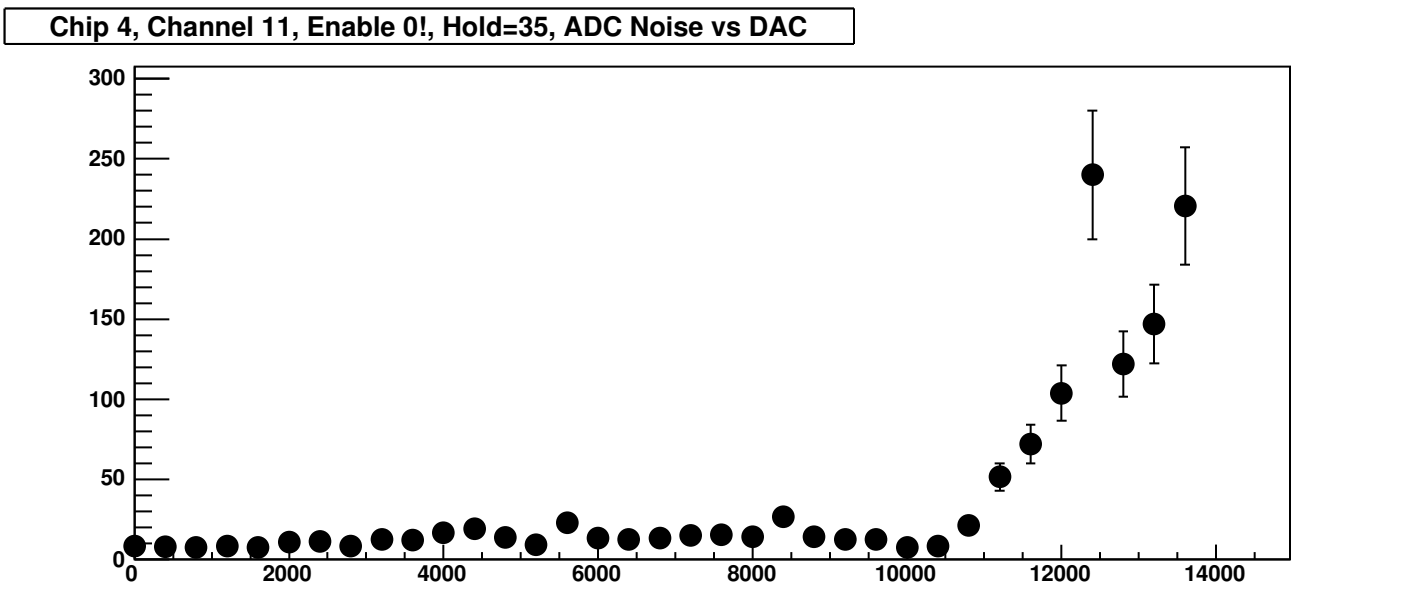

Chip 4, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

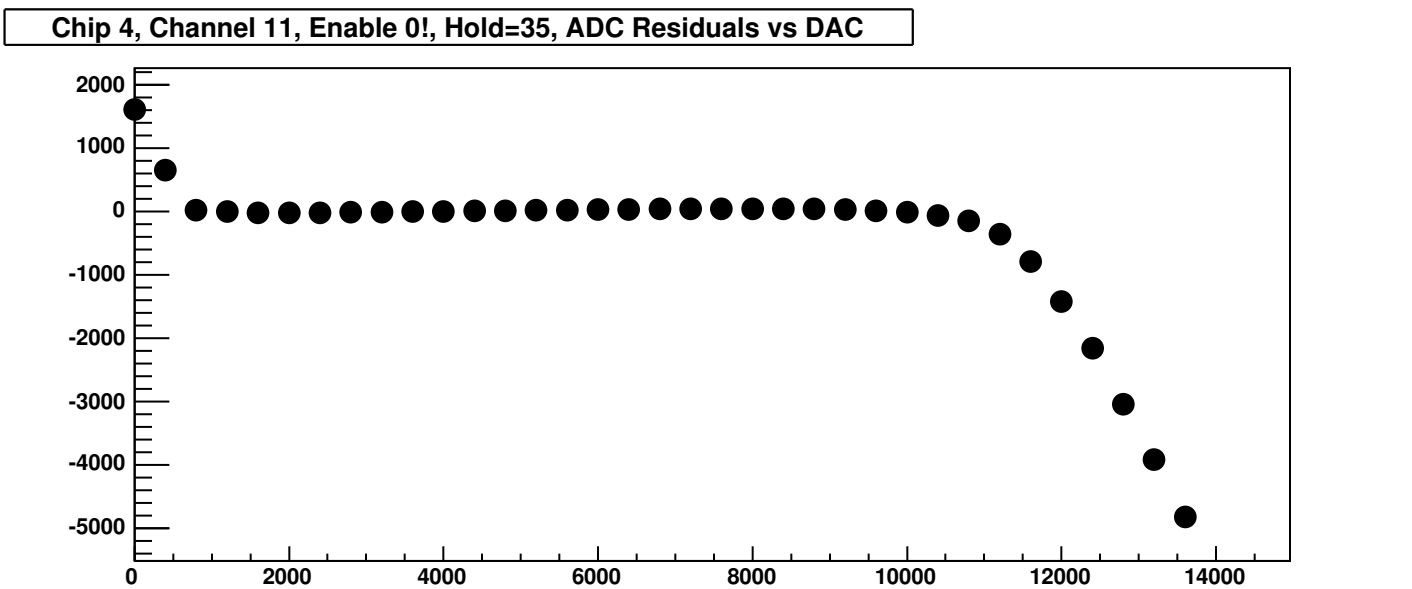

Chip 4, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.044e+05 / 23
p0 -471 ± 1.173
p1 0.377 ± 0.0002362
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

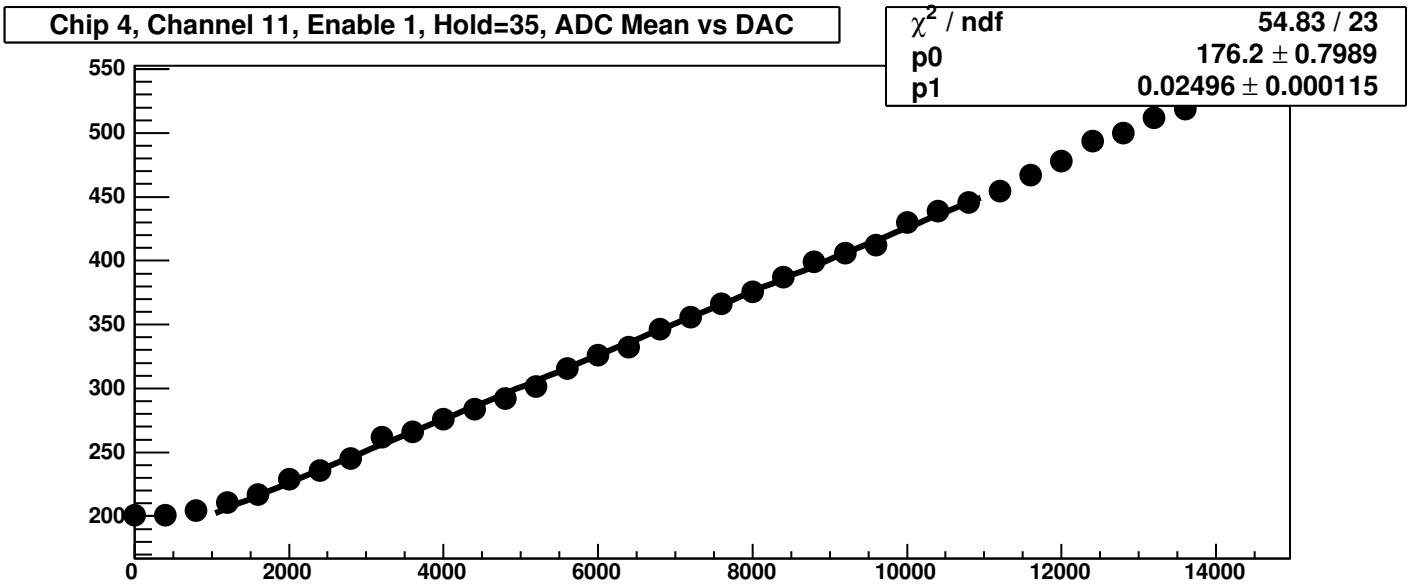

Chip 4, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

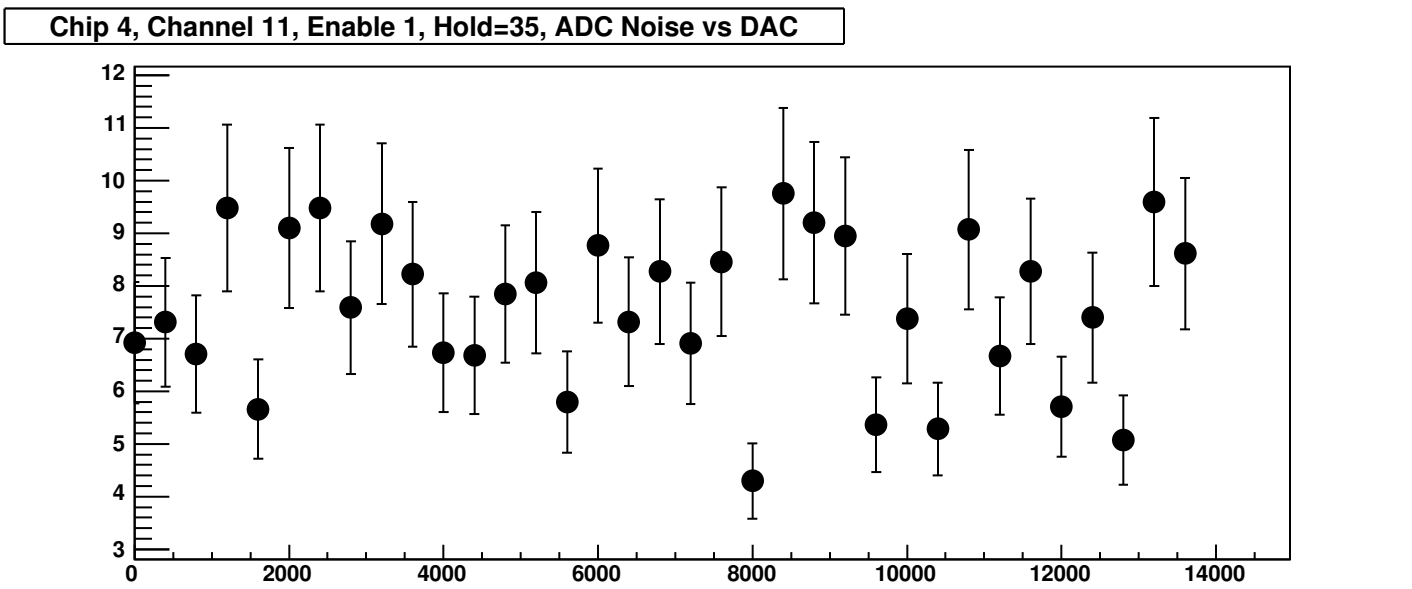

Chip 4, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

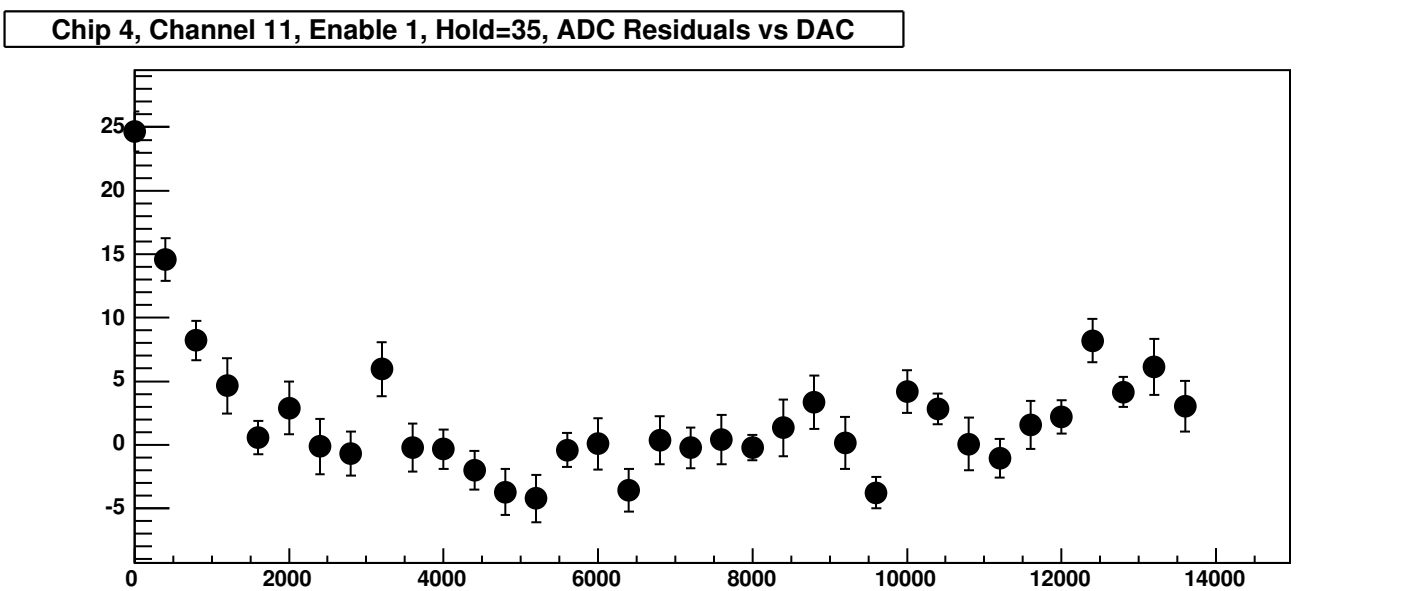

Chip 4, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 3511 / 23
p0 77.45 ± 1.063
p1 2.449 ± 0.000165
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

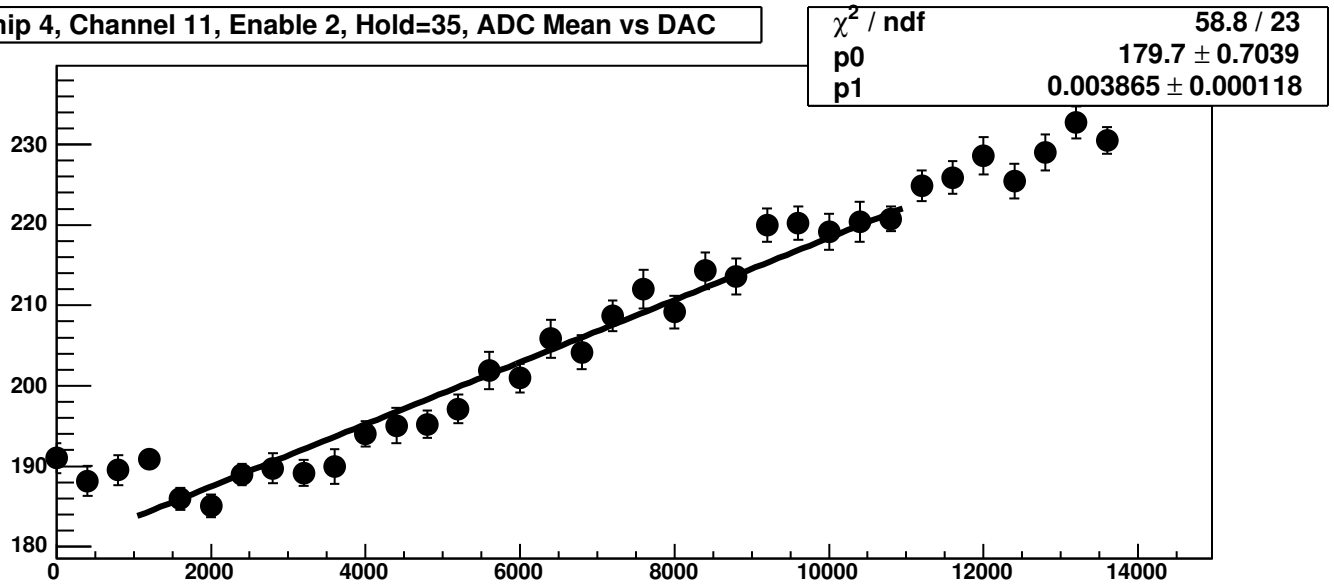

Chip 4, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 54.83 / 23
p0 176.2 ± 0.7989
p1 0.02496 ± 0.000115
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

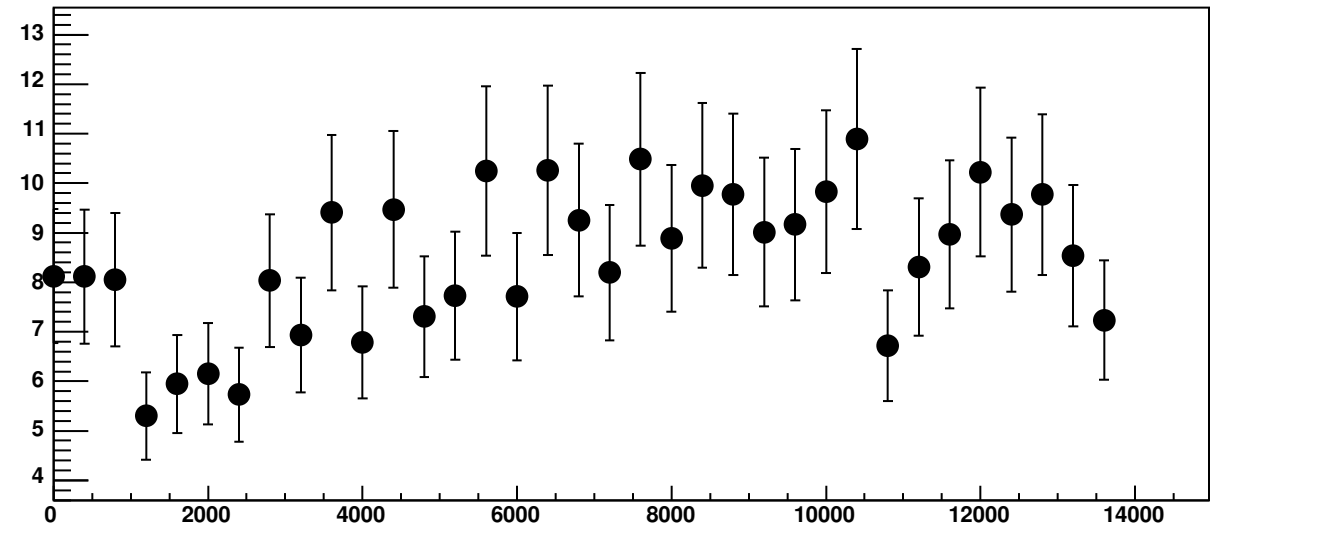

Chip 4, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

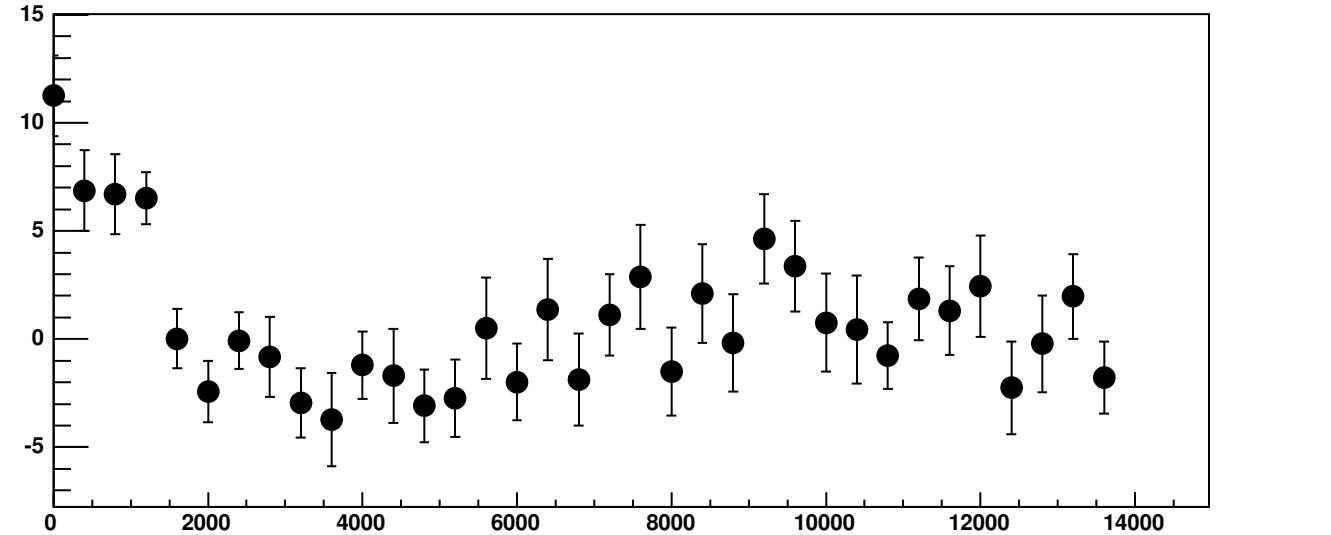
Chip 4, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

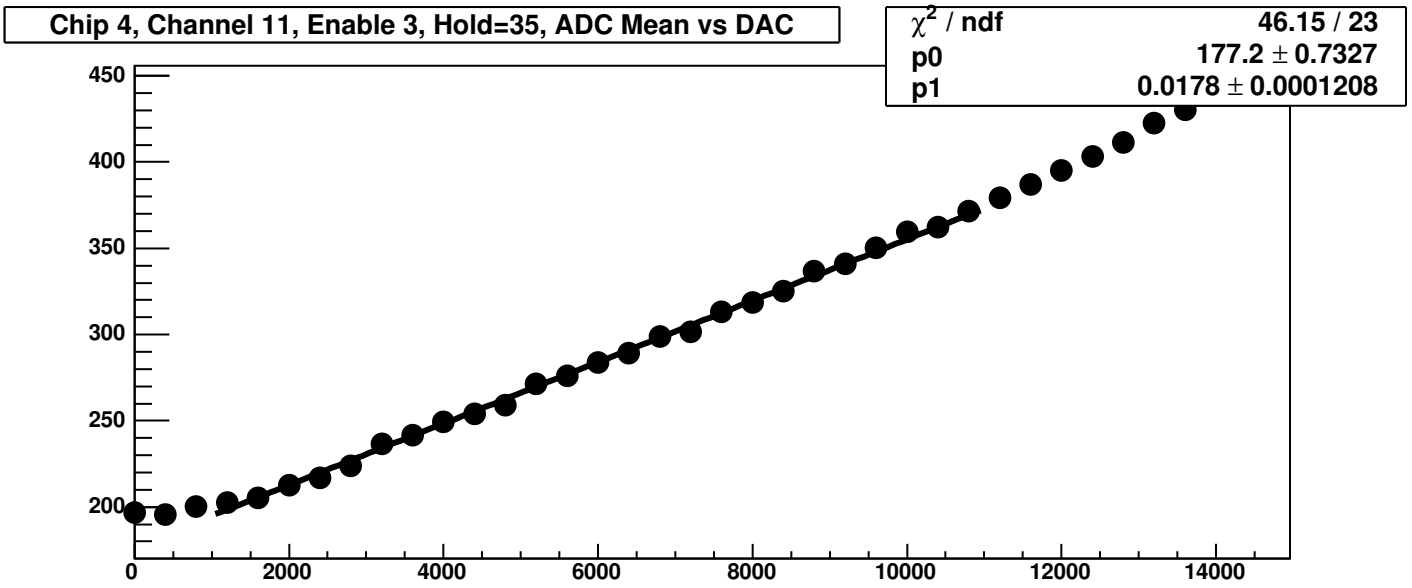


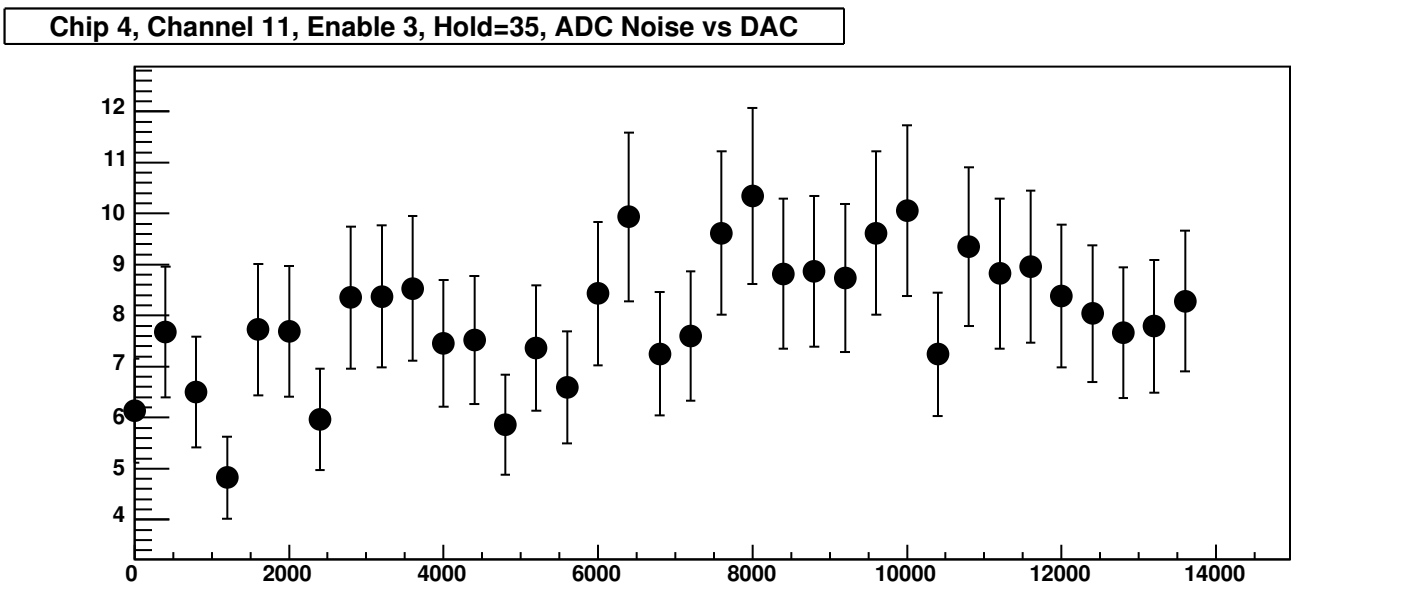
Chip 4, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

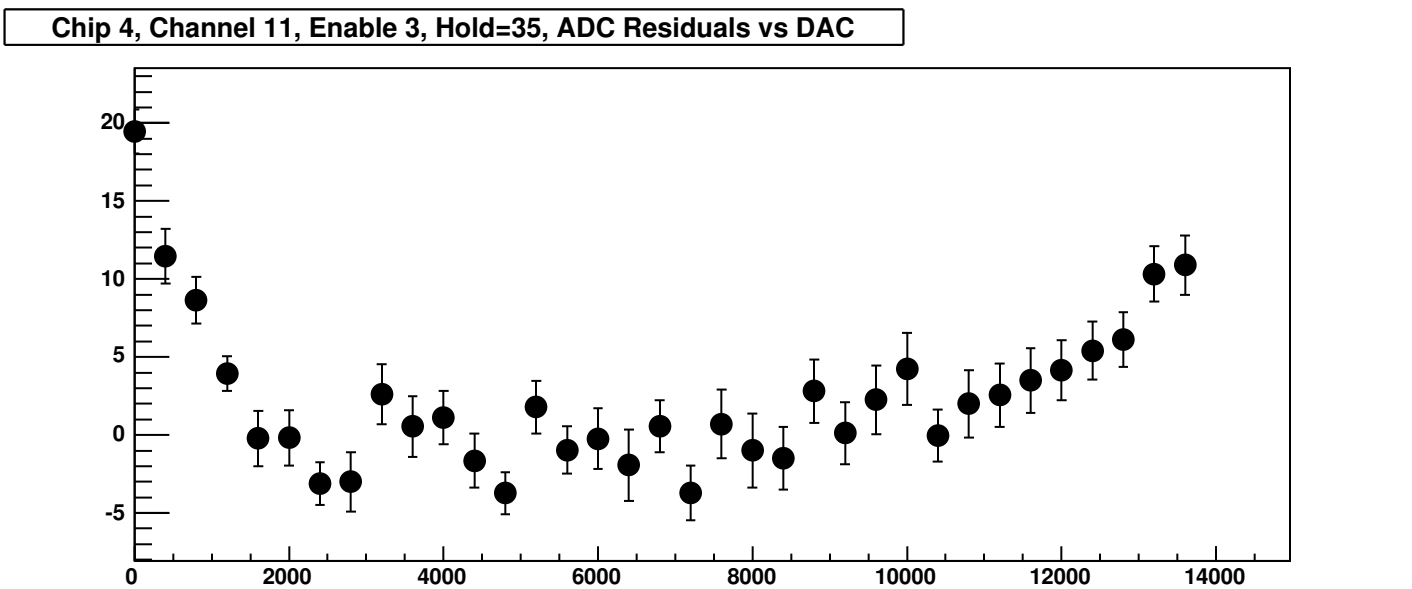


Chip 4, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

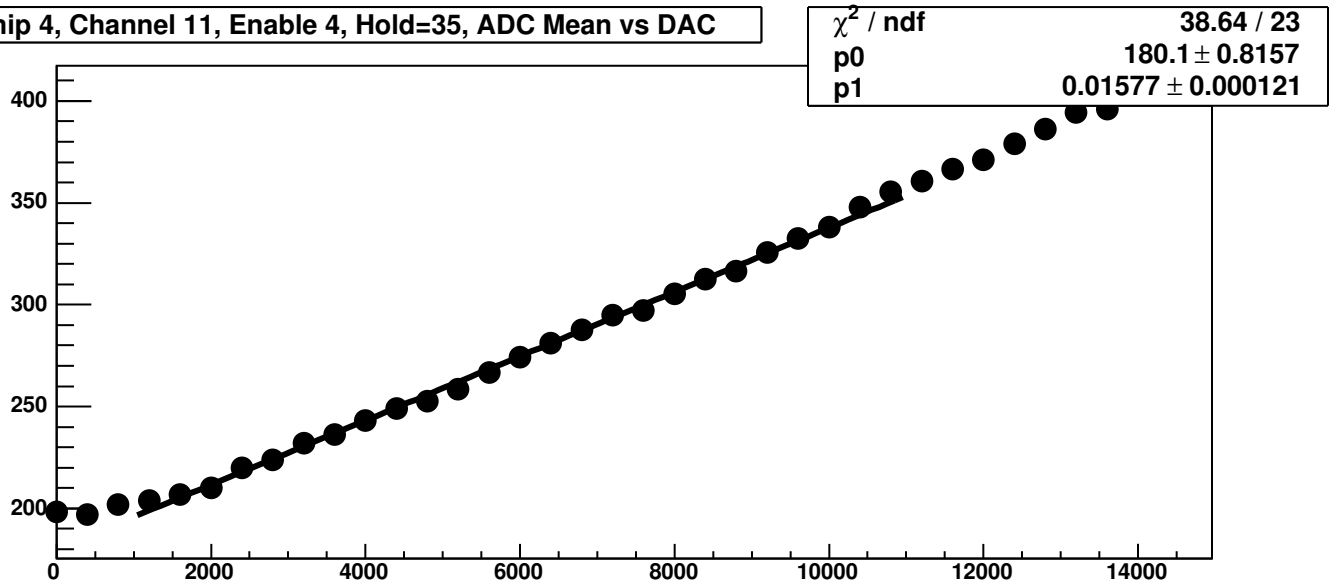



Chip 4, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 46.15 / 23
p0 177.2 ± 0.7327
p1 0.0178 ± 0.0001208
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

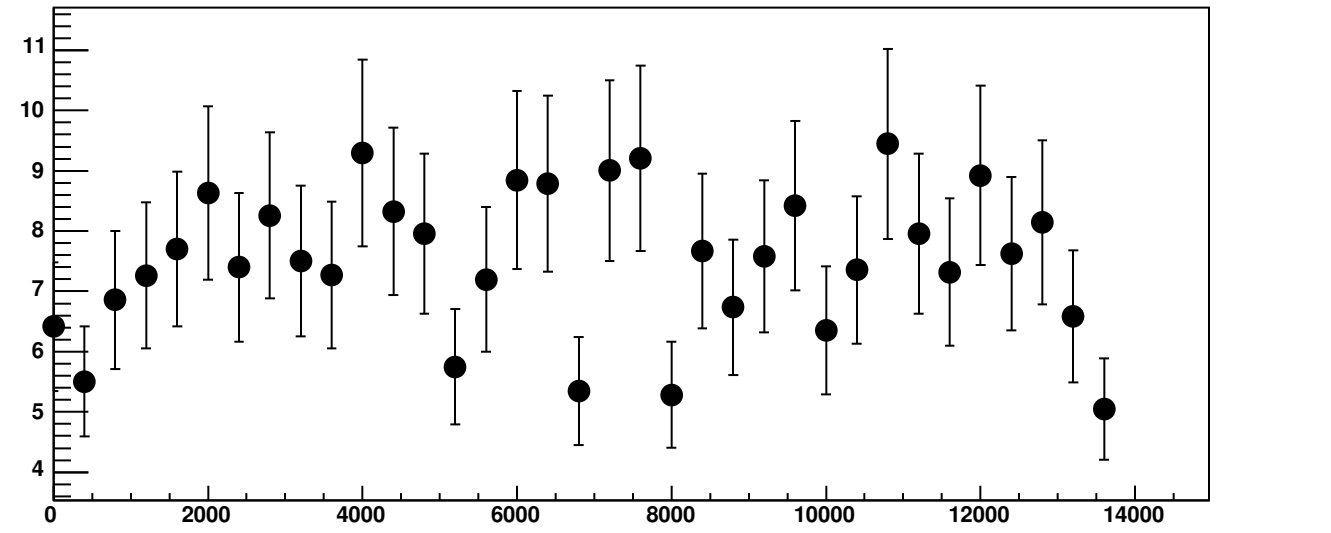

Chip 4, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

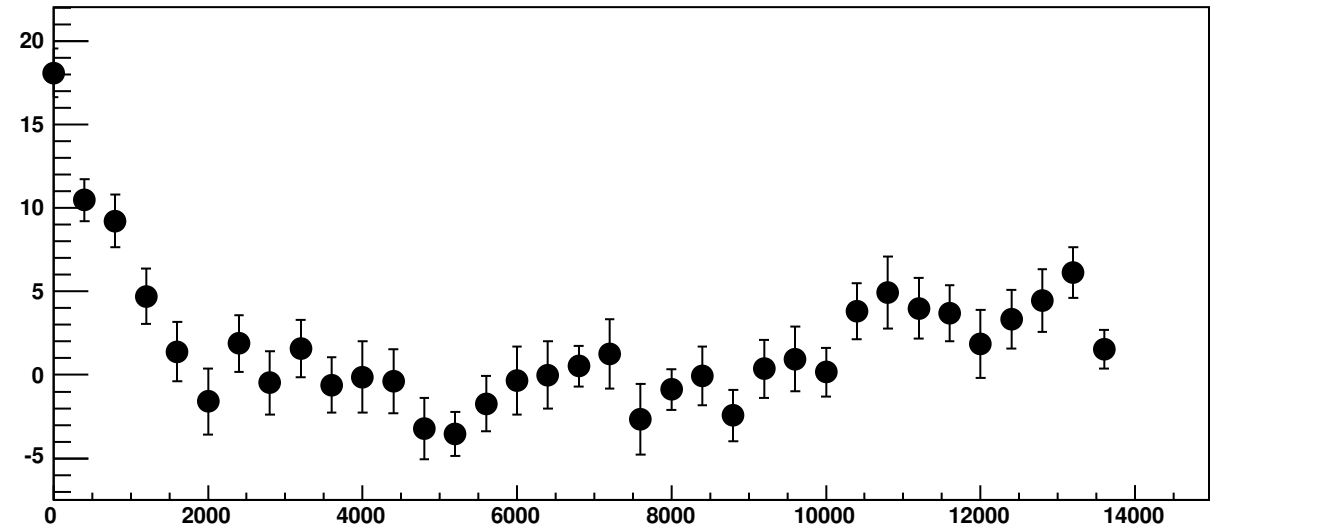
Chip 4, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



Chip 4, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

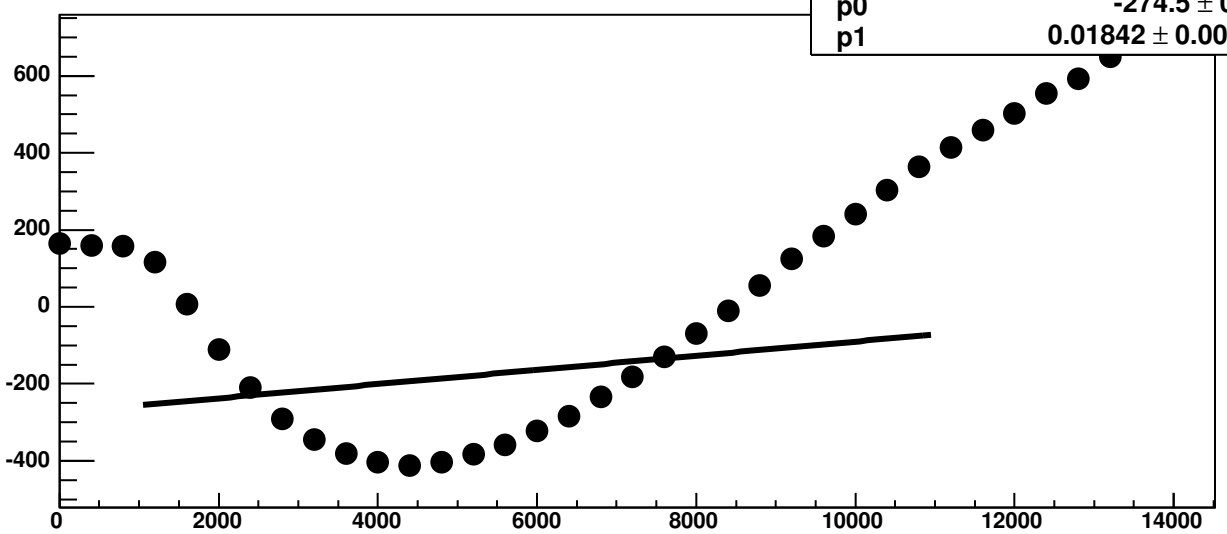


Chip 4, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

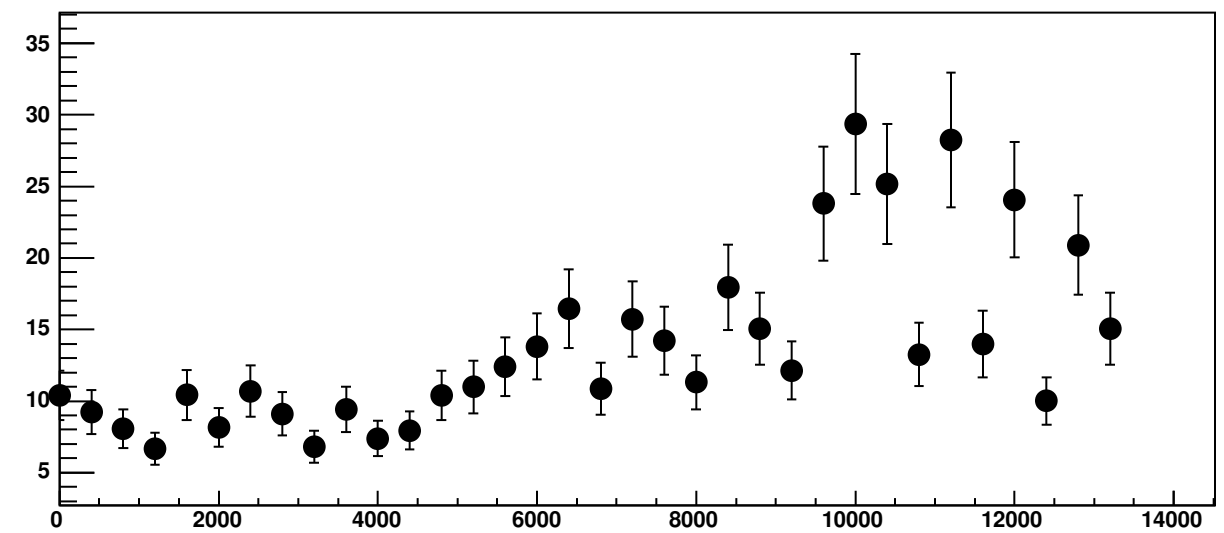


Chip 4, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

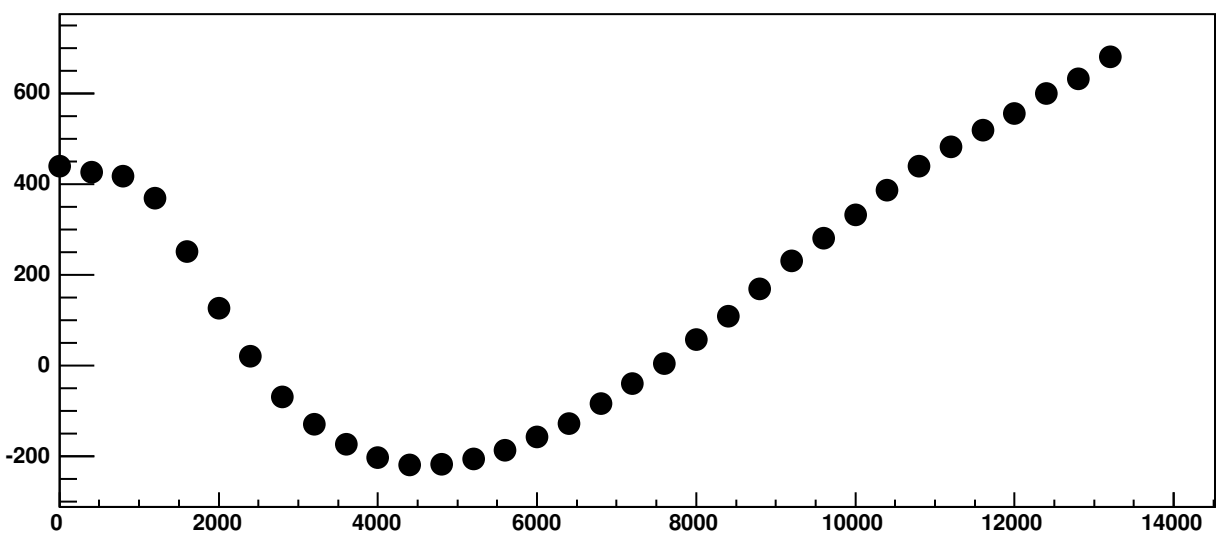
χ^2 / ndf	1.819e+05 / 23
p0	-274.5 ± 0.9932
p1	0.01842 ± 0.0001915

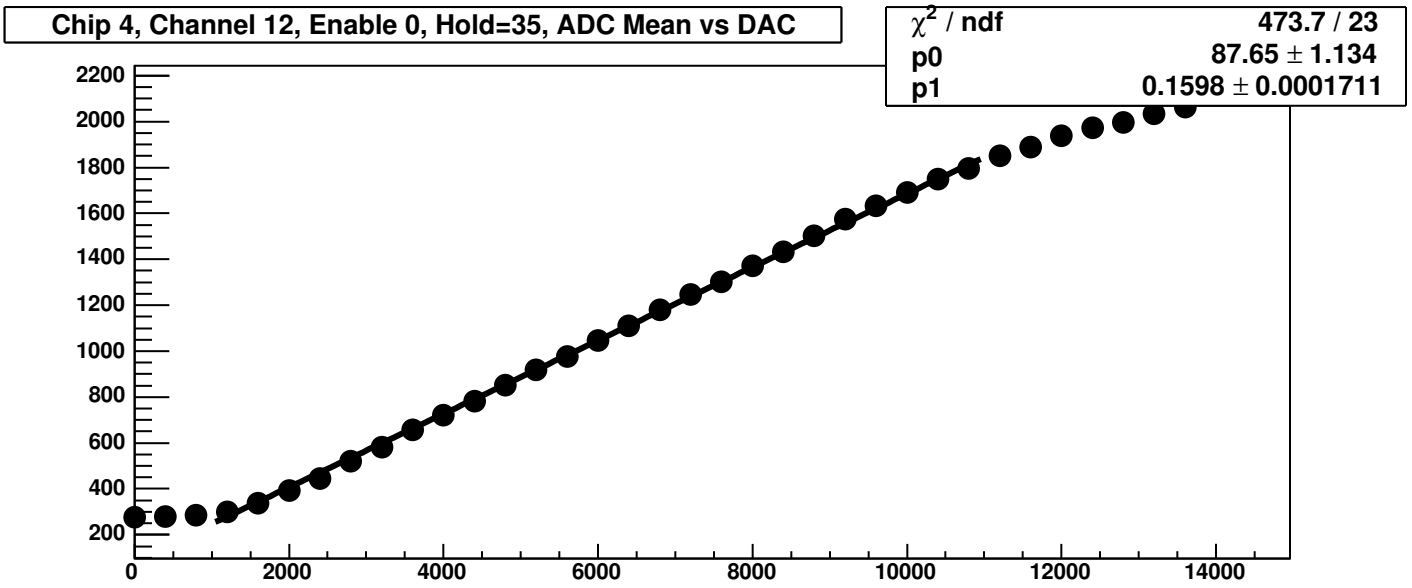


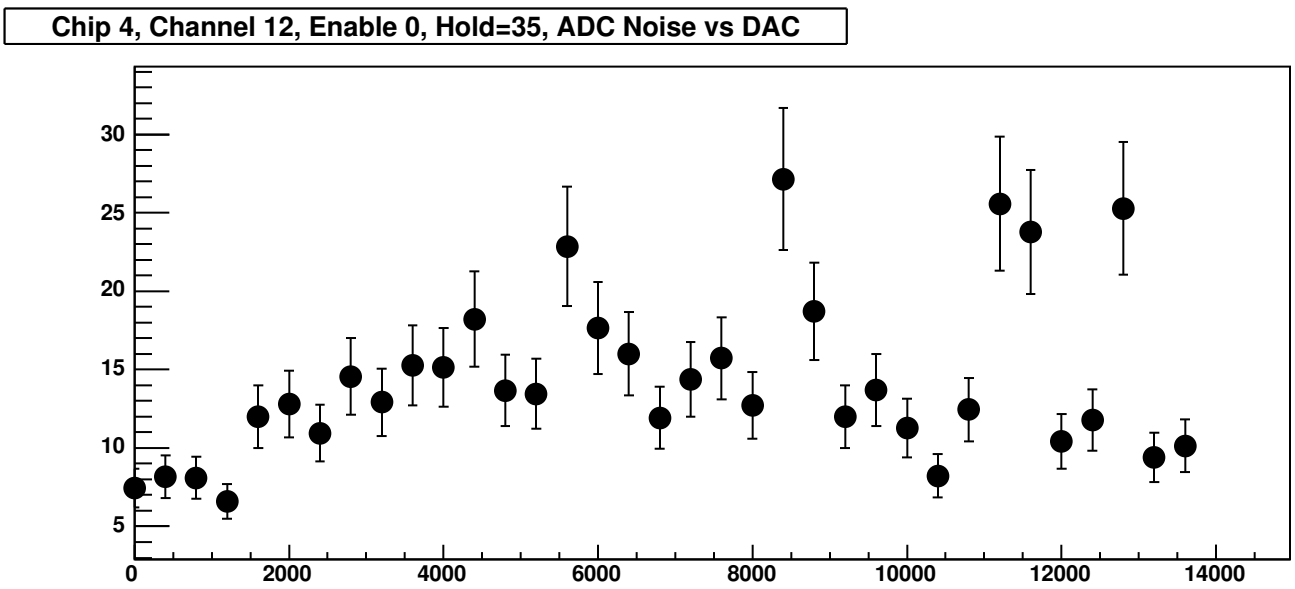
Chip 4, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

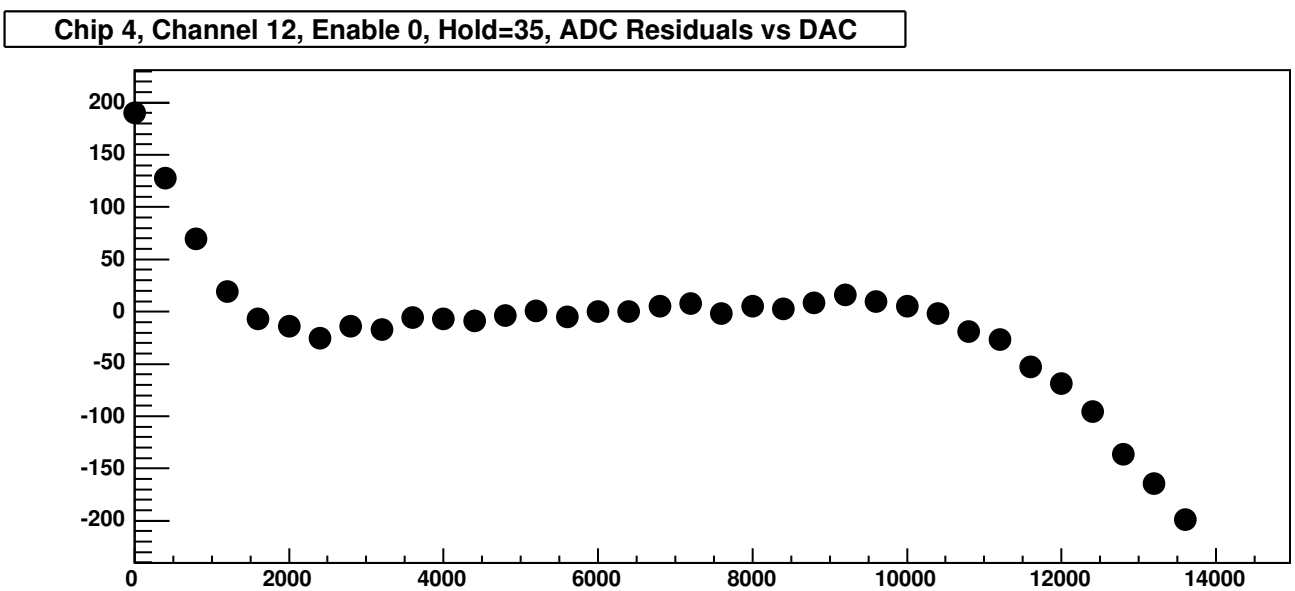


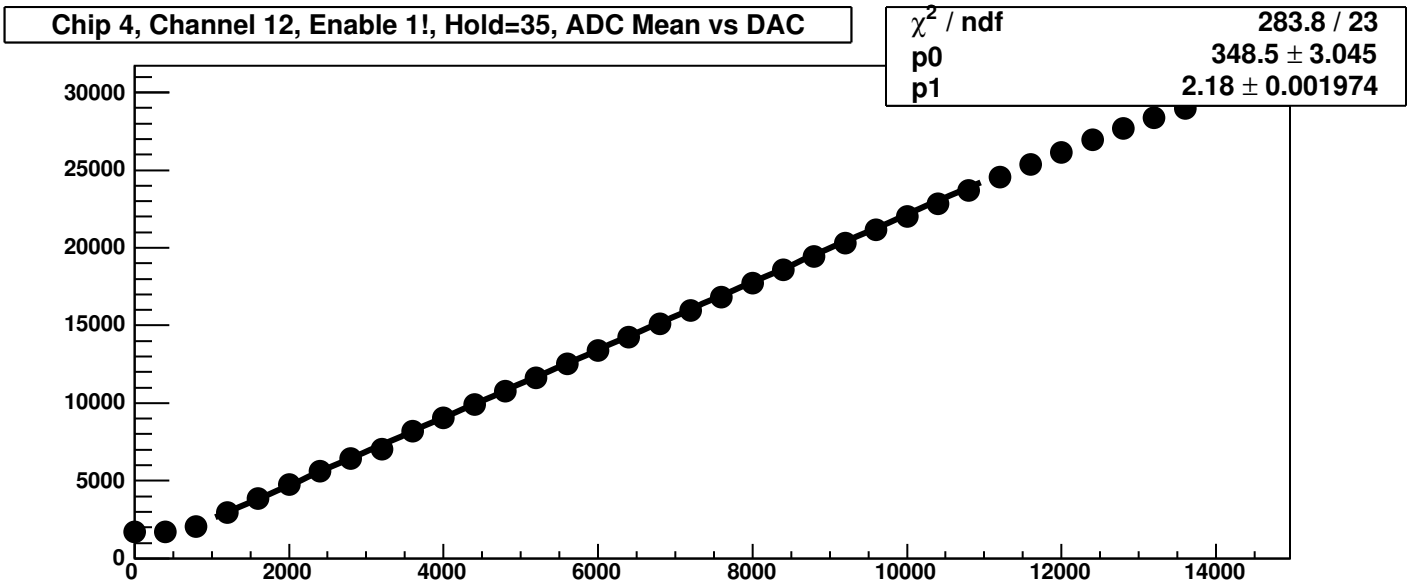
Chip 4, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

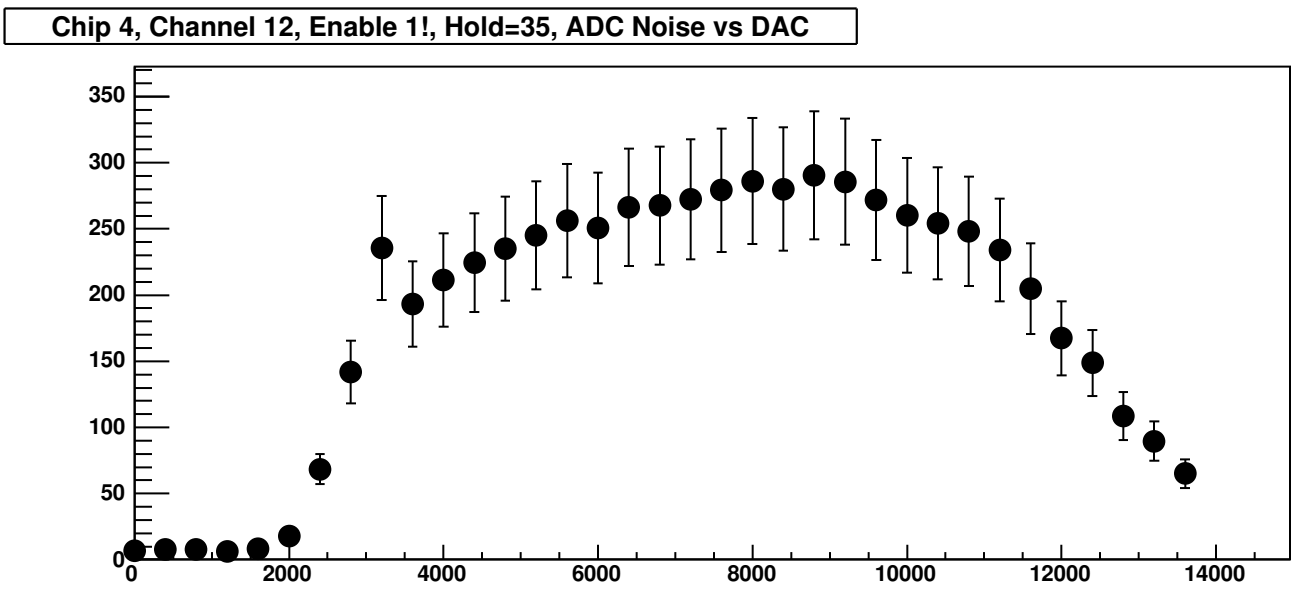


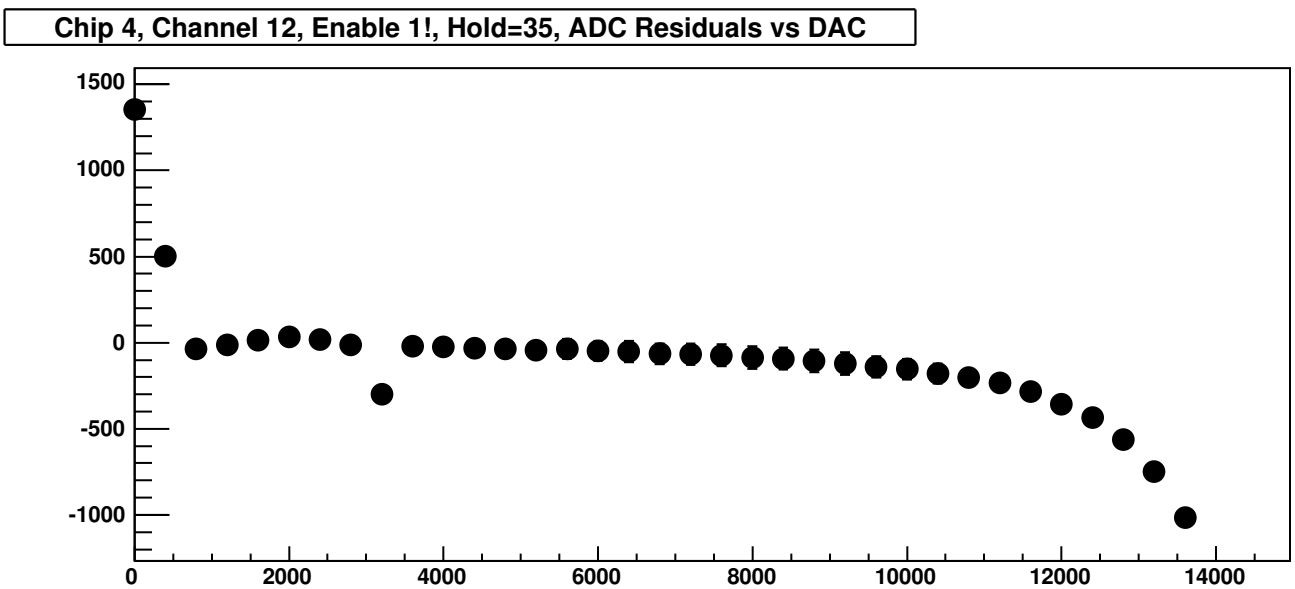

Chip 4, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 473.7 / 23
p0 87.65 ± 1.134
p1 0.1598 ± 0.0001711
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

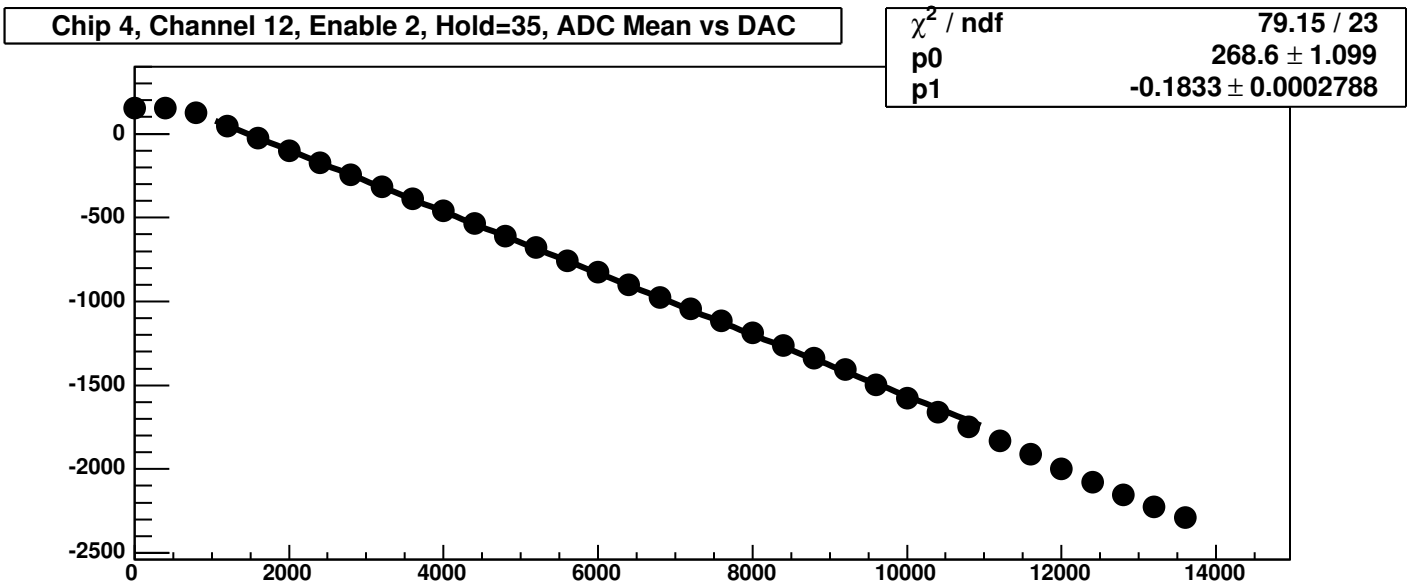

Chip 4, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

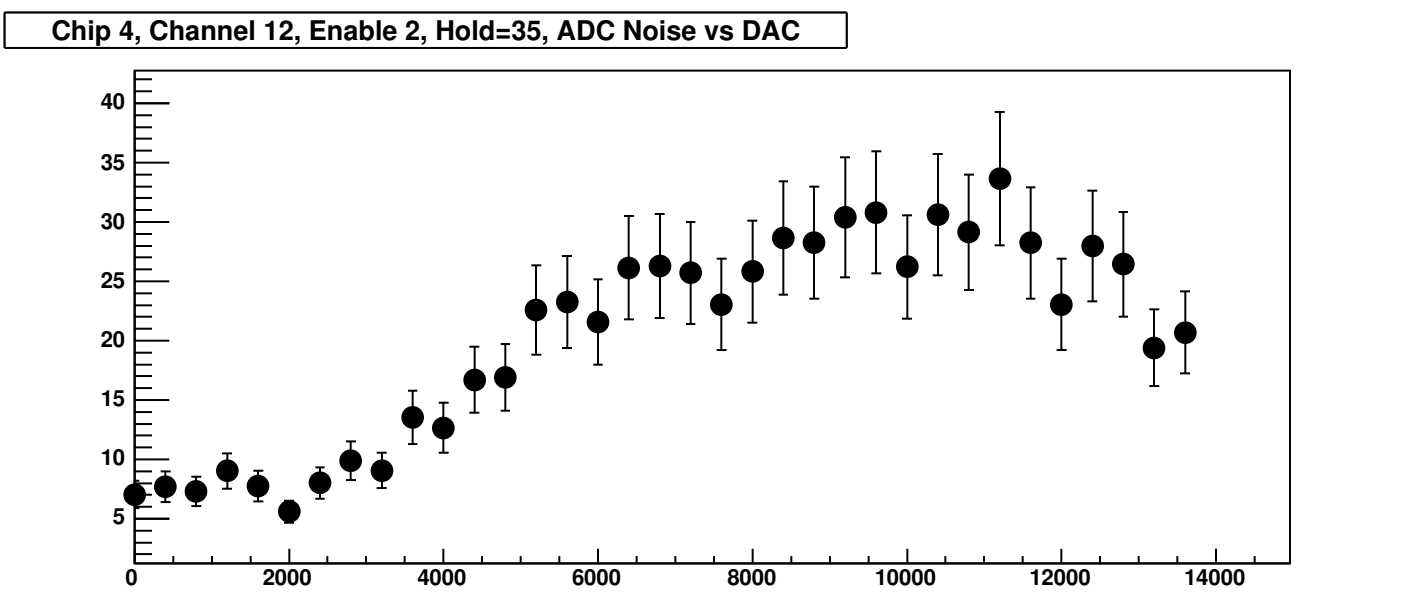

Chip 4, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

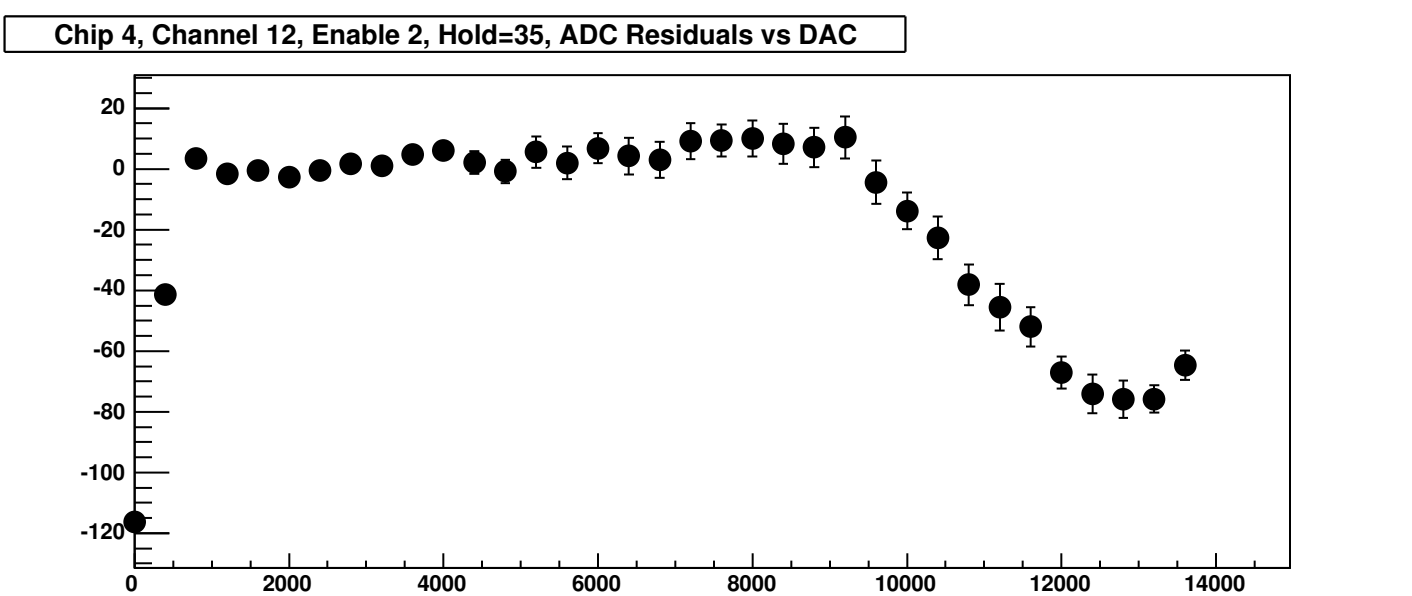

Chip 4, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 283.8 / 23
p0 348.5 ± 3.045
p1 2.18 ± 0.001974
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

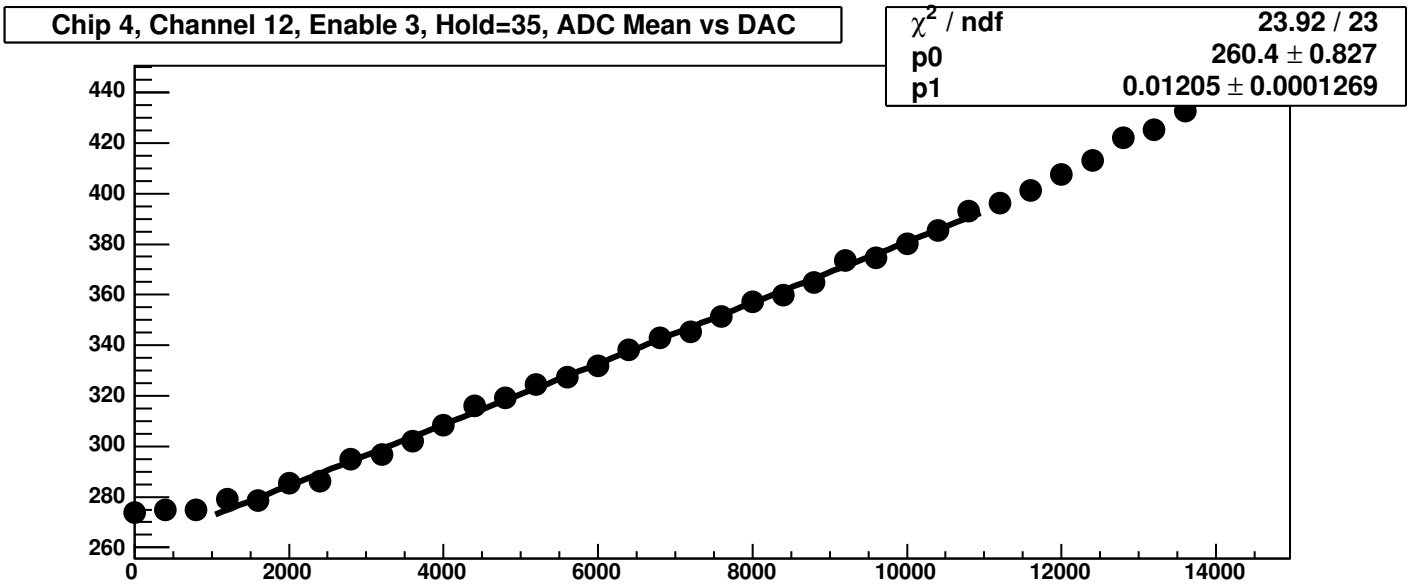

Chip 4, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

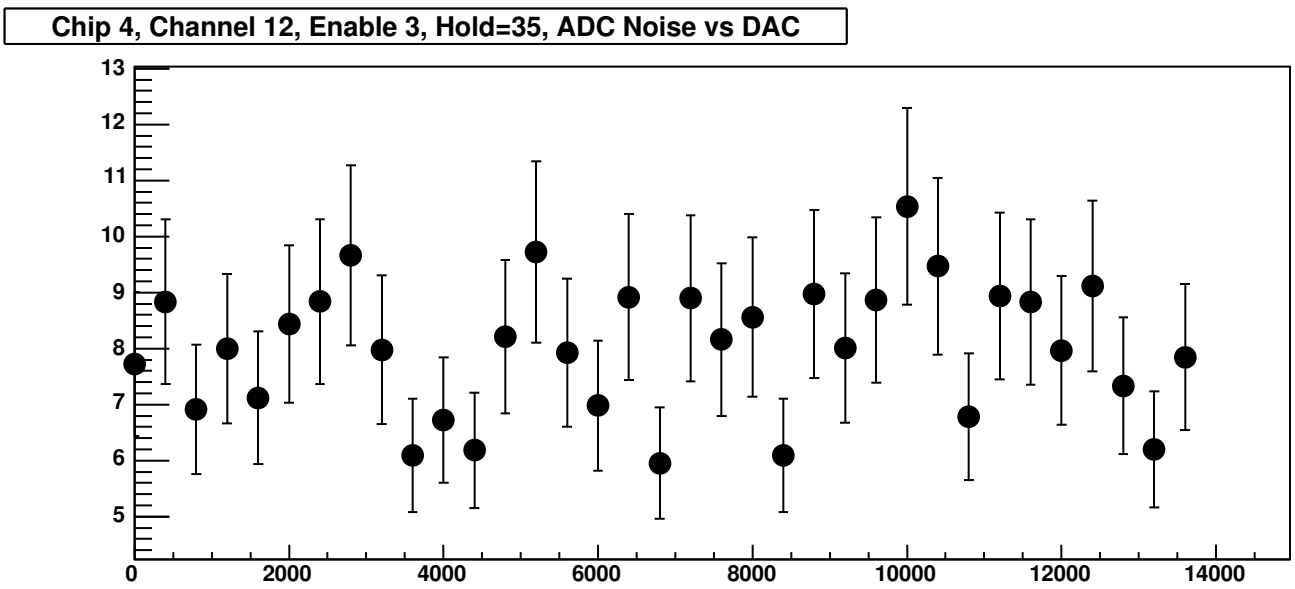

Chip 4, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

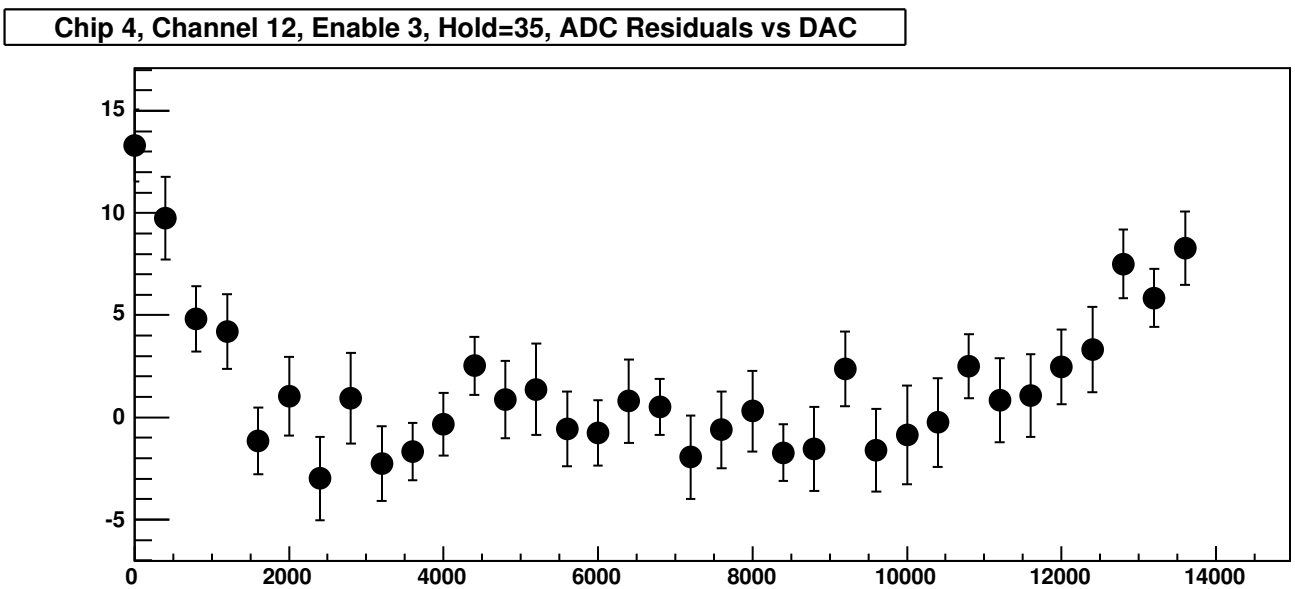

Chip 4, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 79.15 / 23
p0 268.6 ± 1.099
p1 -0.1833 ± 0.0002788
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

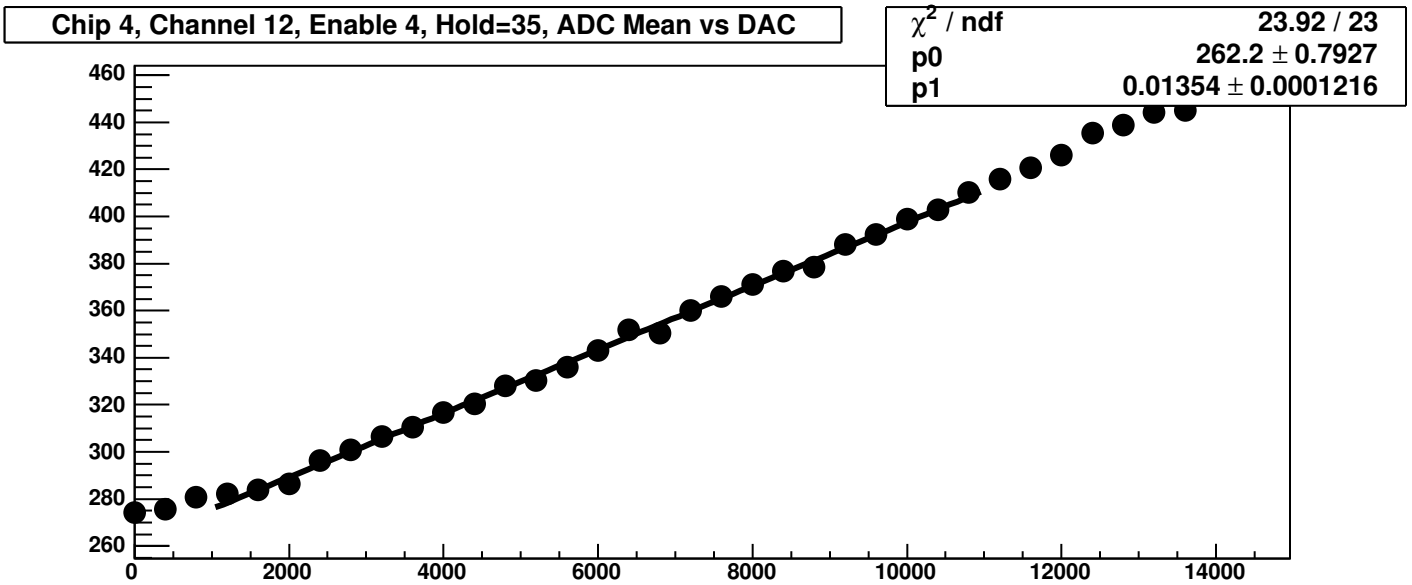

Chip 4, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

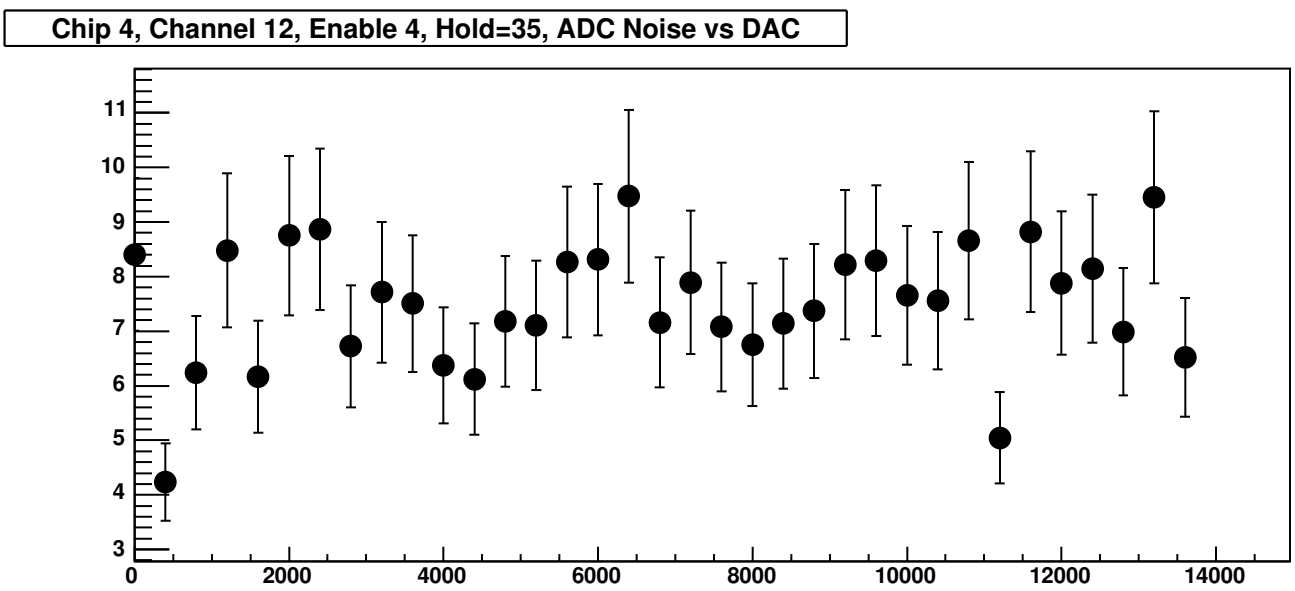

Chip 4, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
2000
4000
6000
8000
10000
12000
14000

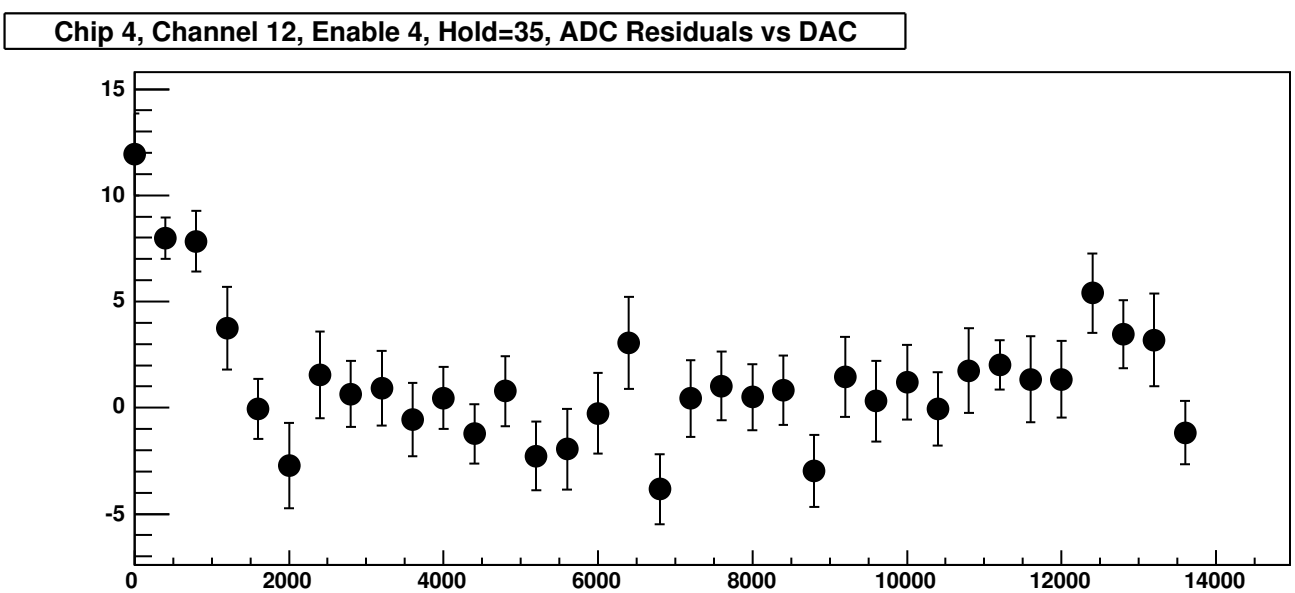

Chip 4, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.92 / 23
p0 260.4 ± 0.827
p1 0.01205 ± 0.0001269
440
420
400
380
360
340
320
300
280
260
0
2000
4000
6000
8000
10000
12000
14000

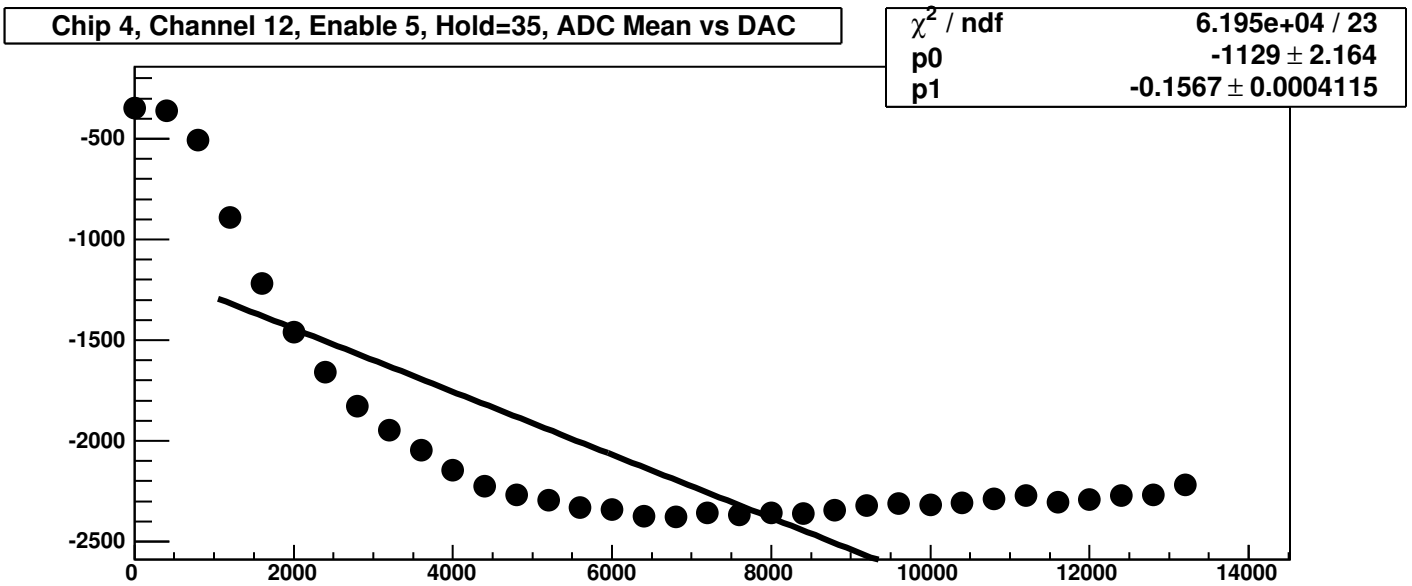

Chip 4, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

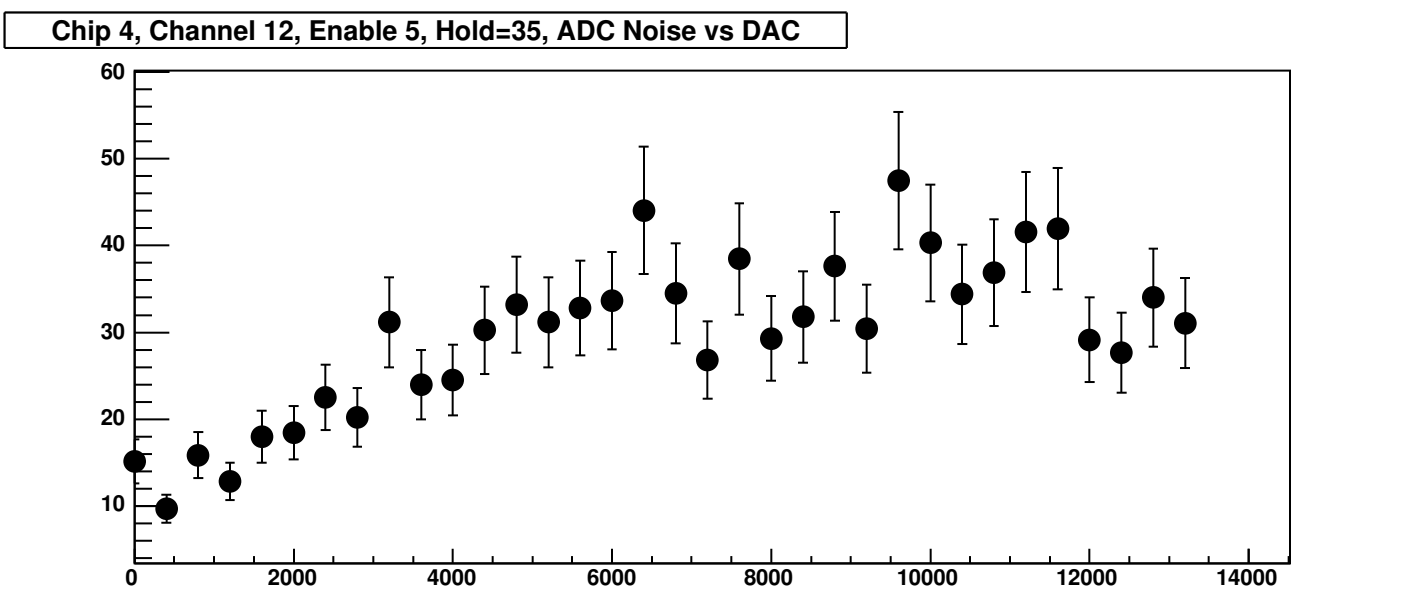

Chip 4, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

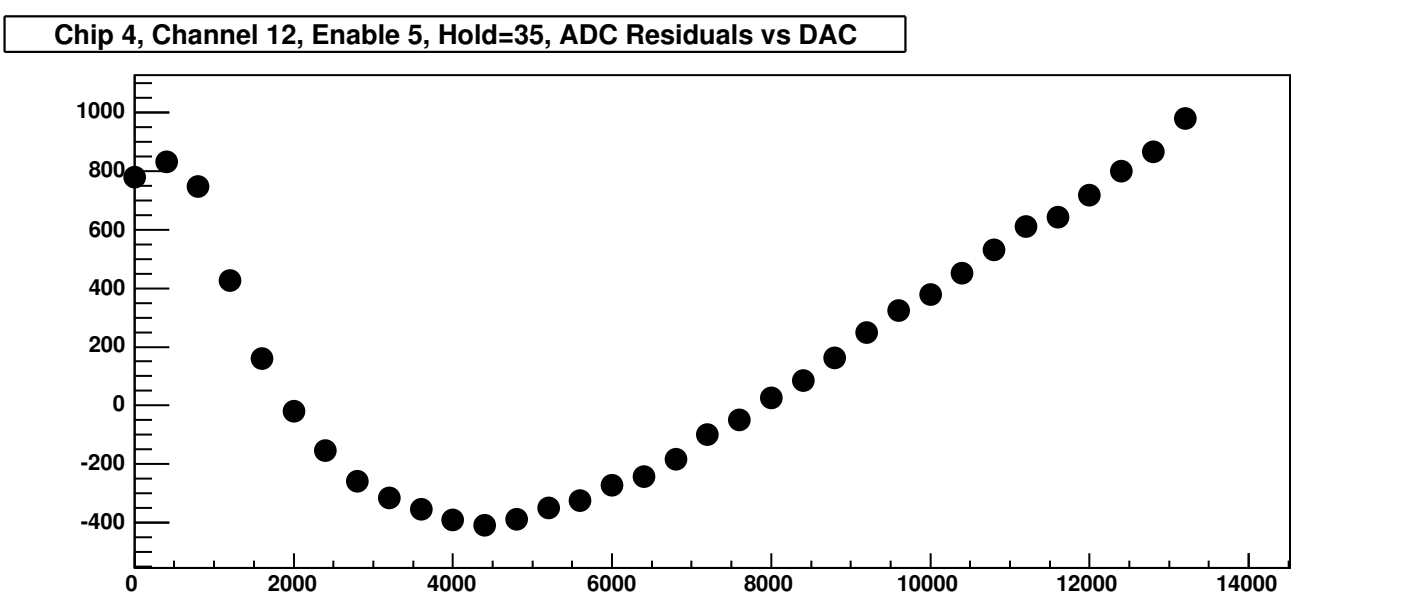

Chip 4, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 23.92 / 23
p0 262.2 ± 0.7927
p1 0.01354 ± 0.0001216
460
440
420
400
380
360
340
320
300
280
260
0
2000
4000
6000
8000
10000
12000
14000

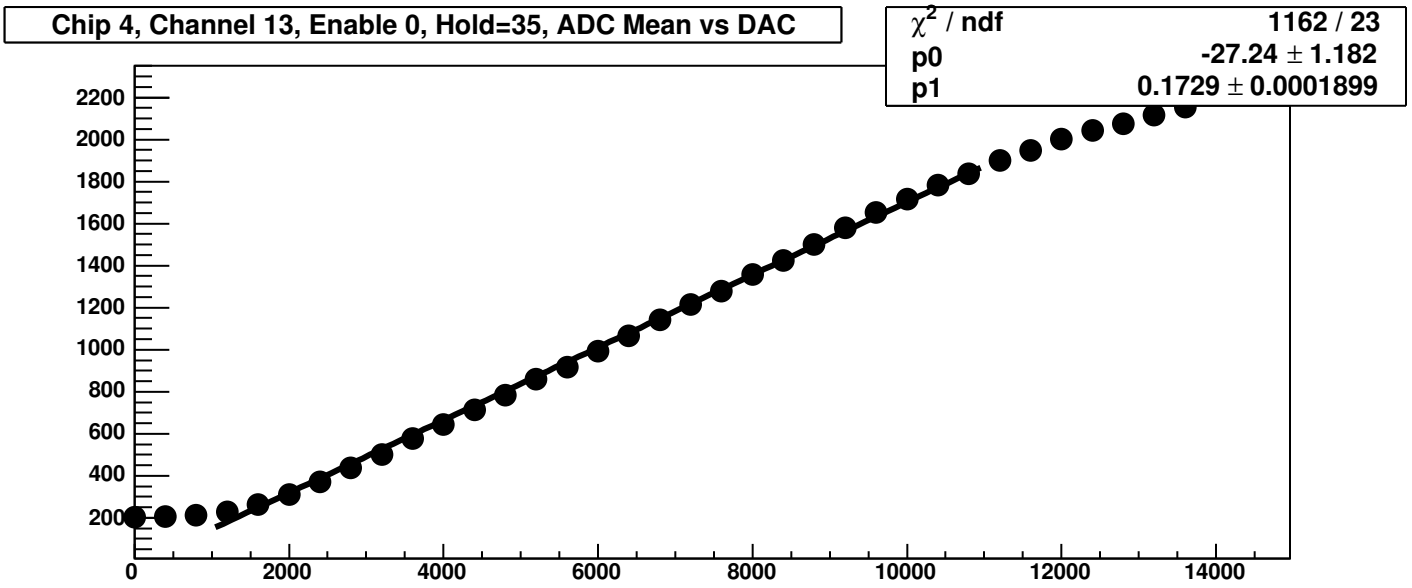

Chip 4, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

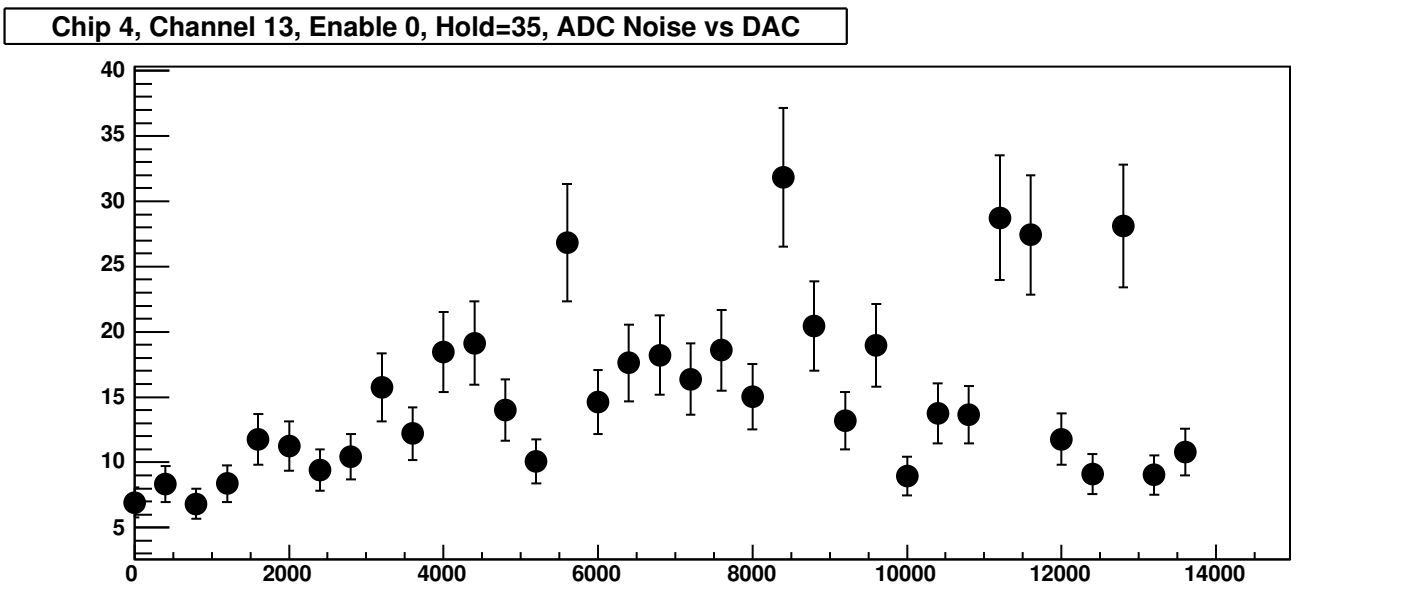

Chip 4, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

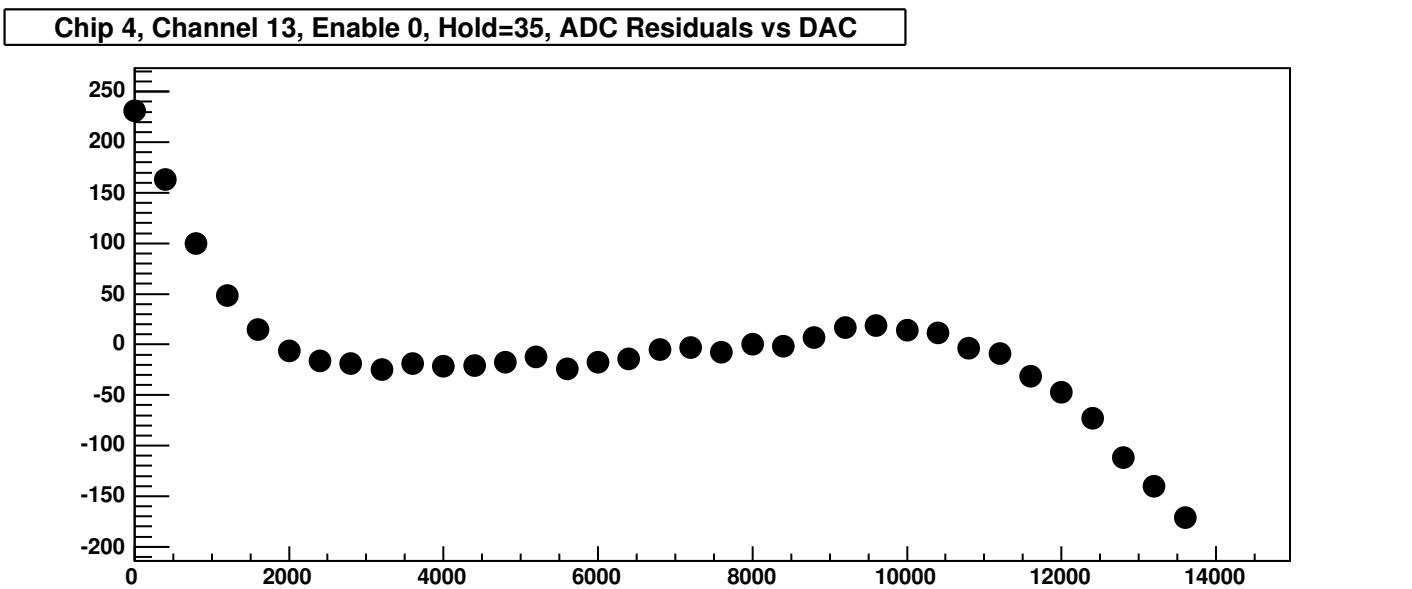

Chip 4, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 6.195e+04 / 23
p0 -1129 ± 2.164
p1 -0.1567 ± 0.0004115
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

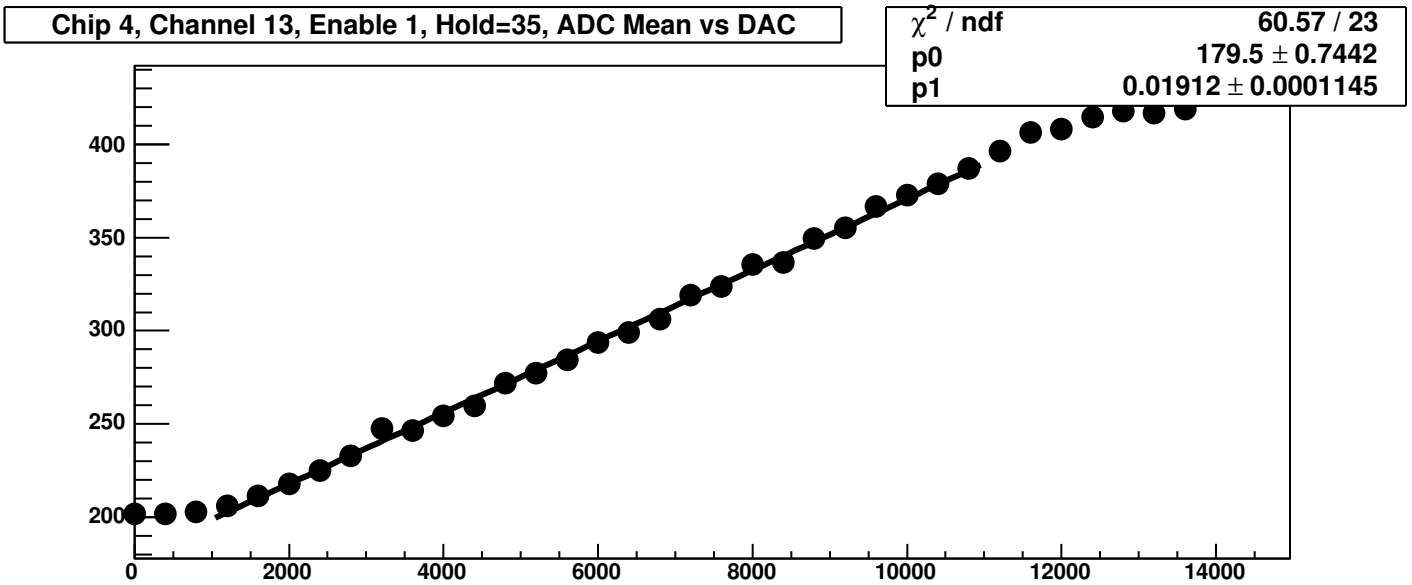

Chip 4, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

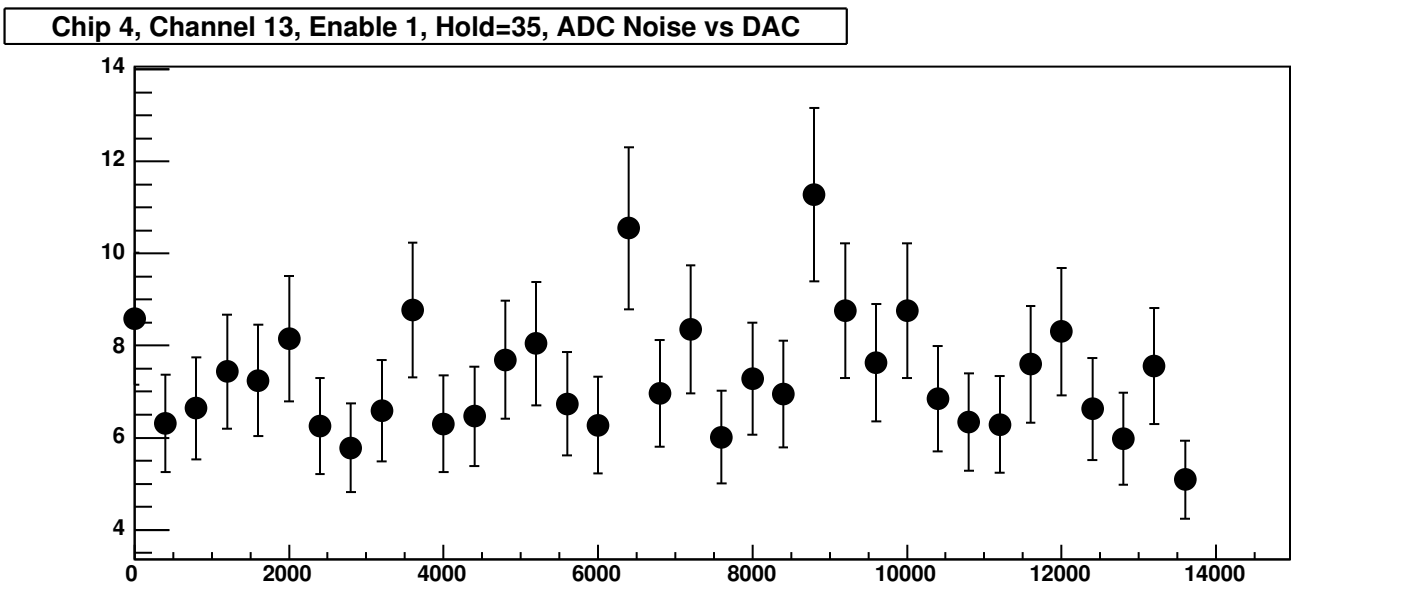

Chip 4, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

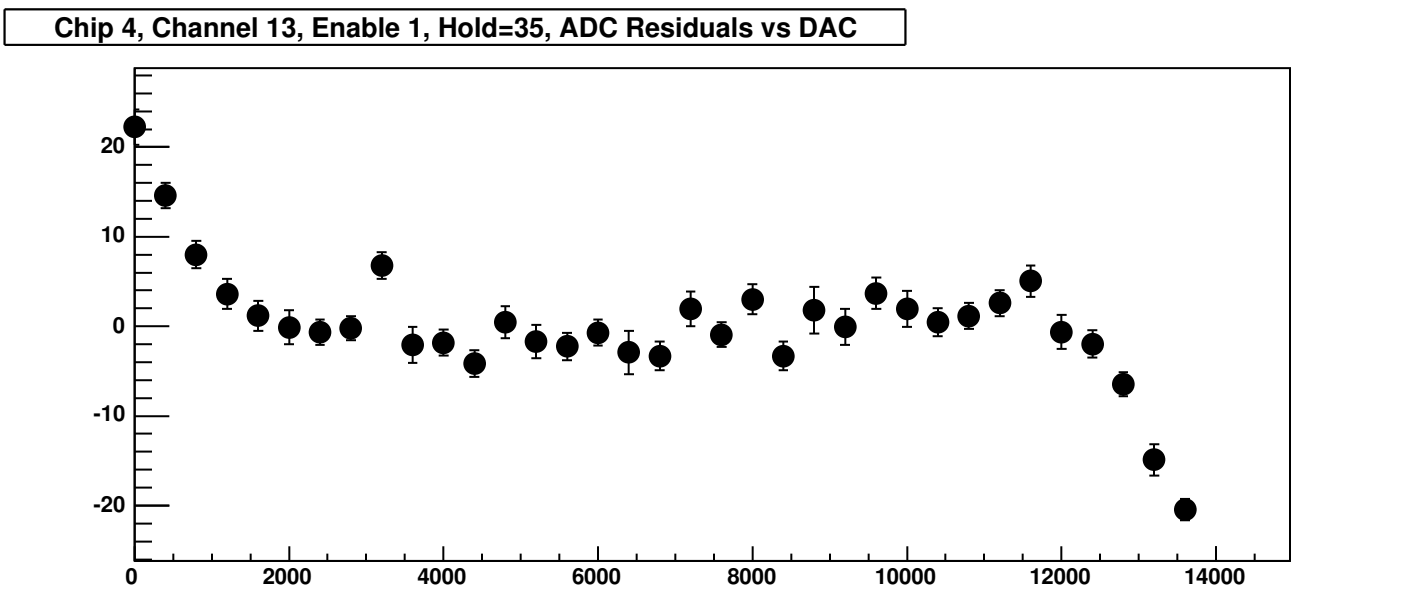

Chip 4, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1162 / 23
p0 -27.24 ± 1.182
p1 0.1729 ± 0.0001899
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

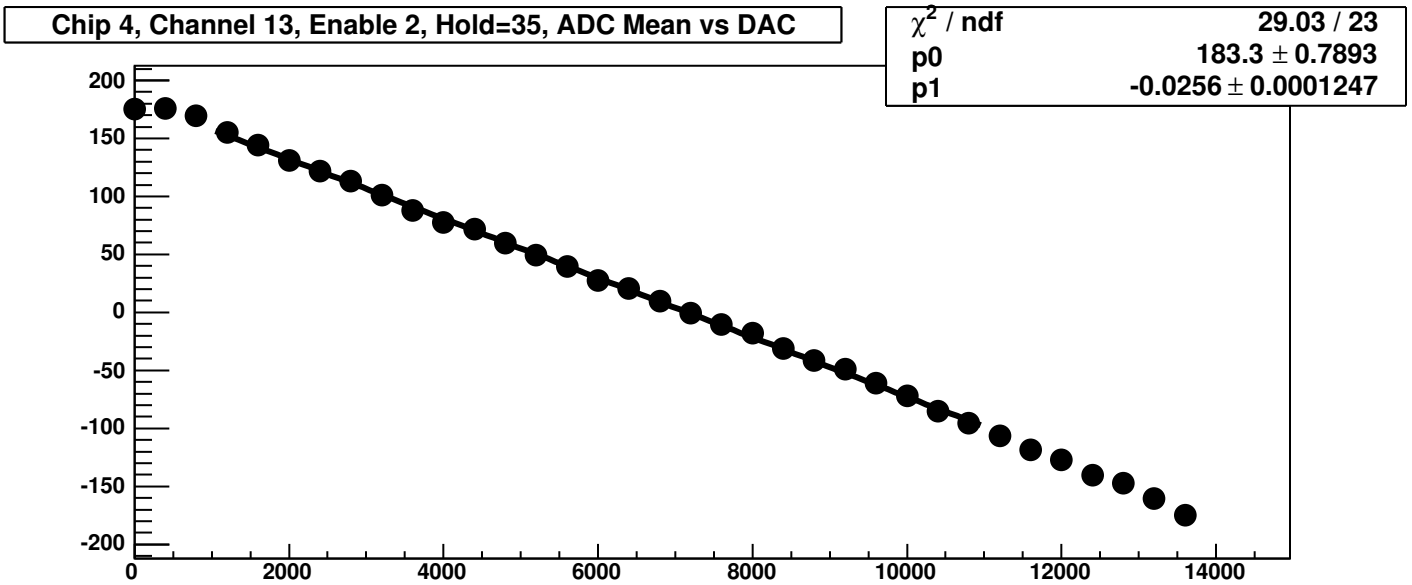

Chip 4, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

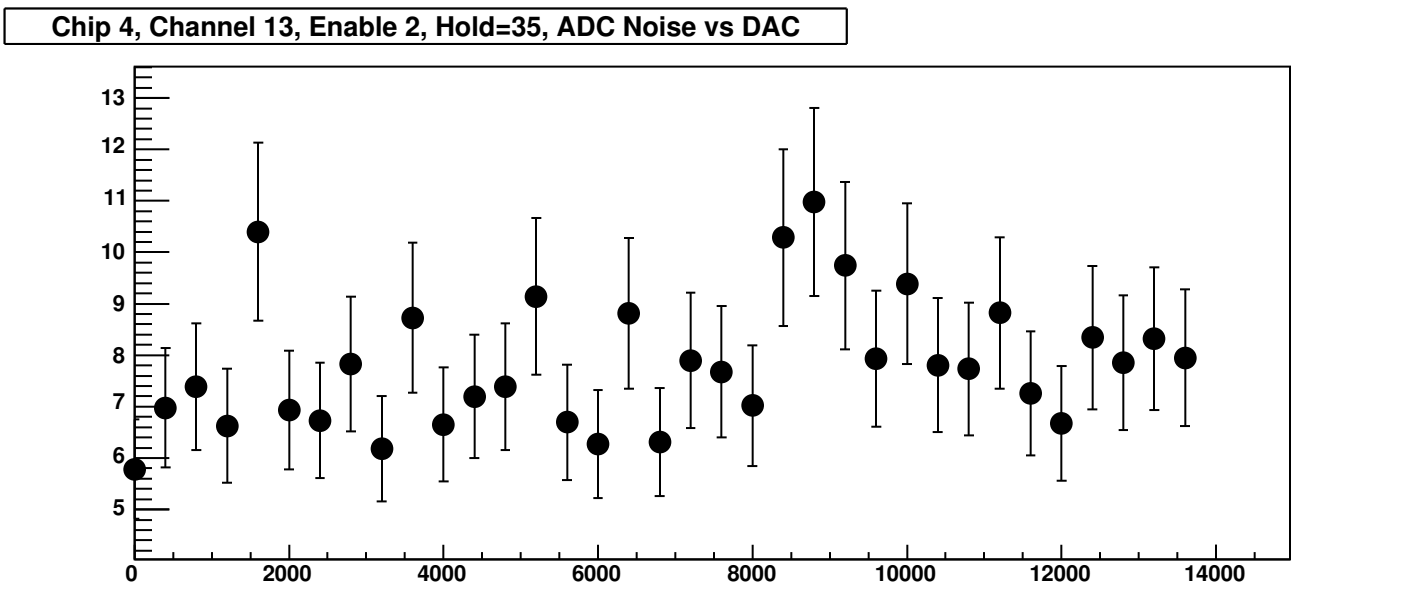

Chip 4, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

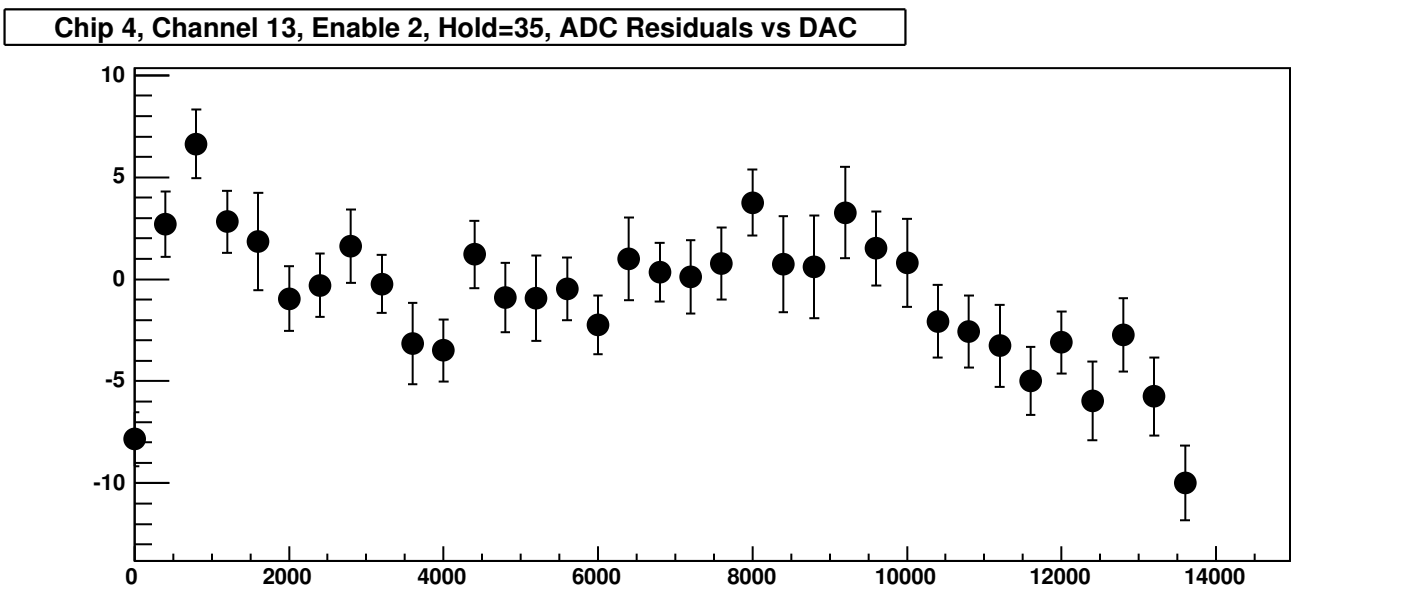

Chip 4, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 60.57 / 23
p0 179.5 ± 0.7442
p1 0.01912 ± 0.0001145
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

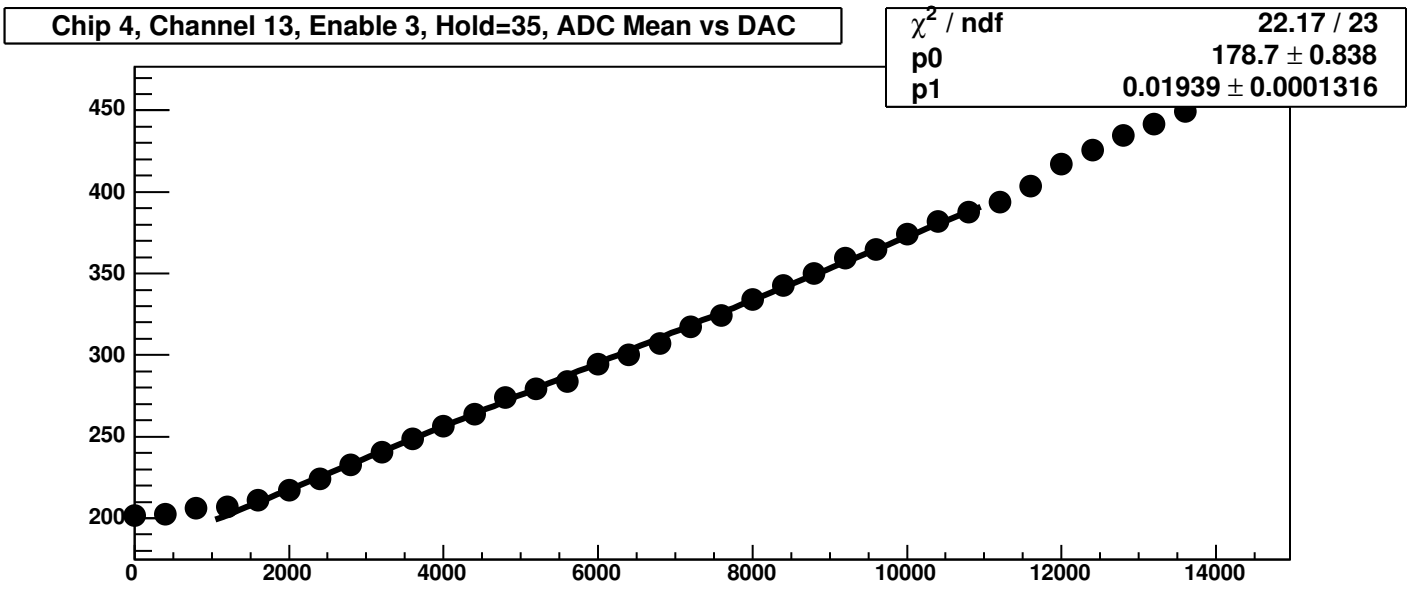

Chip 4, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

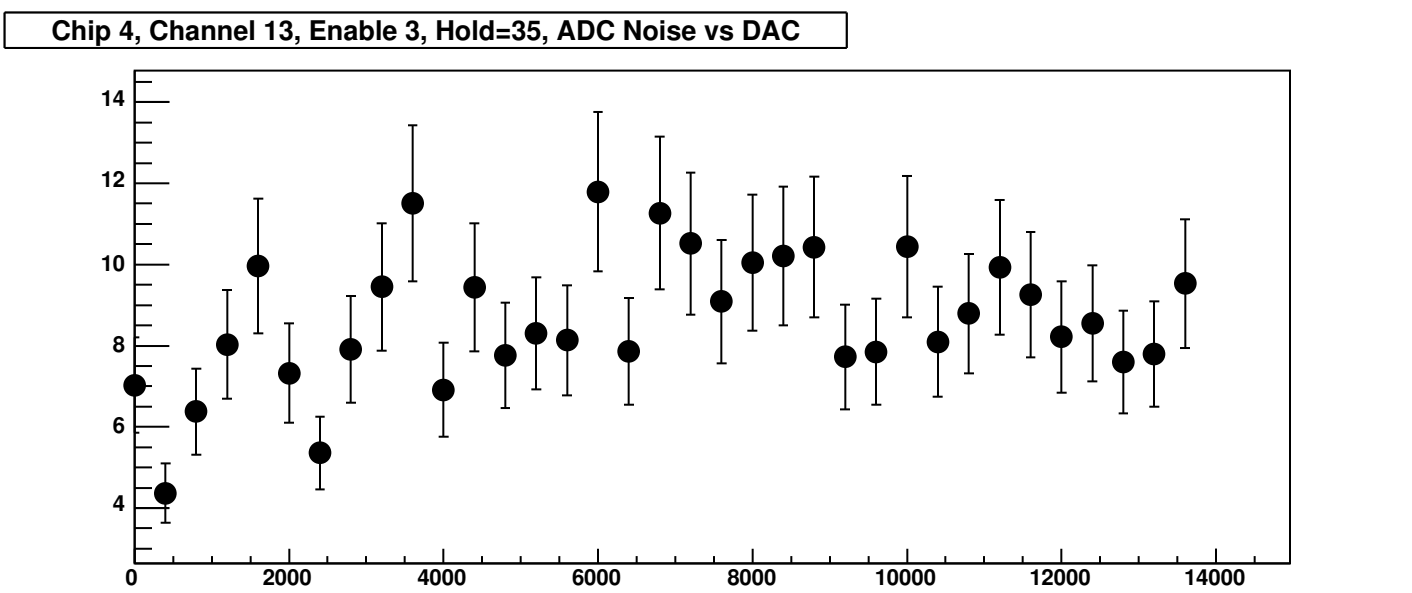

Chip 4, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

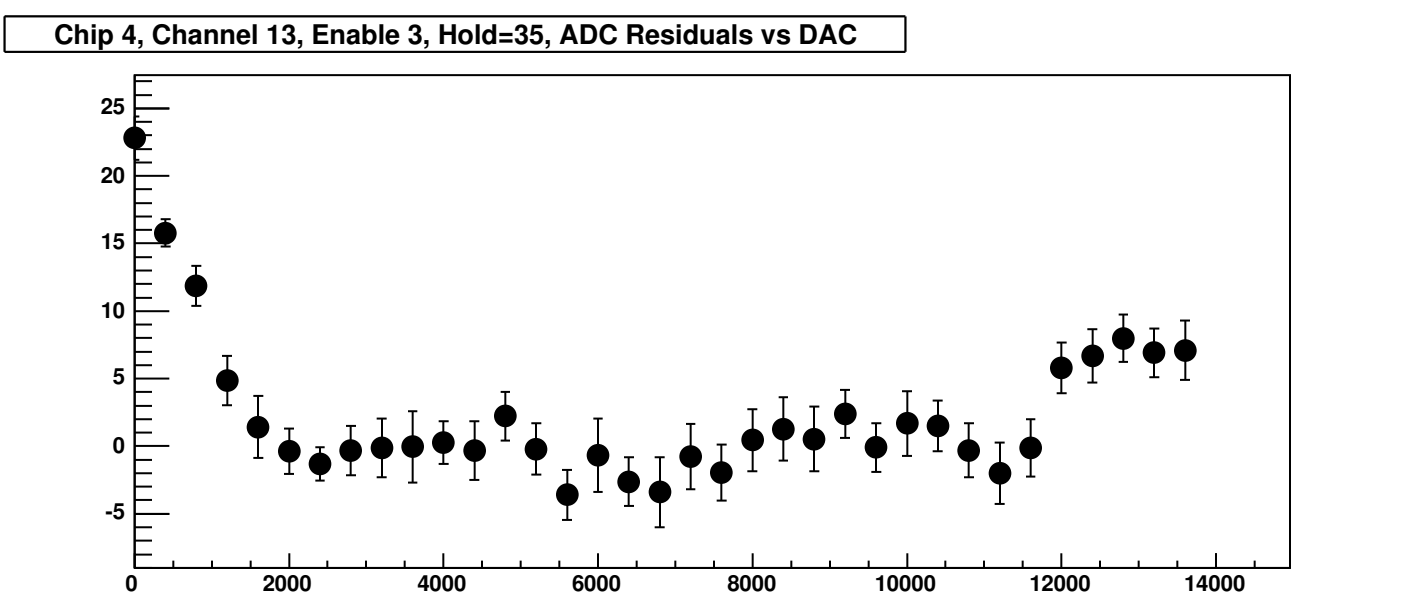

Chip 4, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 29.03 / 23
p0 183.3 ± 0.7893
p1 -0.0256 ± 0.0001247
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

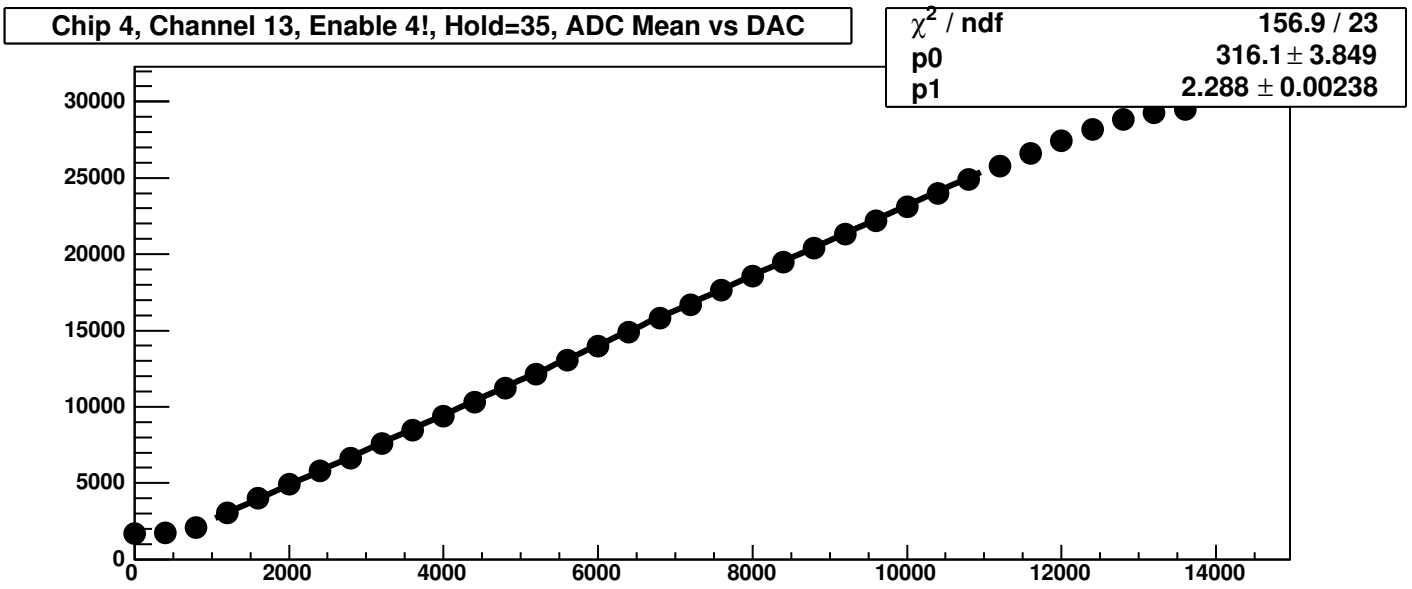

Chip 4, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

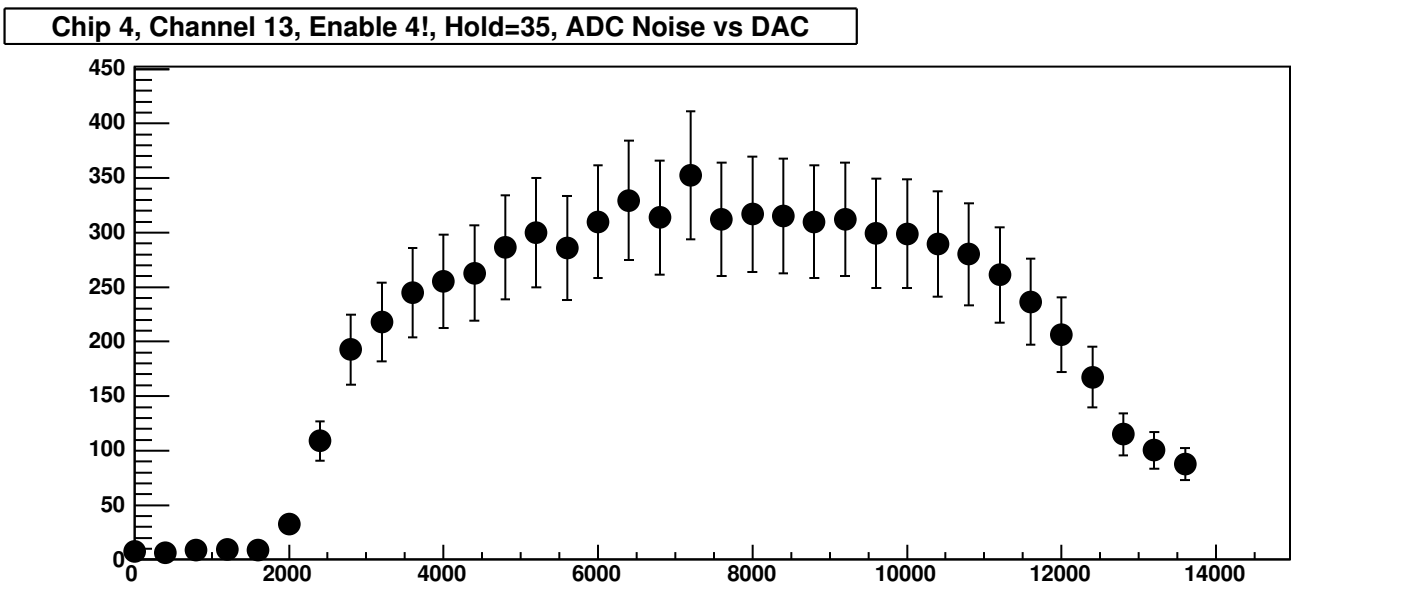

Chip 4, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

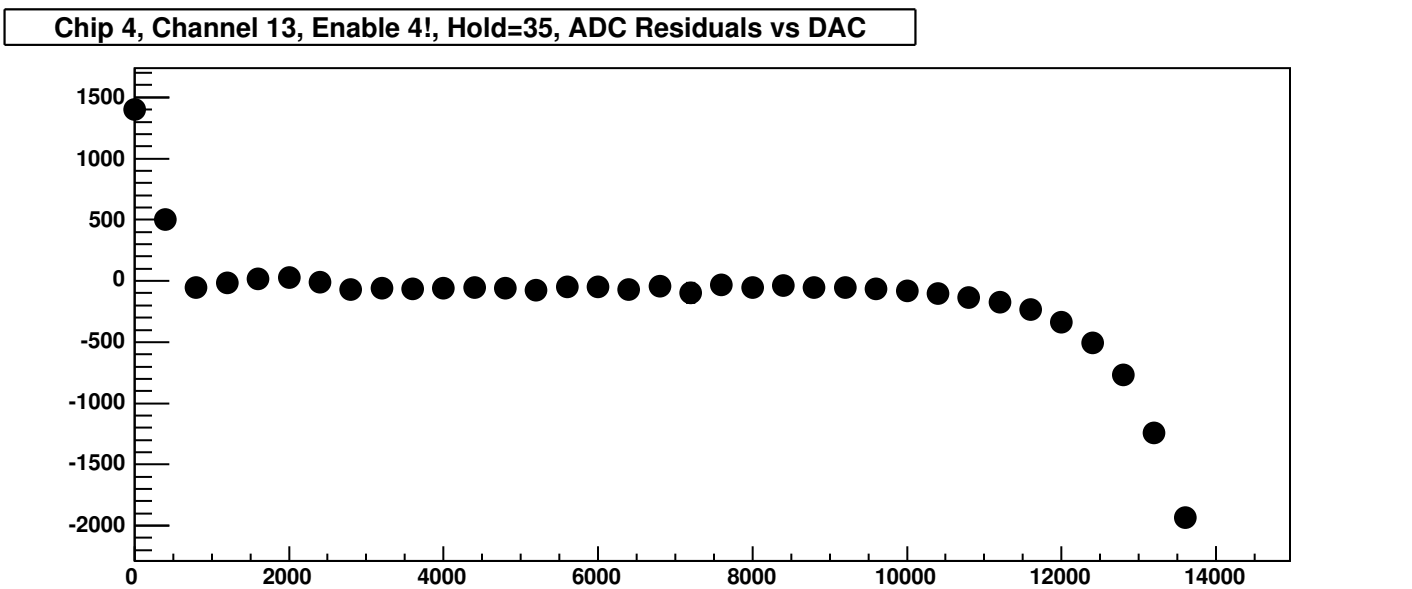

Chip 4, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 22.17 / 23
p0 178.7 ± 0.838
p1 0.01939 ± 0.0001316
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

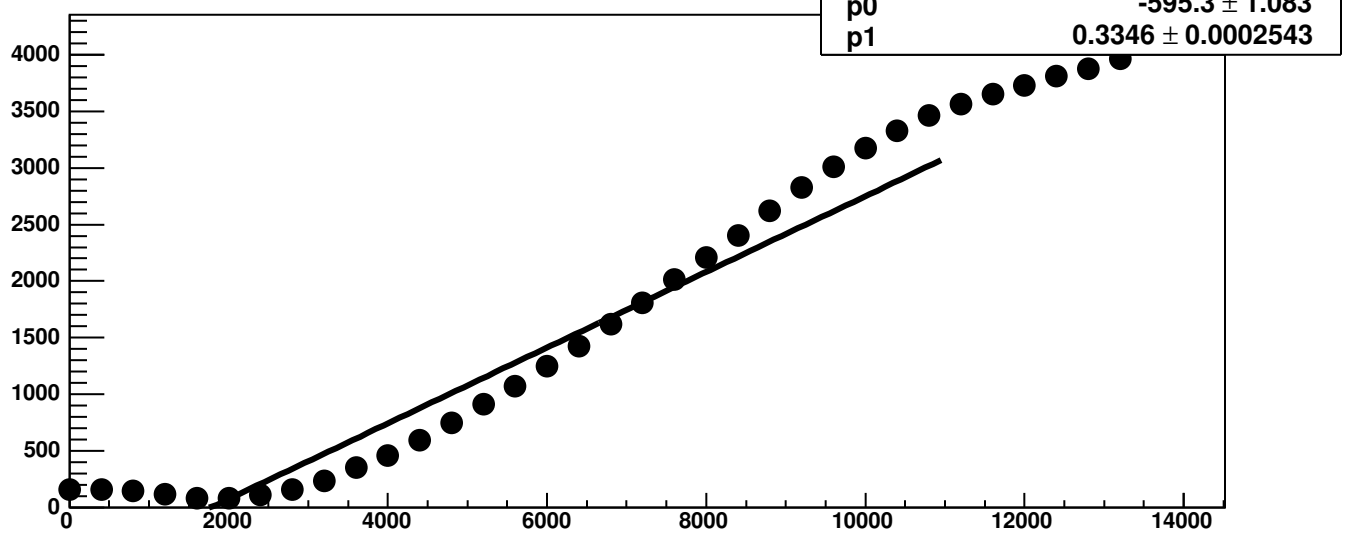

Chip 4, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 156.9 / 23
p0 316.1 ± 3.849
p1 2.288 ± 0.00238
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

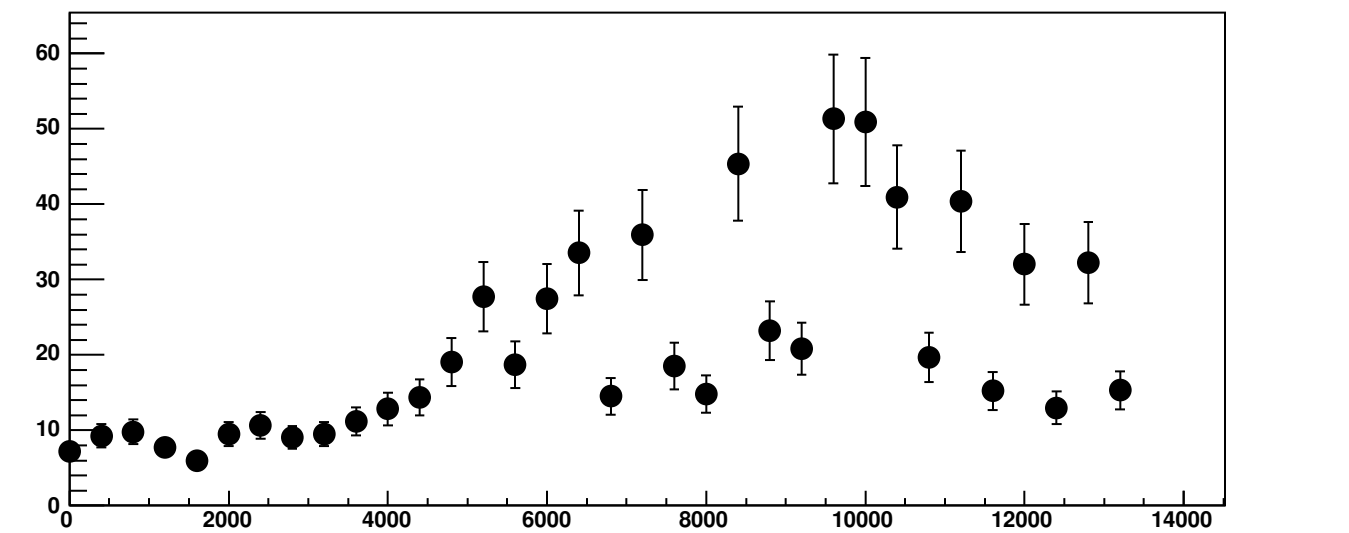

Chip 4, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

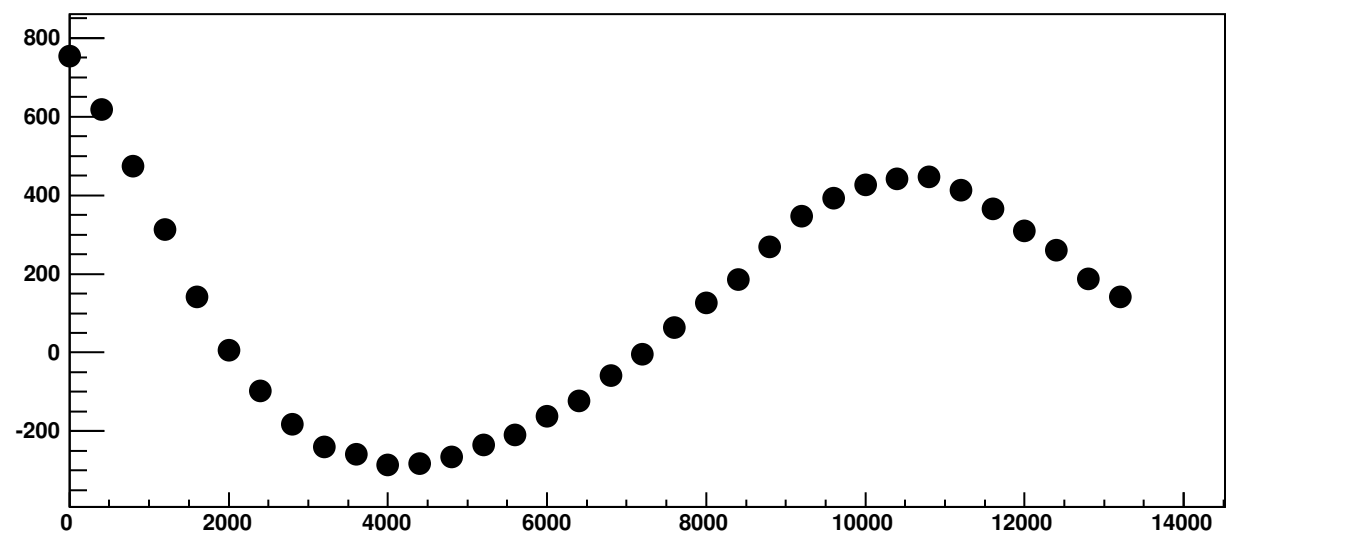
Chip 4, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

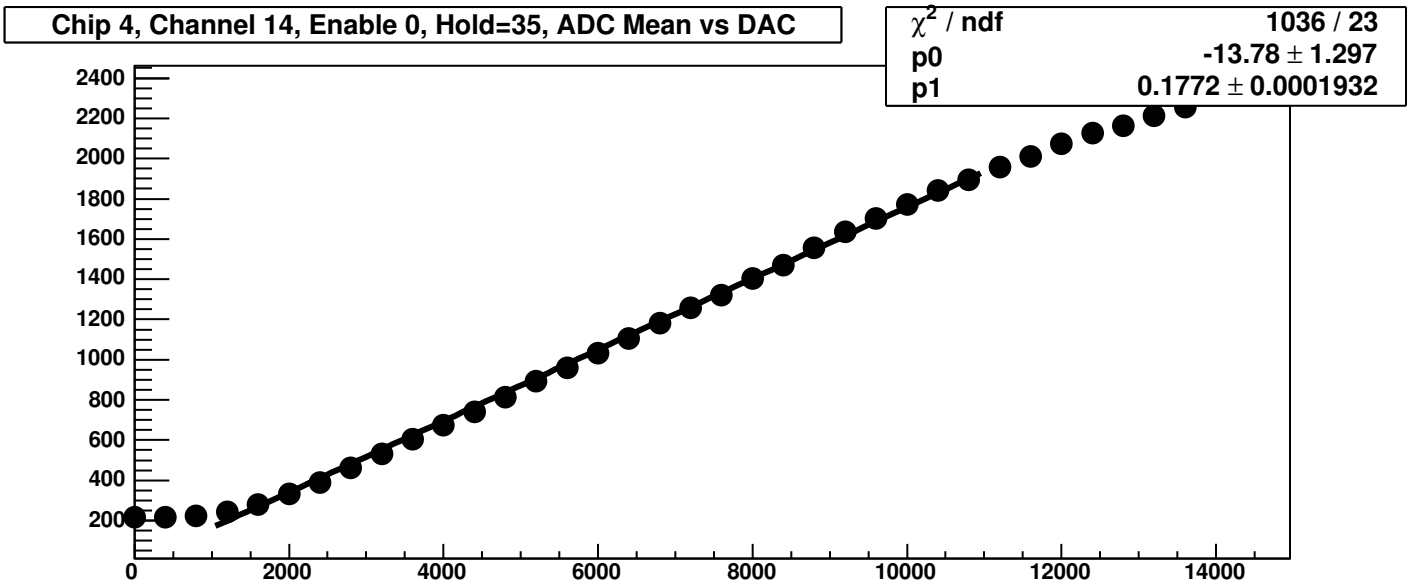


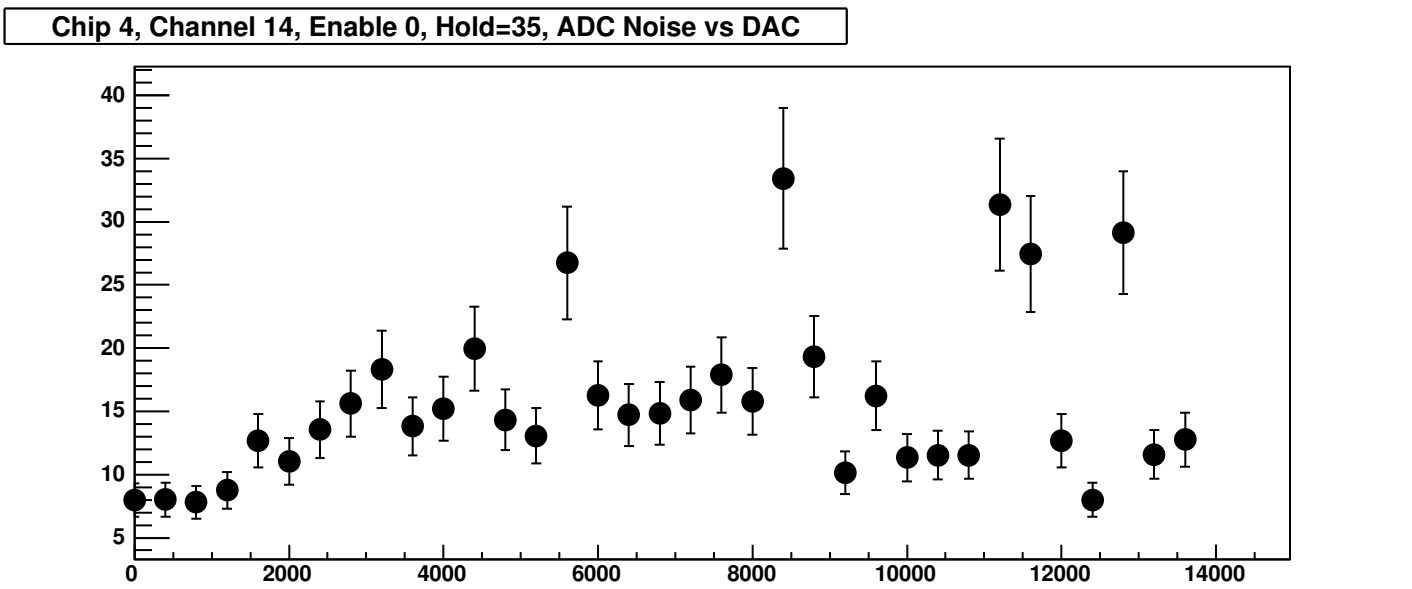
Chip 4, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

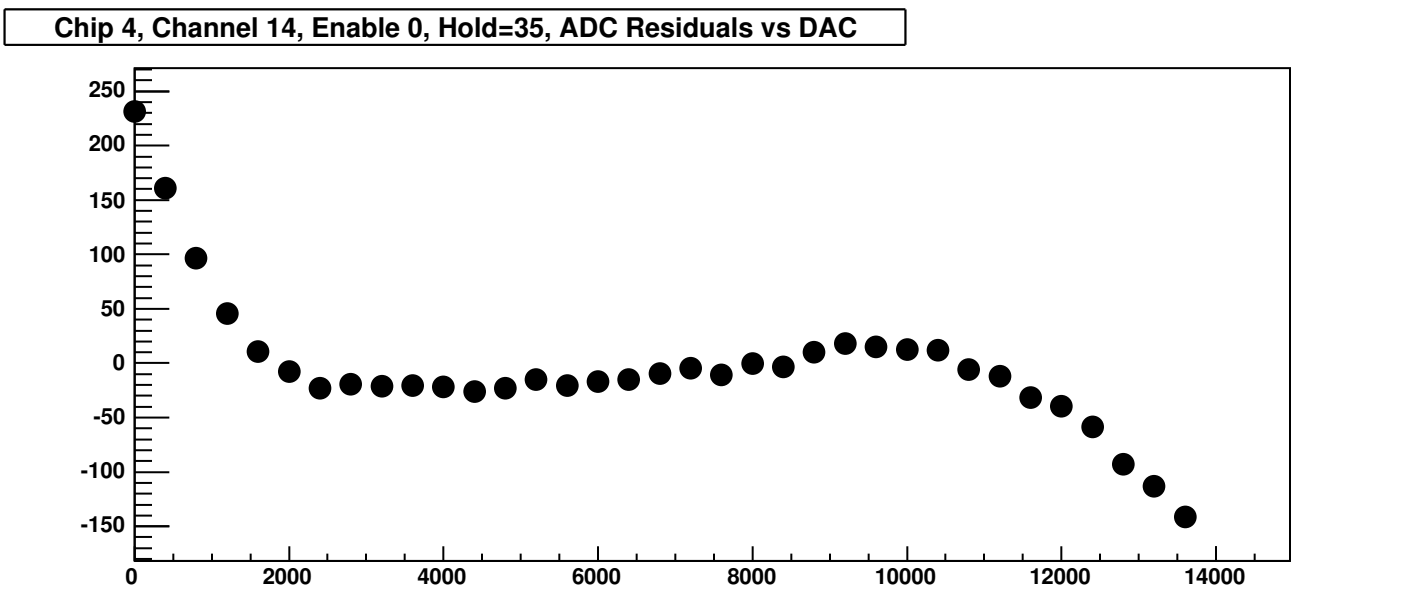


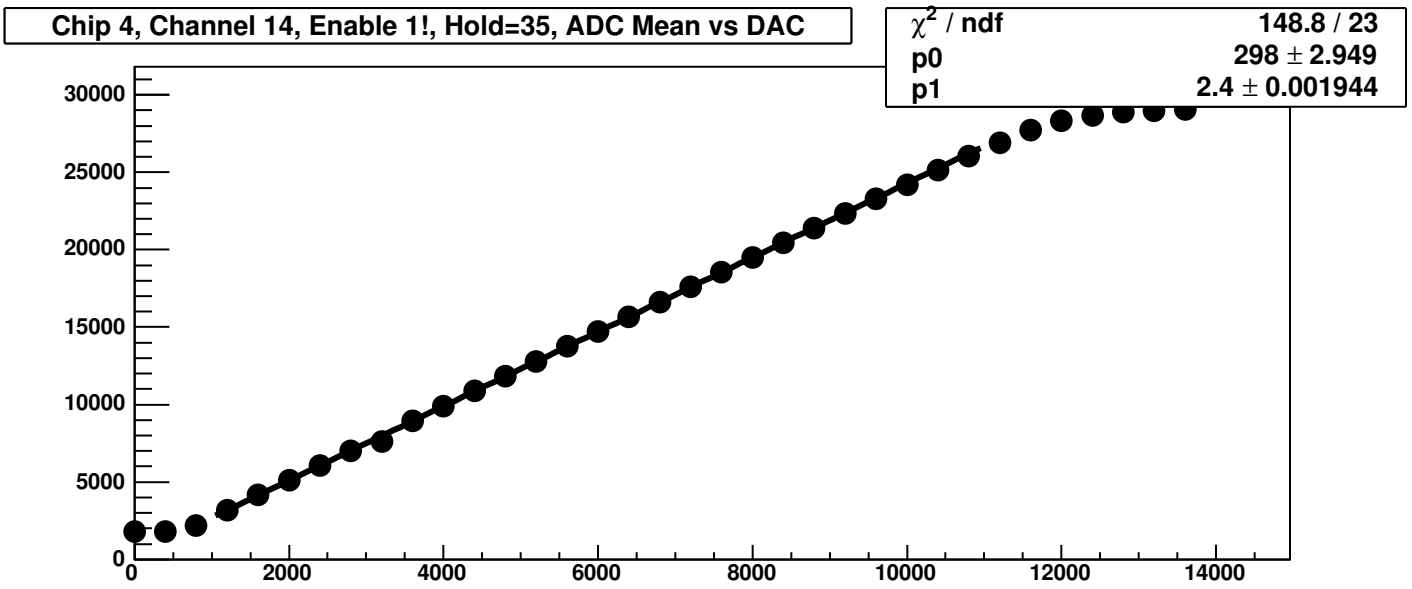
Chip 4, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

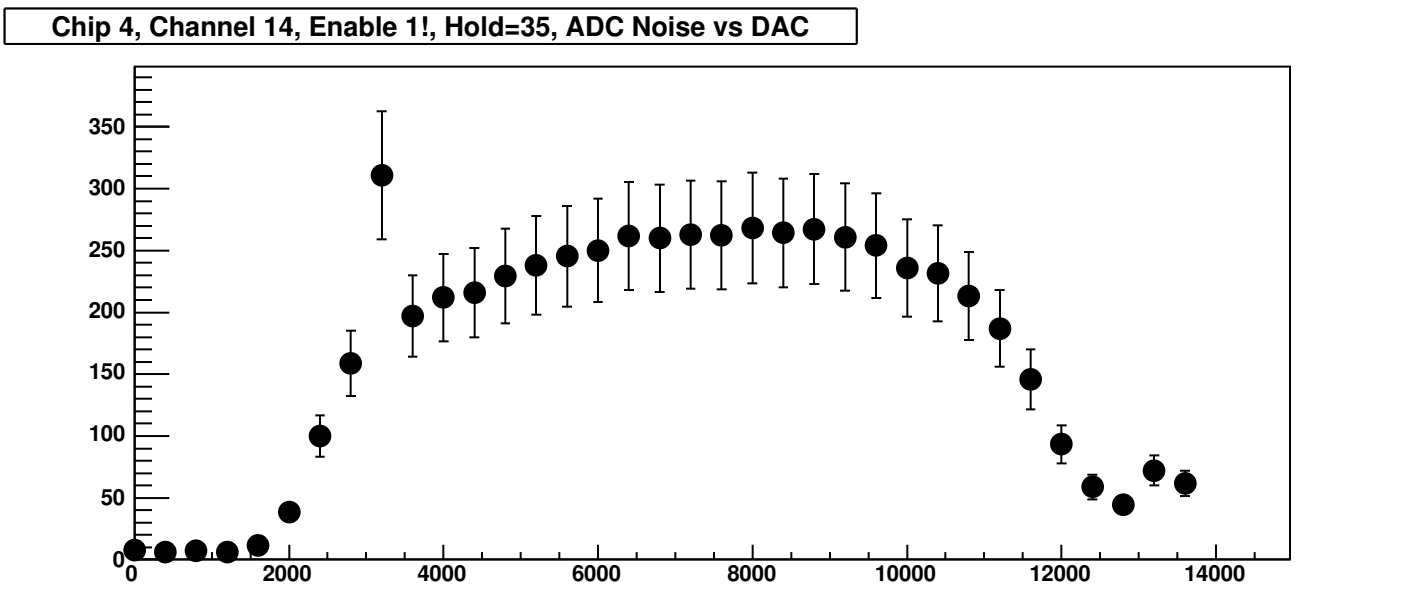


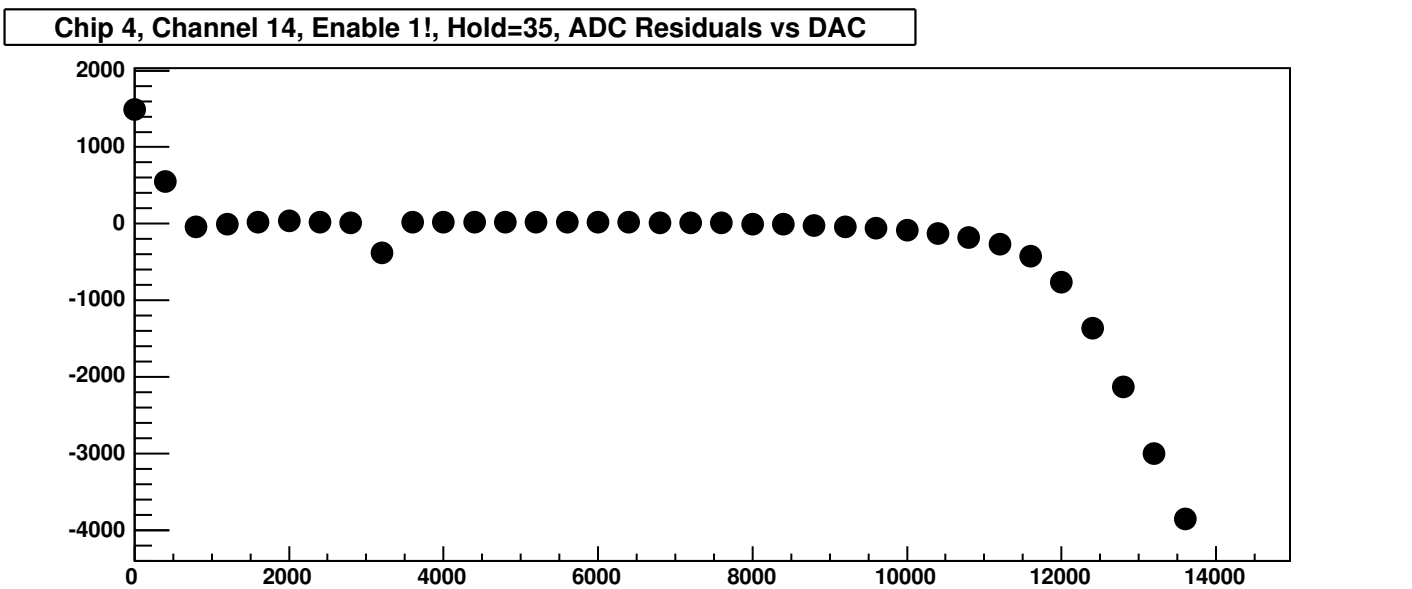

Chip 4, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1036 / 23
p0 -13.78 ± 1.297
p1 0.1772 ± 0.0001932
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

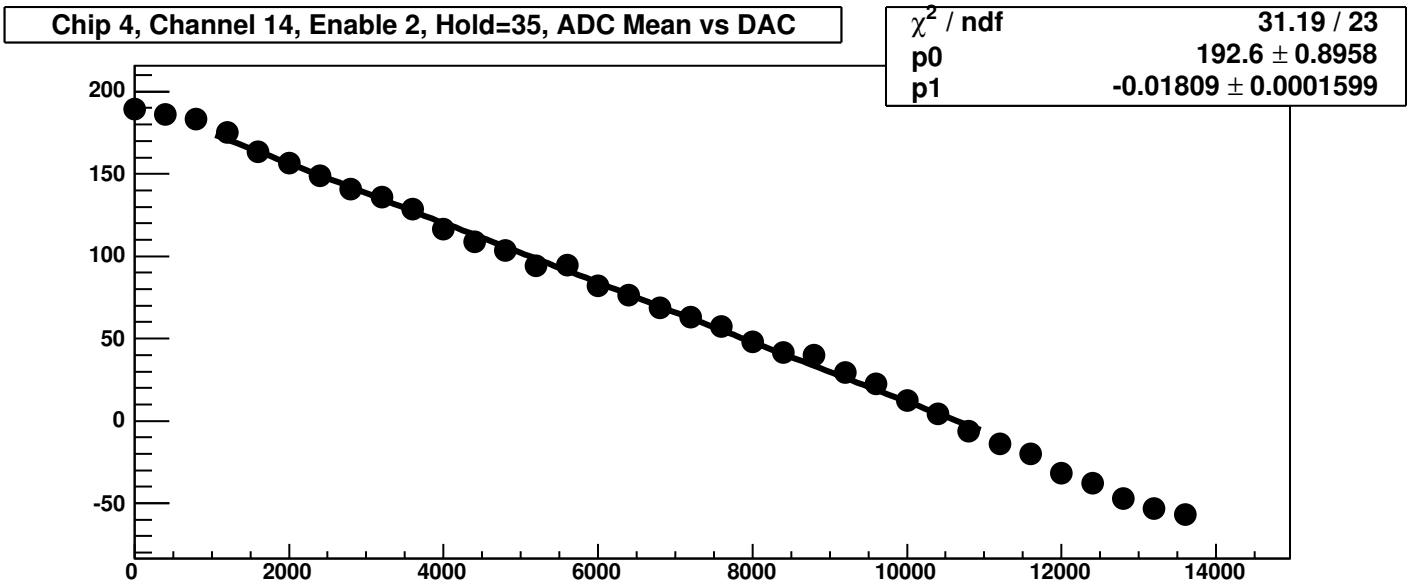

Chip 4, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

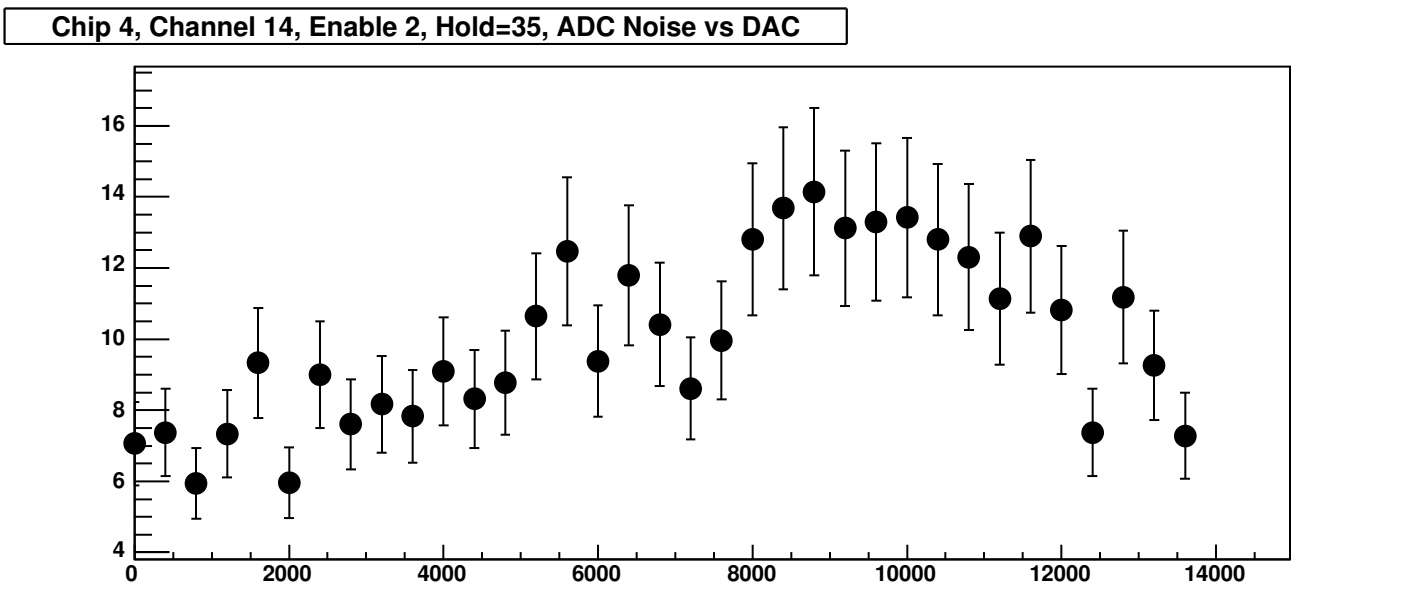

Chip 4, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

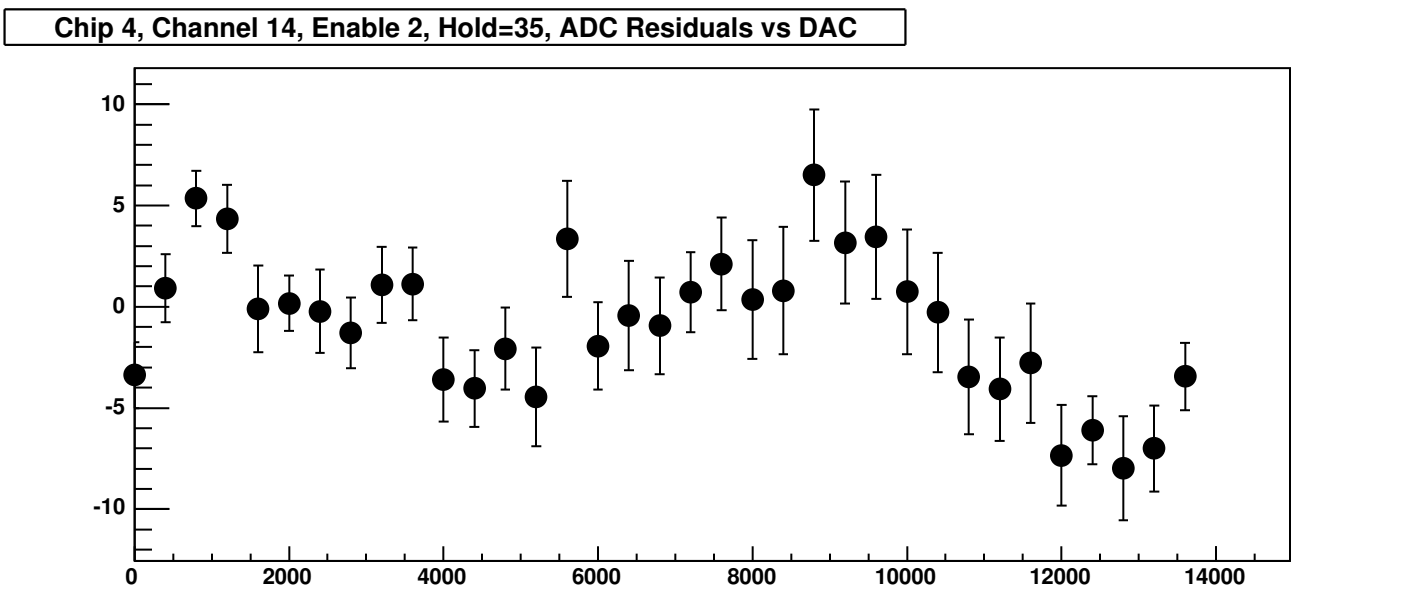

Chip 4, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 148.8 / 23
p0 298 ± 2.949
p1 2.4 ± 0.001944
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

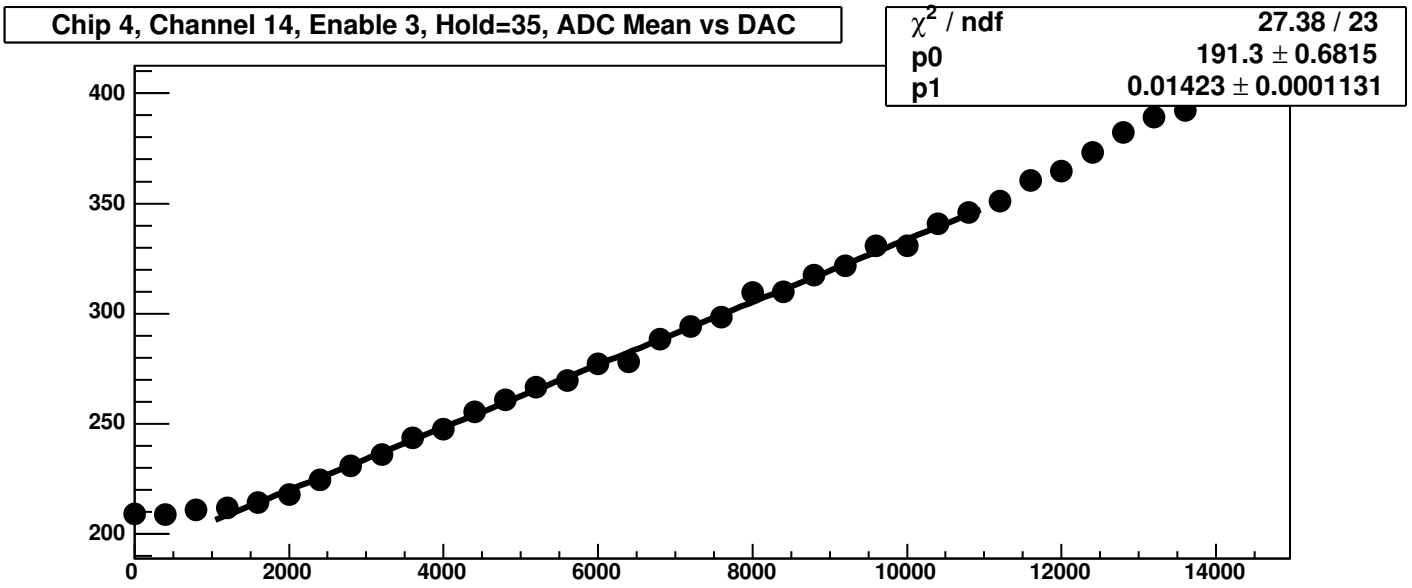

Chip 4, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

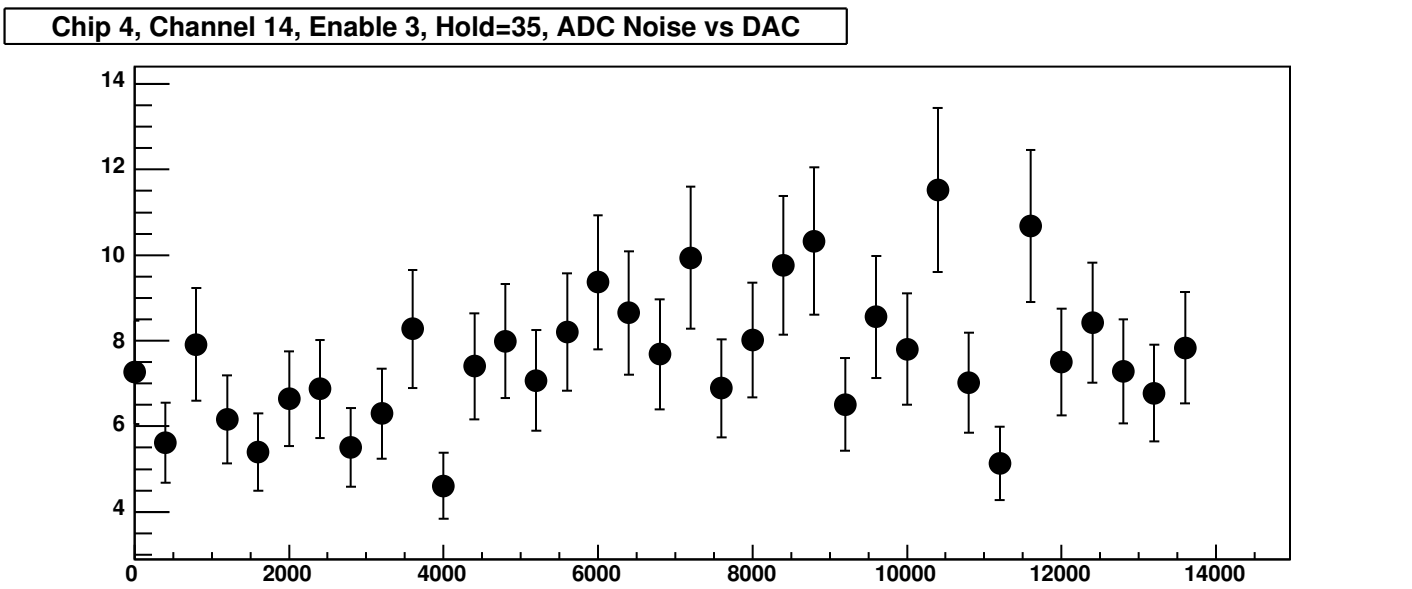

Chip 4, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

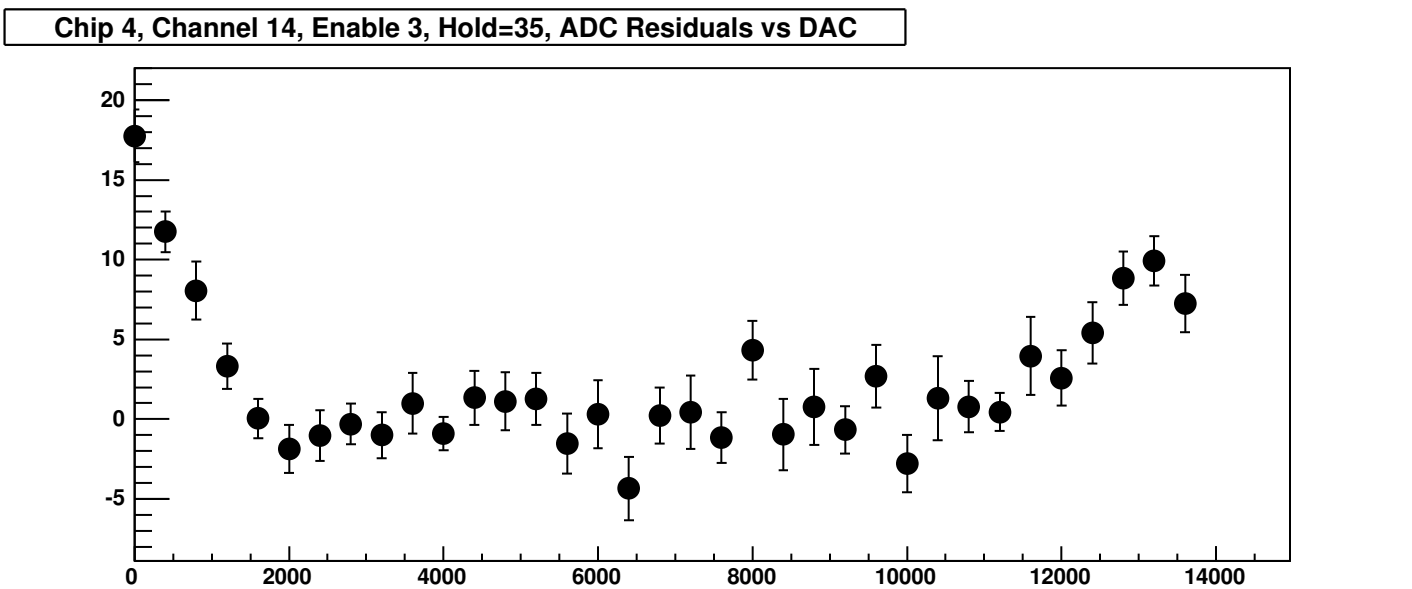

Chip 4, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 31.19 / 23
p0 192.6 ± 0.8958
p1 -0.01809 ± 0.0001599
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

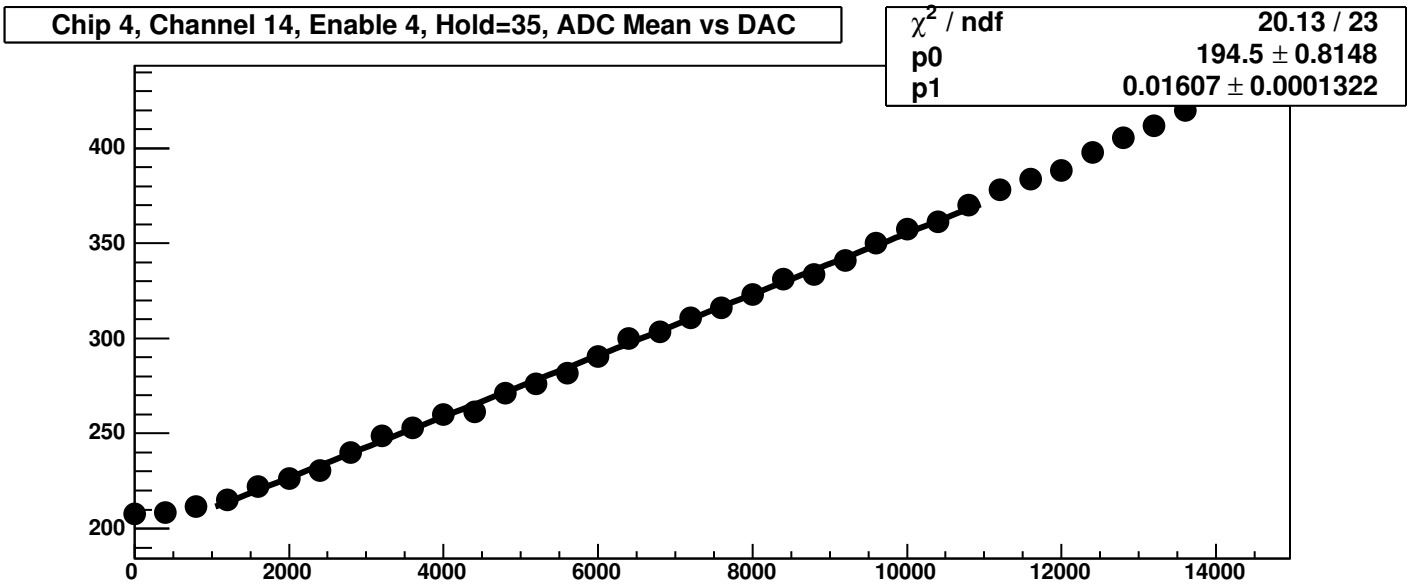

Chip 4, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

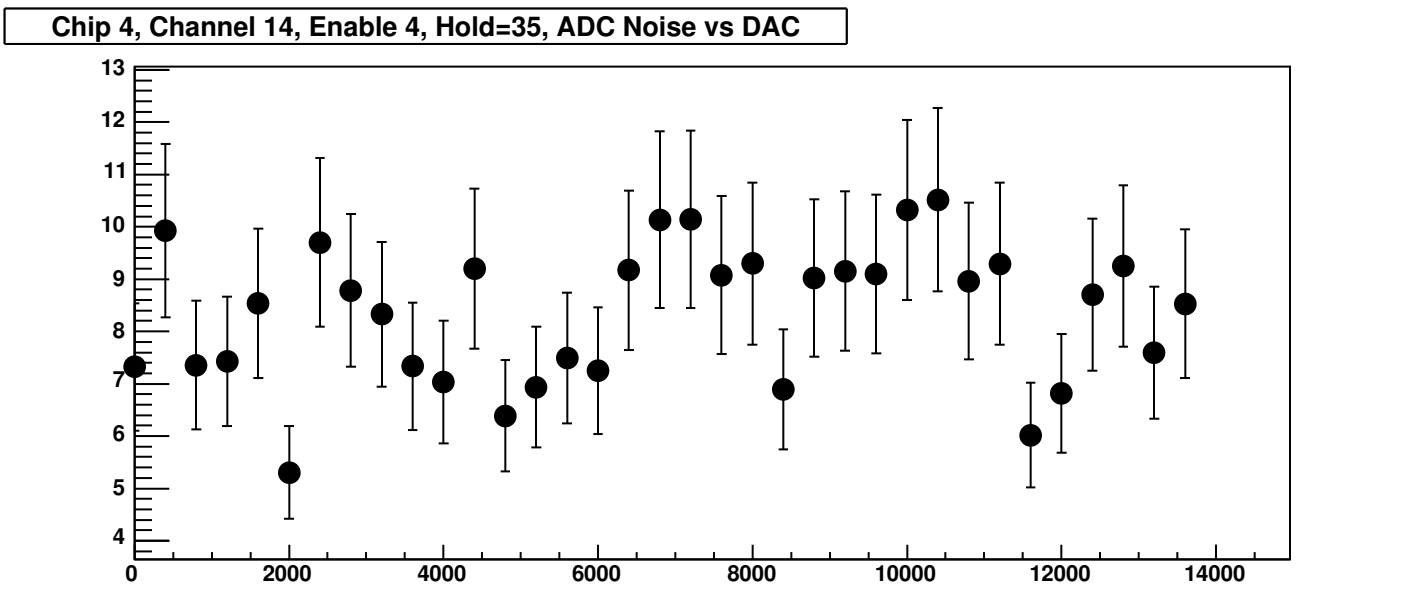

Chip 4, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

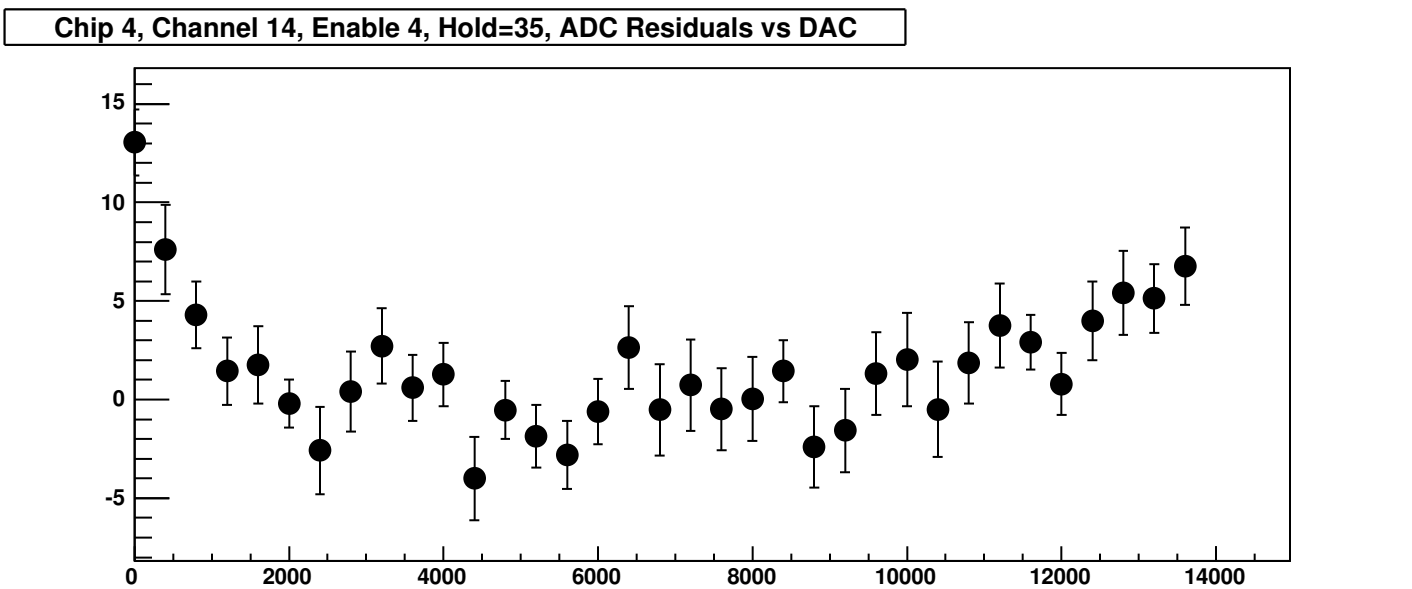

Chip 4, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 27.38 / 23
p0 191.3 ± 0.6815
p1 0.01423 ± 0.0001131
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

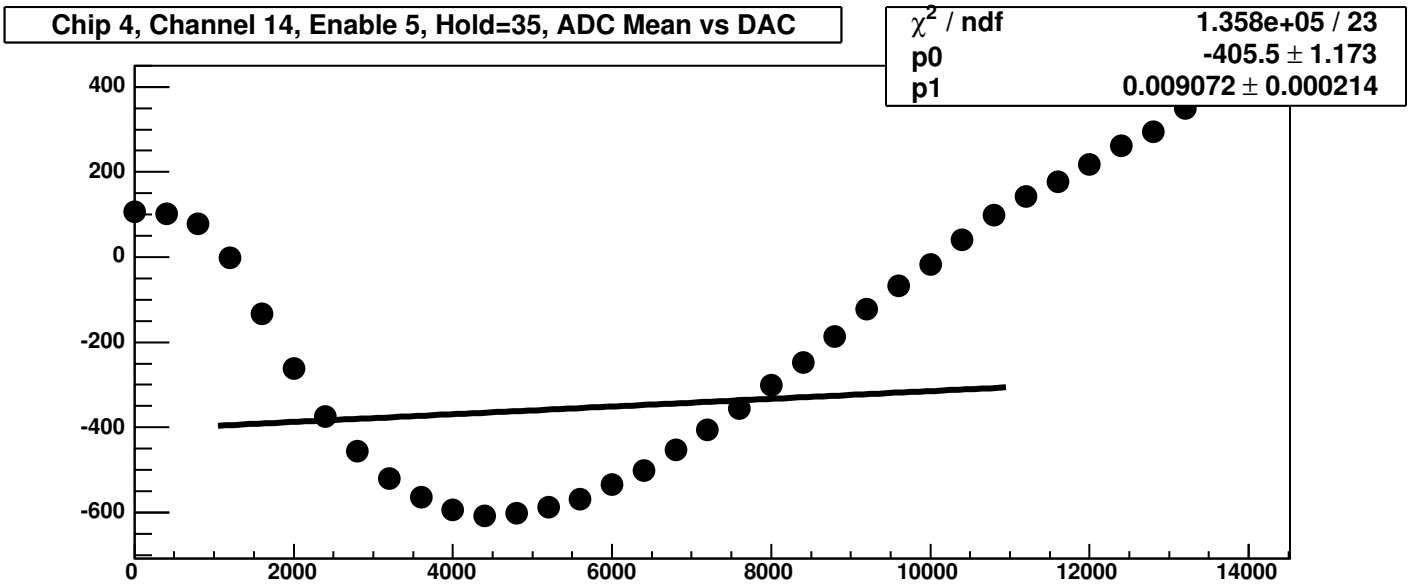

Chip 4, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

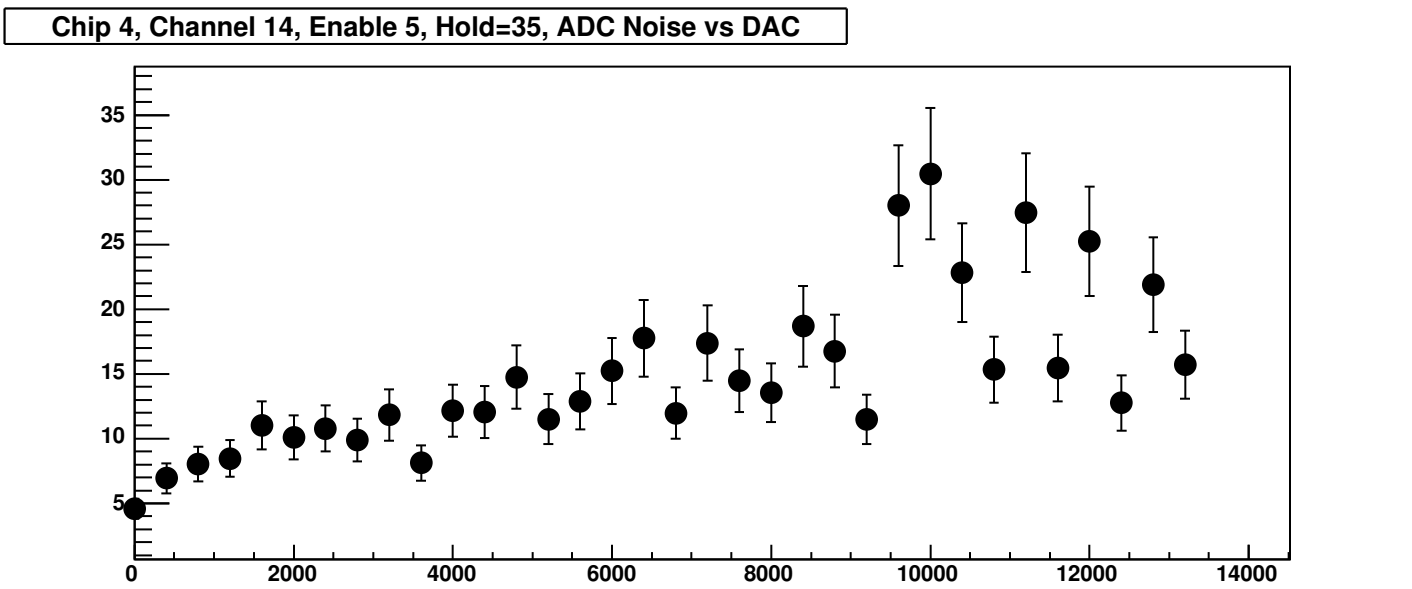

Chip 4, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

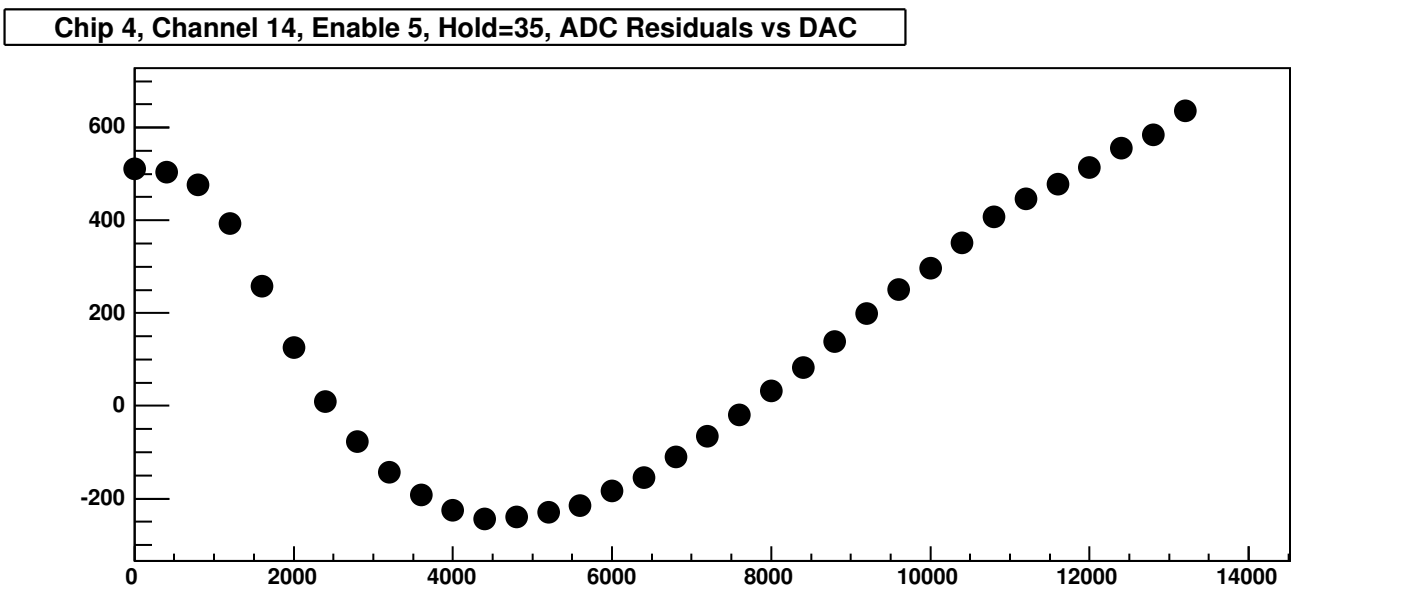

Chip 4, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 20.13 / 23
p0 194.5 ± 0.8148
p1 0.01607 ± 0.0001322
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

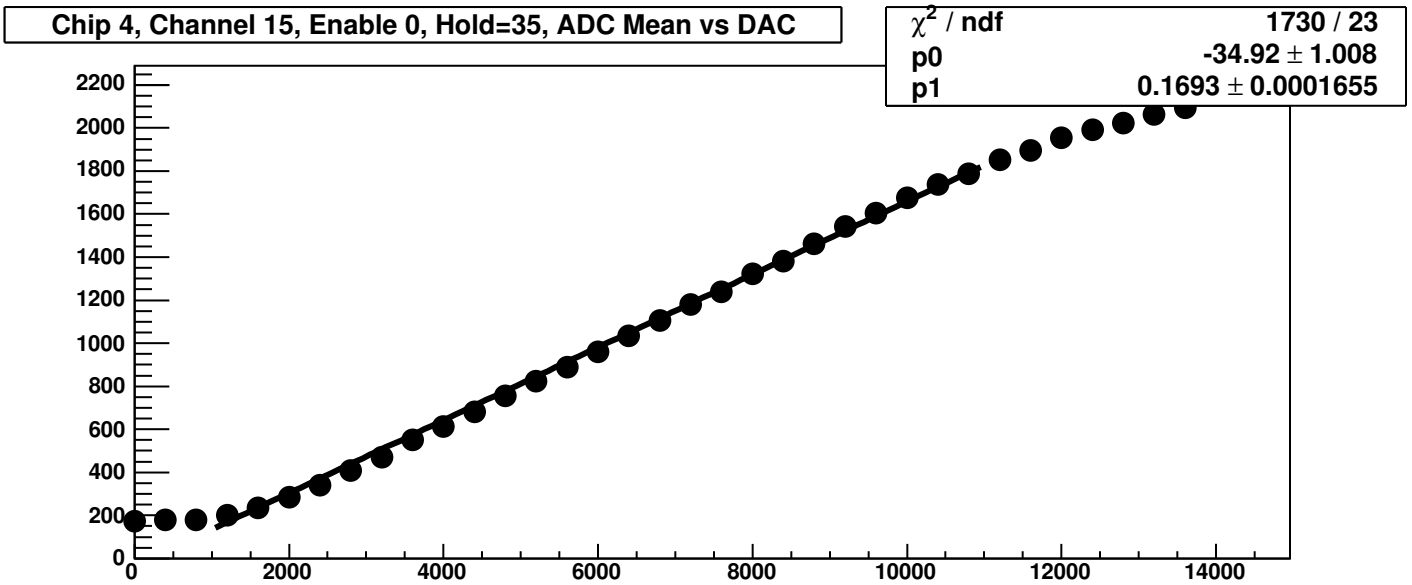

Chip 4, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

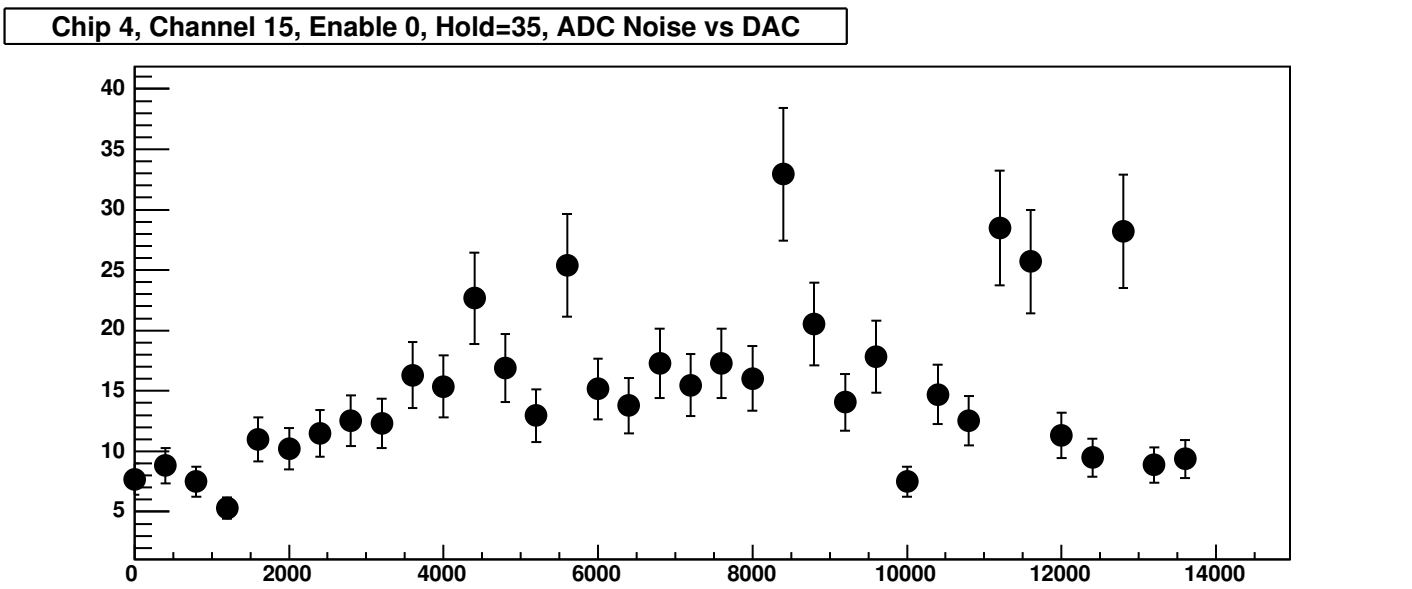

Chip 4, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

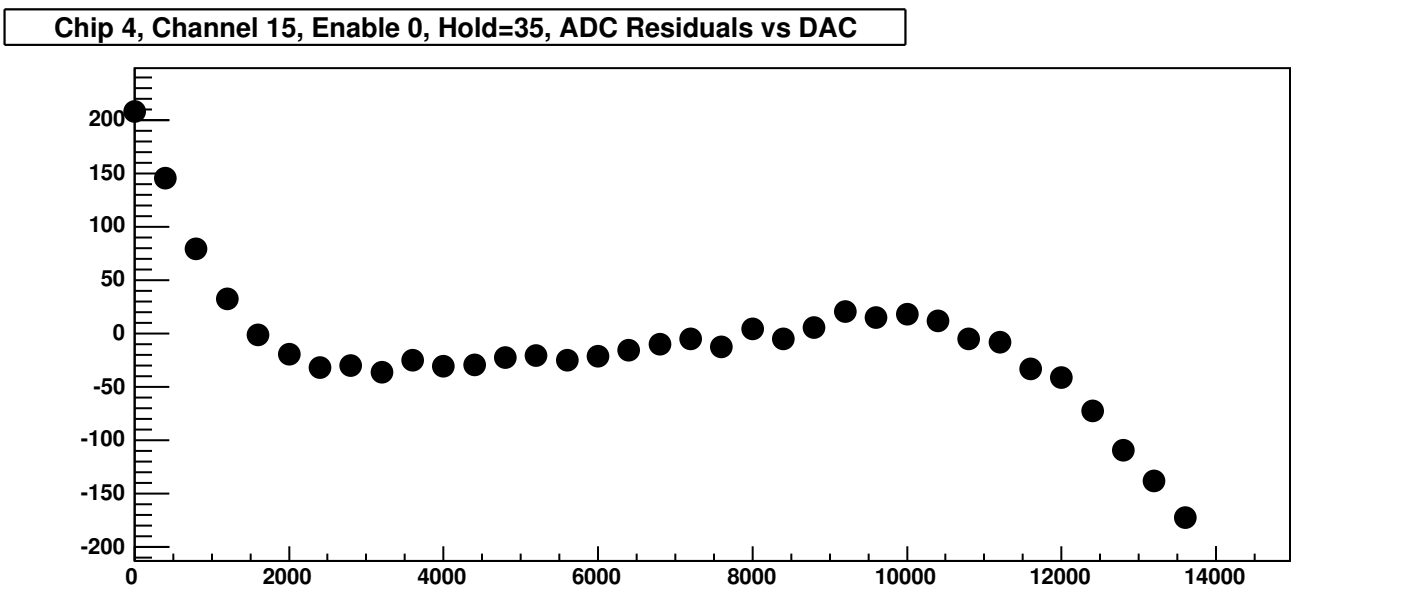

Chip 4, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.358e+05 / 23
p0 -405.5 ± 1.173
p1 0.009072 ± 0.000214
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

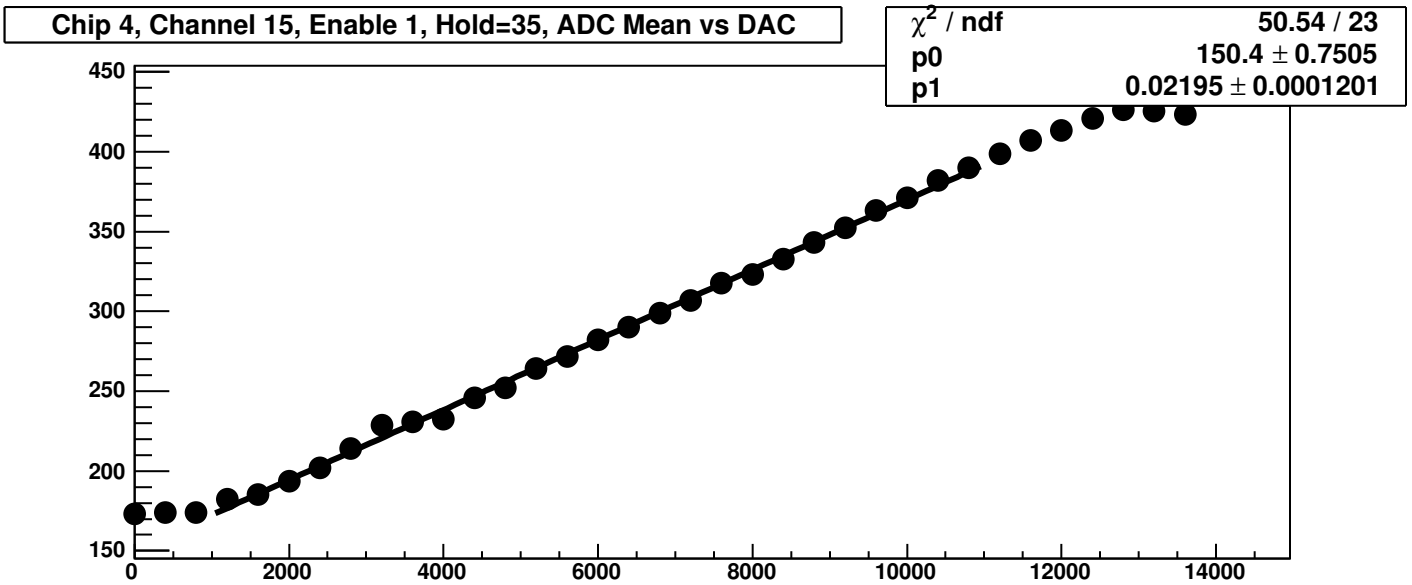

Chip 4, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

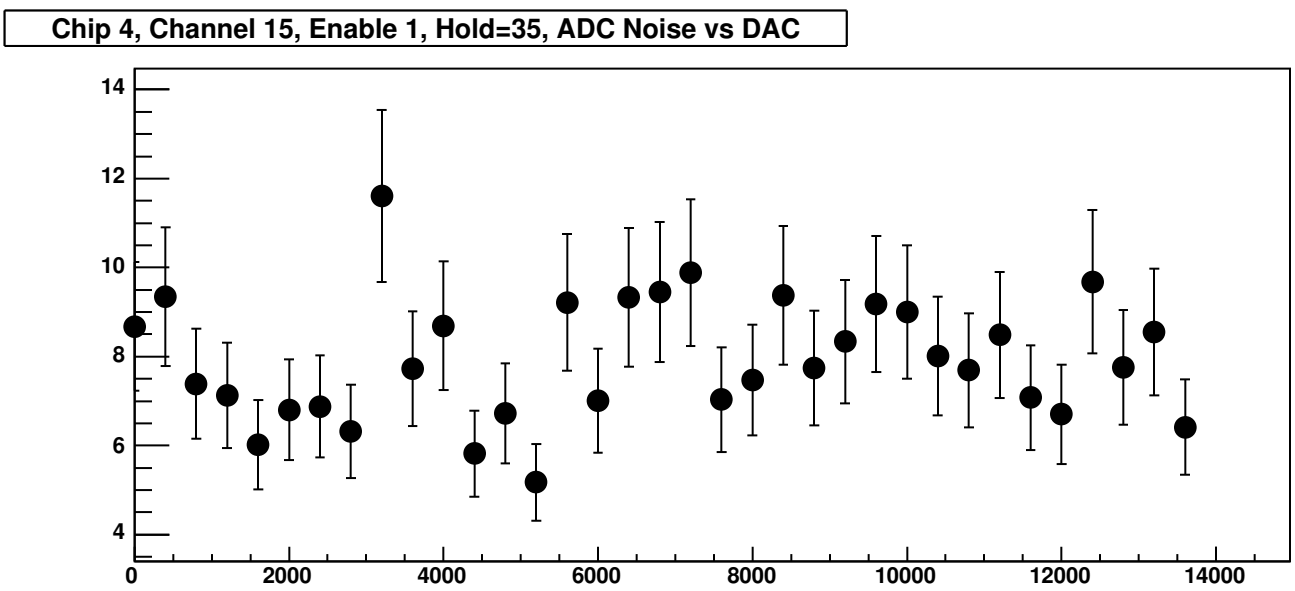

Chip 4, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

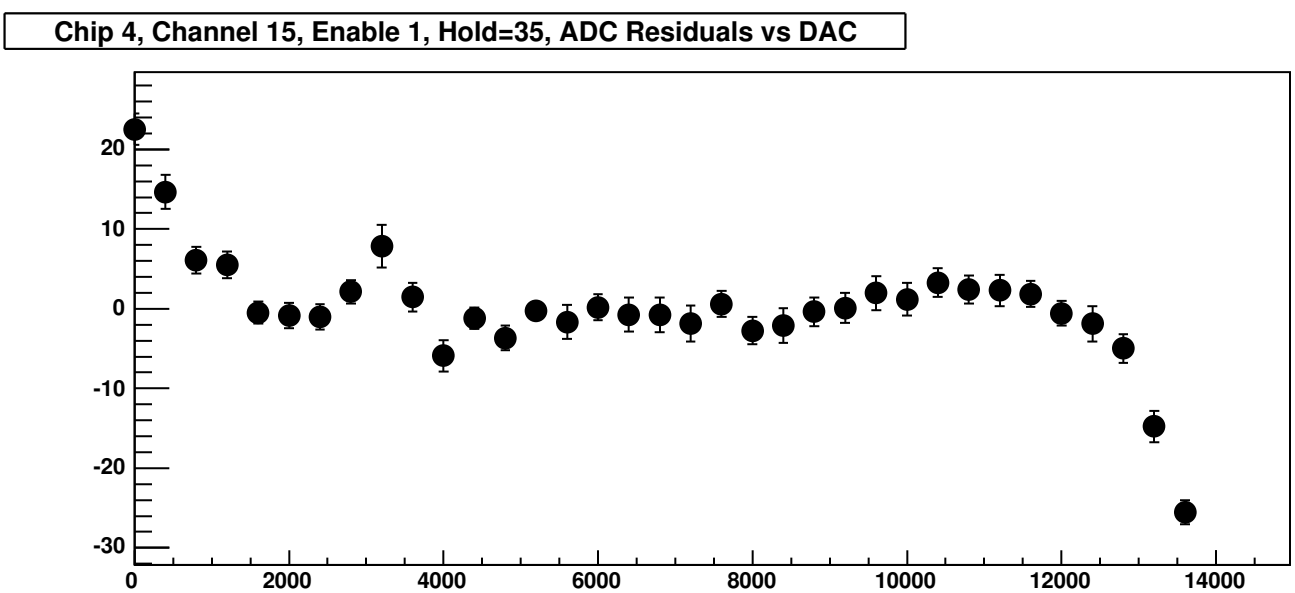

Chip 4, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1730 / 23
p0 -34.92 ± 1.008
p1 0.1693 ± 0.0001655
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

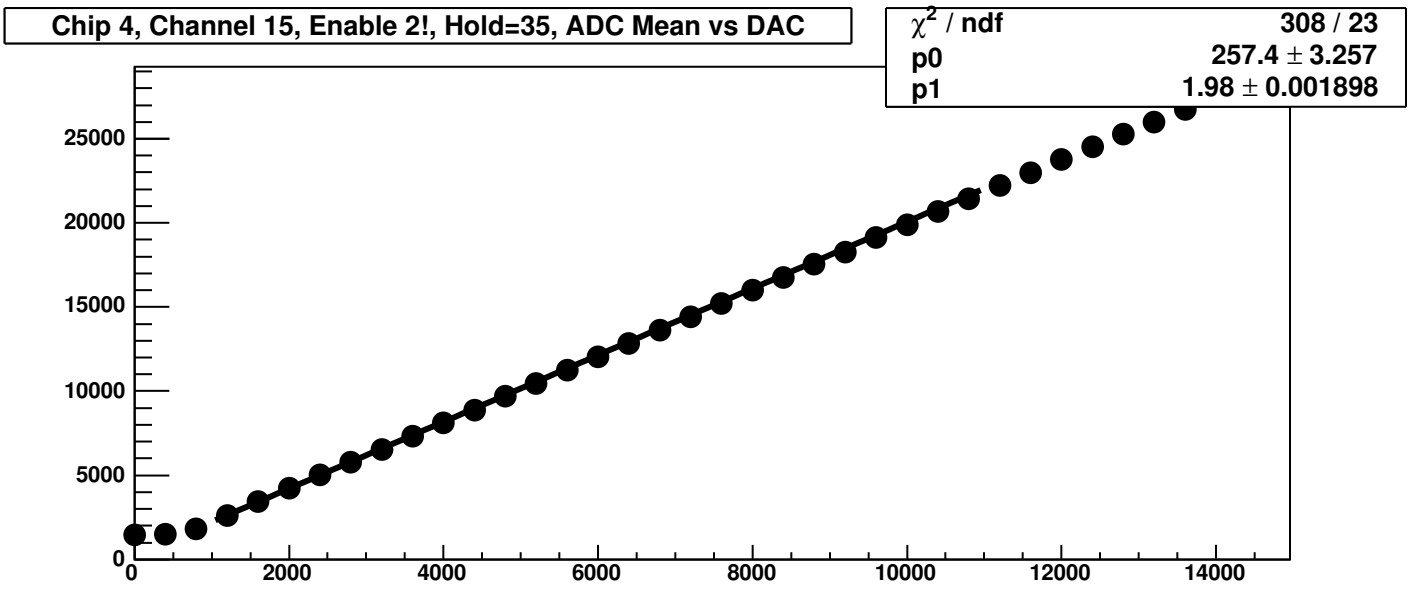

Chip 4, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

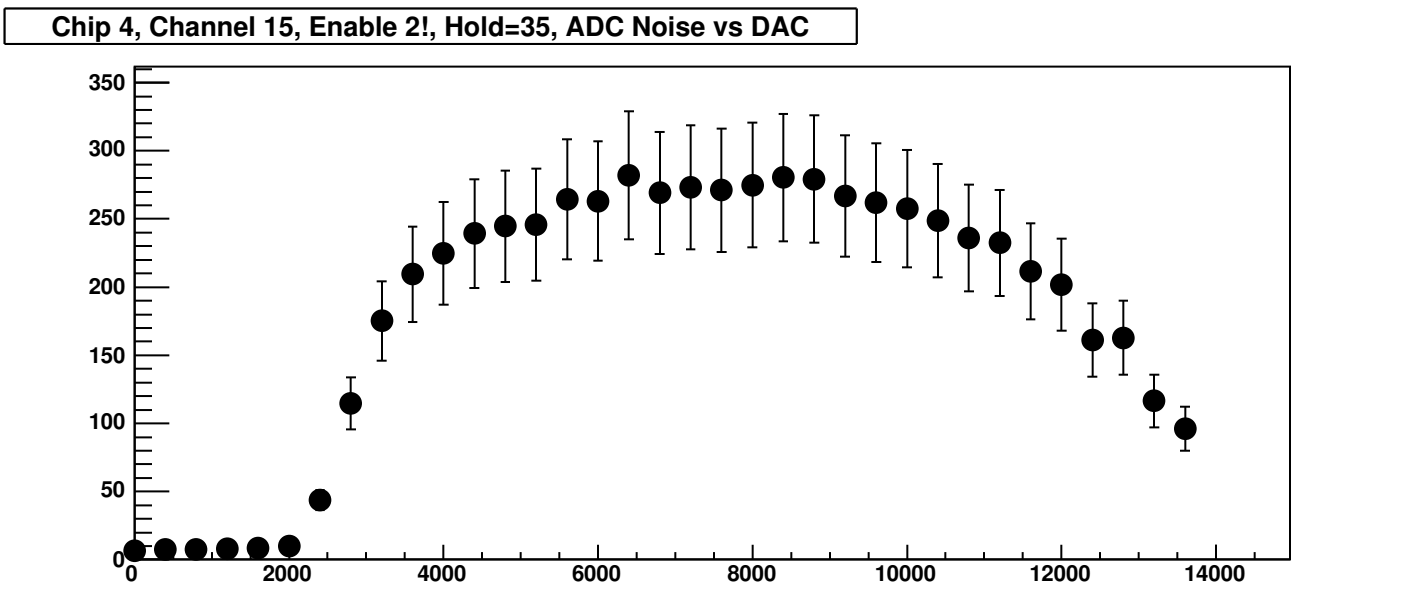

Chip 4, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

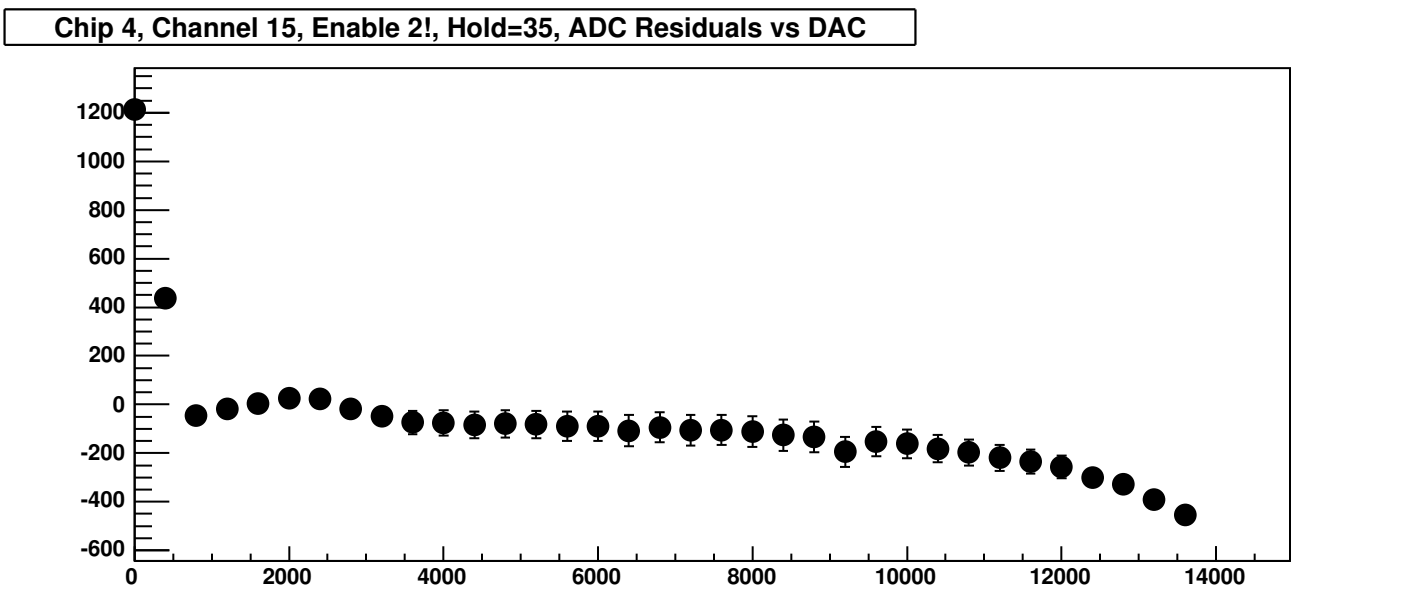

Chip 4, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 50.54 / 23
p0 150.4 ± 0.7505
p1 0.02195 ± 0.0001201
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

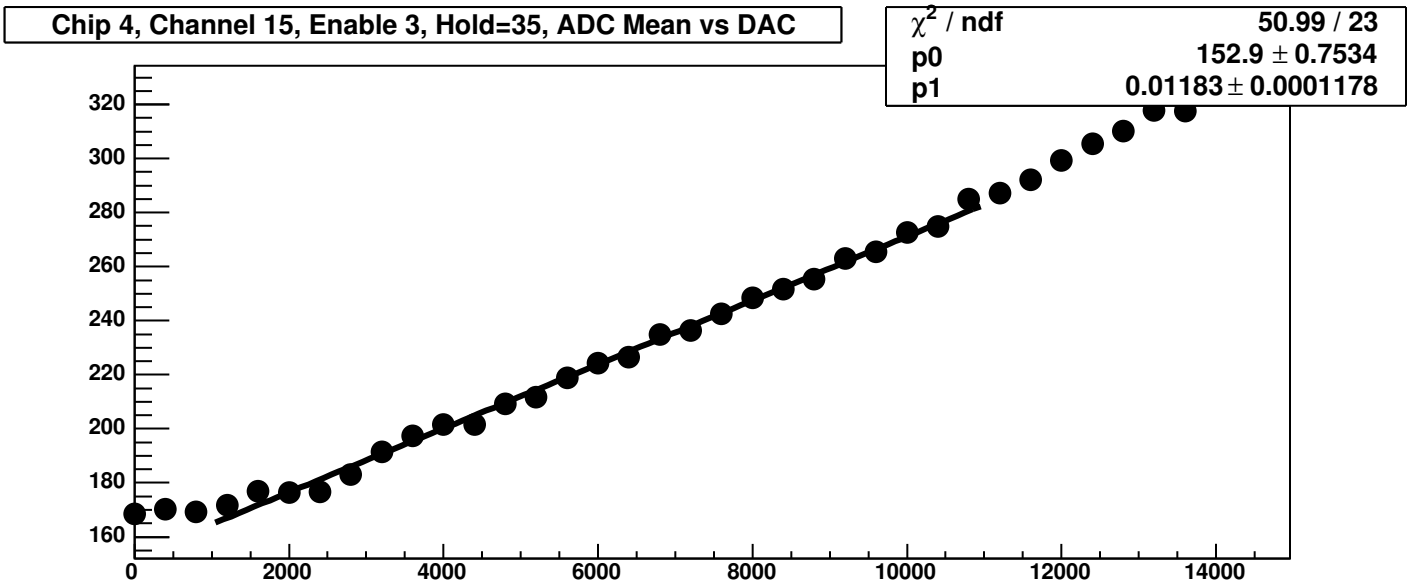

Chip 4, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

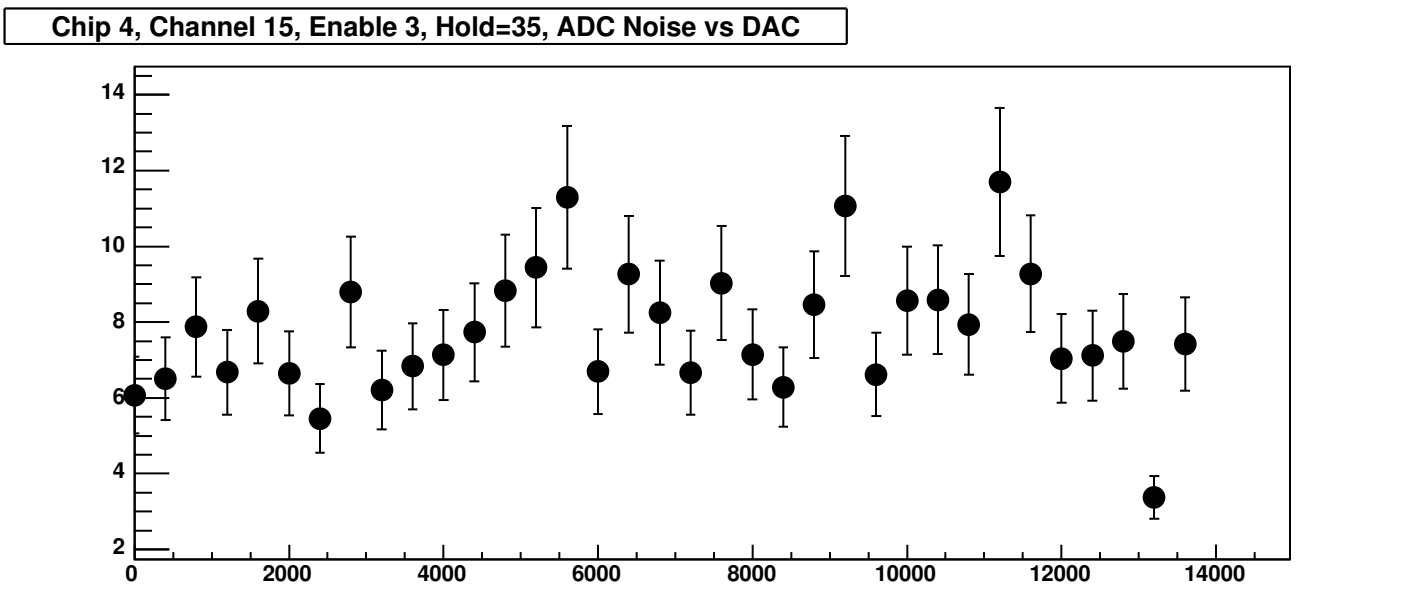

Chip 4, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

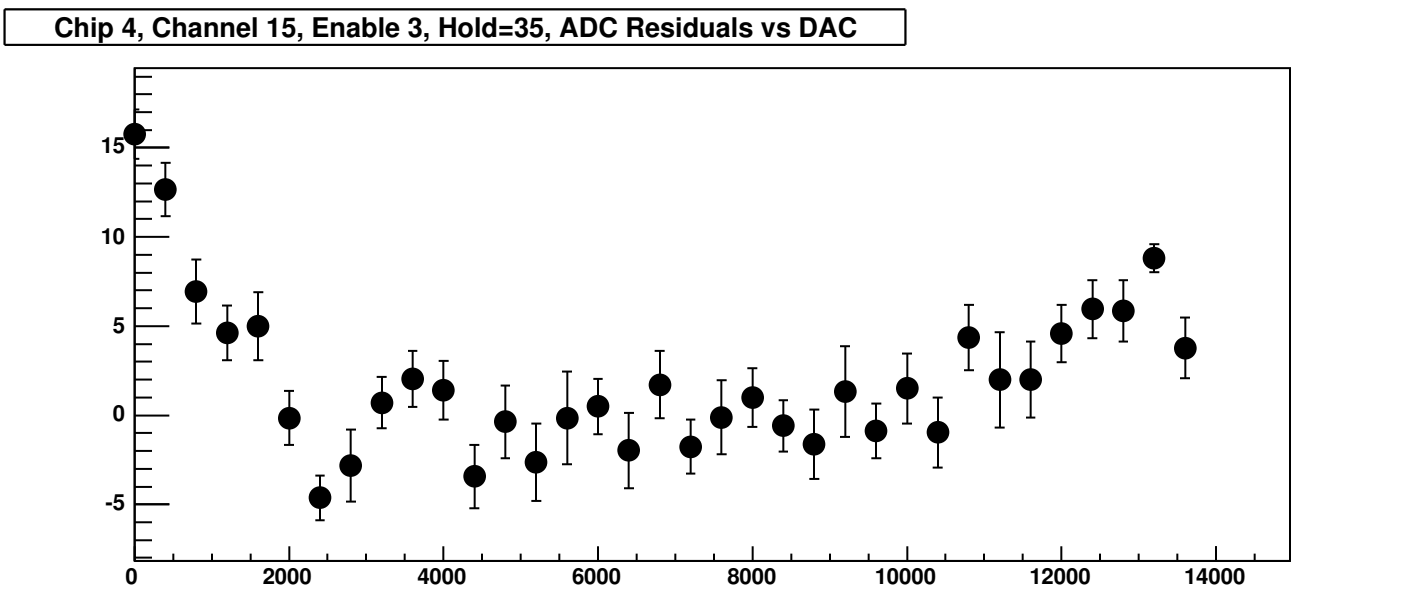

Chip 4, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 308 / 23
p0 257.4 ± 3.257
p1 1.98 ± 0.001898
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

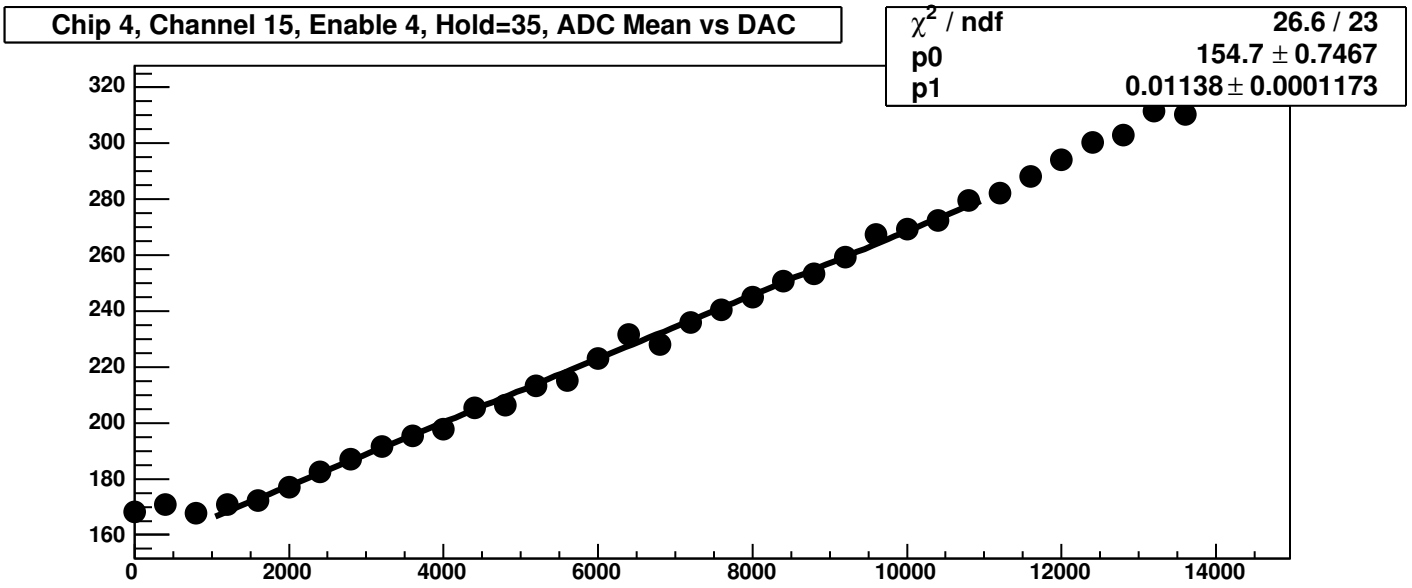

Chip 4, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

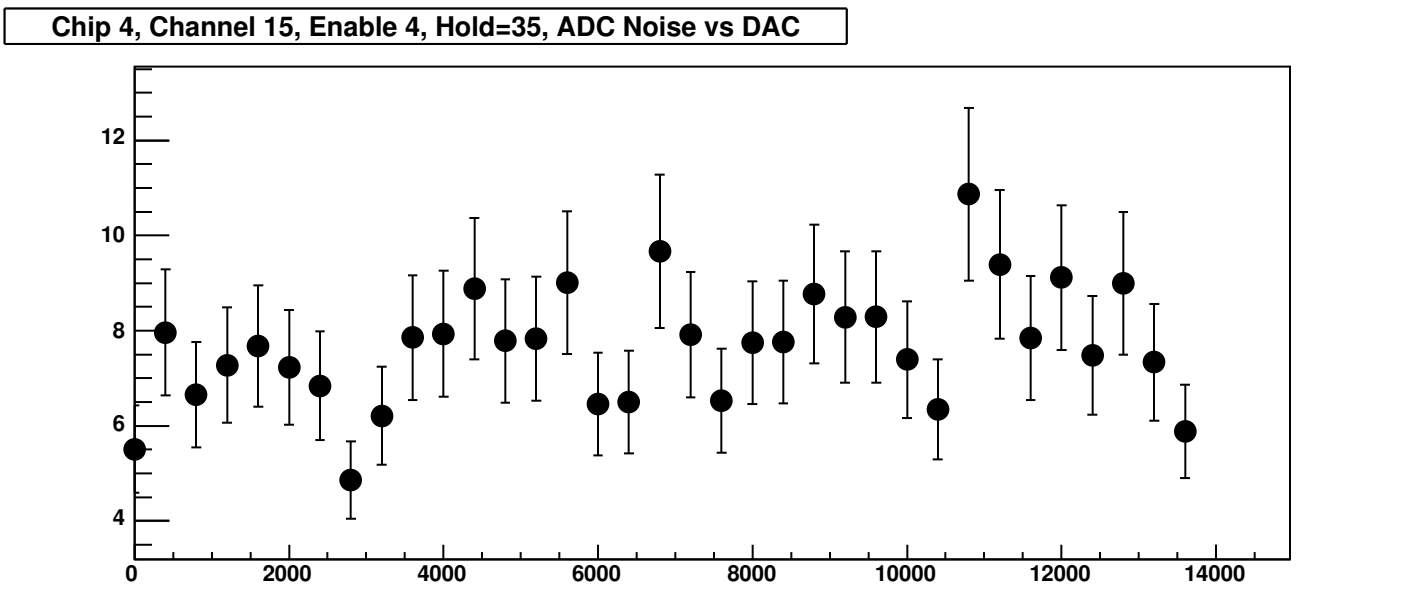

Chip 4, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

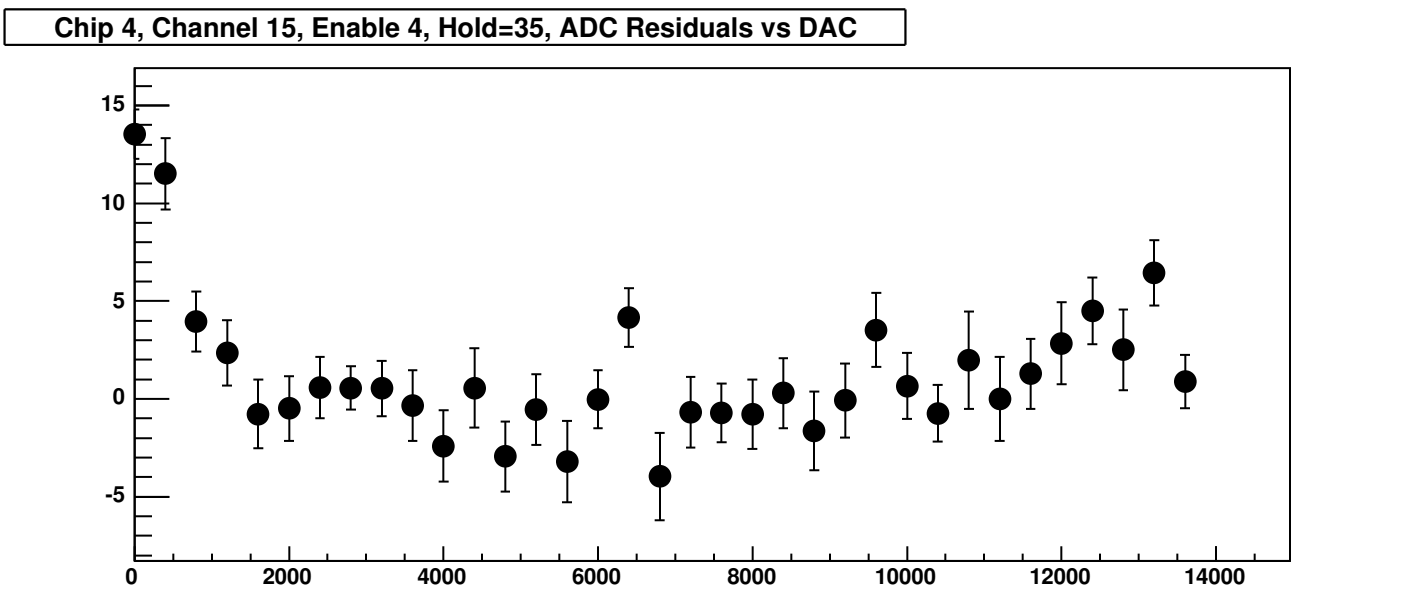

Chip 4, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 50.99 / 23
p0 152.9 ± 0.7534
p1 0.01183 ± 0.0001178
320
300
280
260
240
220
200
180
160
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

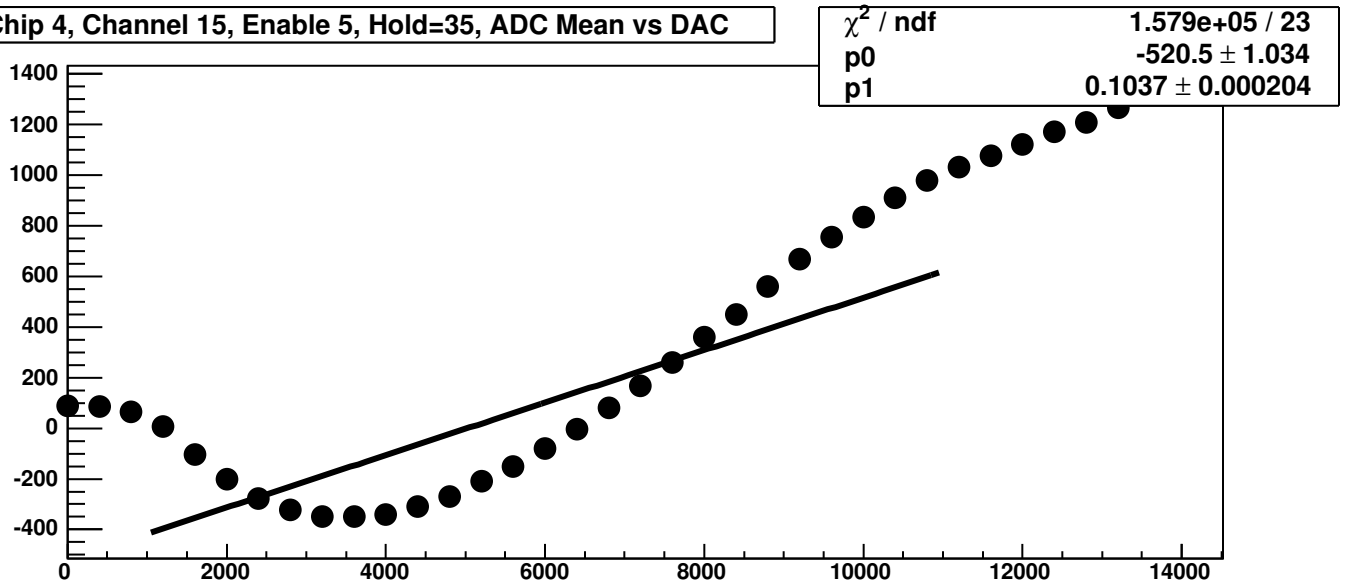

Chip 4, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.6 / 23
p0 154.7 ± 0.7467
p1 0.01138 ± 0.0001173
320
300
280
260
240
220
200
180
160
0
2000
4000
6000
8000
10000
12000
14000

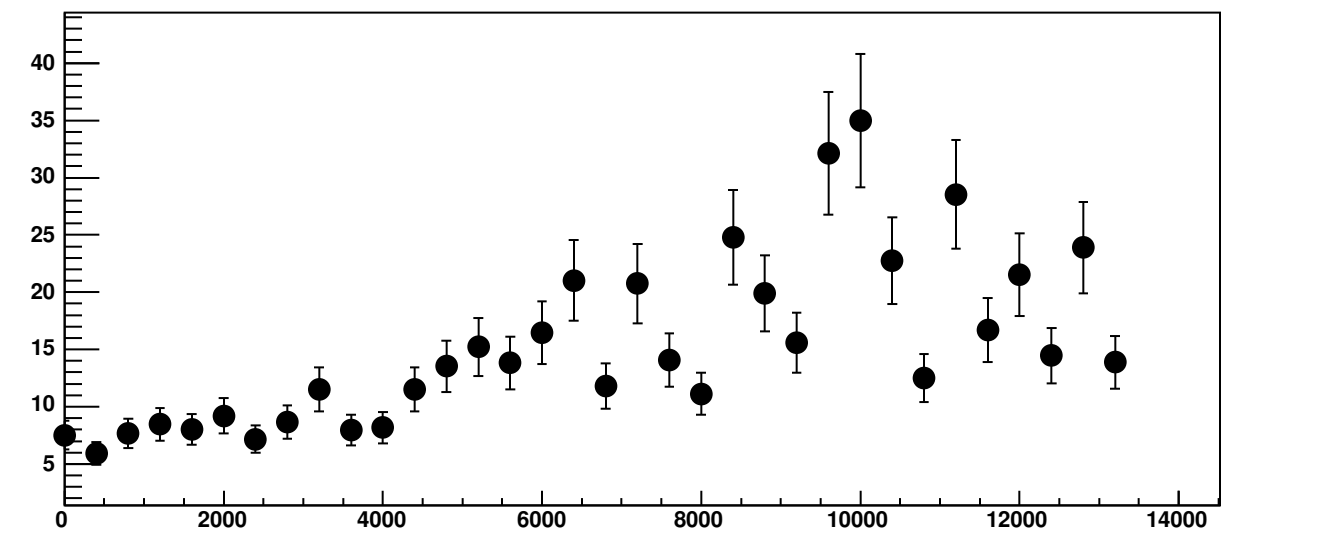

Chip 4, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

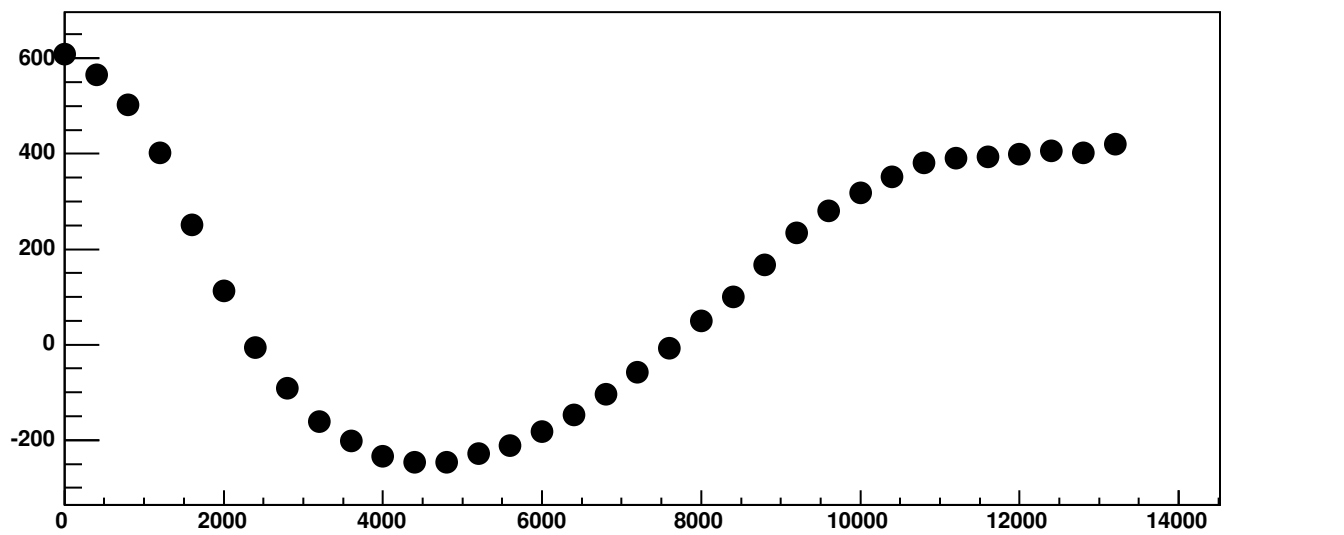
Chip 4, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC

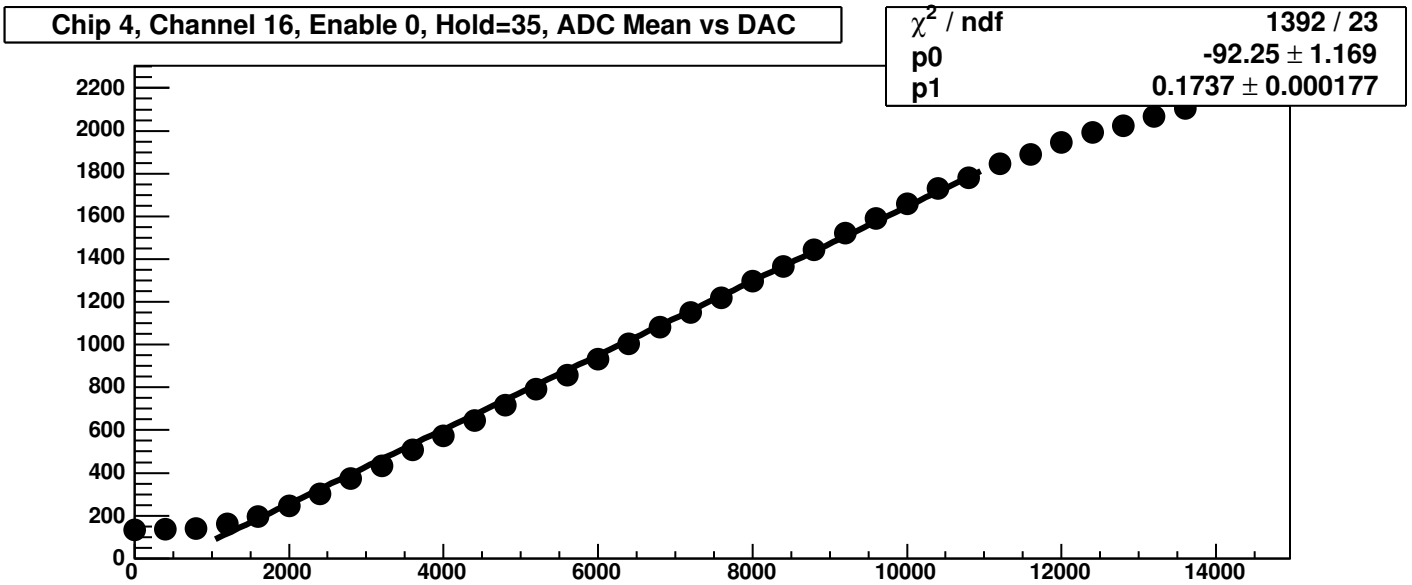


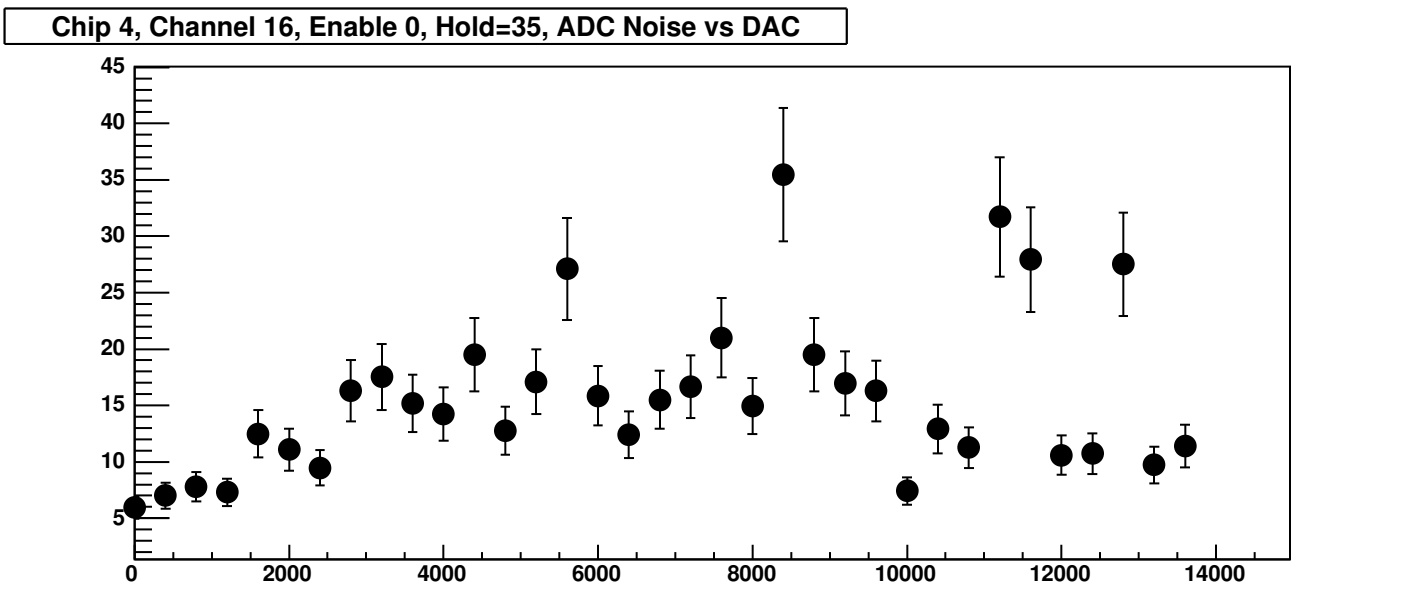
Chip 4, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC

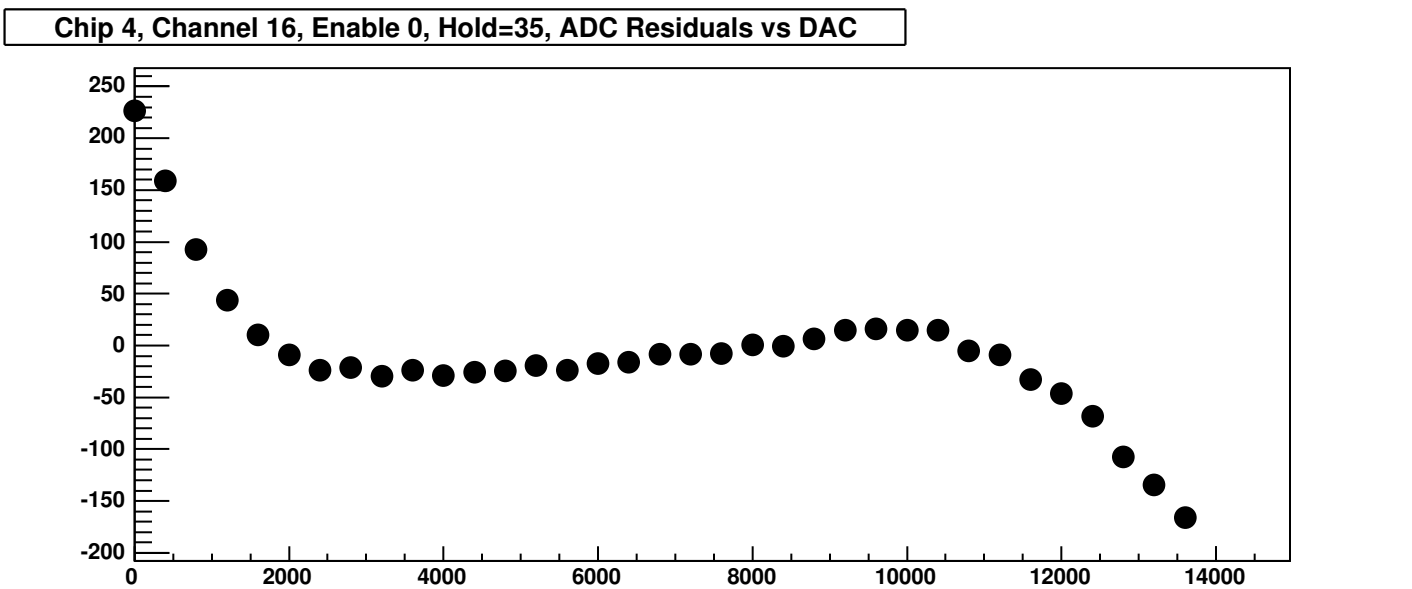


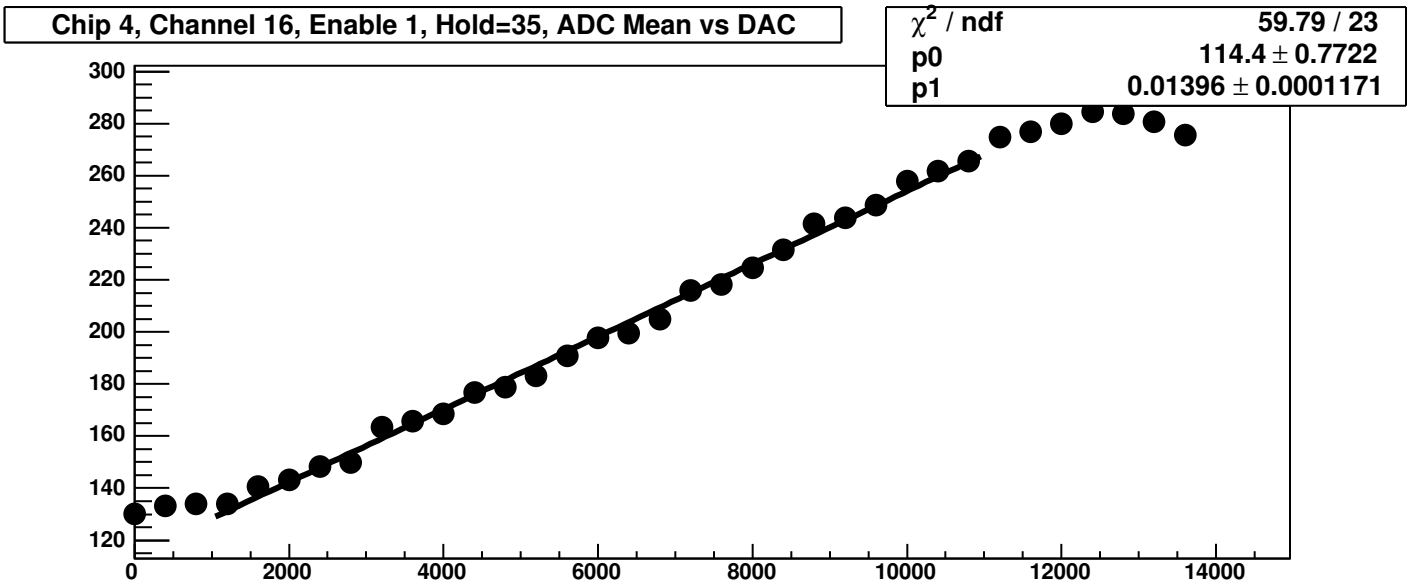
Chip 4, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC

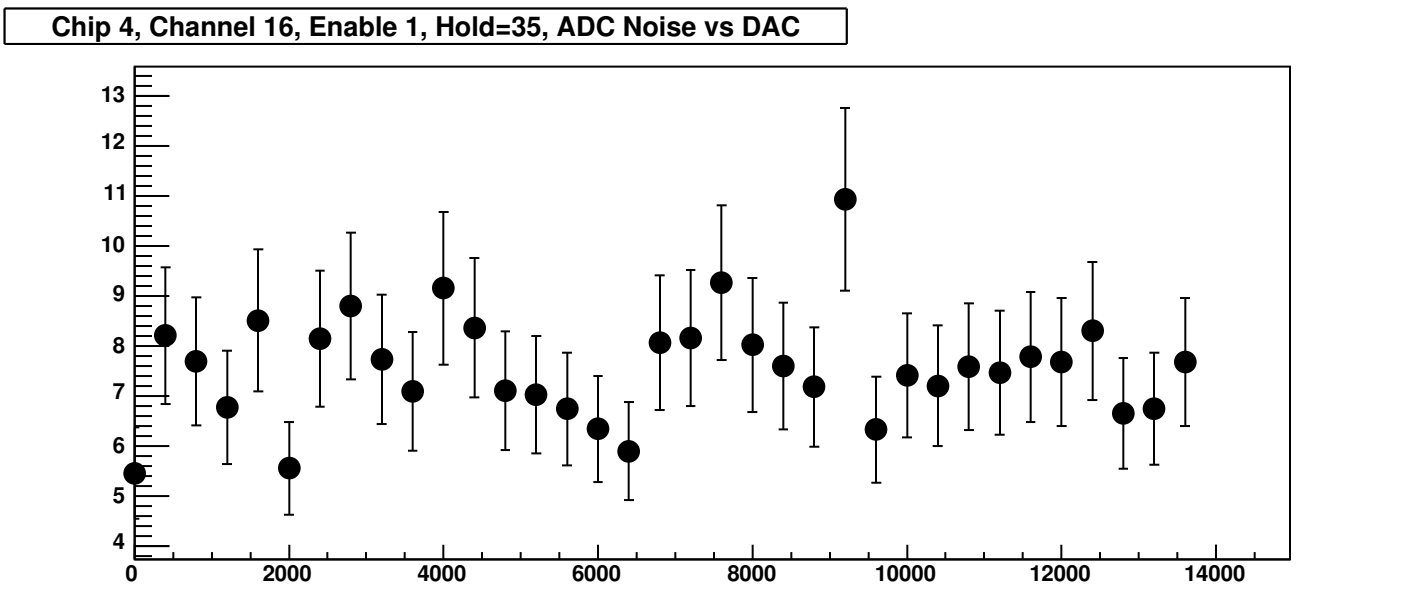


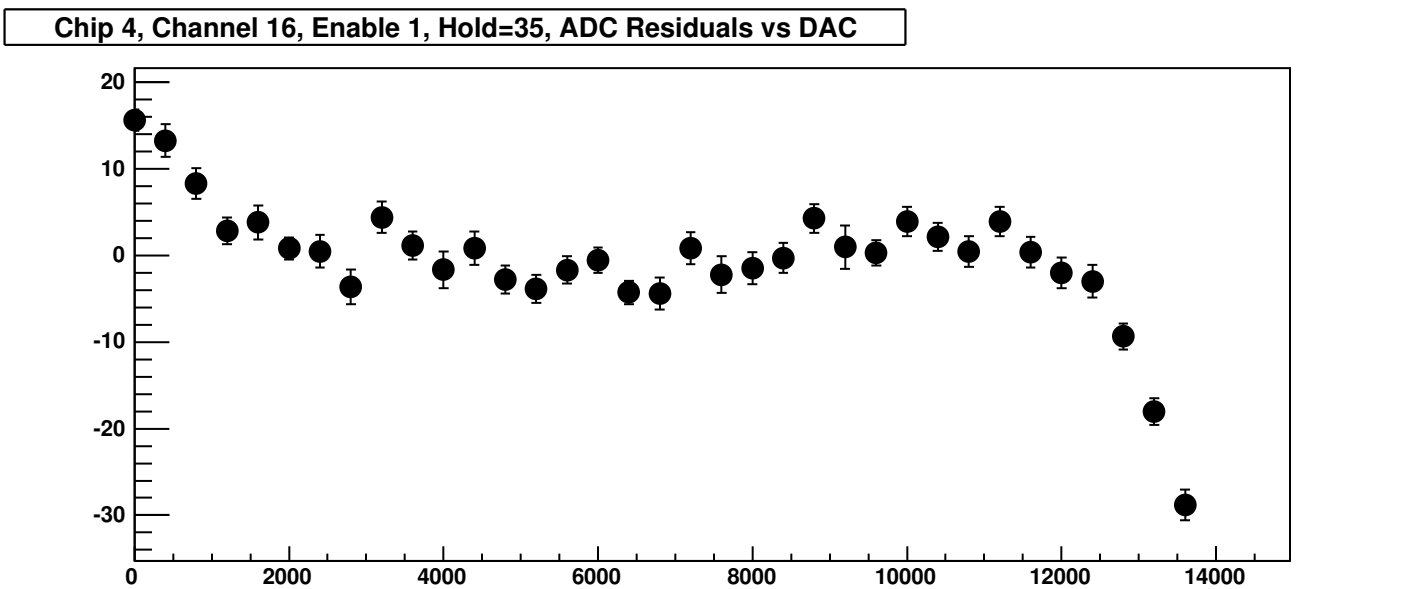

Chip 4, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1392 / 23
p0 -92.25 ± 1.169
p1 0.1737 ± 0.000177
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

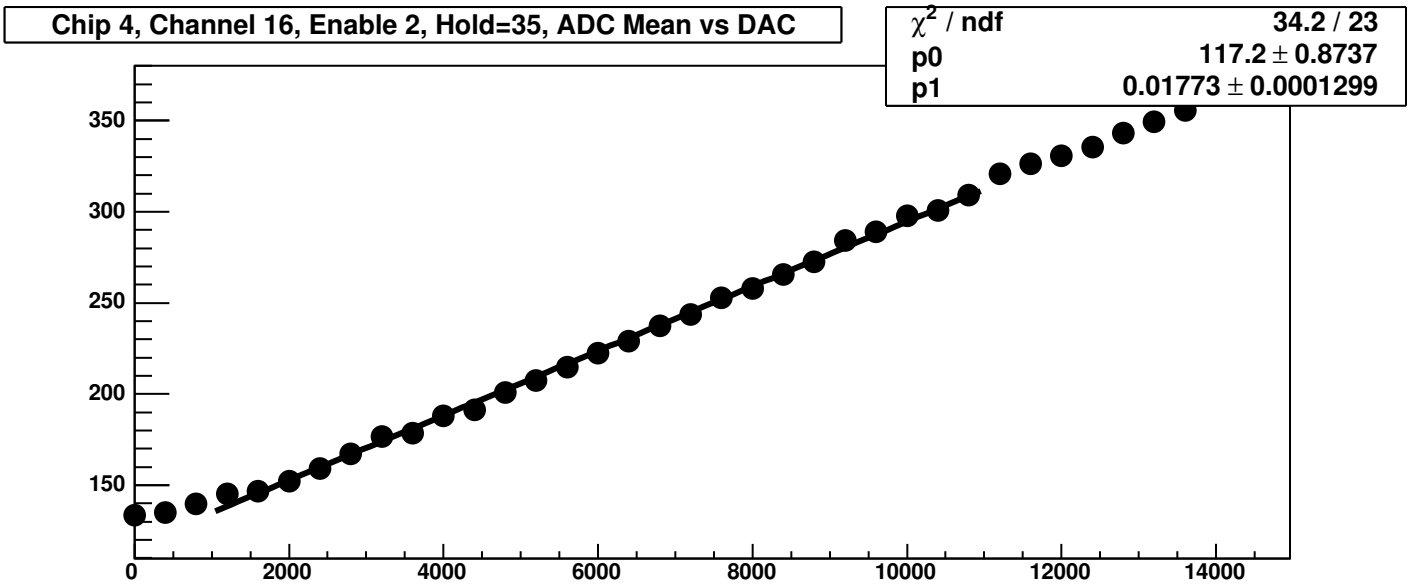

Chip 4, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

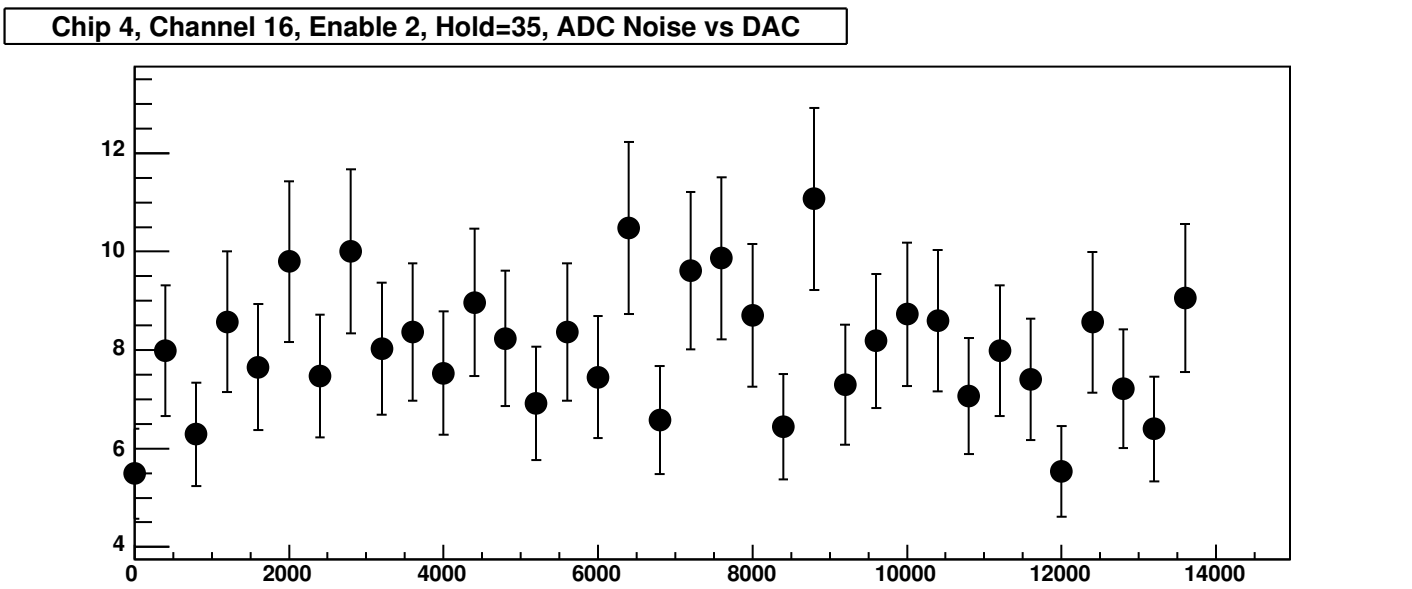

Chip 4, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

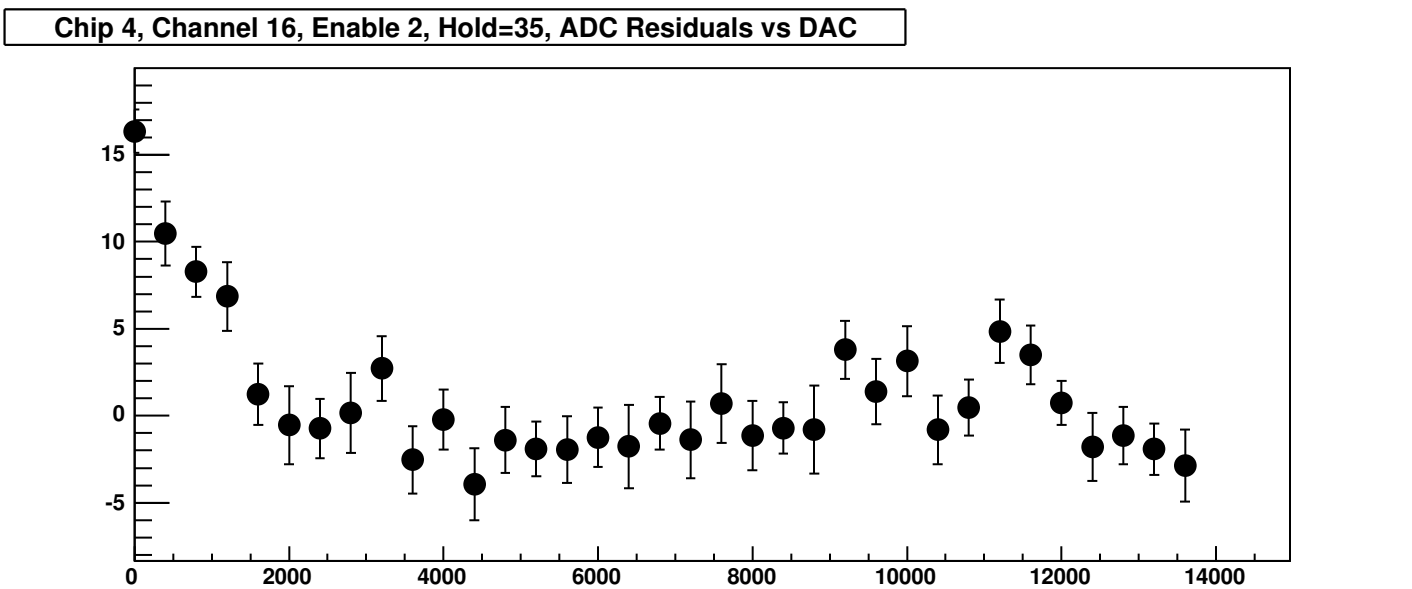

Chip 4, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 59.79 / 23
p0 114.4 ± 0.7722
p1 0.01396 ± 0.0001171
300
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

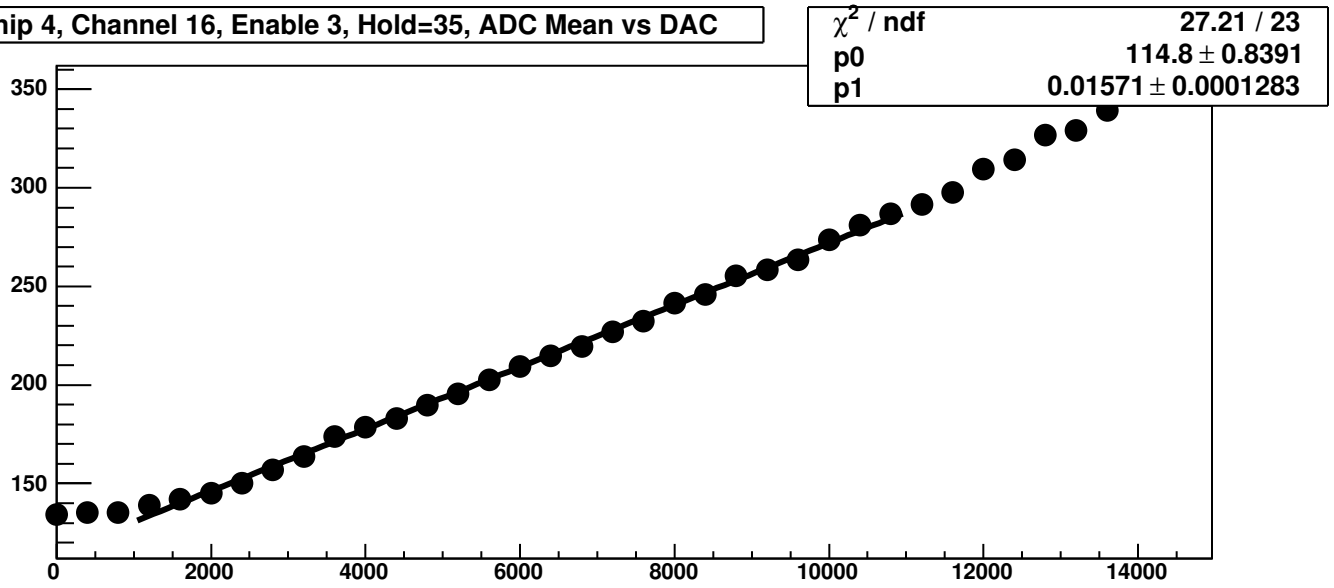

Chip 4, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 34.2 / 23
p0 117.2 ± 0.8737
p1 0.01773 ± 0.0001299
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

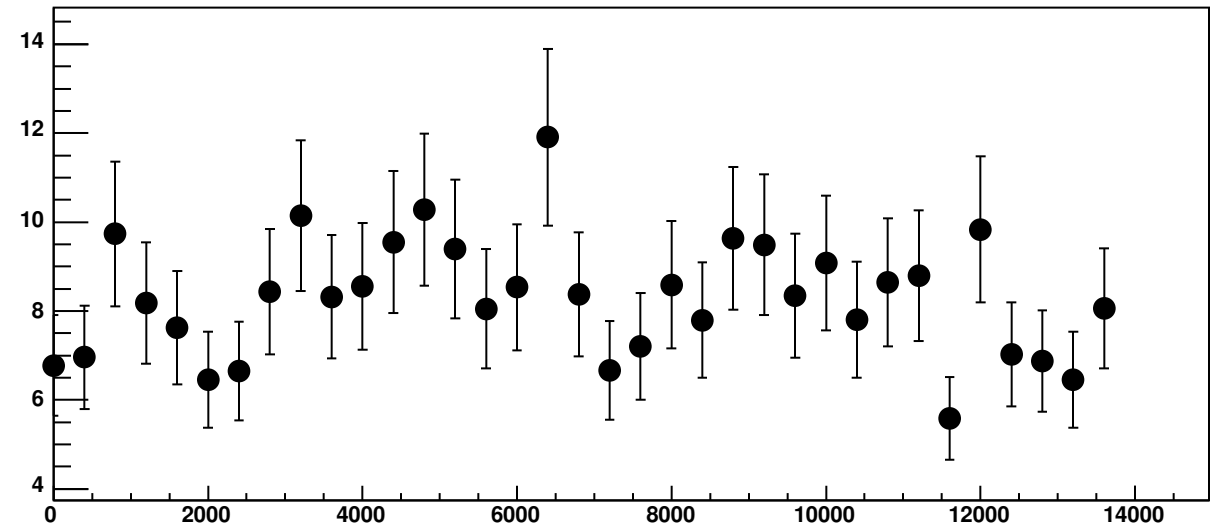

Chip 4, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

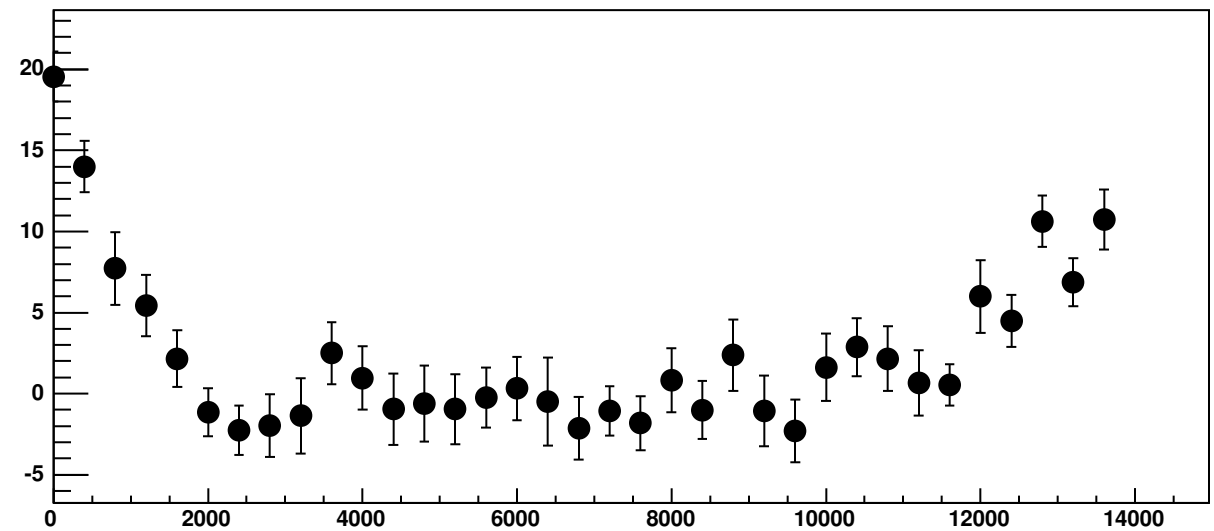
Chip 4, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

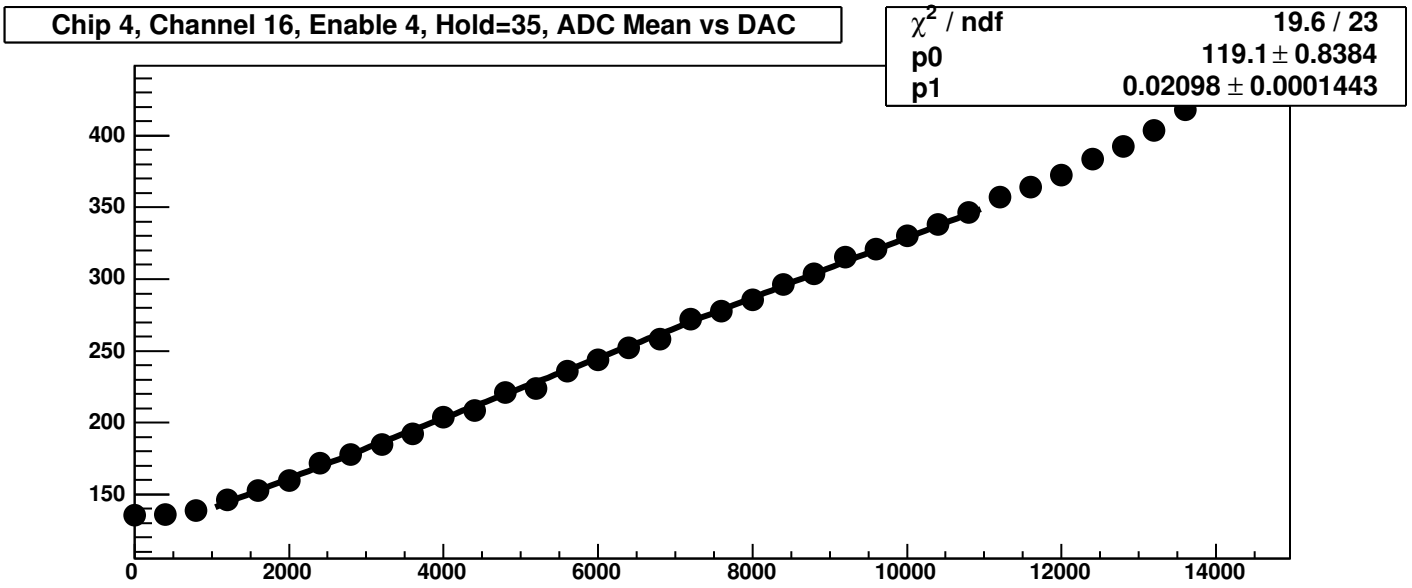


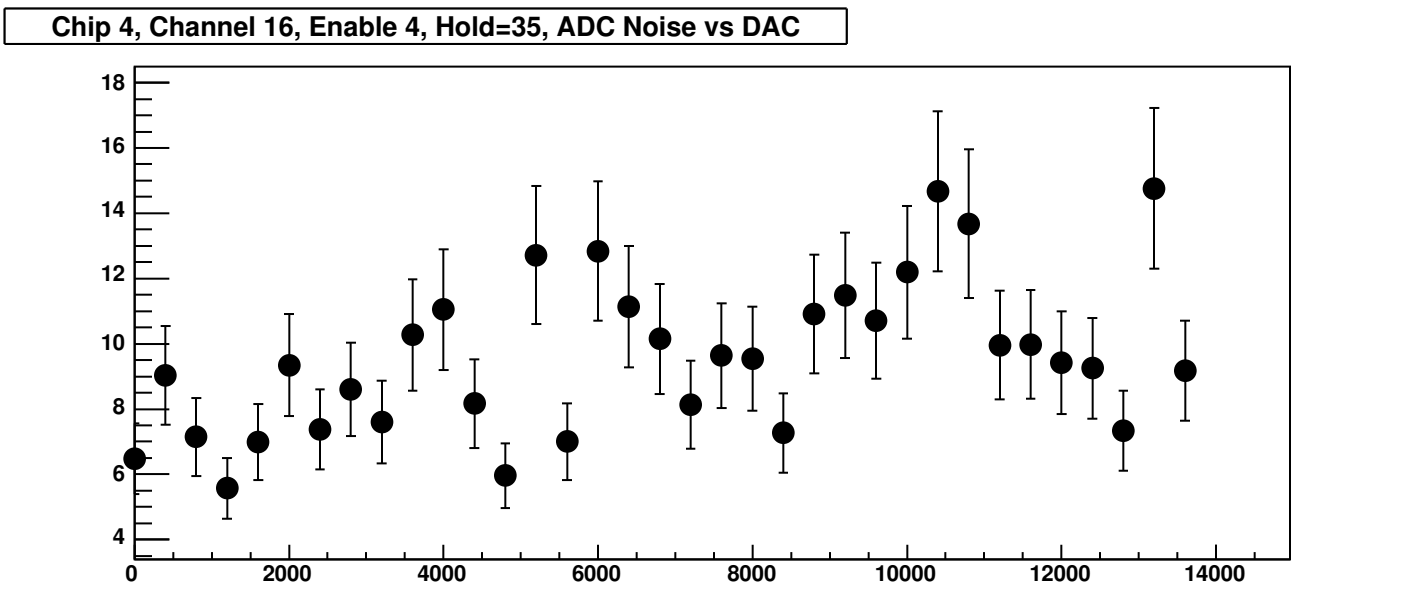
Chip 4, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC

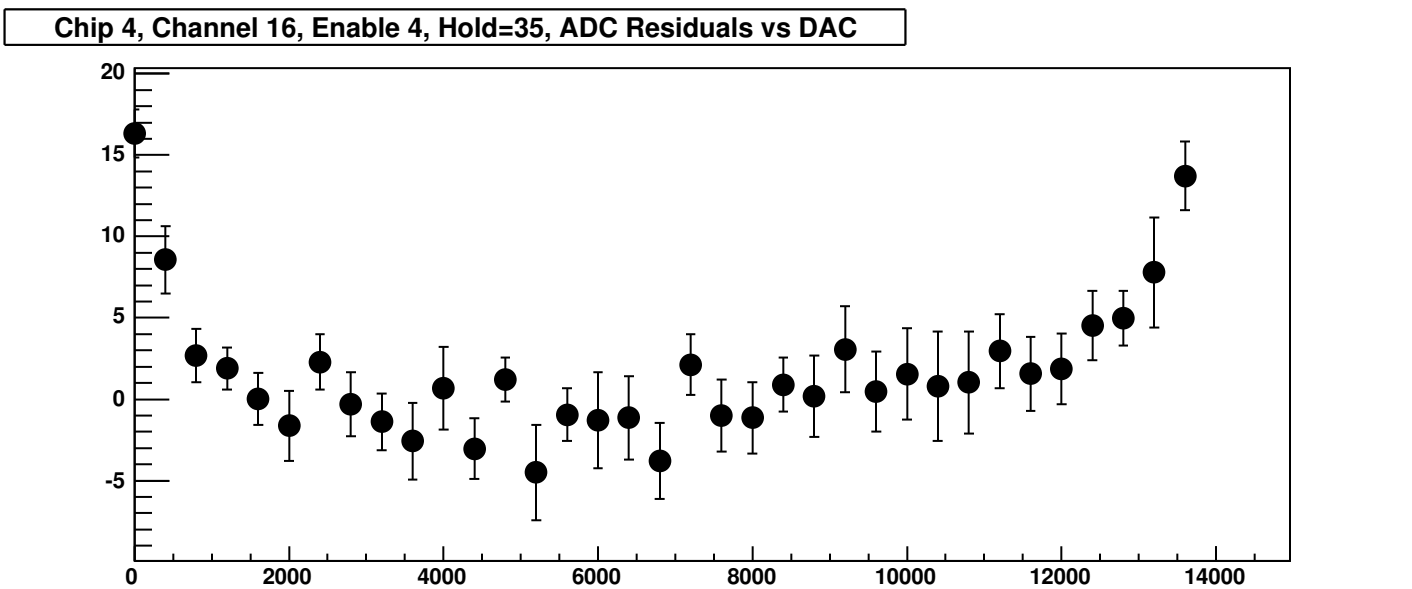


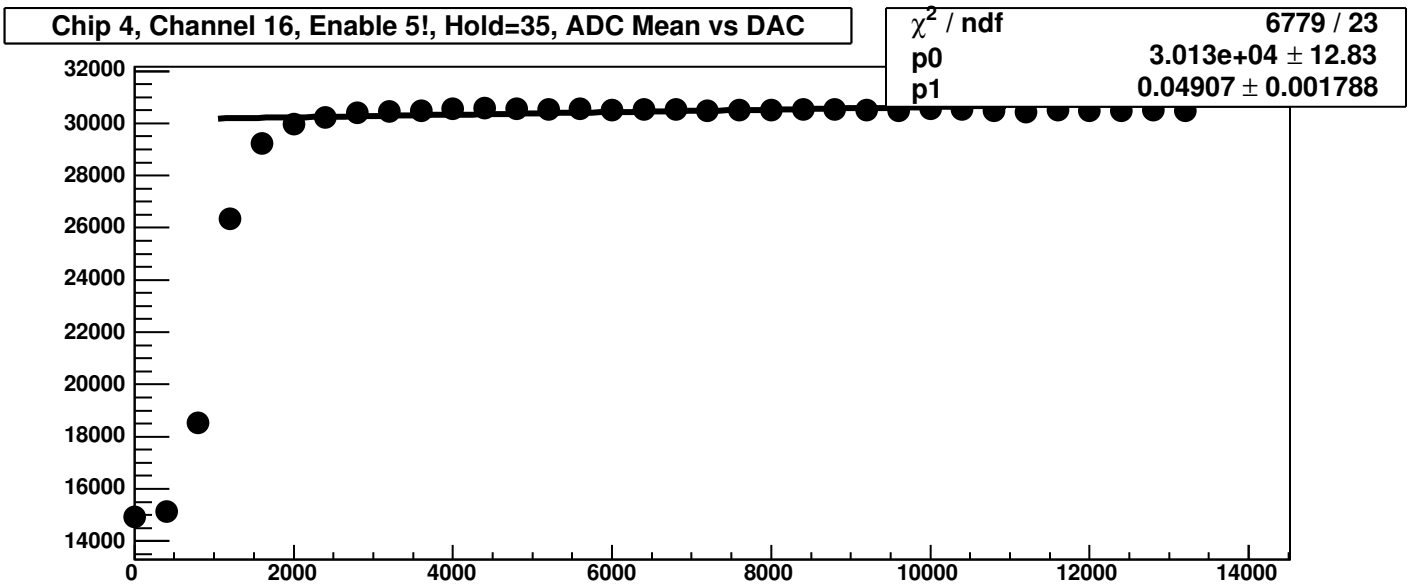
Chip 4, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

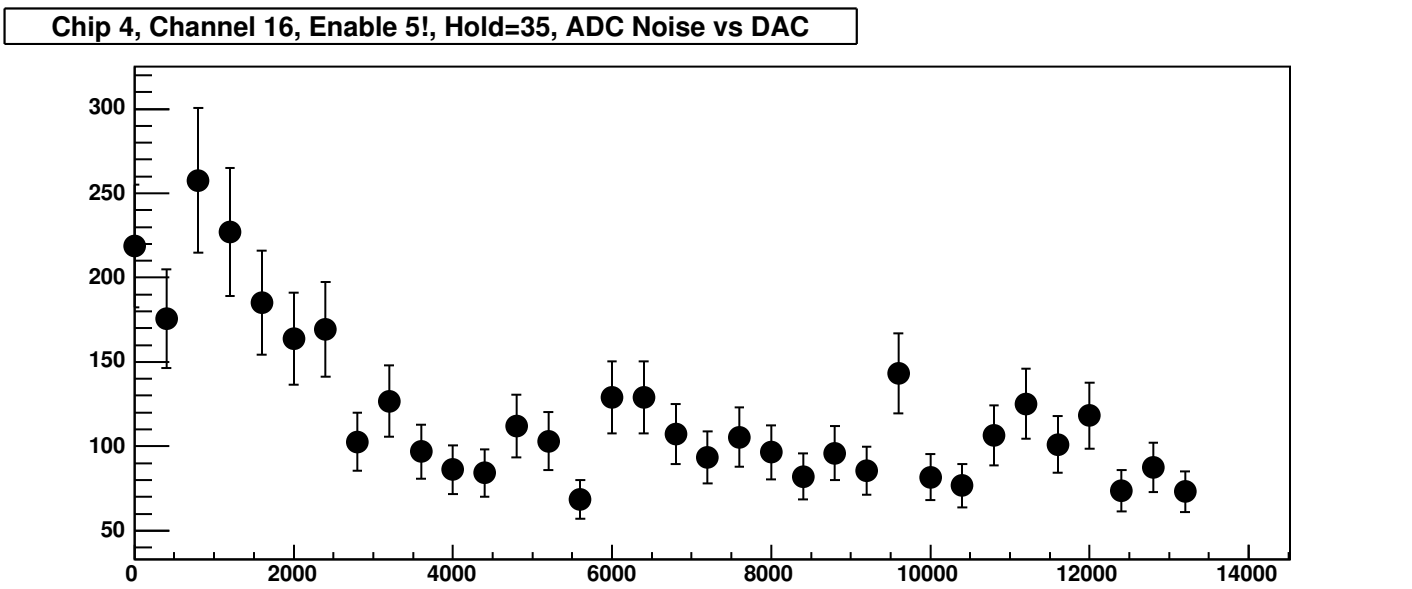


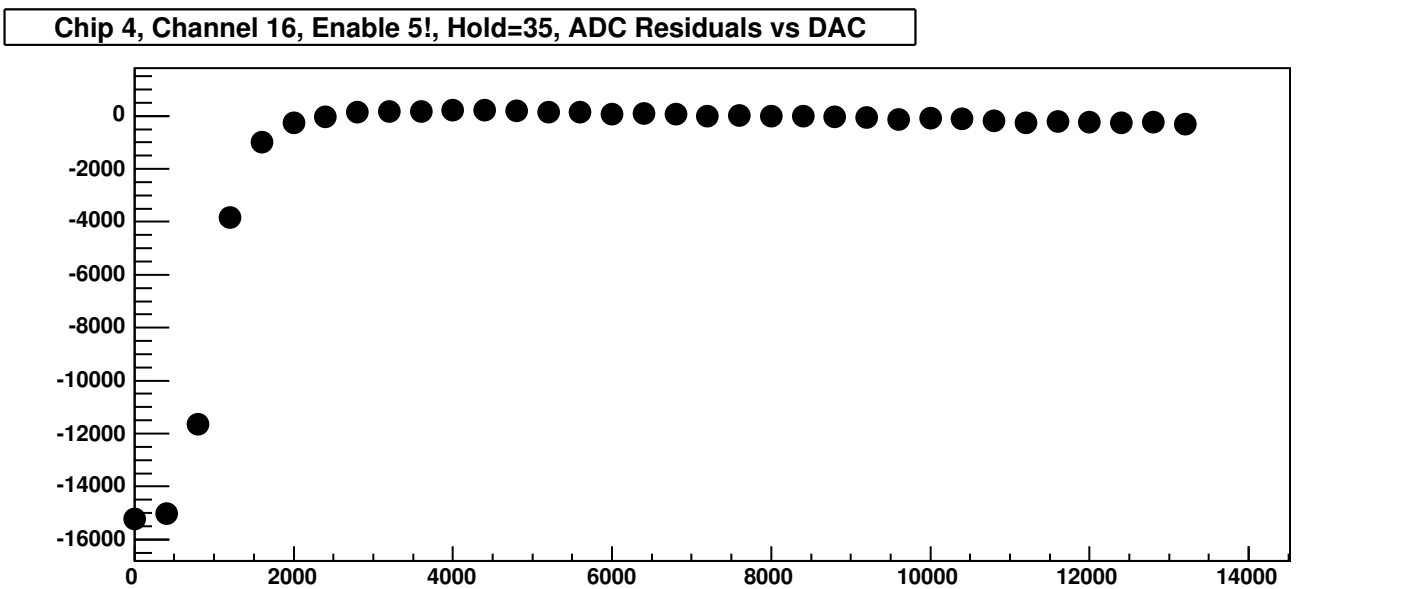

Chip 4, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 19.6 / 23
p0 119.1 ± 0.8384
p1 0.02098 ± 0.0001443
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

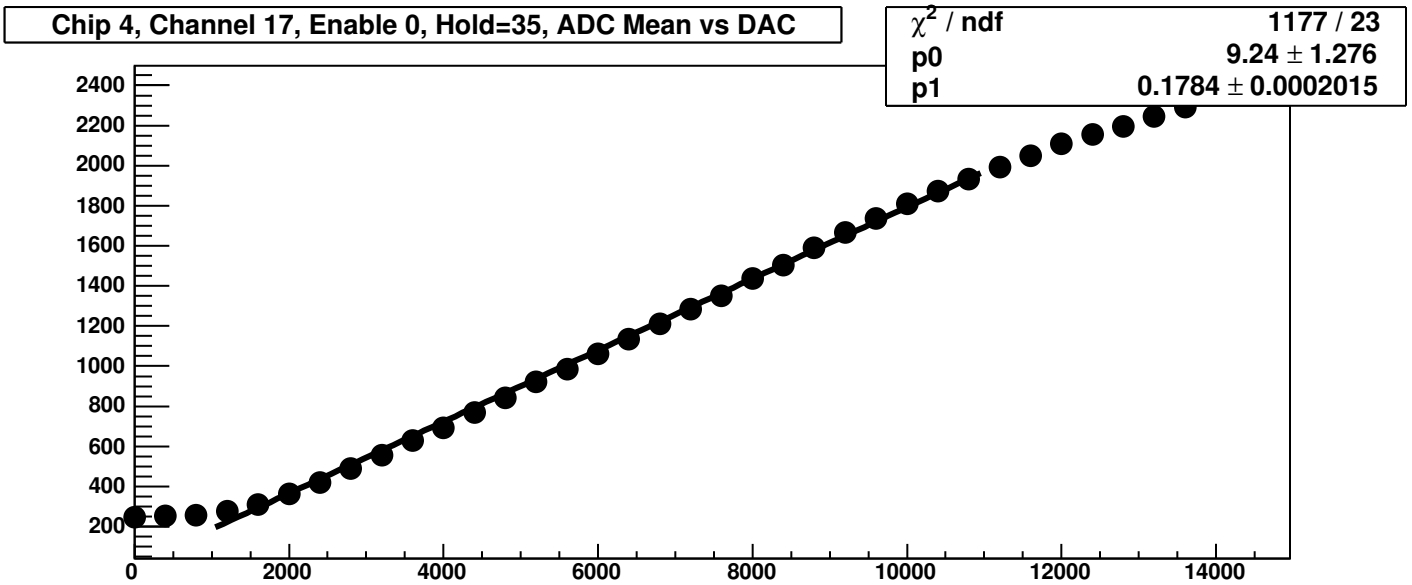

Chip 4, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

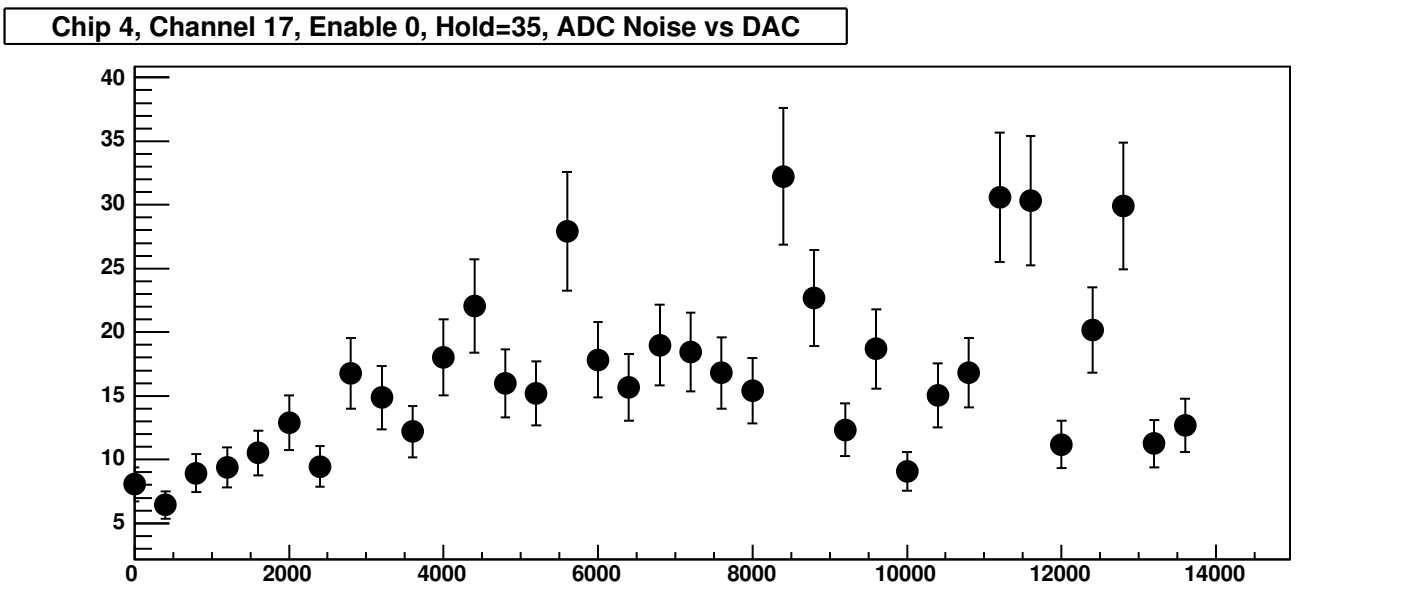

Chip 4, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

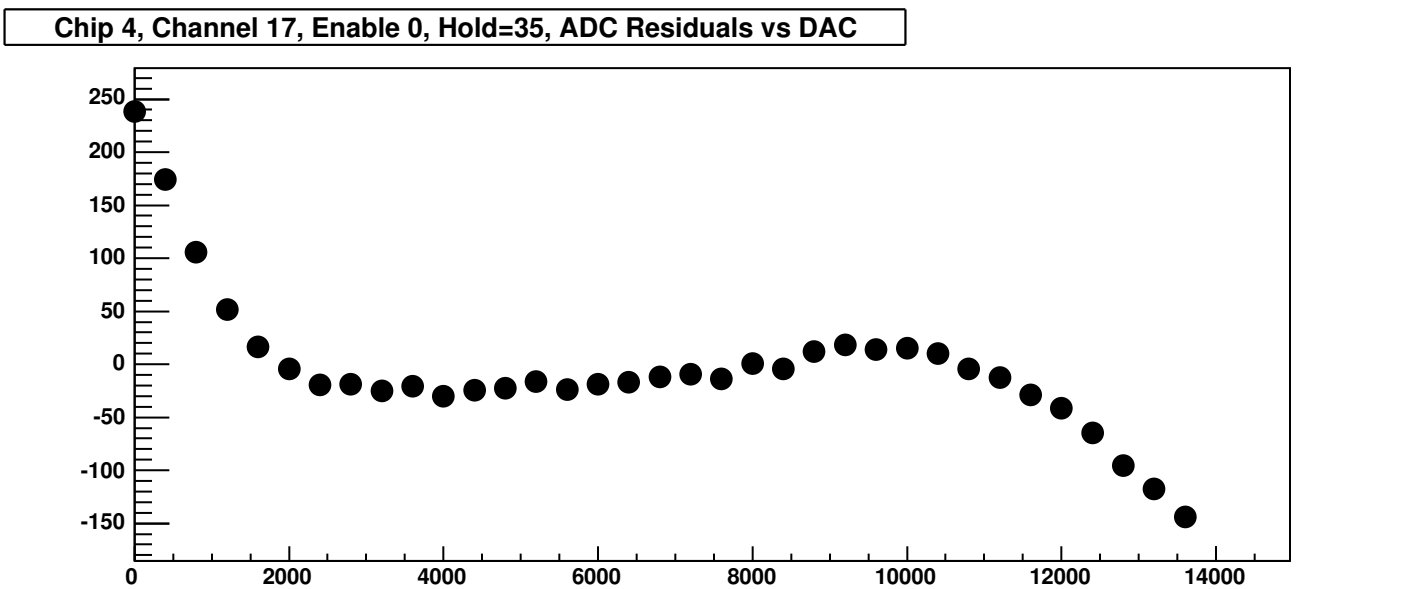

Chip 4, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 6779 / 23
p0 3.013e+04 ± 12.83
p1 0.04907 ± 0.001788
32000
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

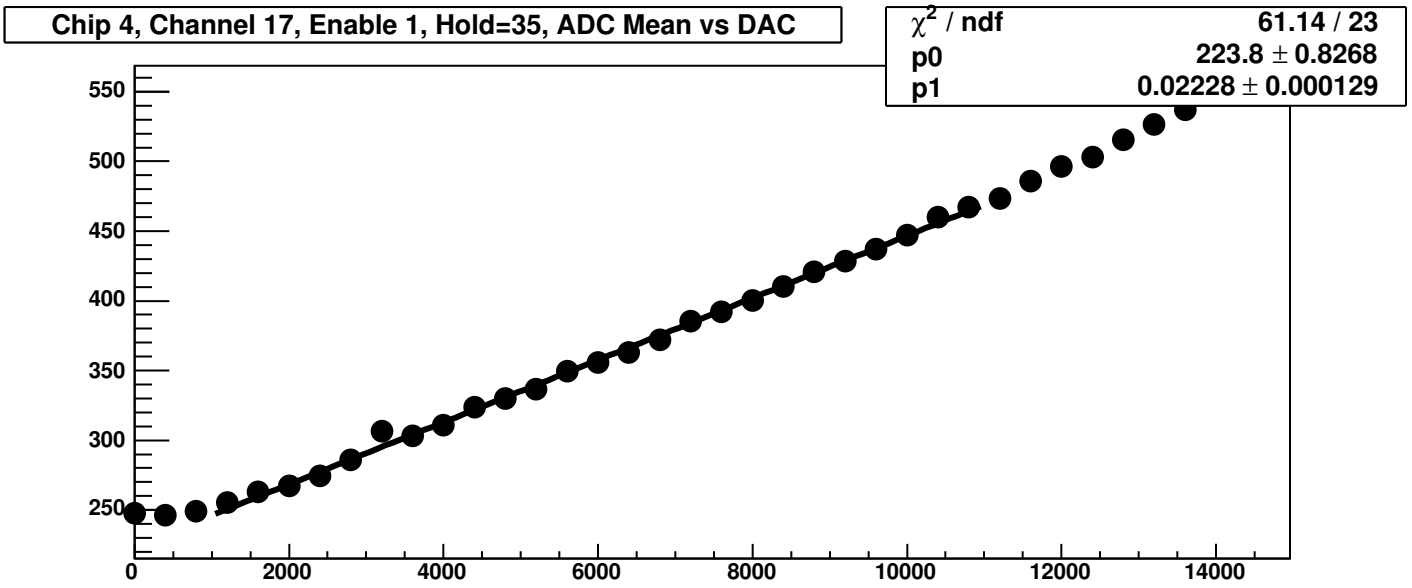

Chip 4, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

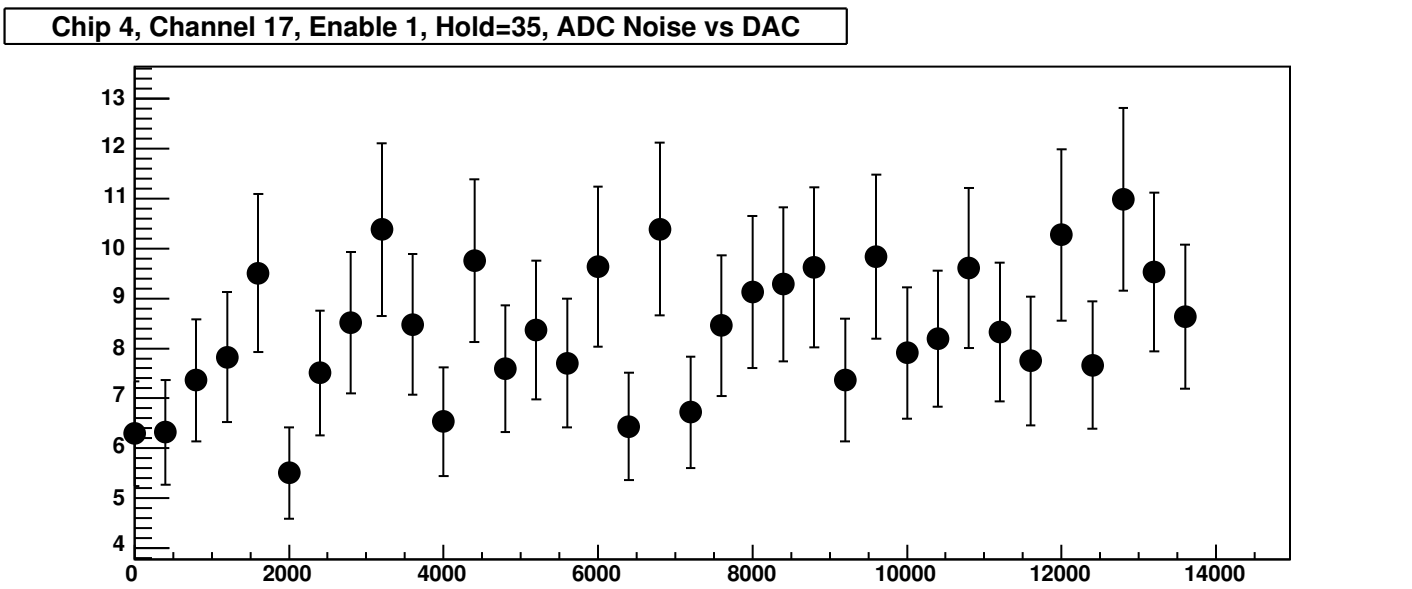

Chip 4, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

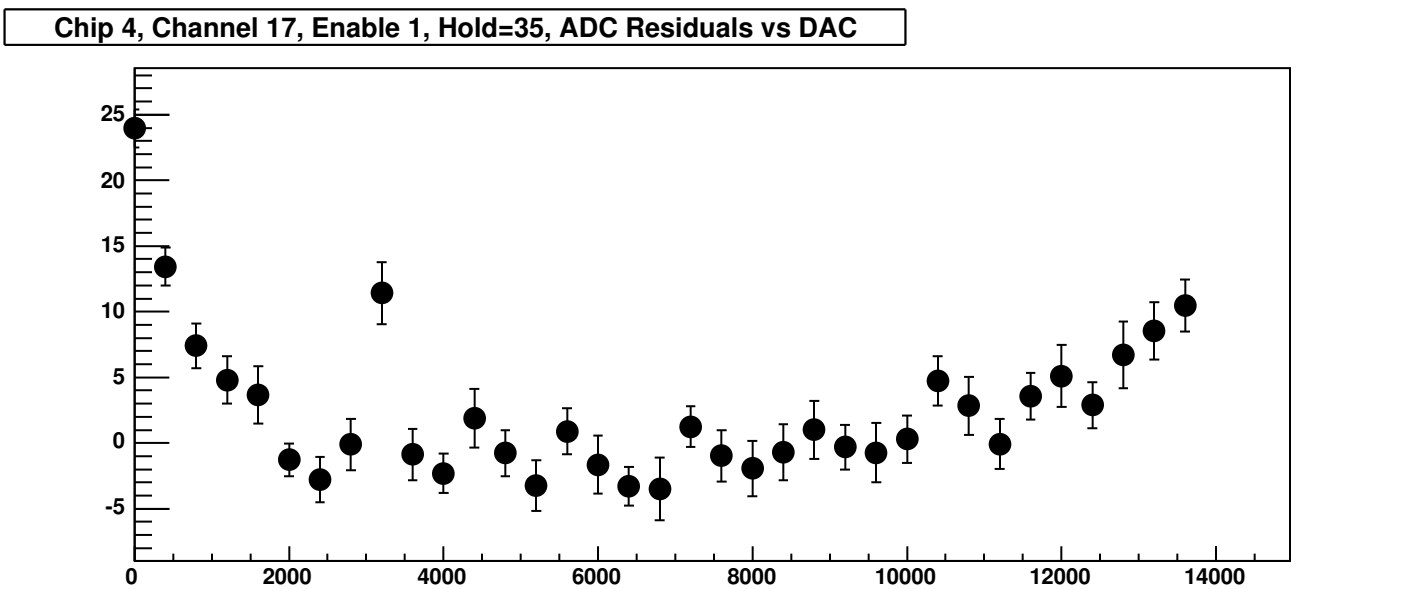

Chip 4, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1177 / 23
p0 9.24 ± 1.276
p1 0.1784 ± 0.0002015
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

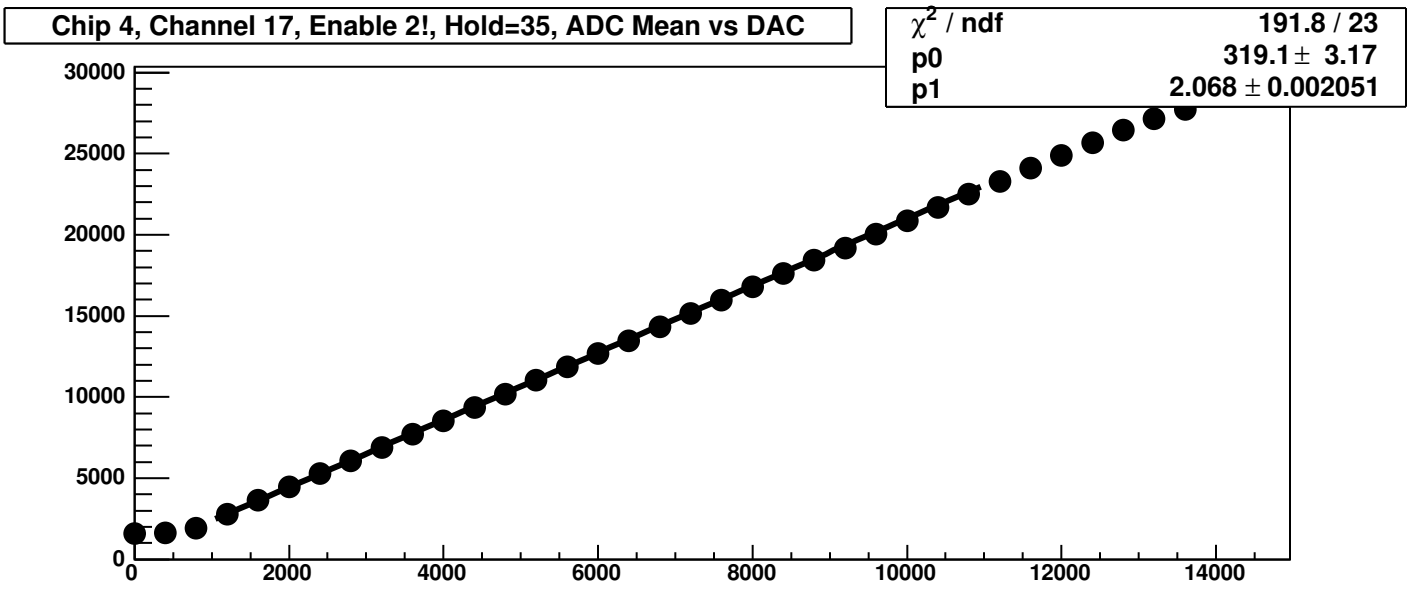

Chip 4, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

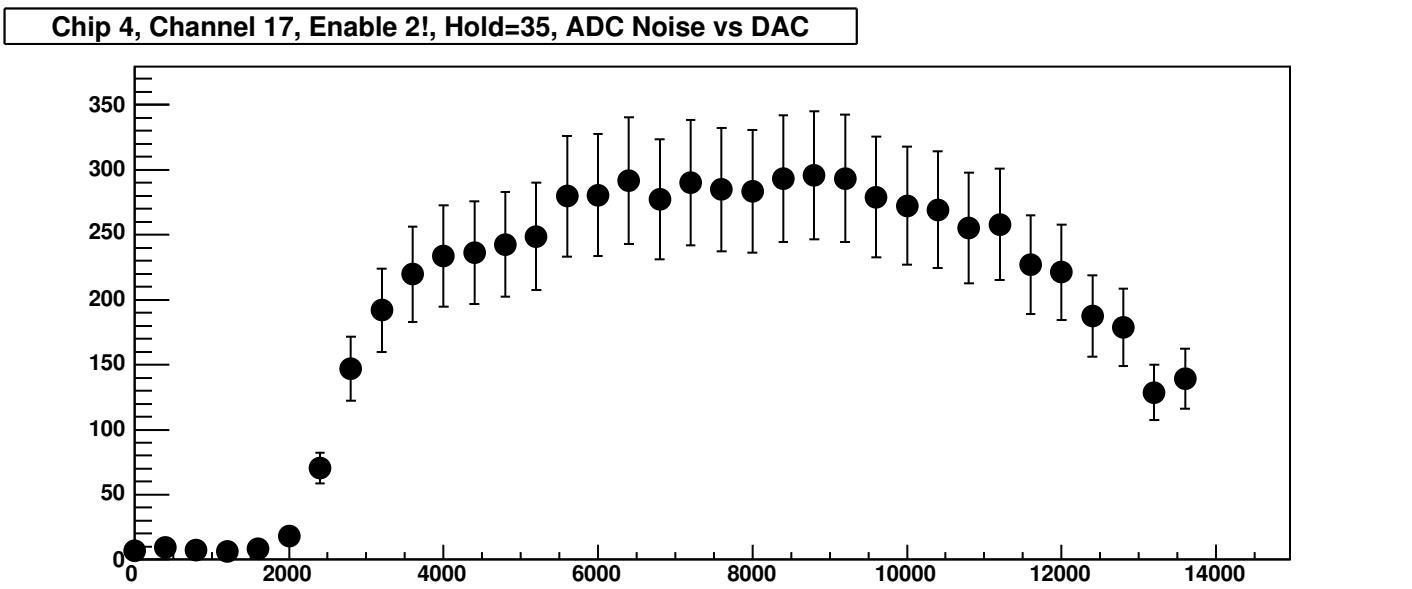

Chip 4, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

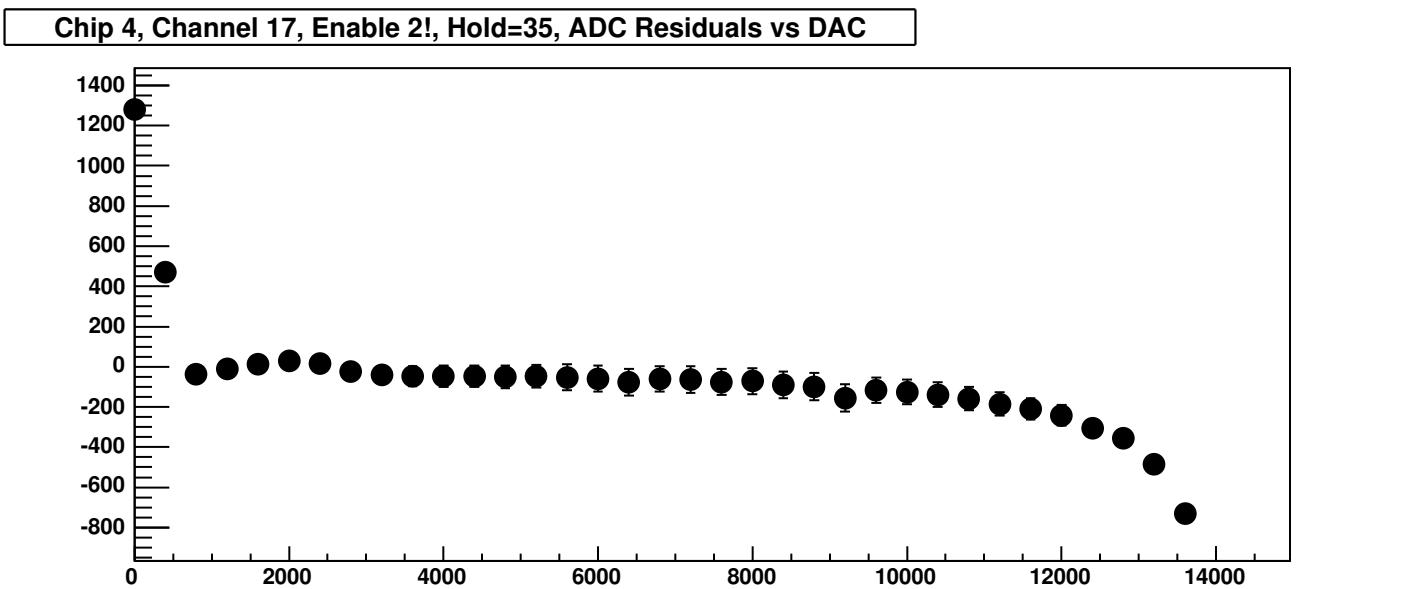

Chip 4, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 61.14 / 23
p0 223.8 ± 0.8268
p1 0.02228 ± 0.000129
550
500
450
400
350
300
250
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

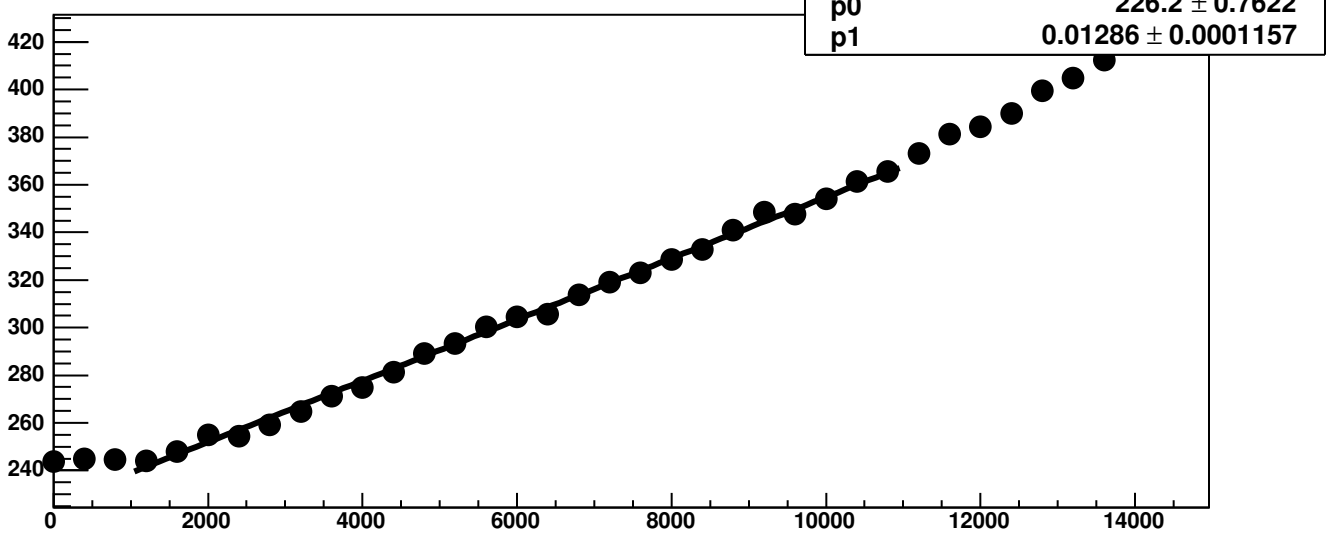

Chip 4, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 191.8 / 23
p0 319.1 ± 3.17
p1 2.068 ± 0.002051
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

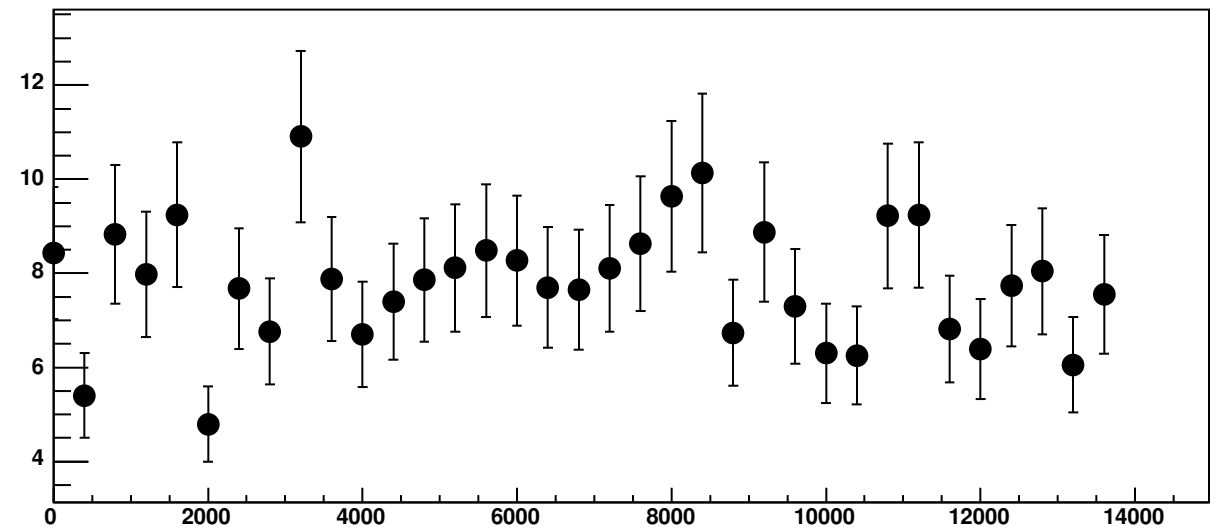

Chip 4, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

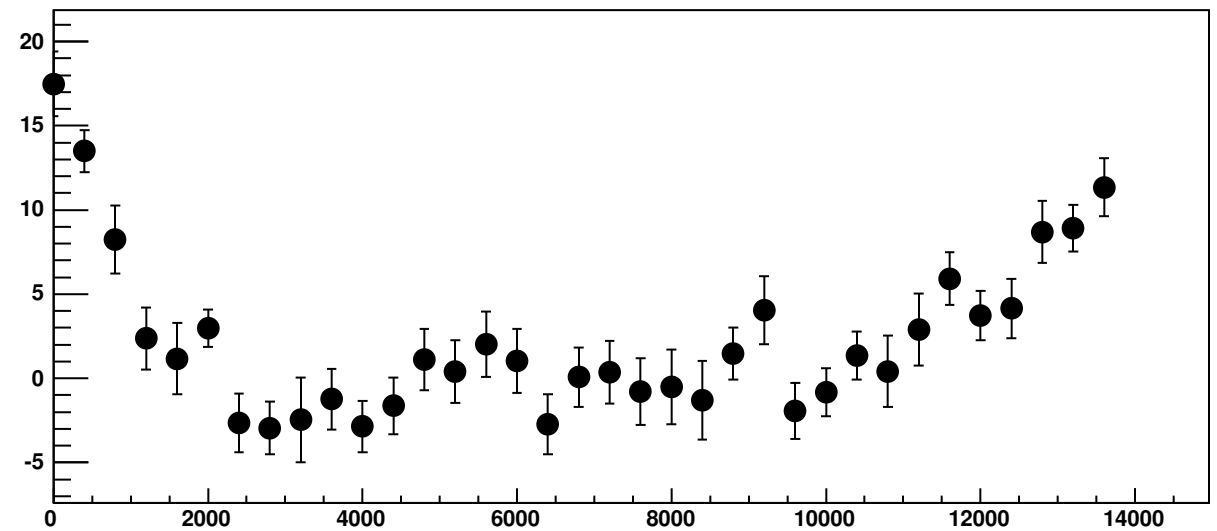
Chip 4, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC



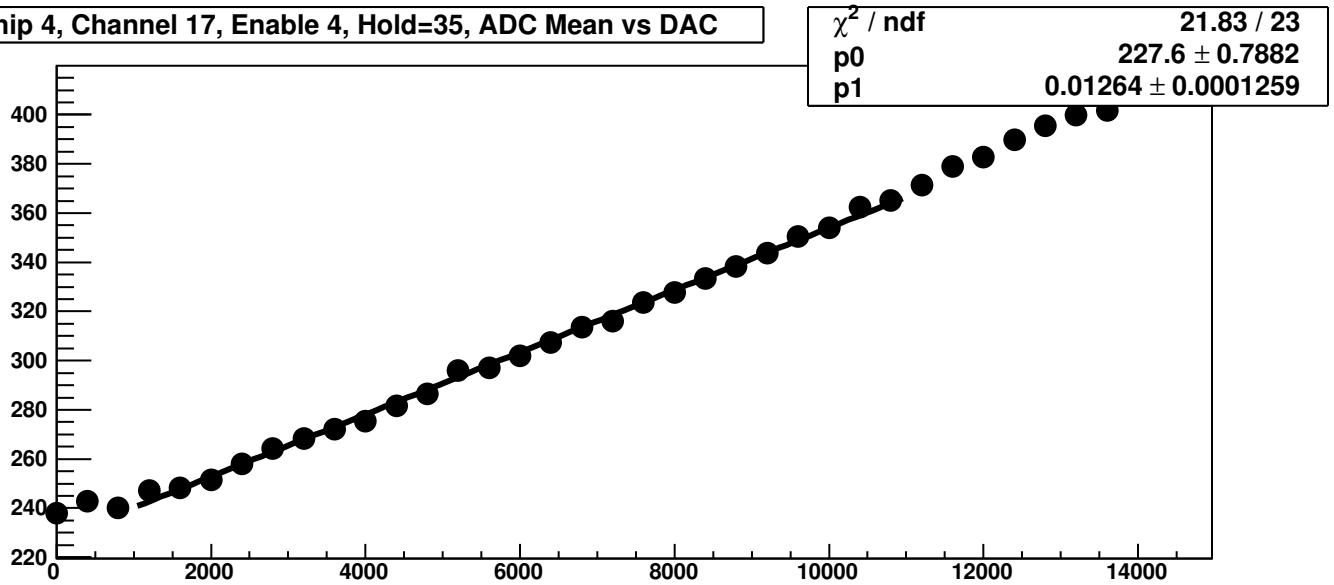
Chip 4, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC



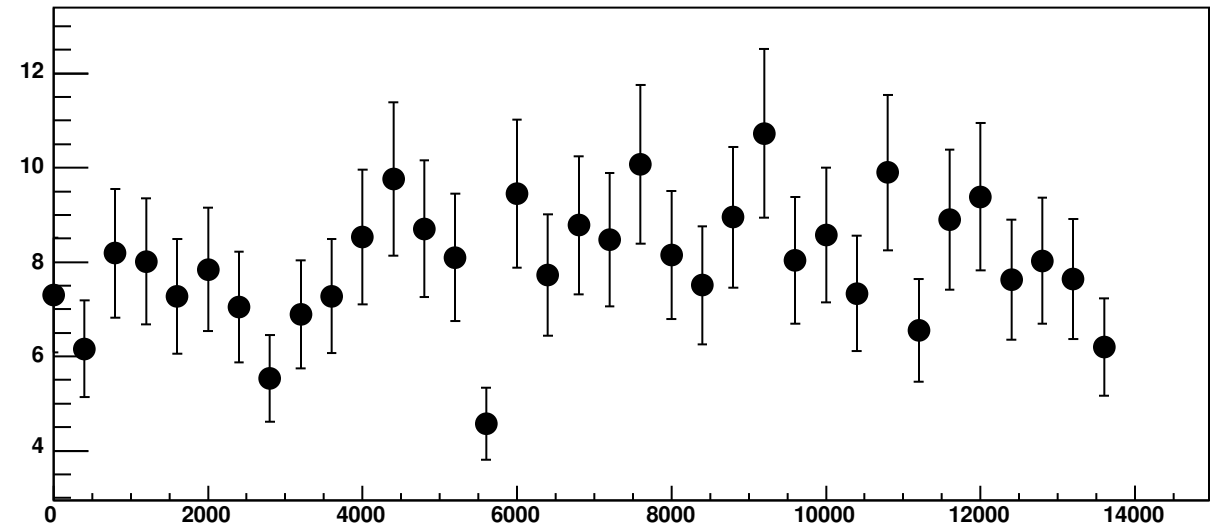
Chip 4, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC



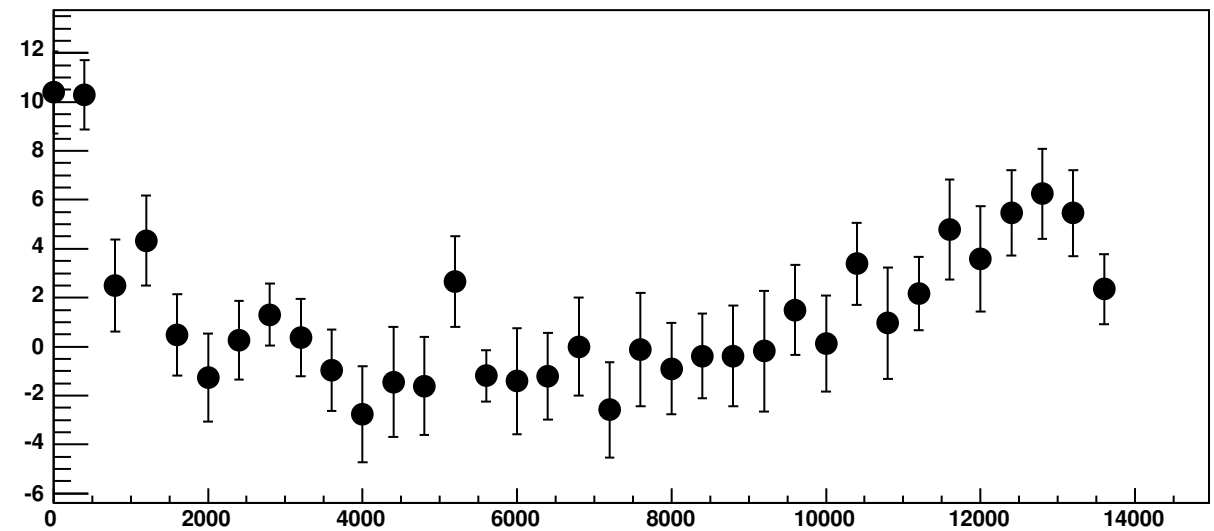
Chip 4, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

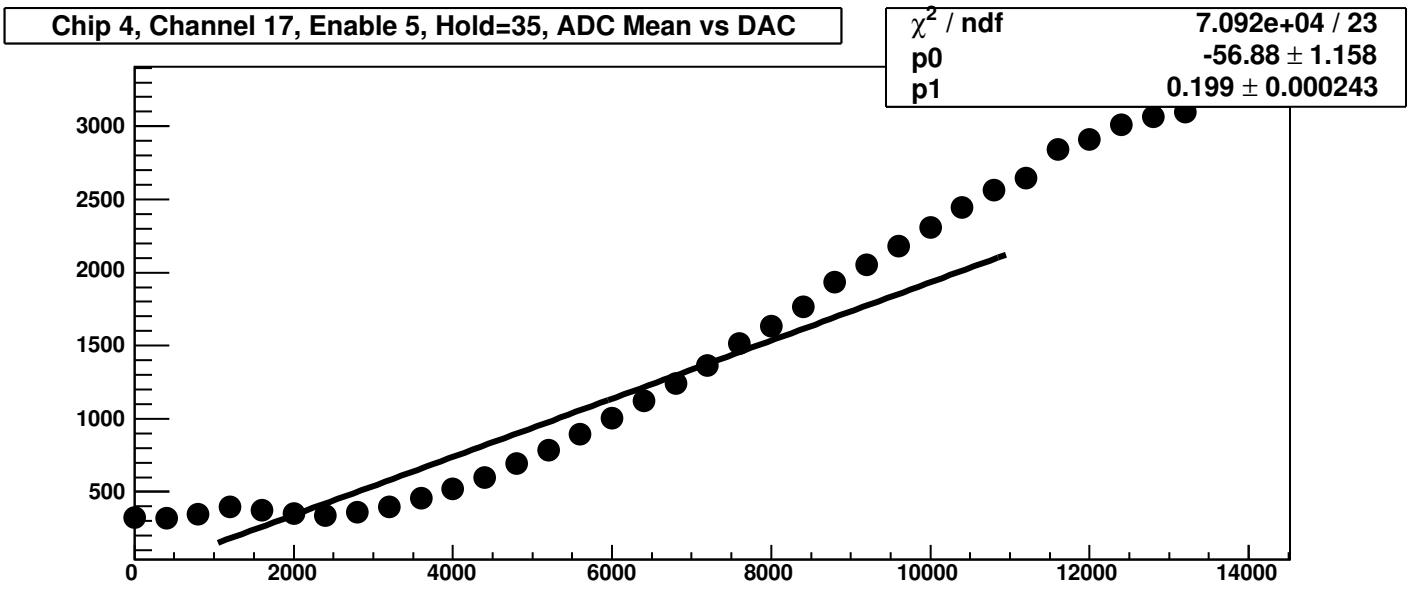


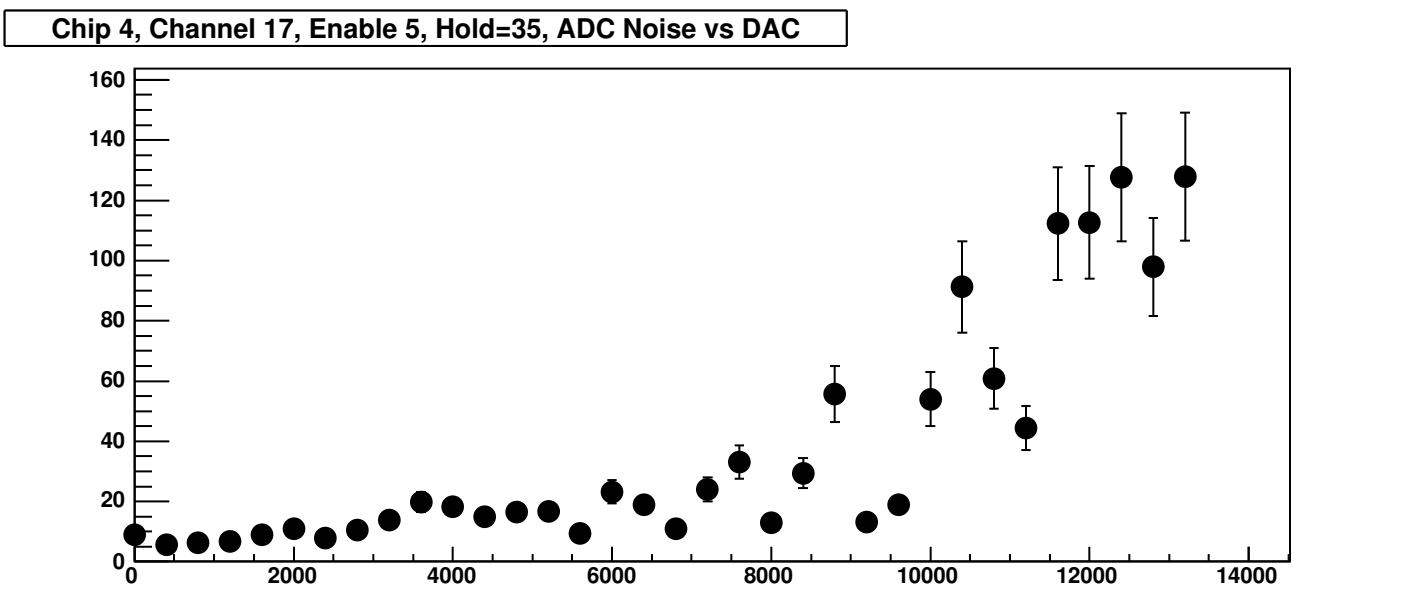
Chip 4, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

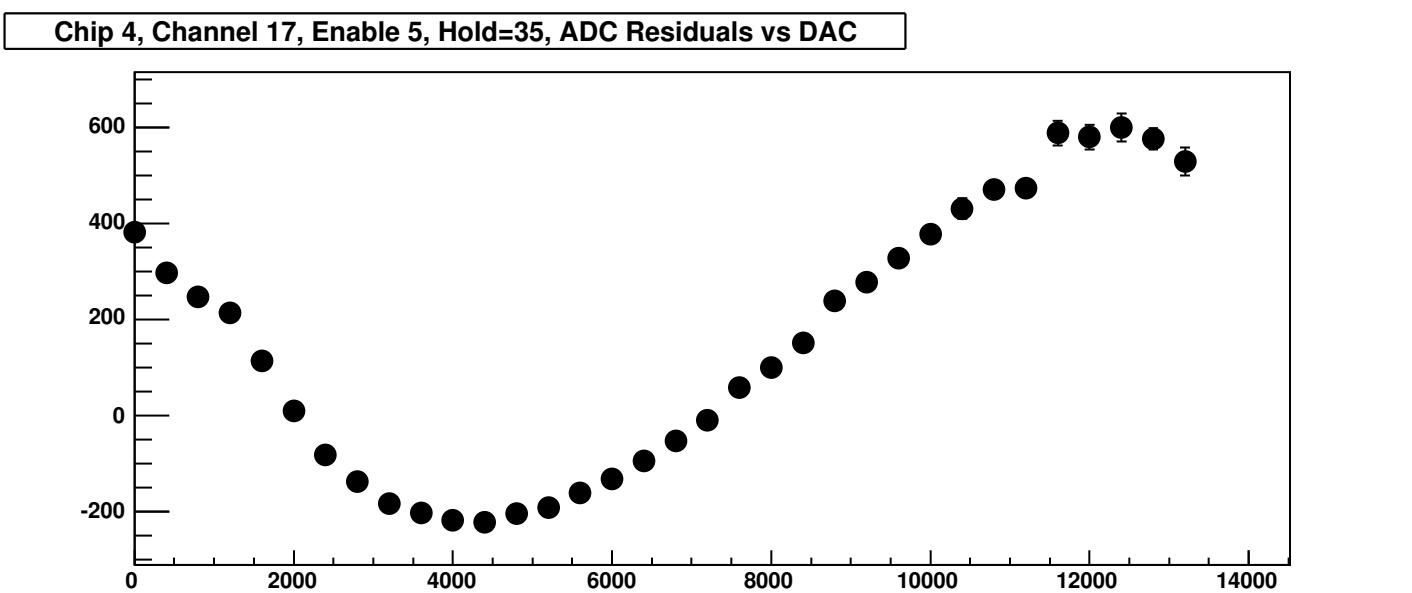


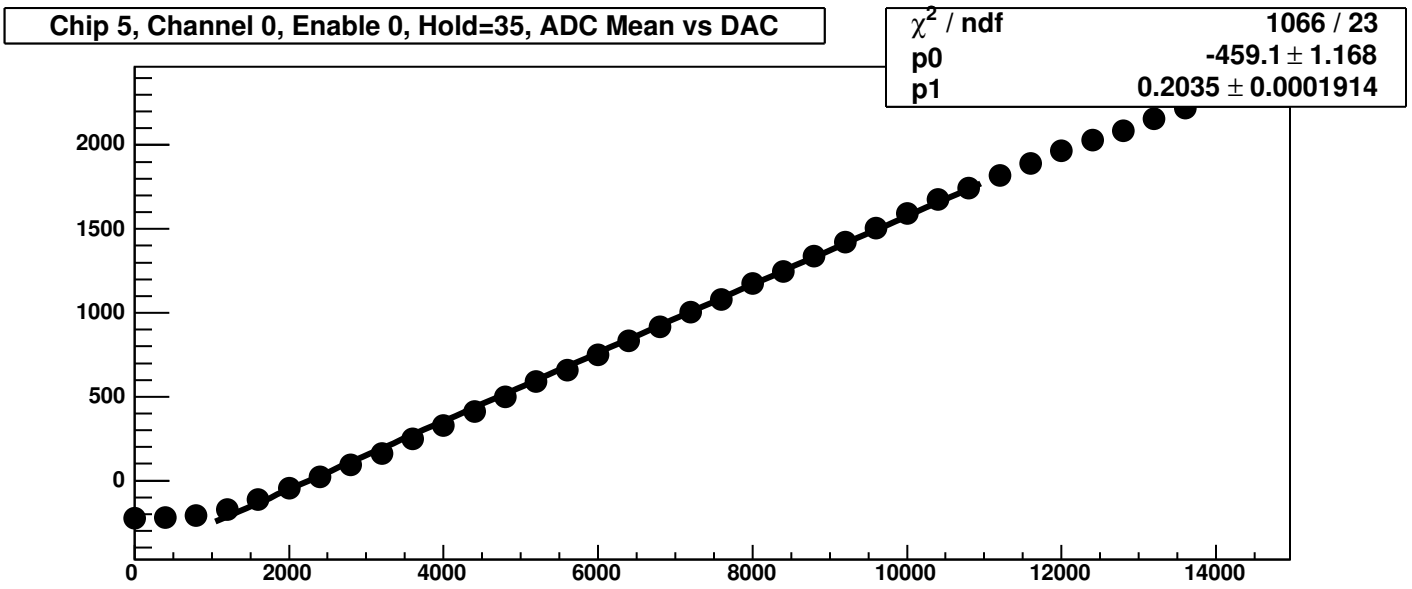
Chip 4, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

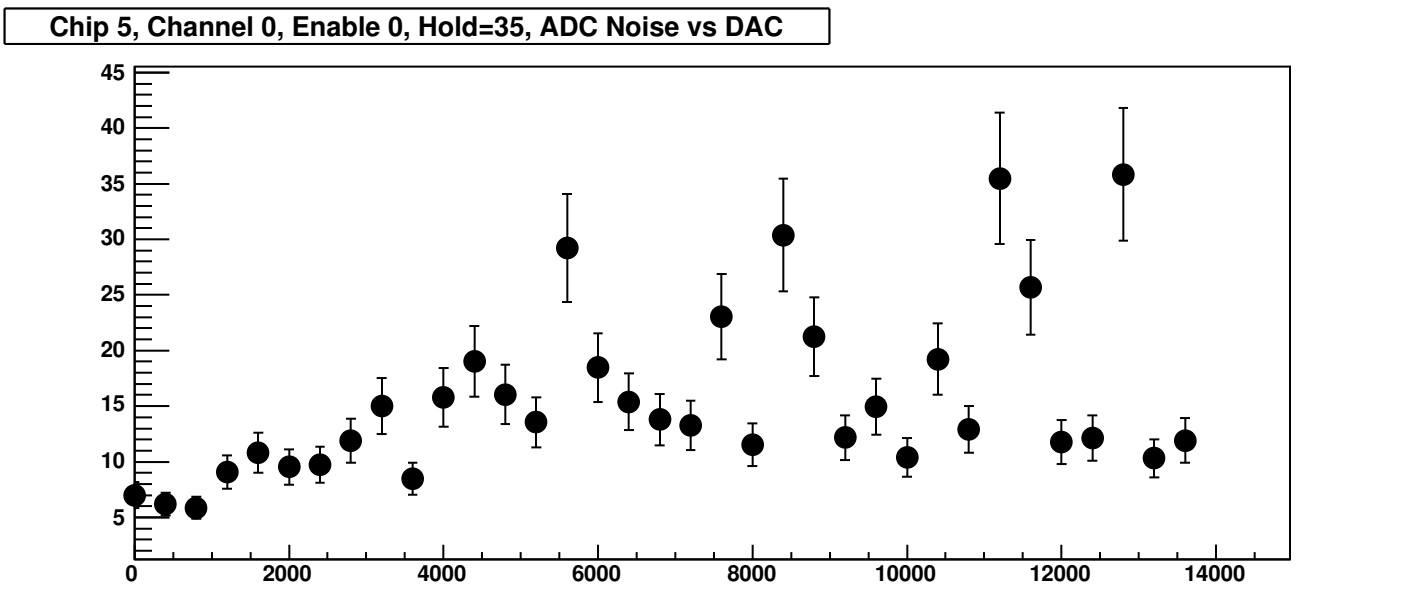


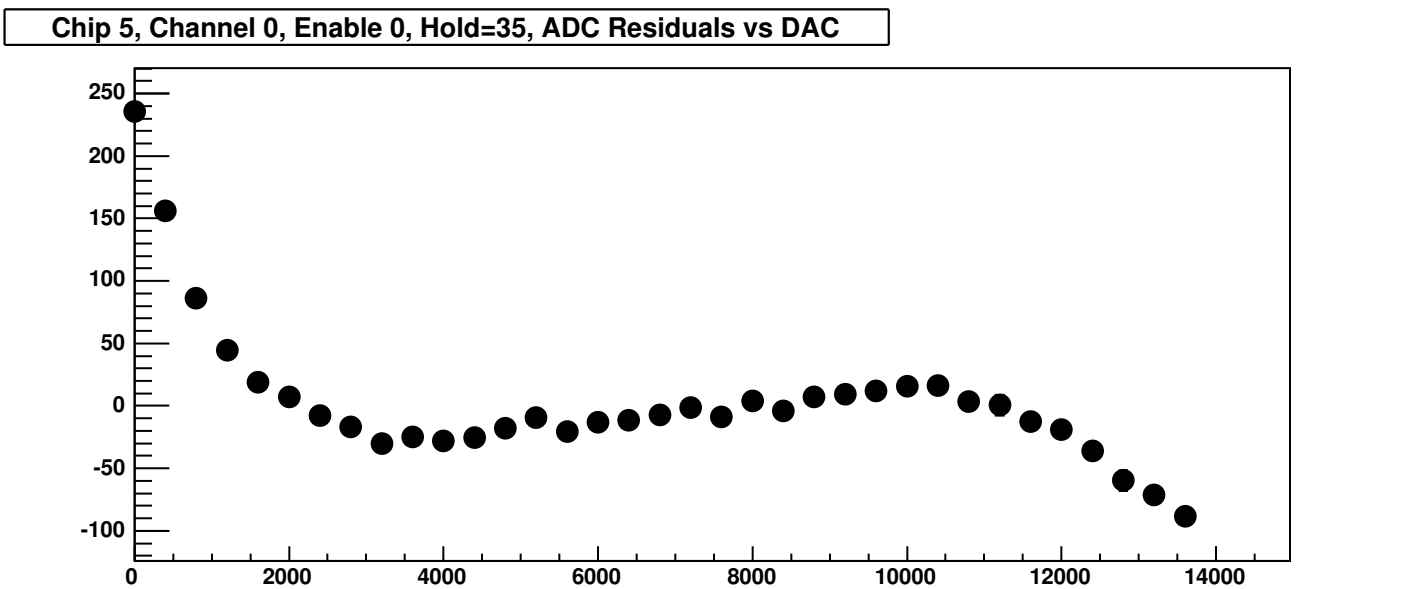

Chip 4, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 7.092e+04 / 23
p0 -56.88 ± 1.158
p1 0.199 ± 0.000243
3000
2500
2000
1500
1000
500
0
2000
4000
6000
8000
10000
12000
14000

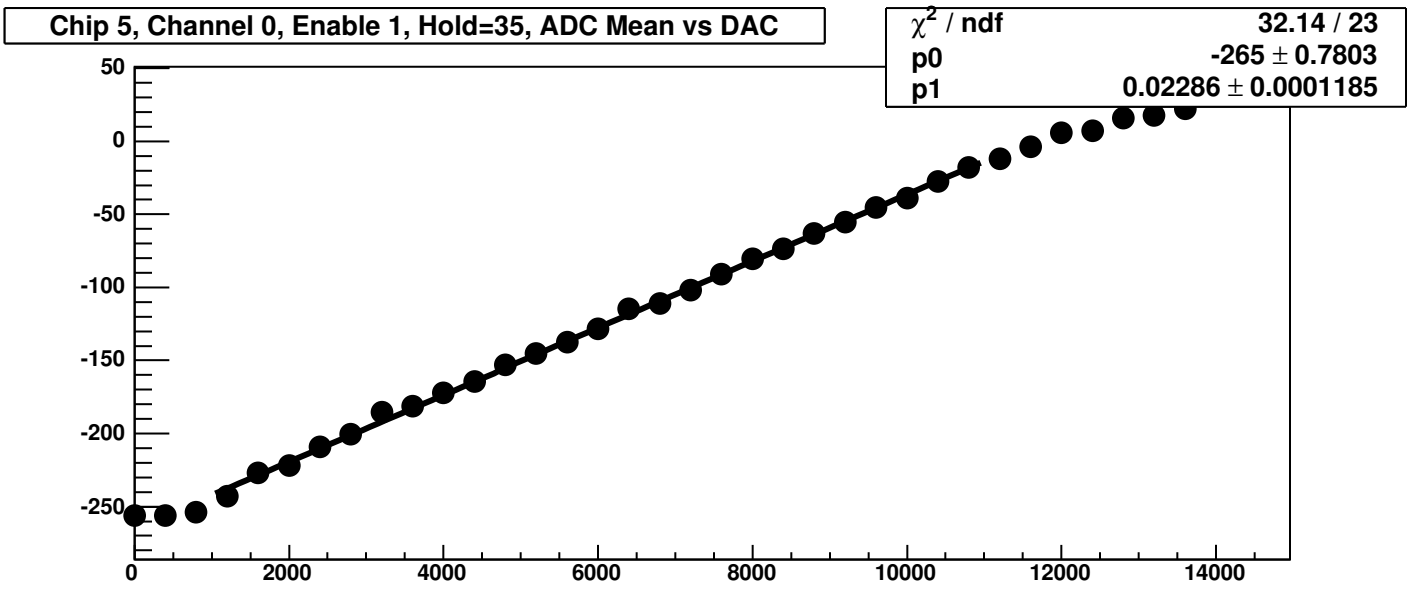

Chip 4, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

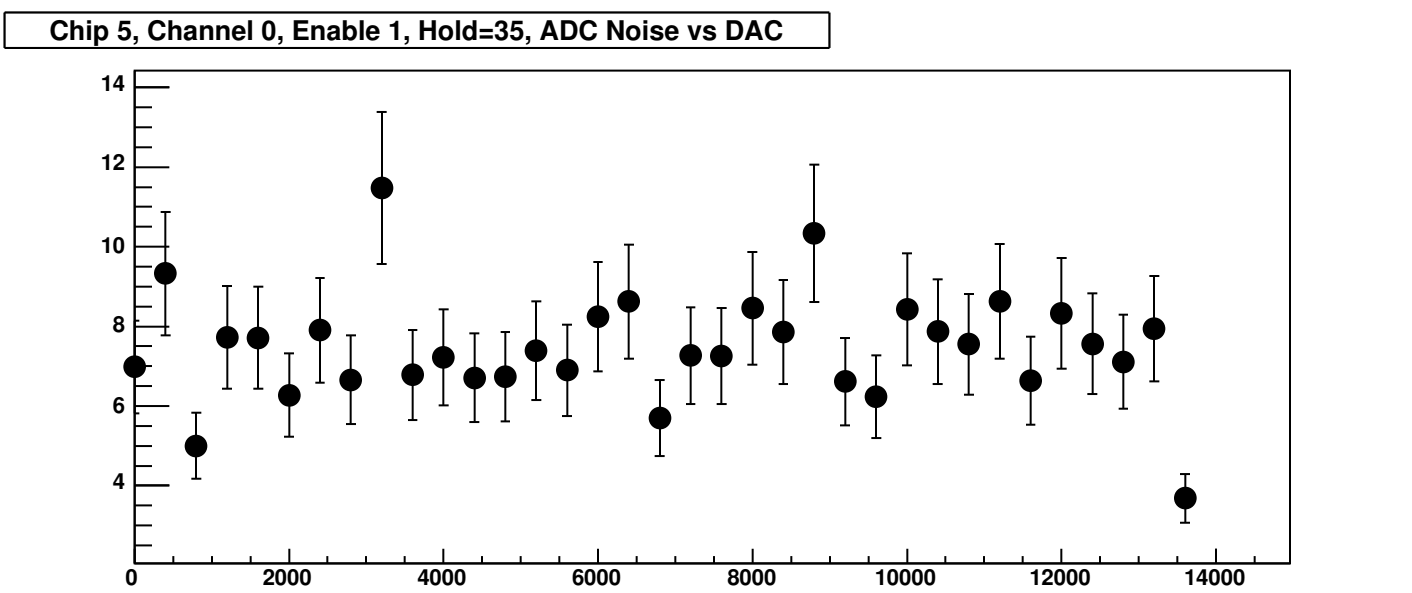

Chip 4, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

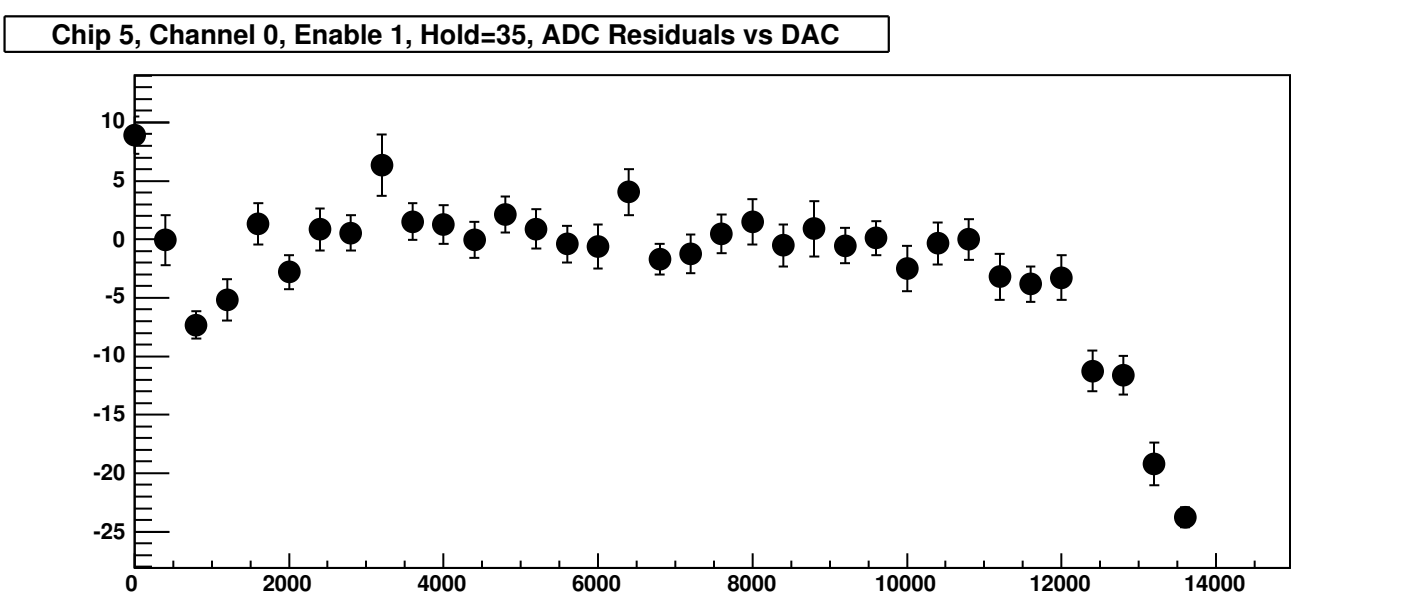

Chip 5, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1066 / 23
p0 -459.1 ± 1.168
p1 0.2035 ± 0.0001914
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

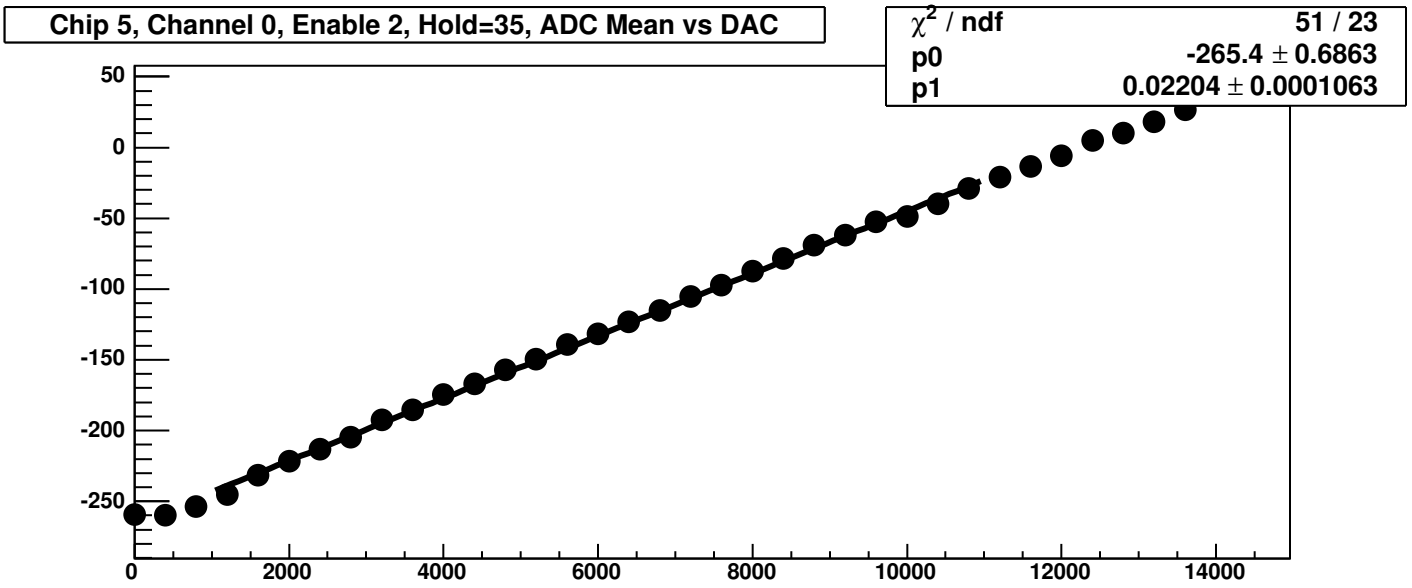

Chip 5, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

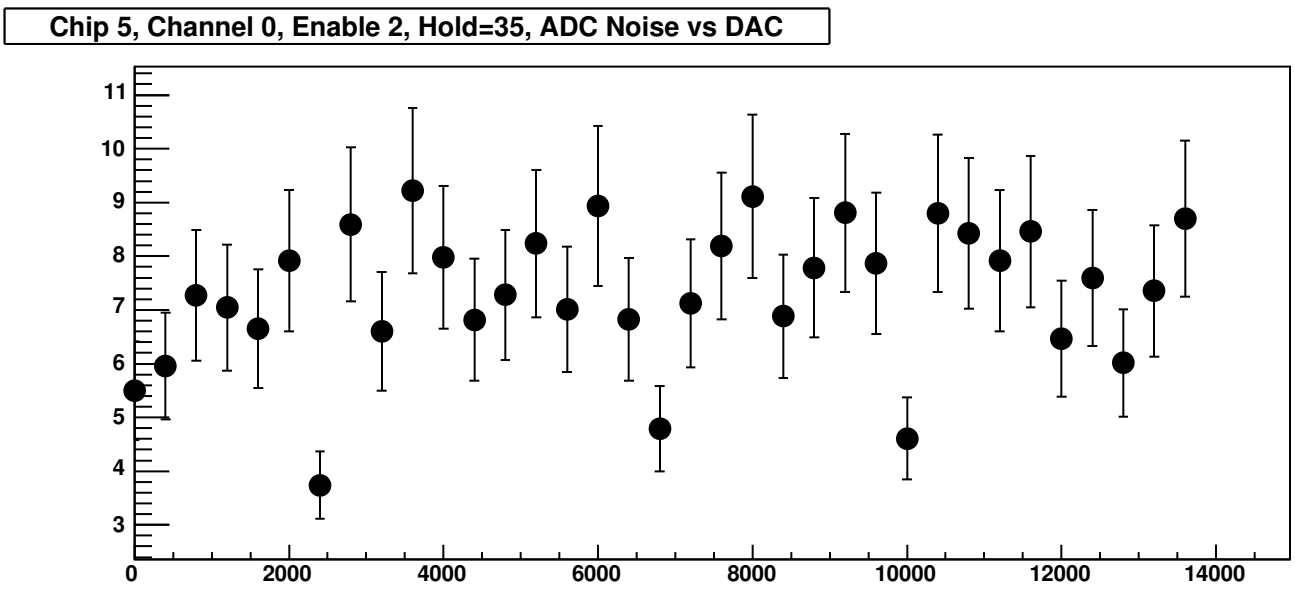

Chip 5, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

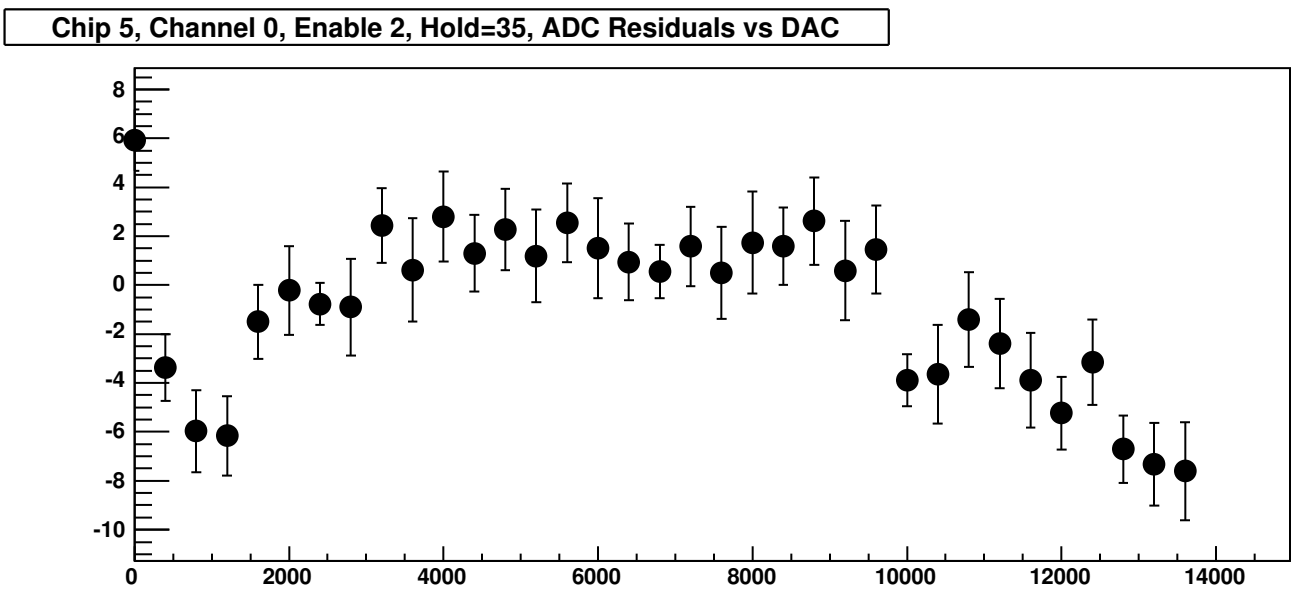

Chip 5, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.14 / 23
p0 -265 ± 0.7803
p1 0.02286 ± 0.0001185
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

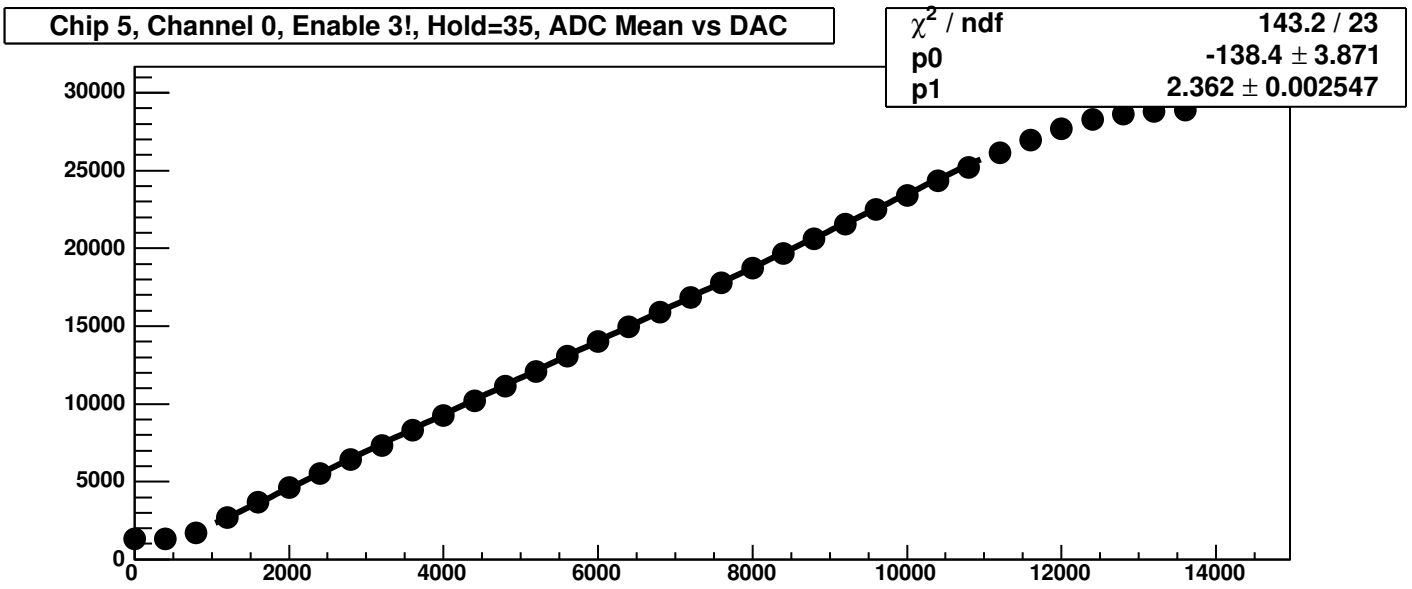

Chip 5, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

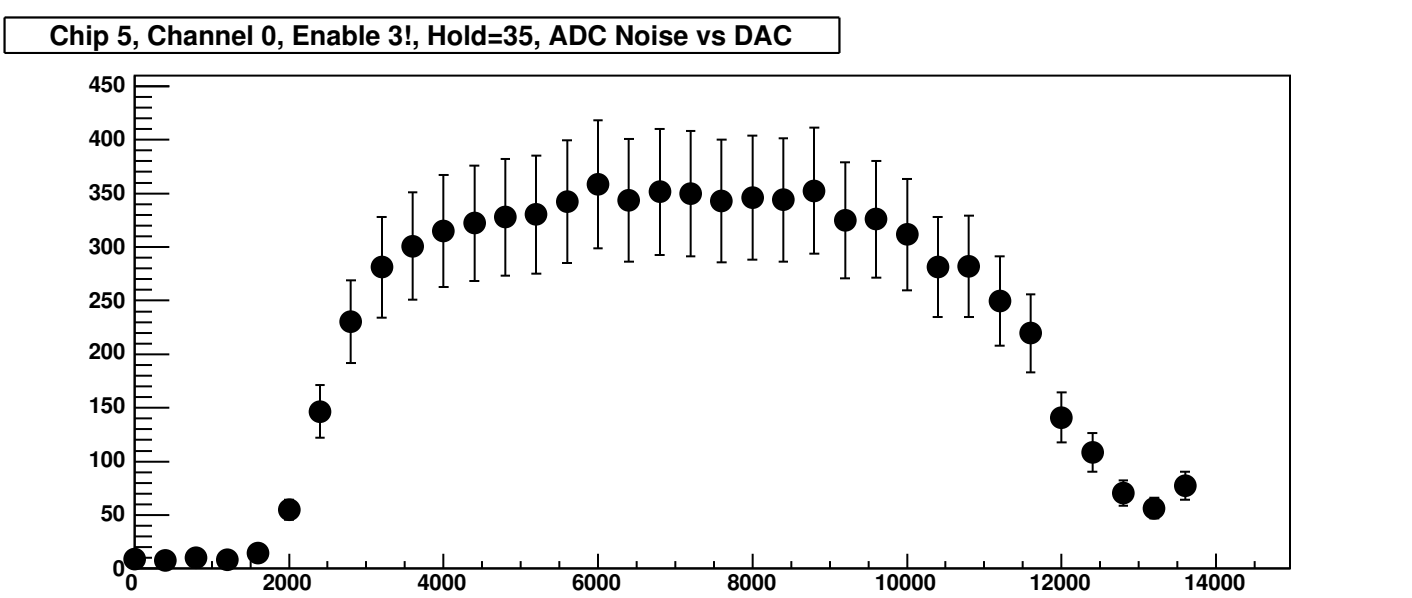

Chip 5, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

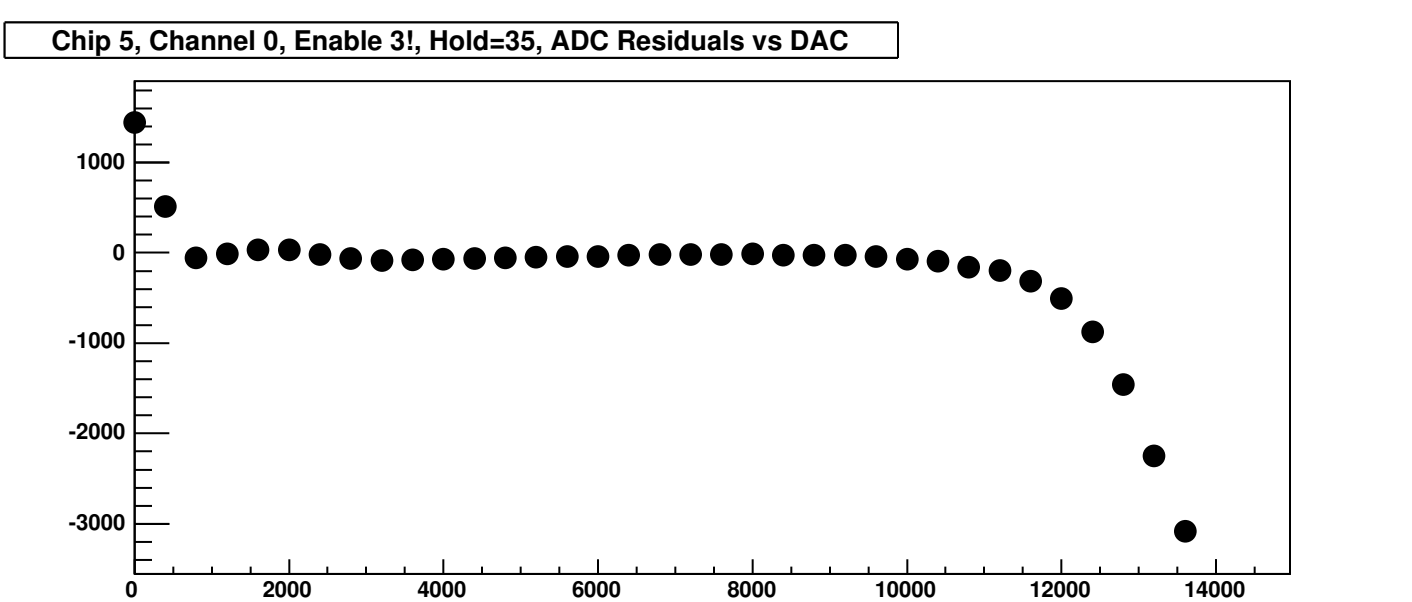

Chip 5, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 51 / 23
p0 -265.4 ± 0.6863
p1 0.02204 ± 0.0001063
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

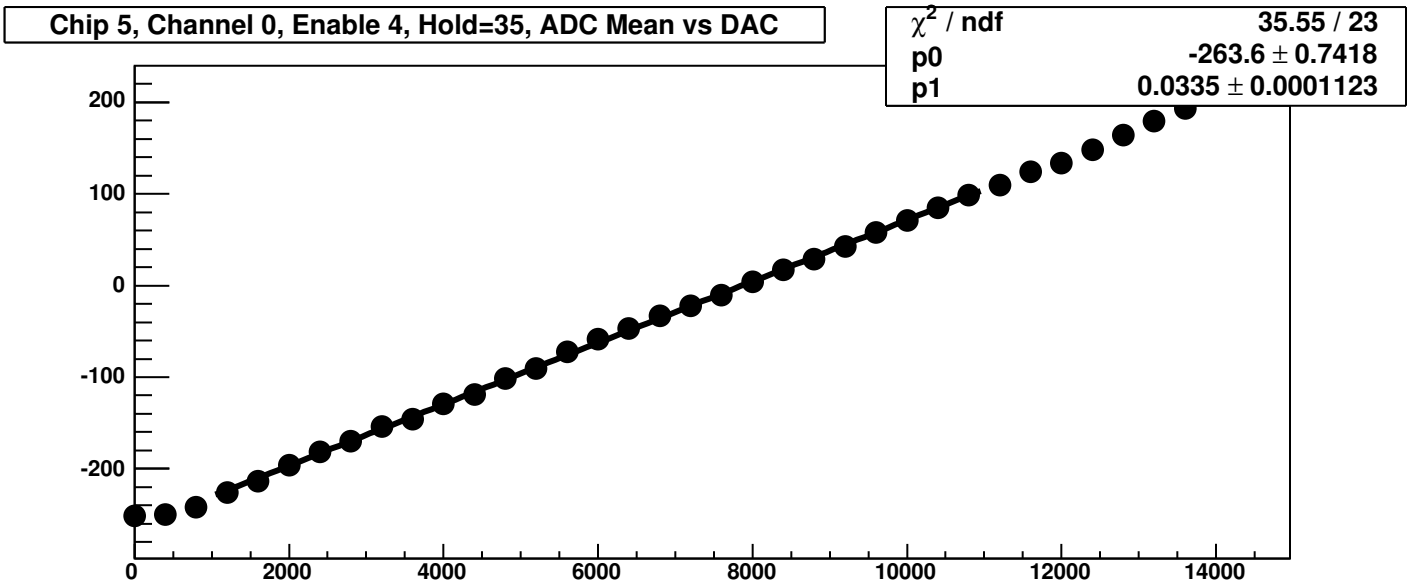

Chip 5, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

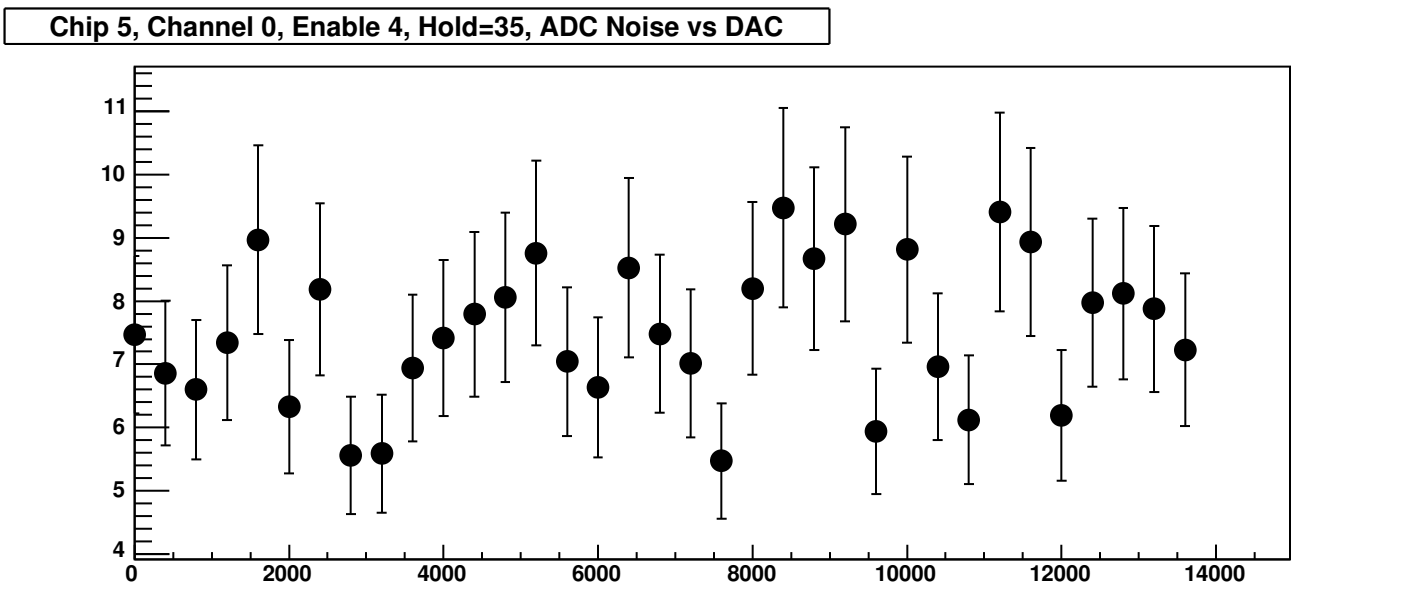

Chip 5, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
0
2000
4000
6000
8000
10000
12000
14000

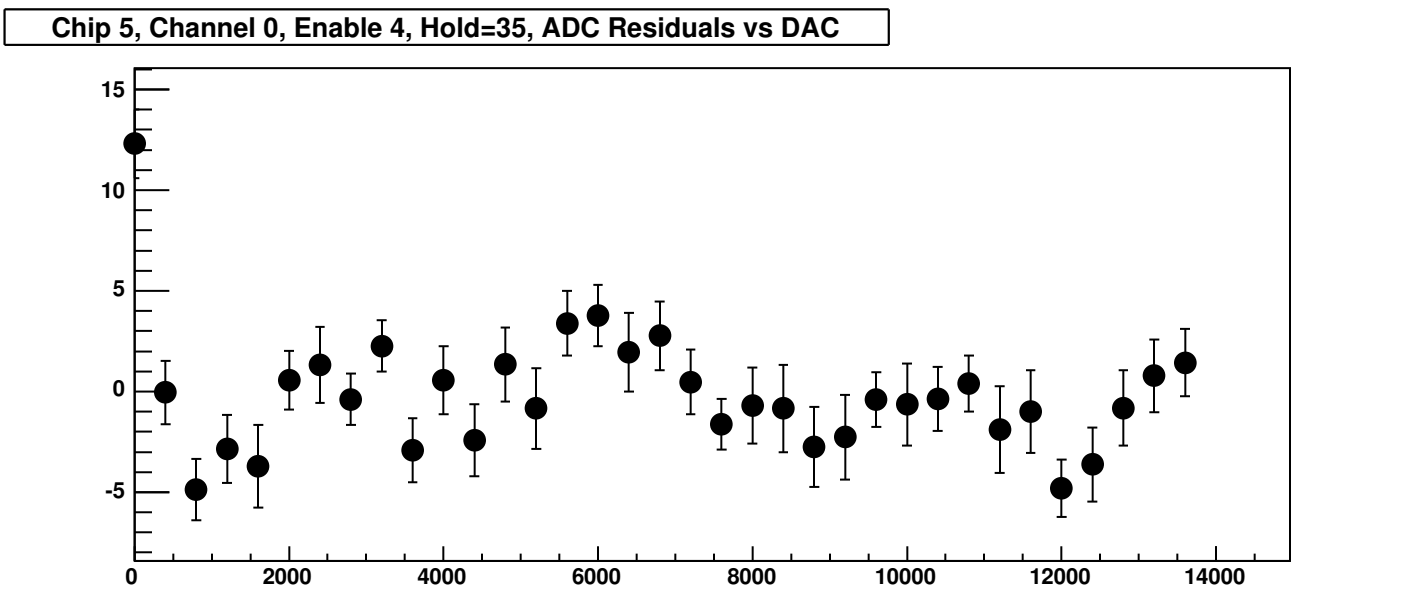

Chip 5, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 143.2 / 23
p0 -138.4 ± 3.871
p1 2.362 ± 0.002547
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

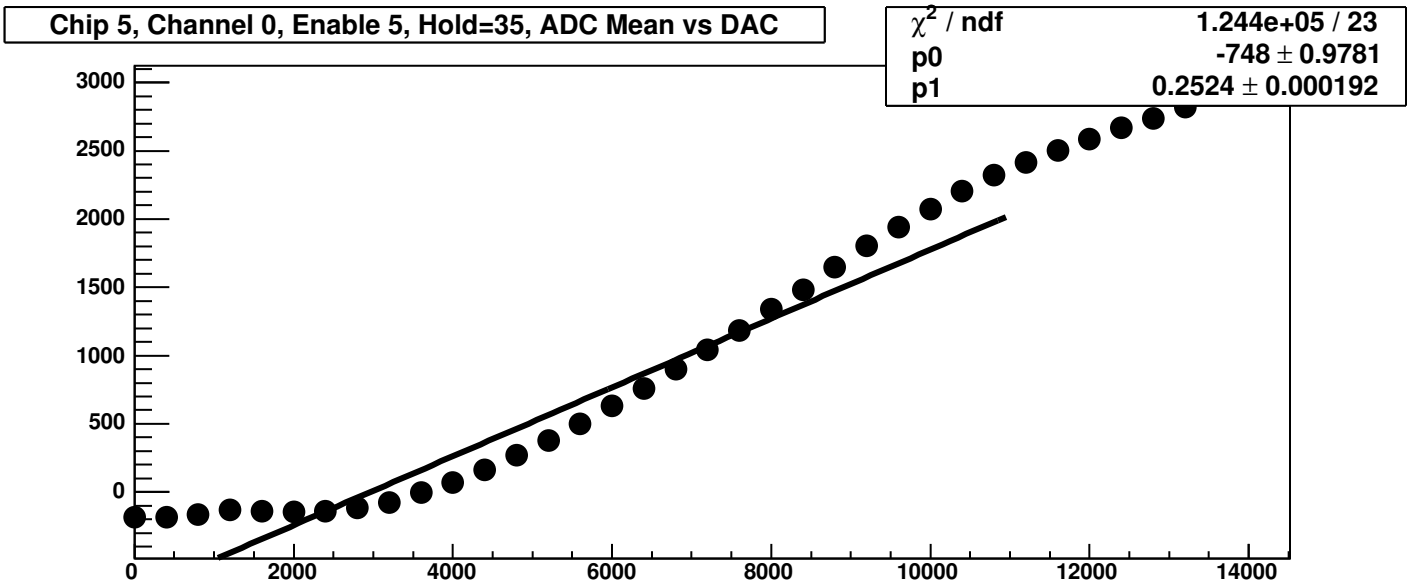

Chip 5, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

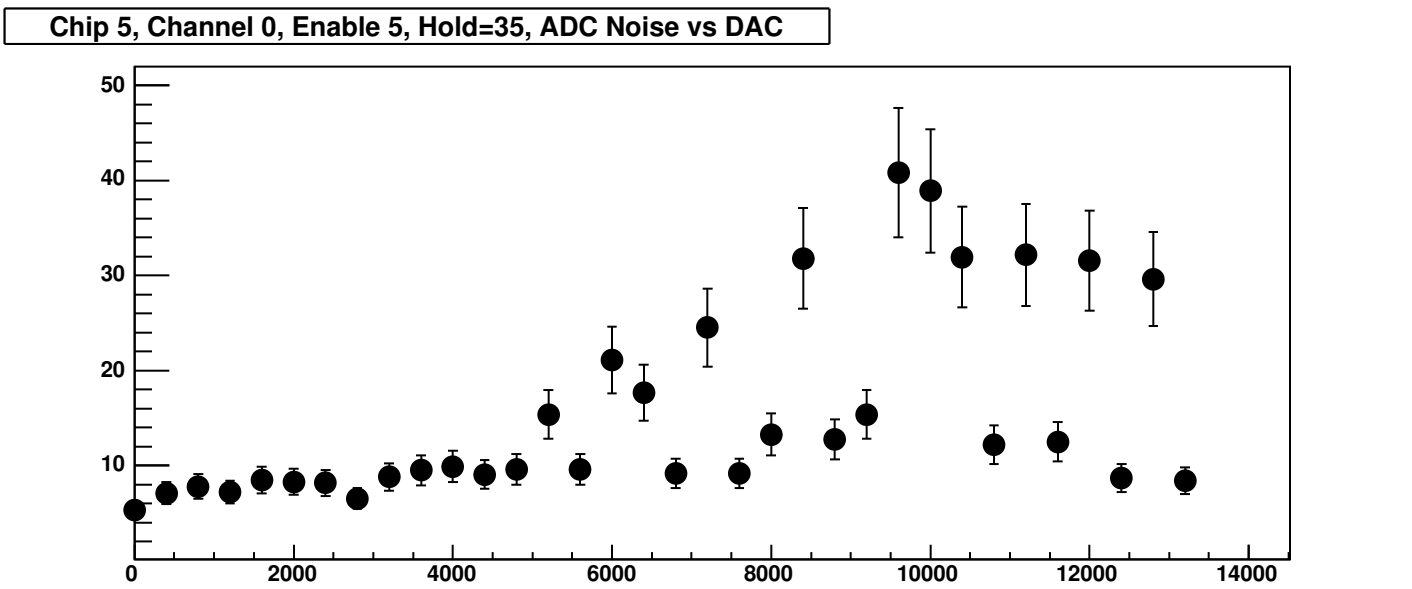

Chip 5, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

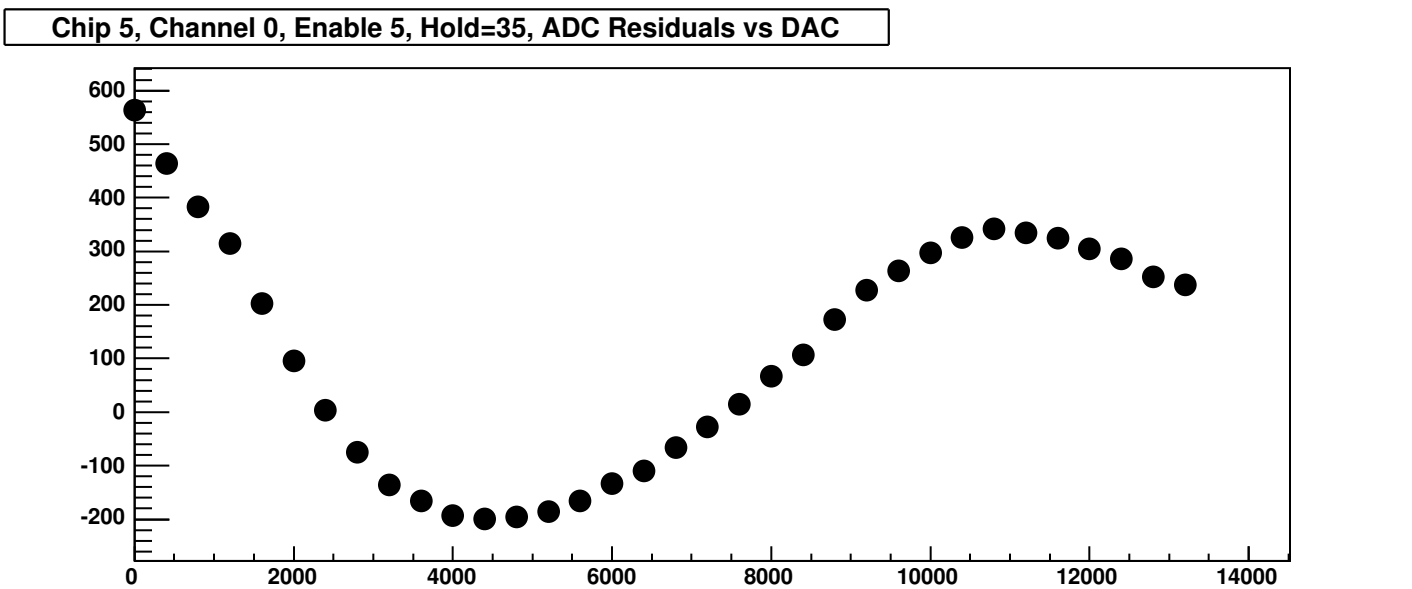

Chip 5, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 35.55 / 23
p0 -263.6 ± 0.7418
p1 0.0335 ± 0.0001123
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

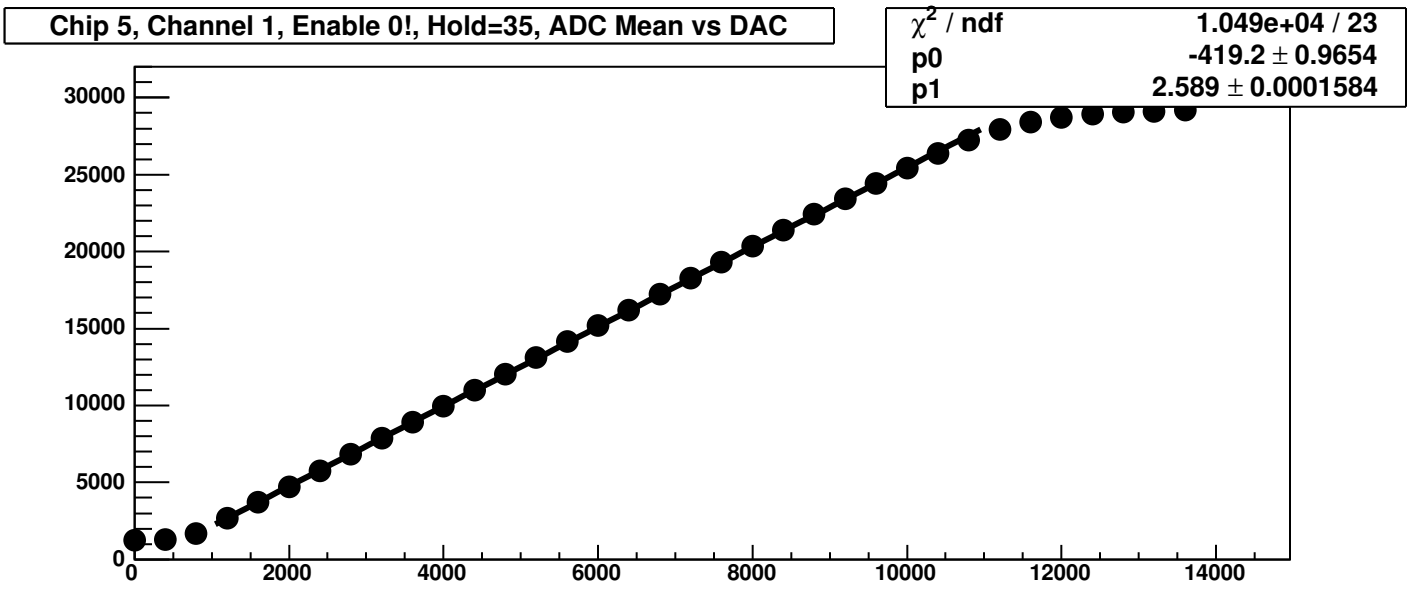

Chip 5, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

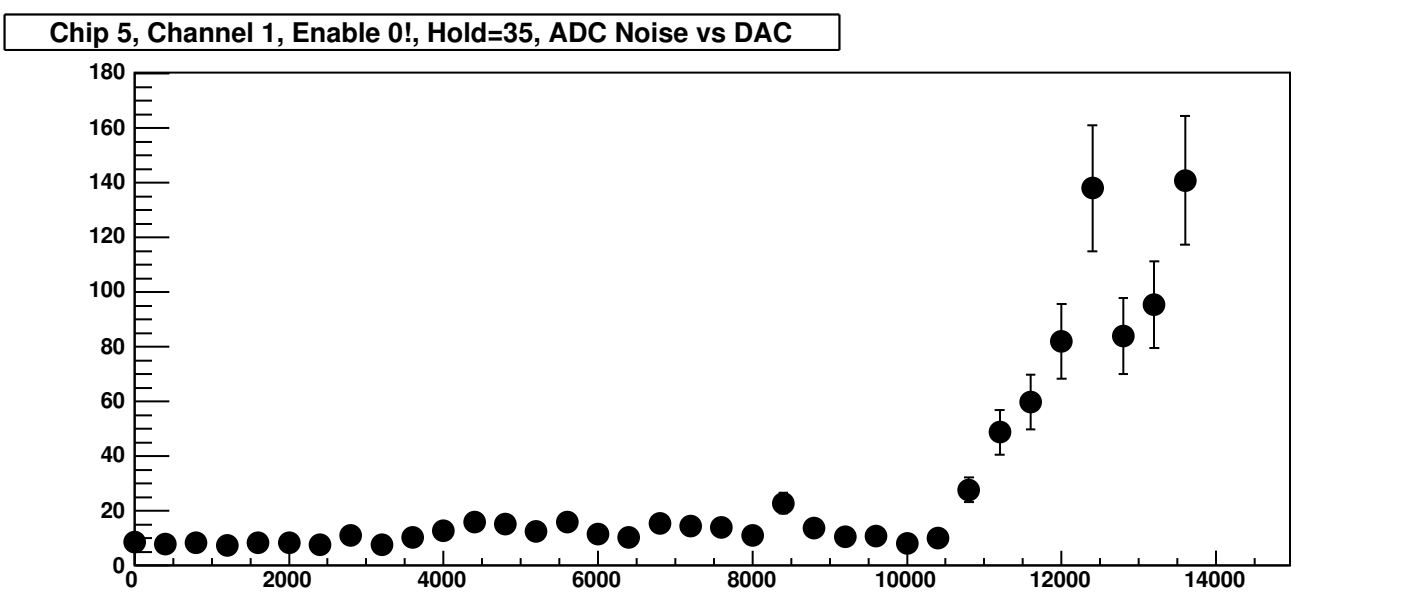

Chip 5, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

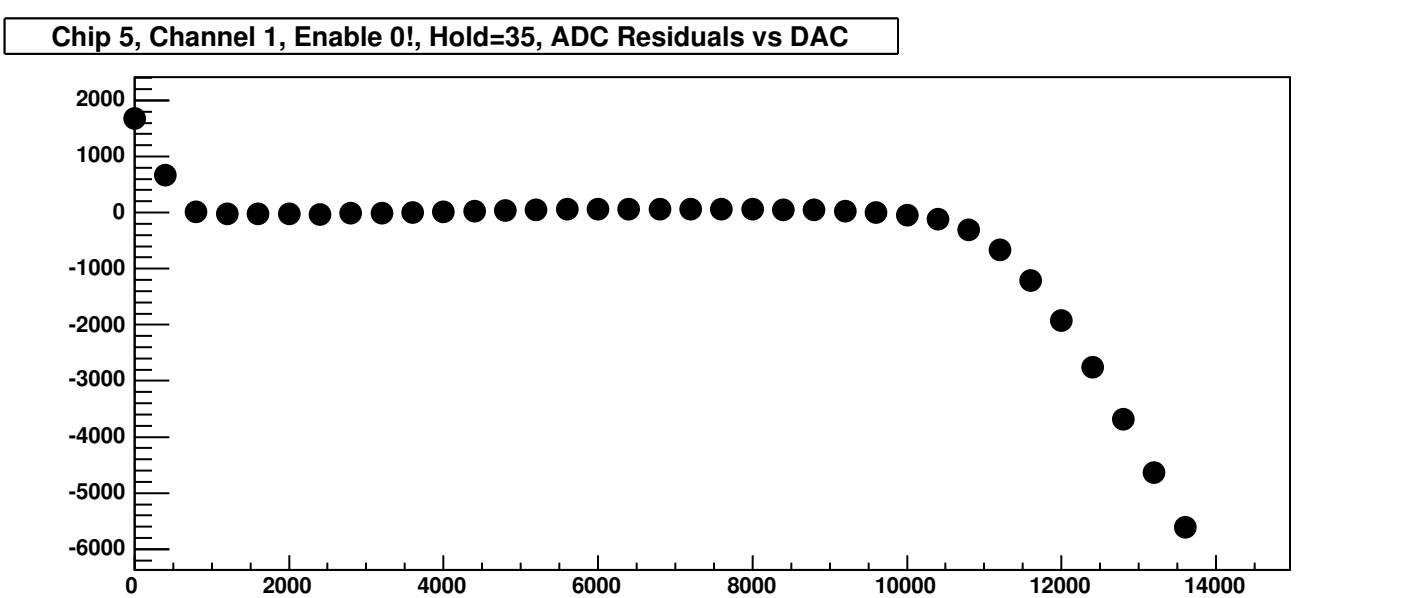

Chip 5, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.244e+05 / 23
p0 -748 ± 0.9781
p1 0.2524 ± 0.000192
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

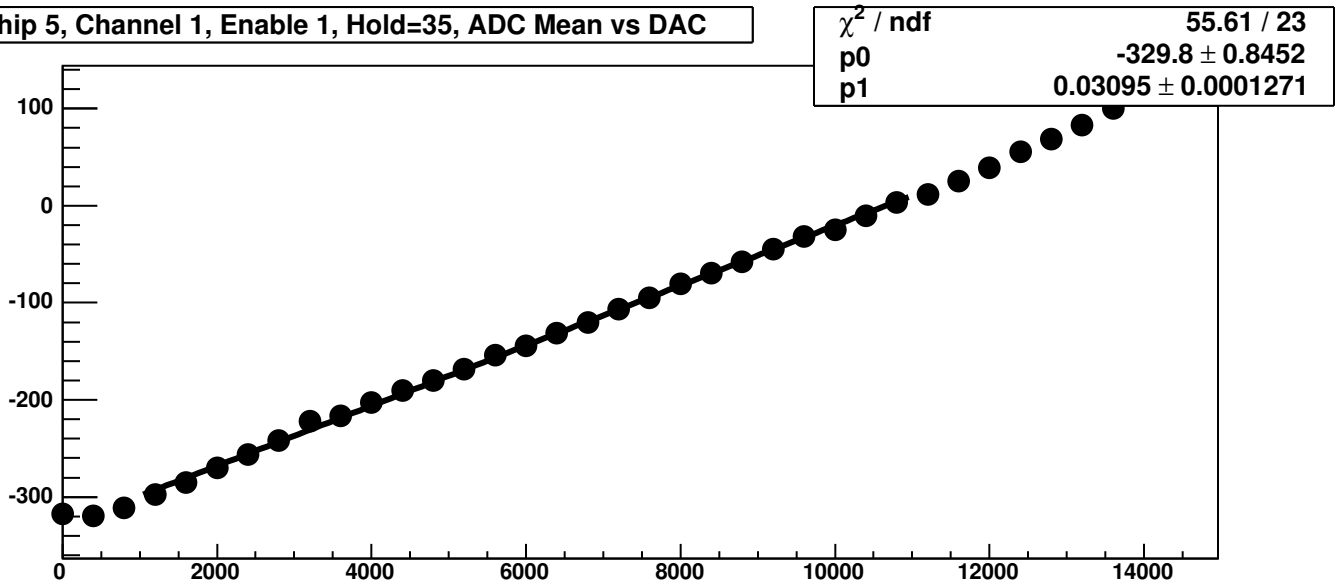

Chip 5, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1.049e+04 / 23
p0 -419.2 ± 0.9654
p1 2.589 ± 0.0001584
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

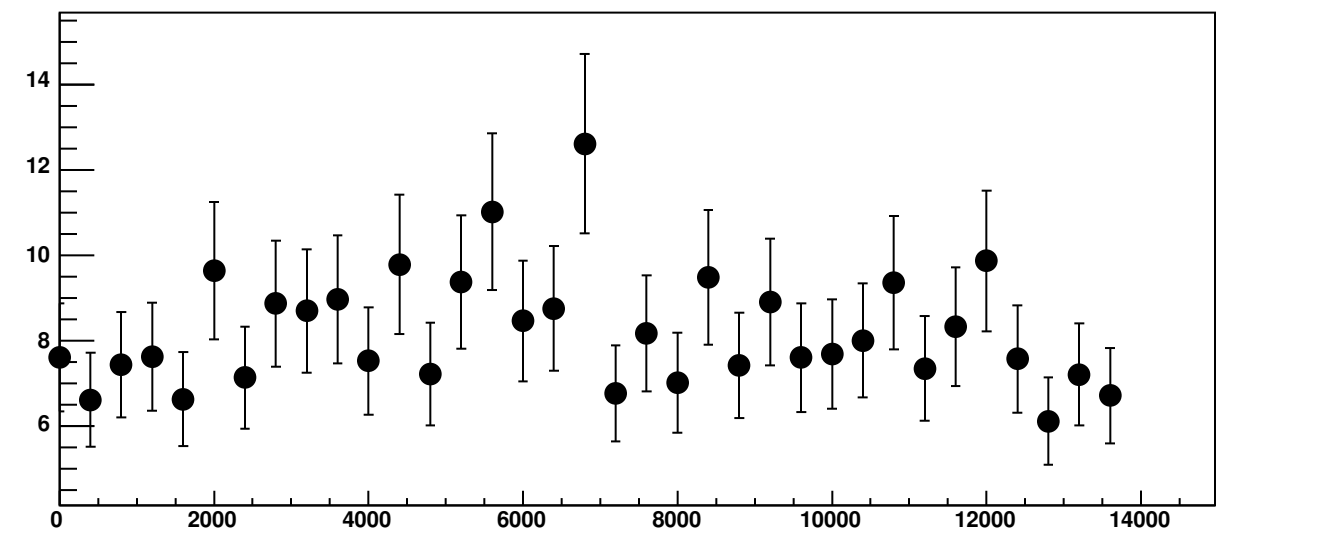

Chip 5, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
2000
4000
6000
8000
10000
12000
14000

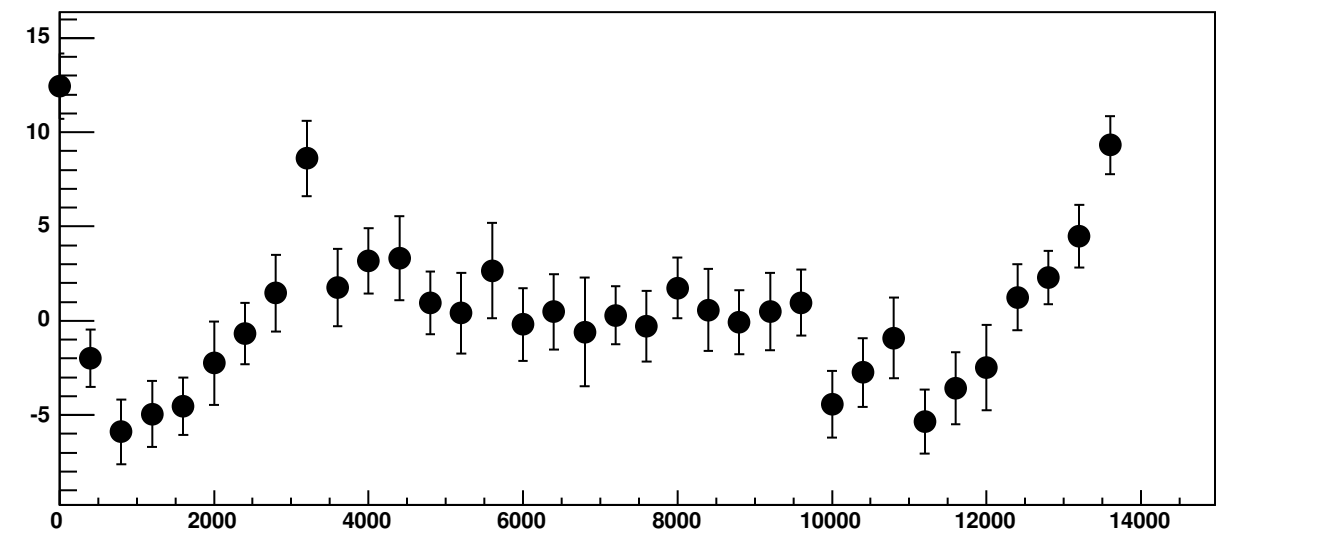
Chip 5, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



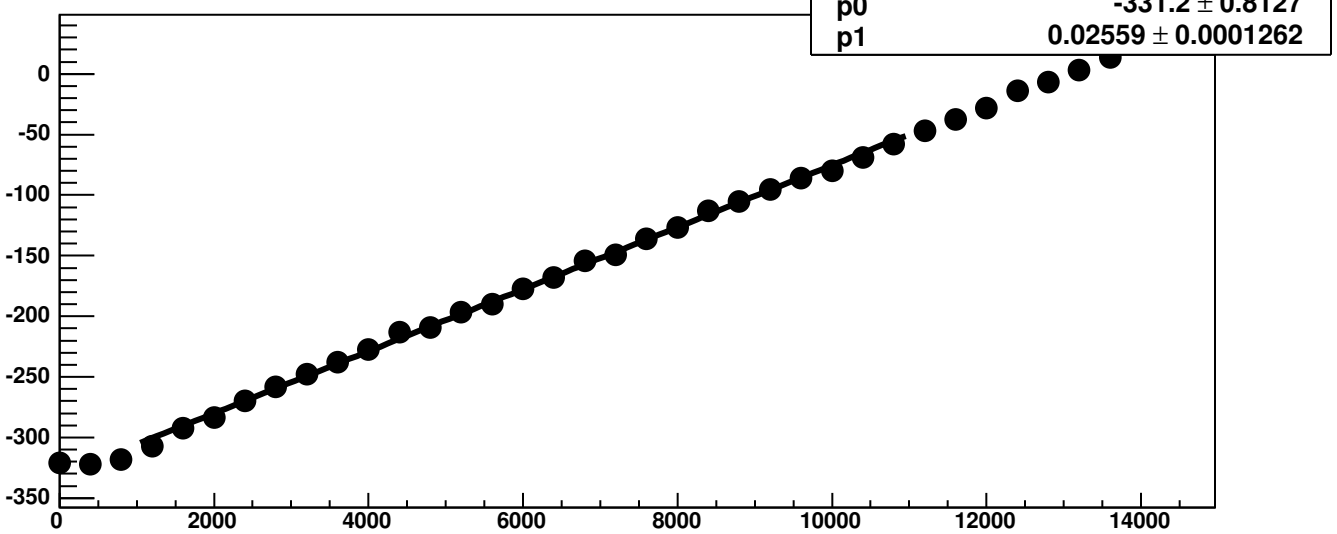
Chip 5, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



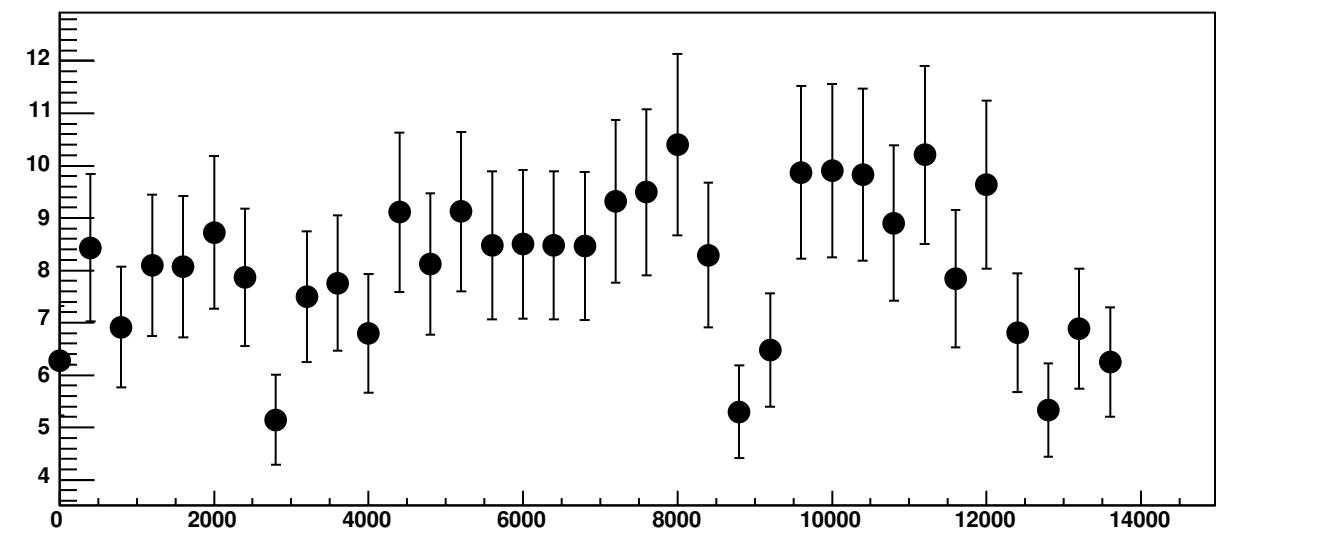
Chip 5, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



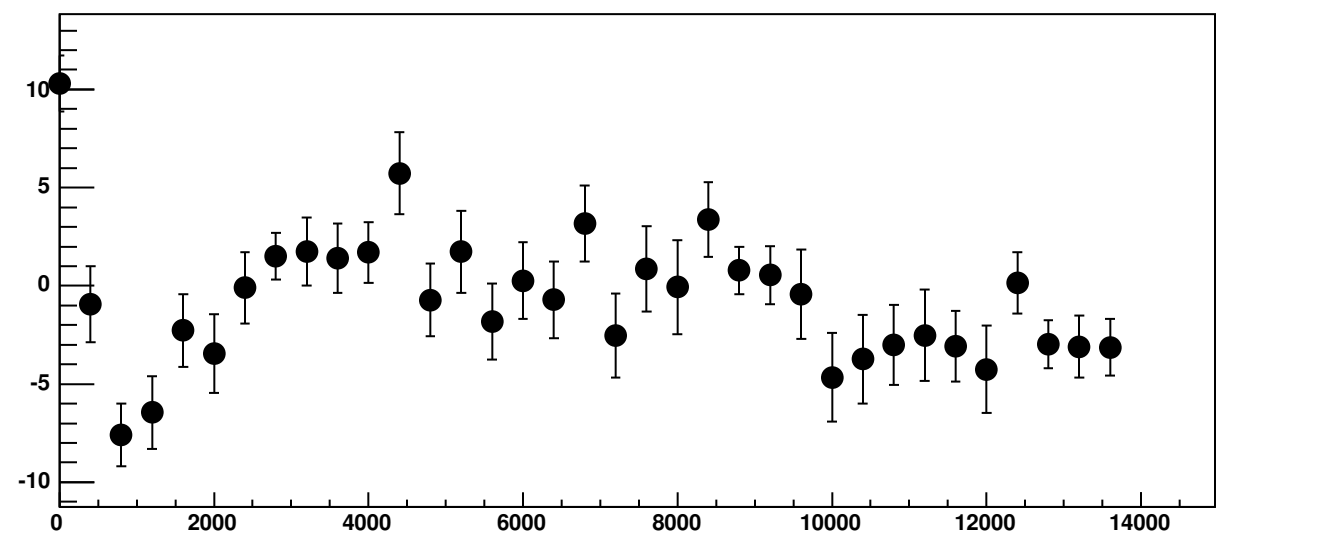
Chip 5, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

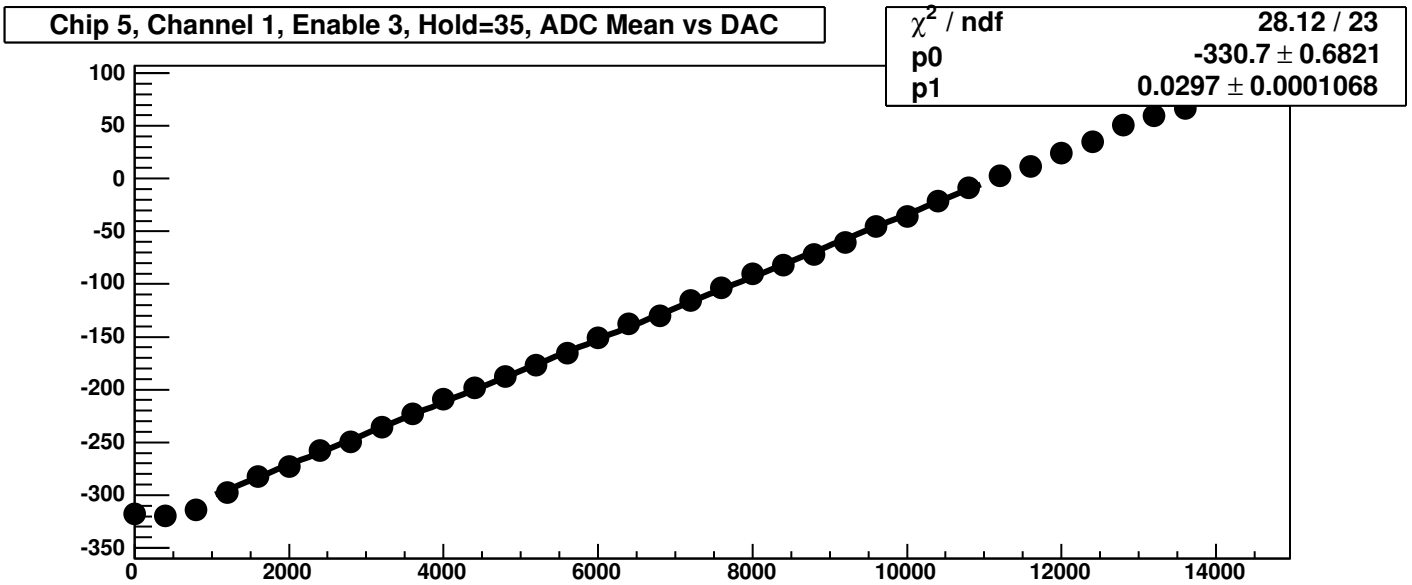


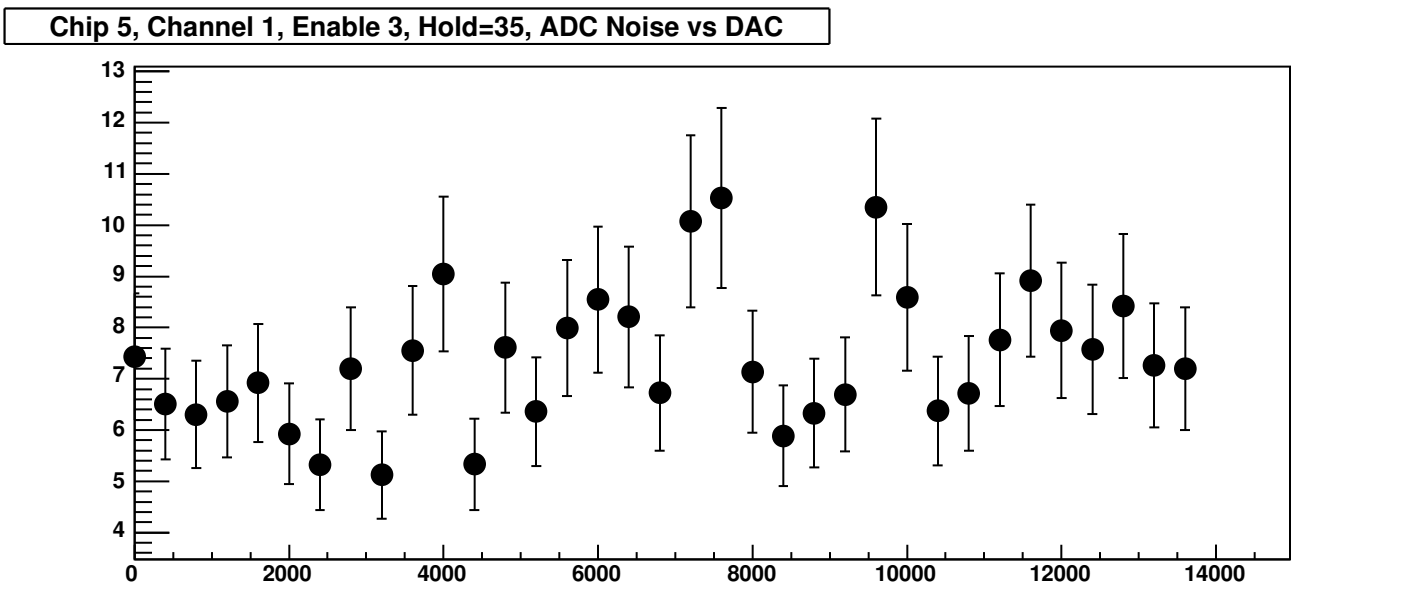
Chip 5, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC

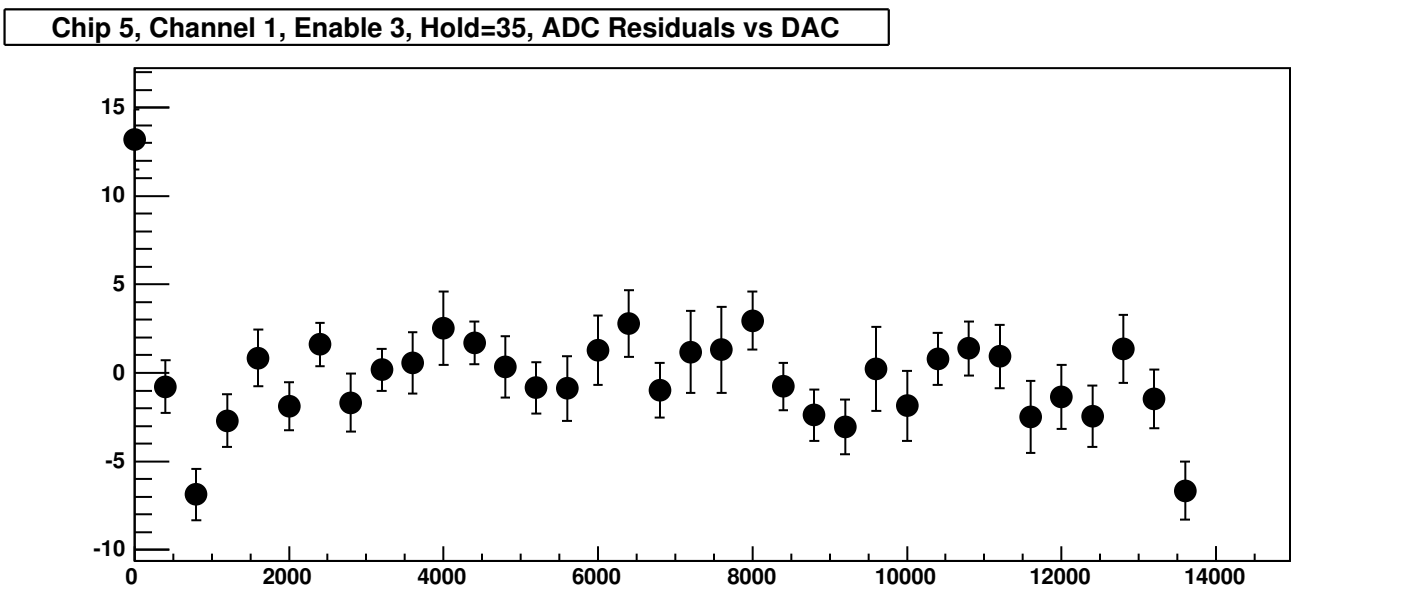


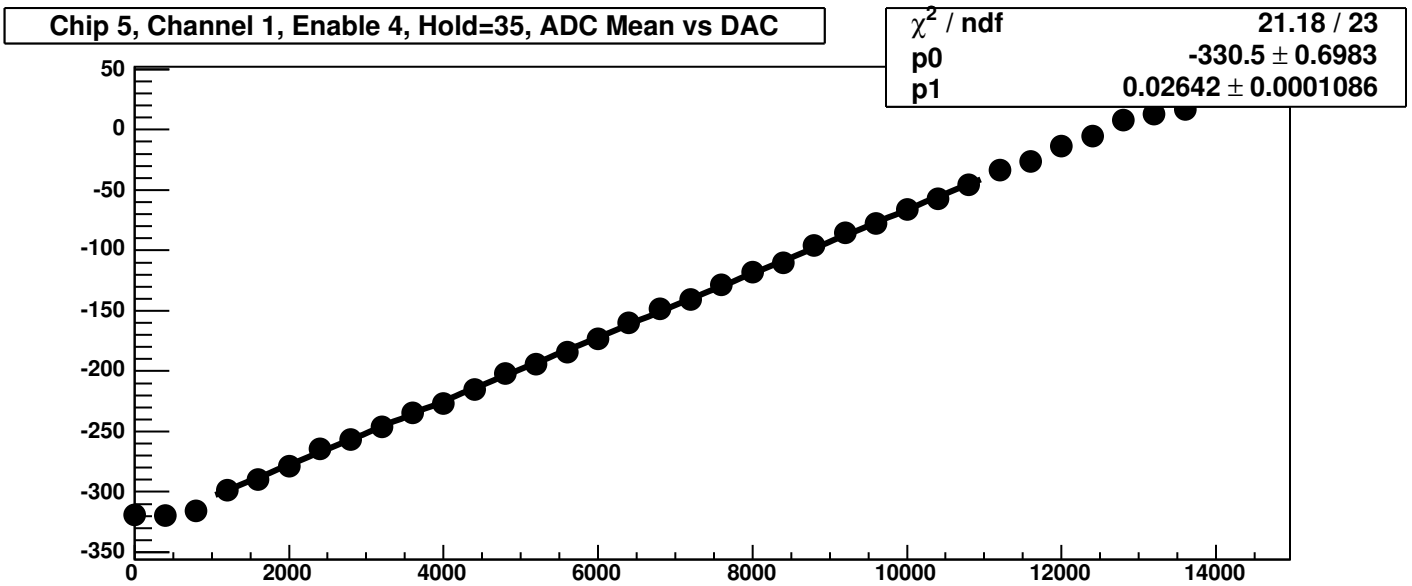
Chip 5, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

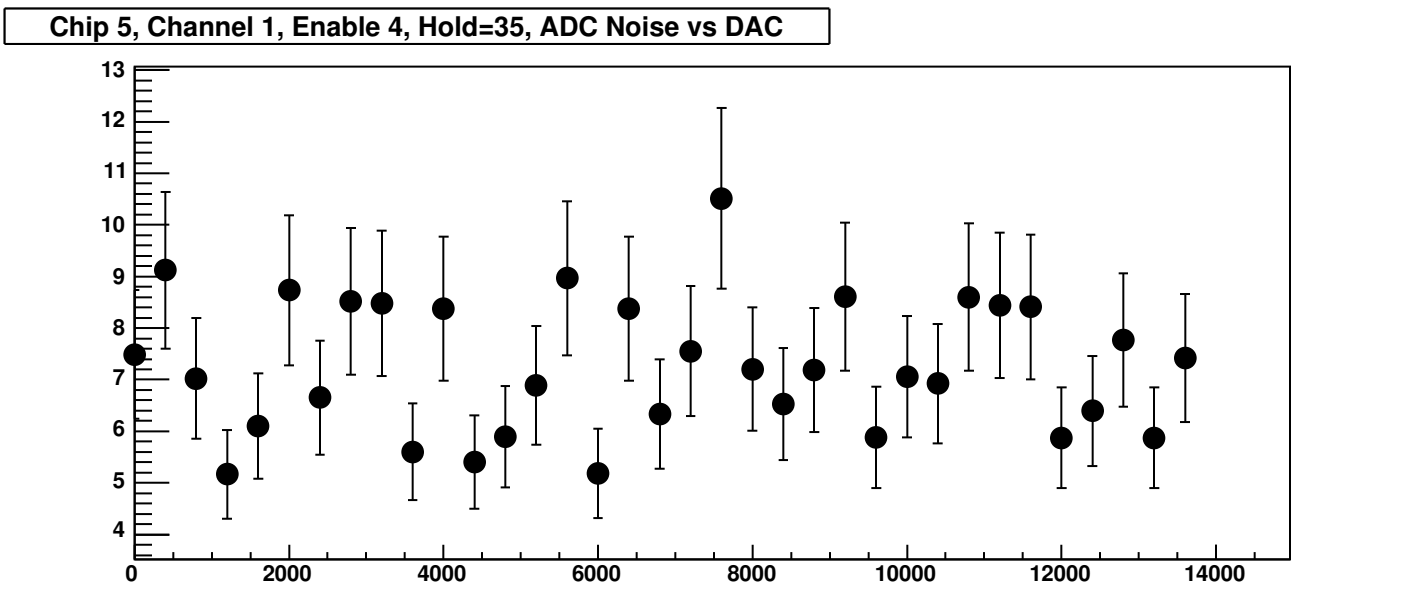


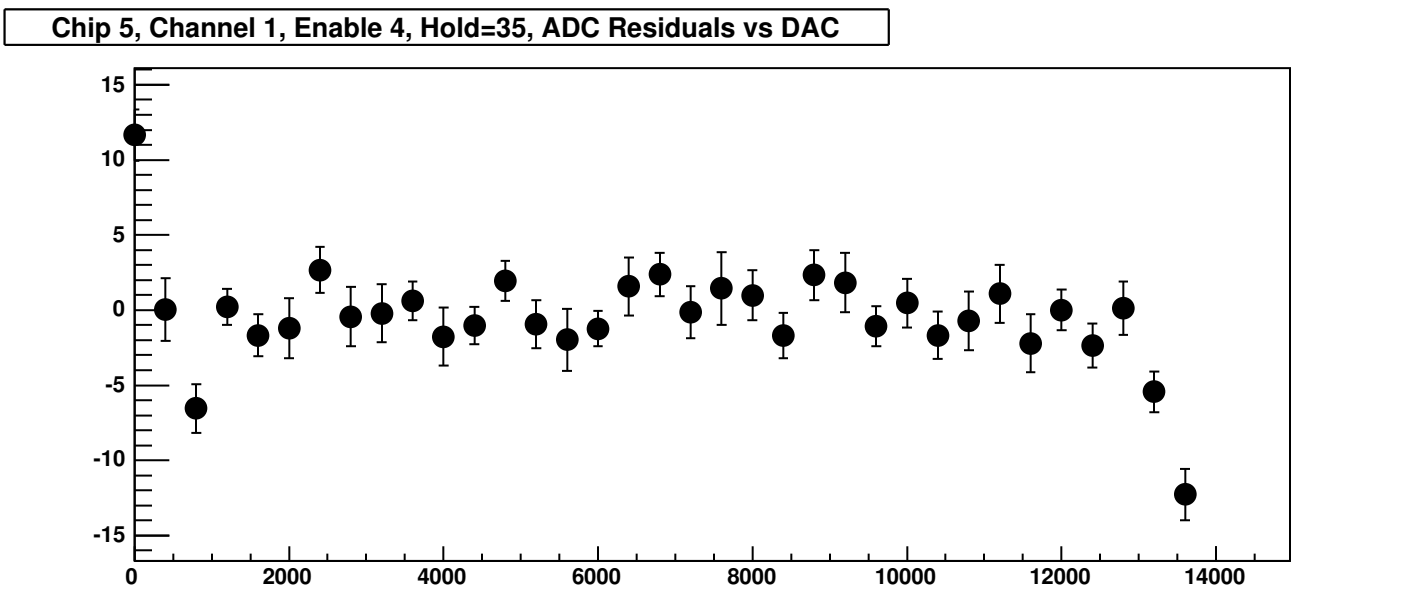

Chip 5, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.12 / 23
p0 -330.7 ± 0.6821
p1 0.0297 ± 0.0001068
100
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

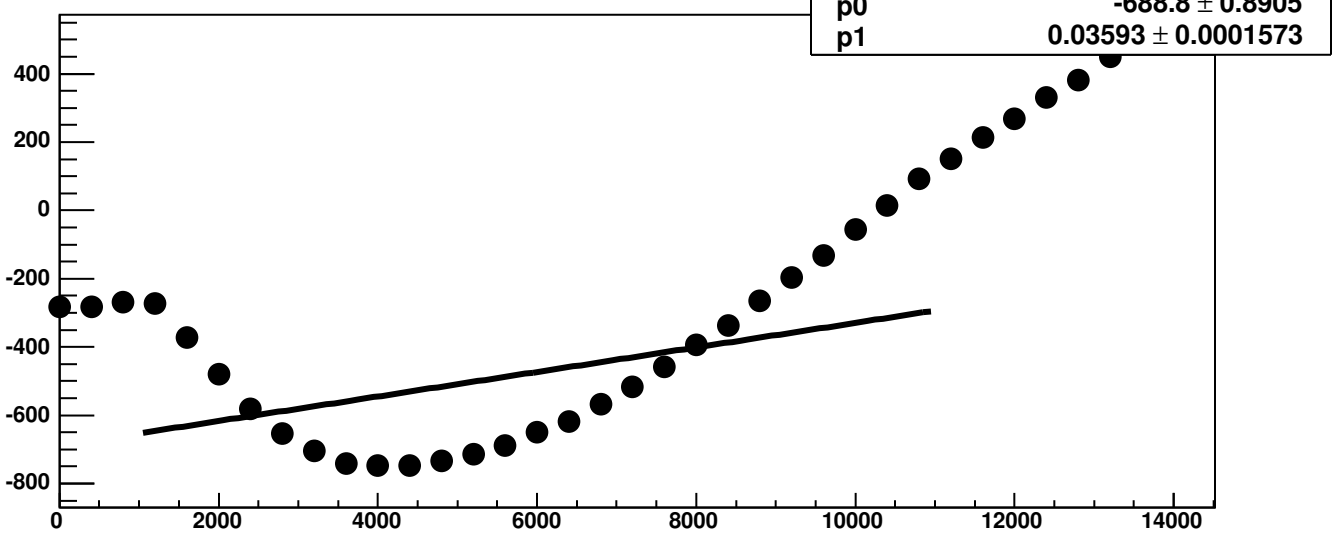

Chip 5, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 21.18 / 23
p0 -330.5 ± 0.6983
p1 0.02642 ± 0.0001086
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

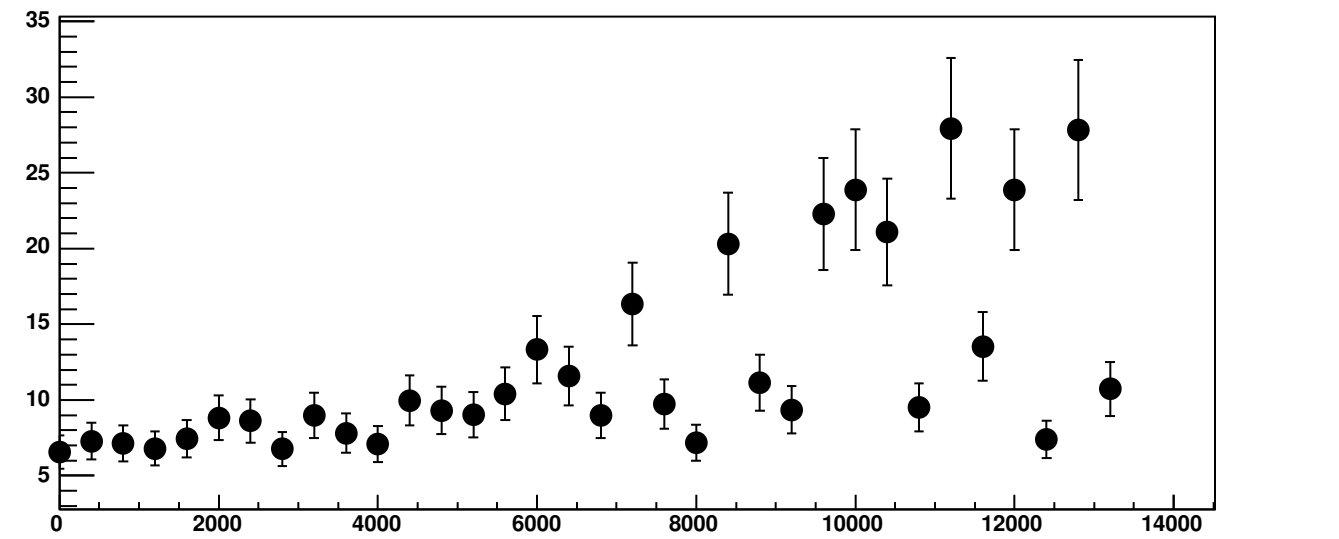

Chip 5, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

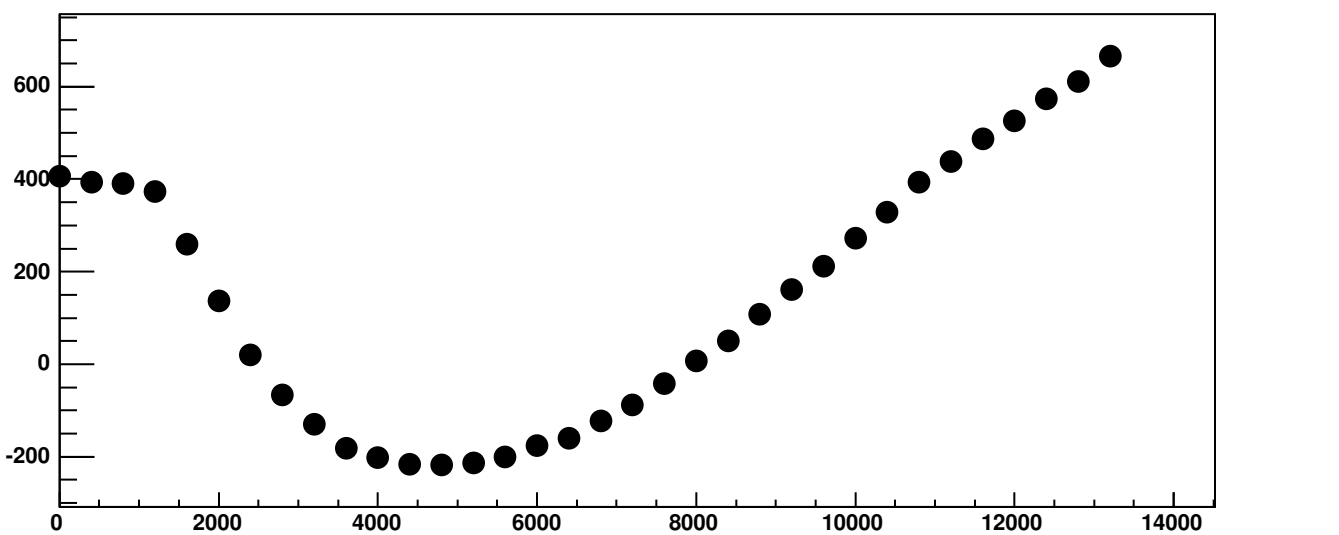
Chip 5, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

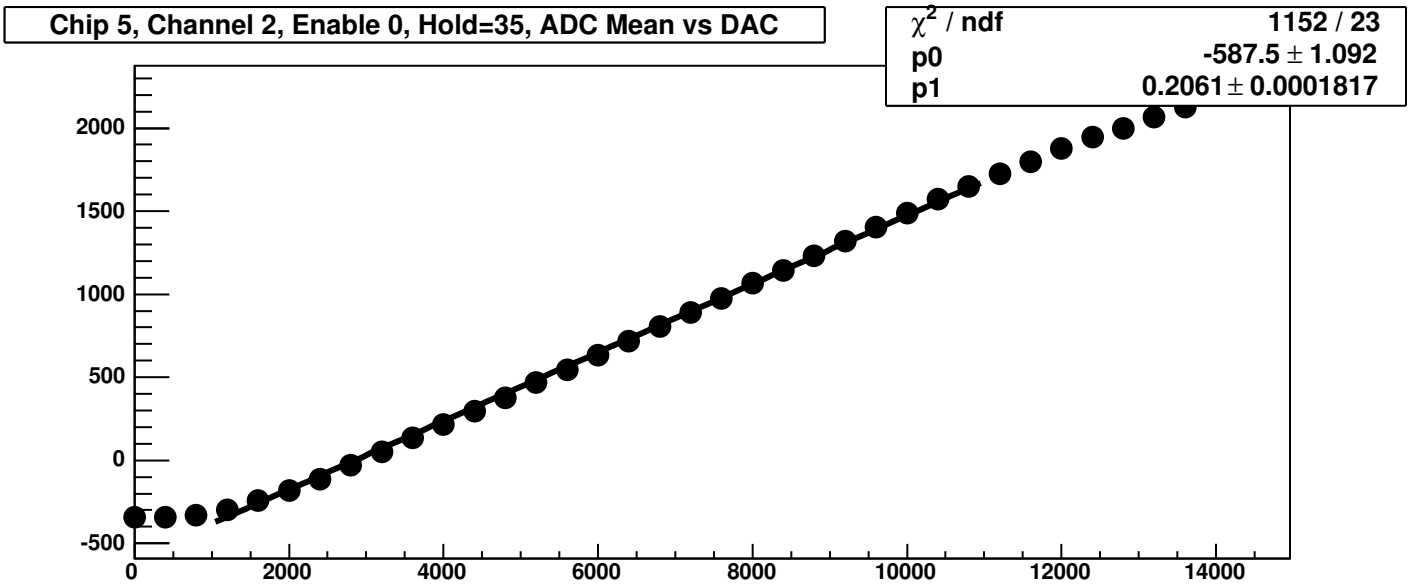


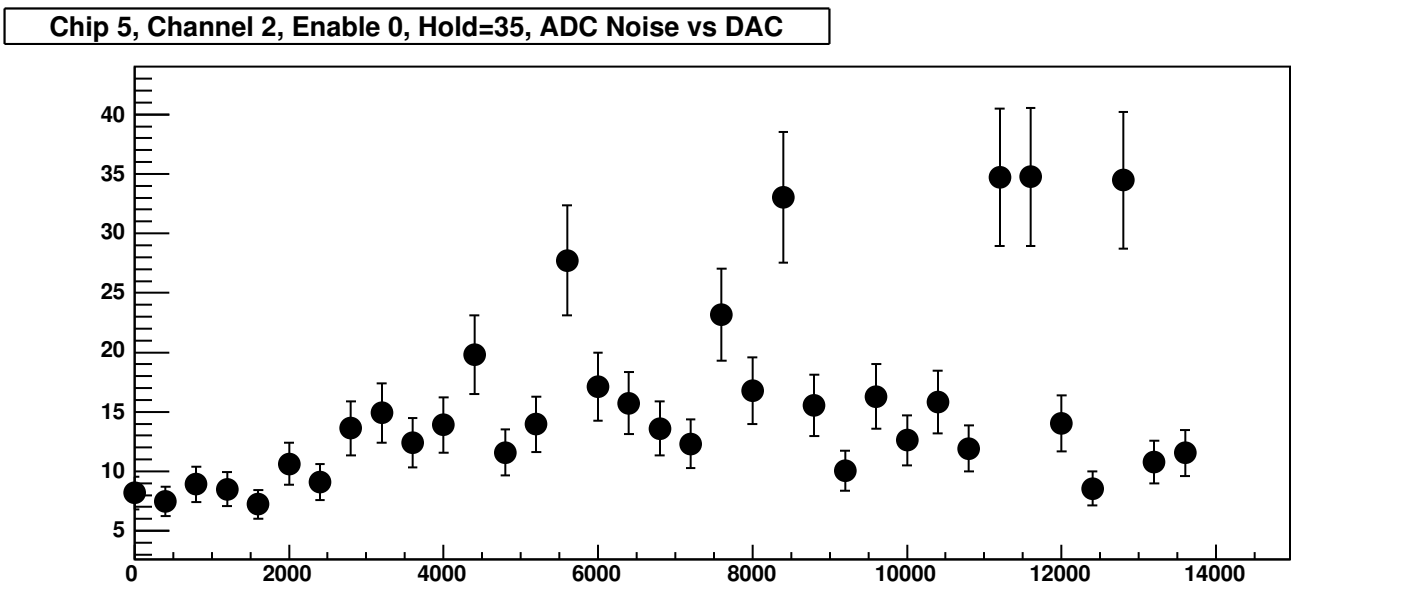
Chip 5, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC

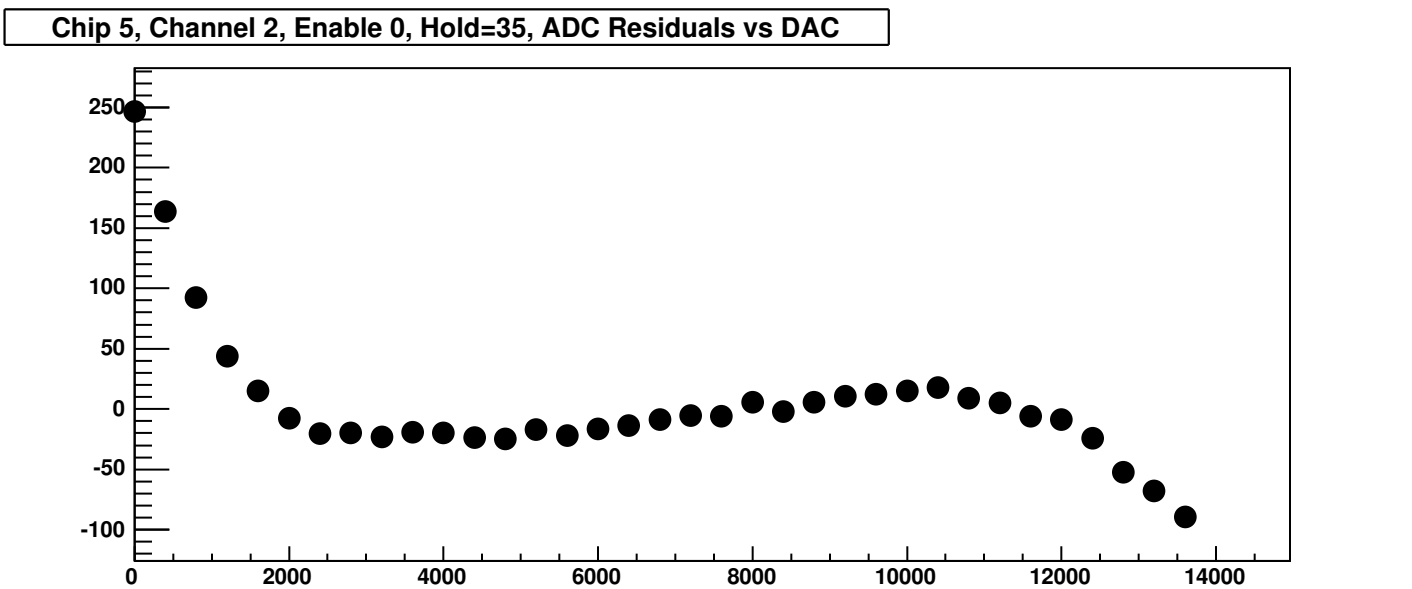


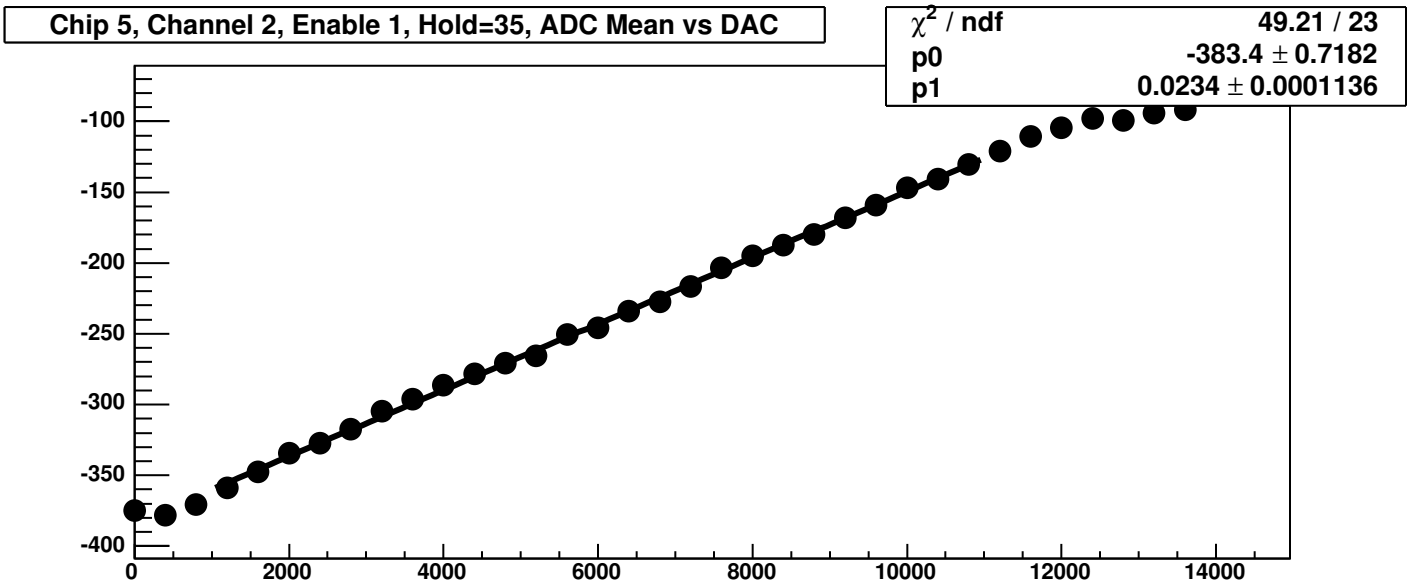
Chip 5, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

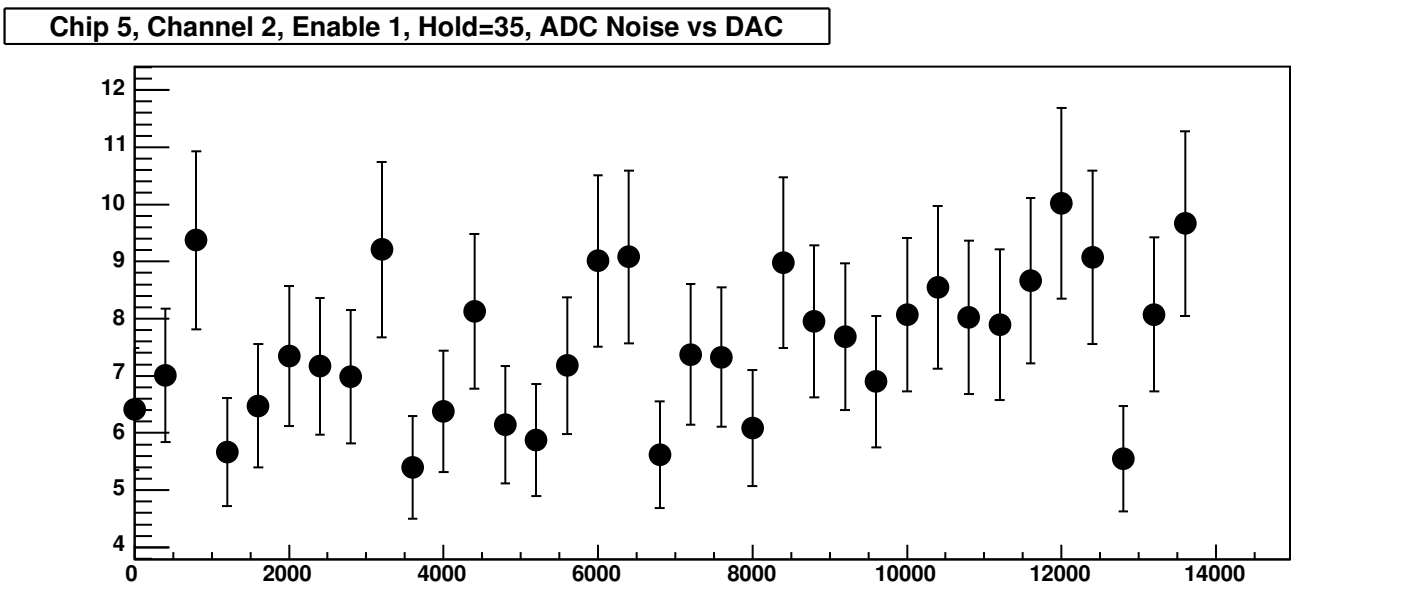


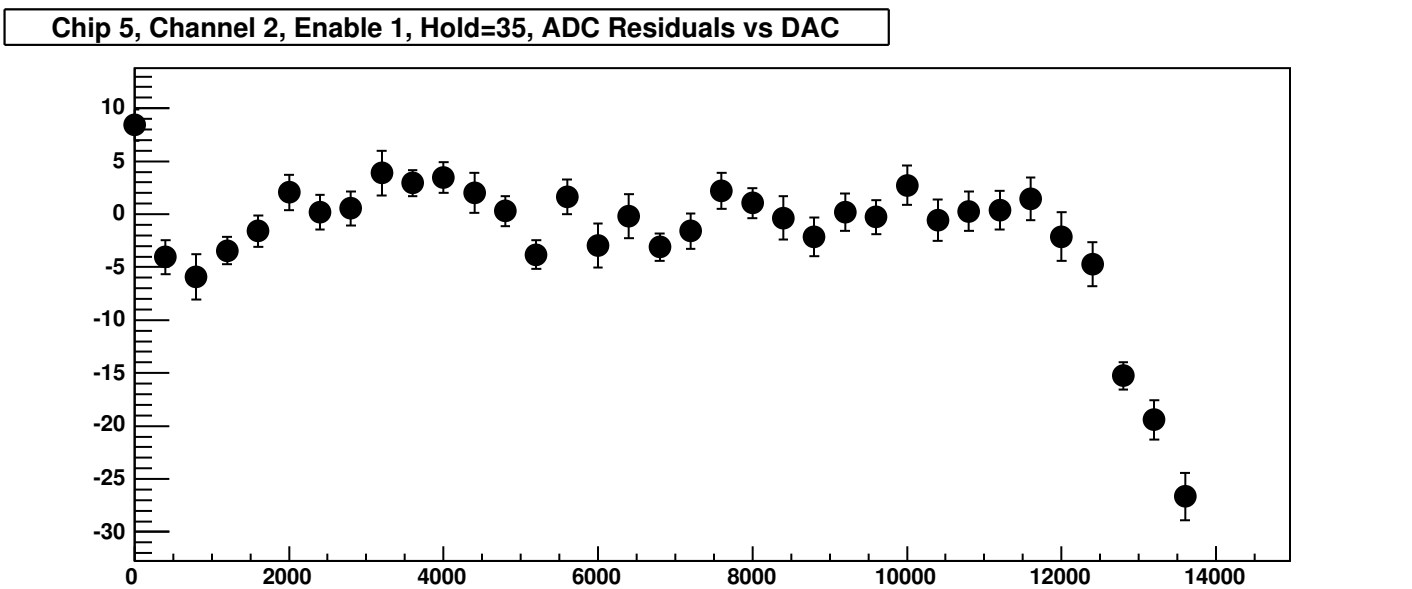

Chip 5, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1152 / 23
p0 -587.5 ± 1.092
p1 0.2061 ± 0.0001817
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

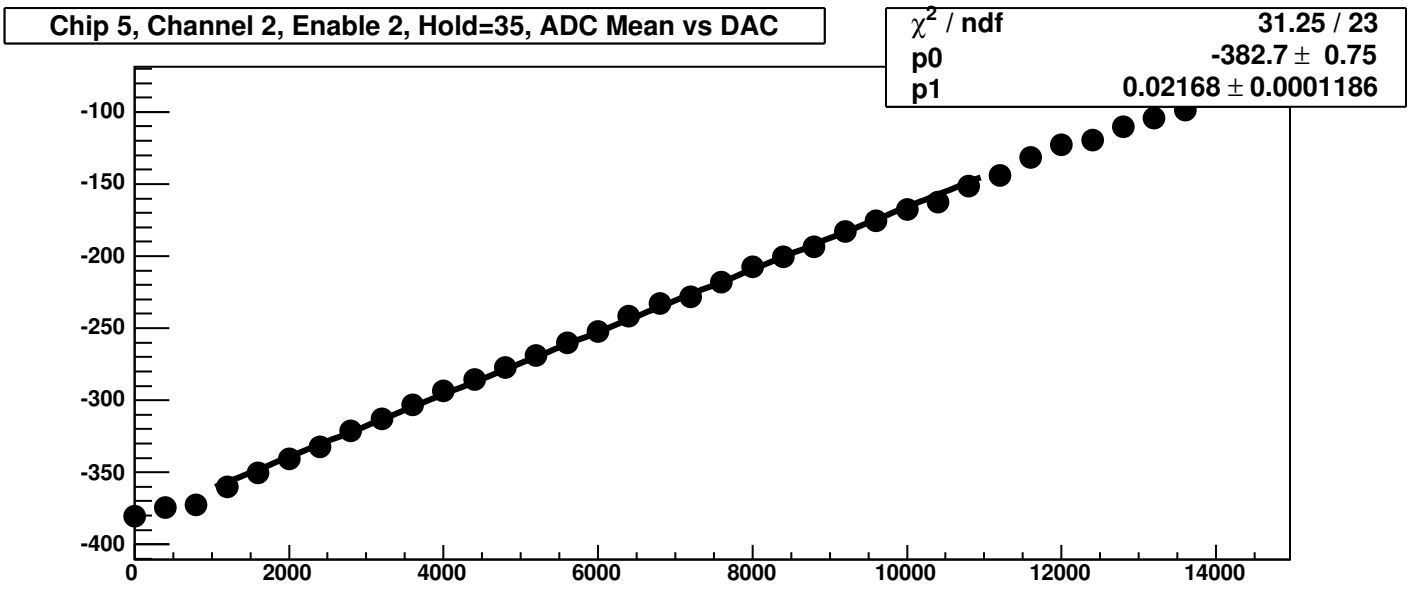

Chip 5, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

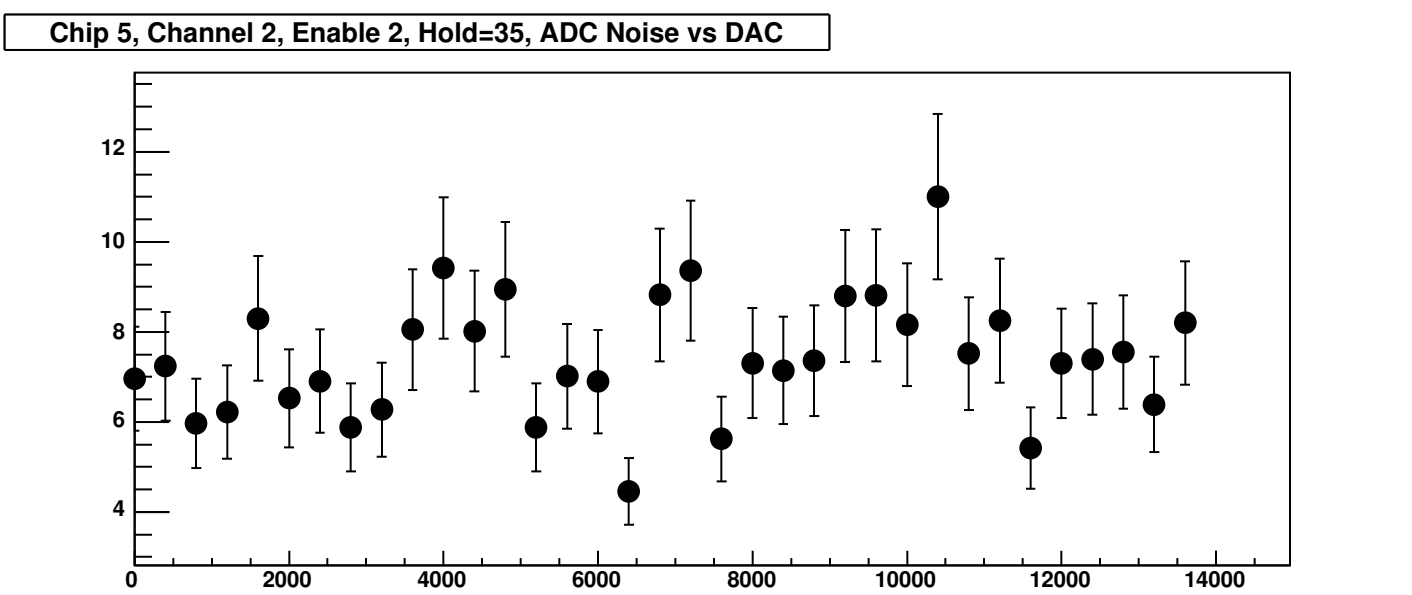

Chip 5, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

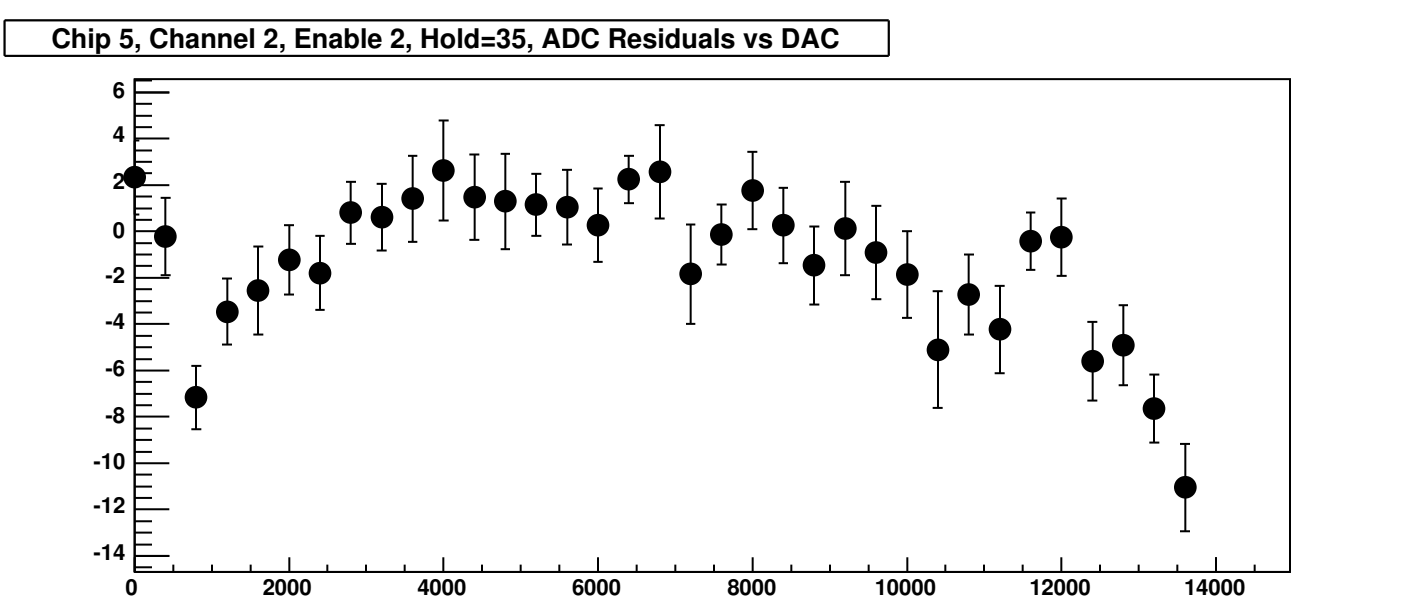

Chip 5, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 49.21 / 23
p0 -383.4 ± 0.7182
p1 0.0234 ± 0.0001136
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

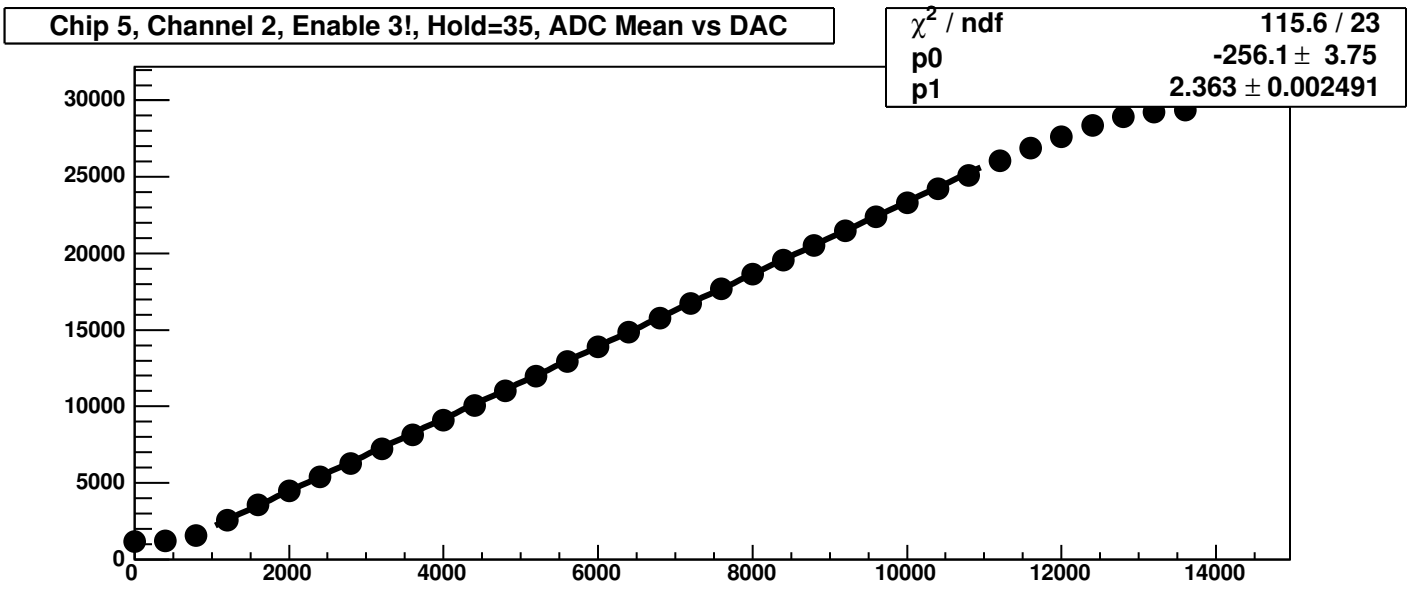

Chip 5, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

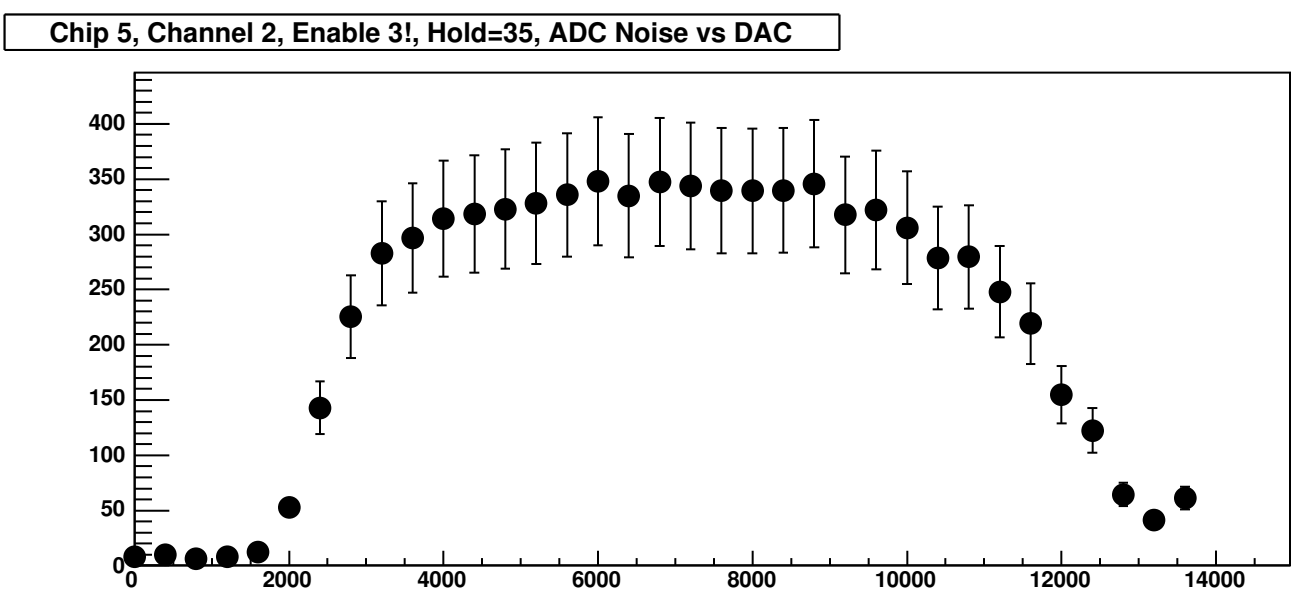

Chip 5, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
0
2000
4000
6000
8000
10000
12000
14000

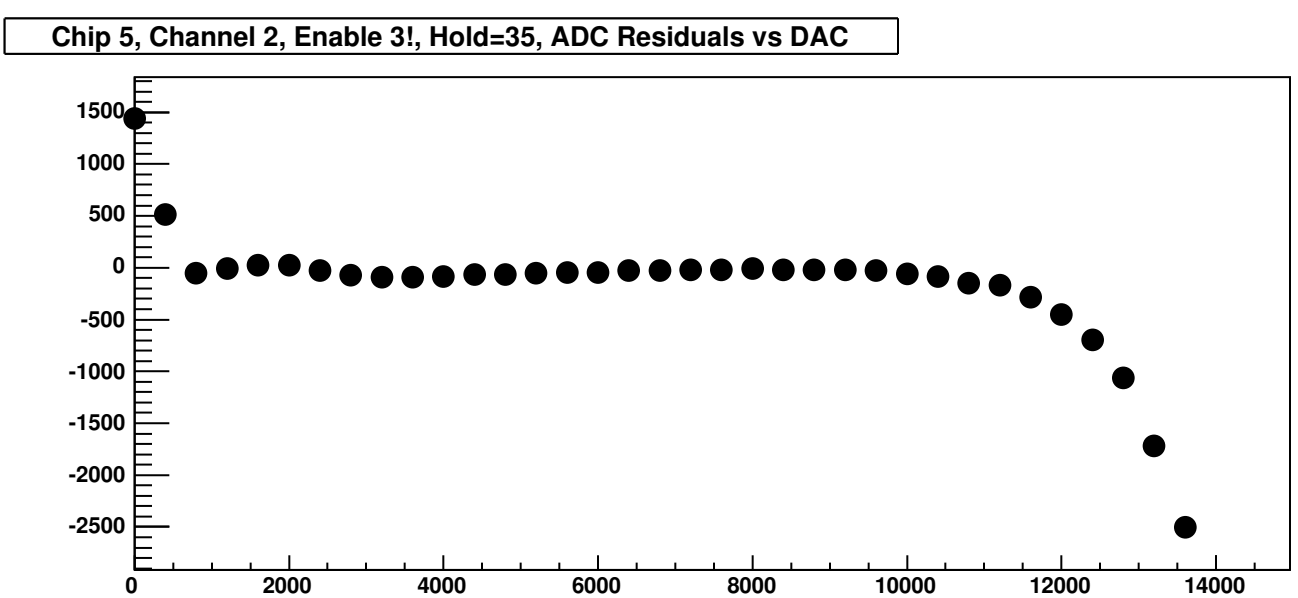

Chip 5, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 31.25 / 23
p0 -382.7 ± 0.75
p1 0.02168 ± 0.0001186
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

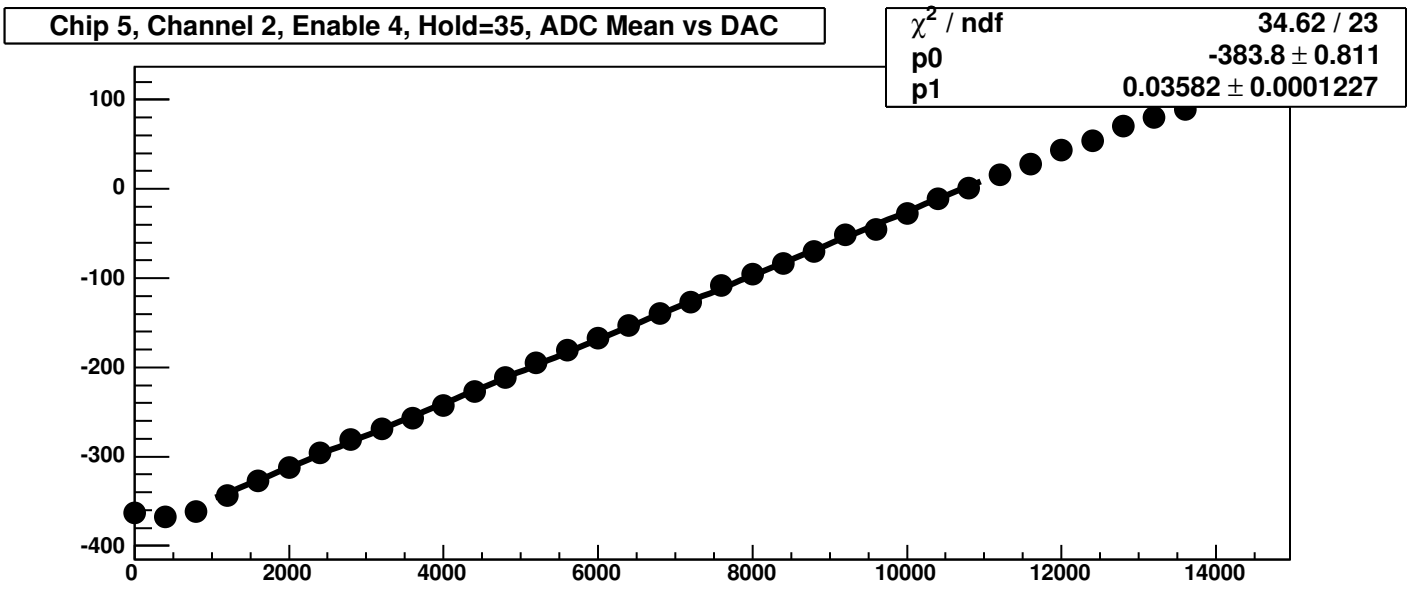

Chip 5, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

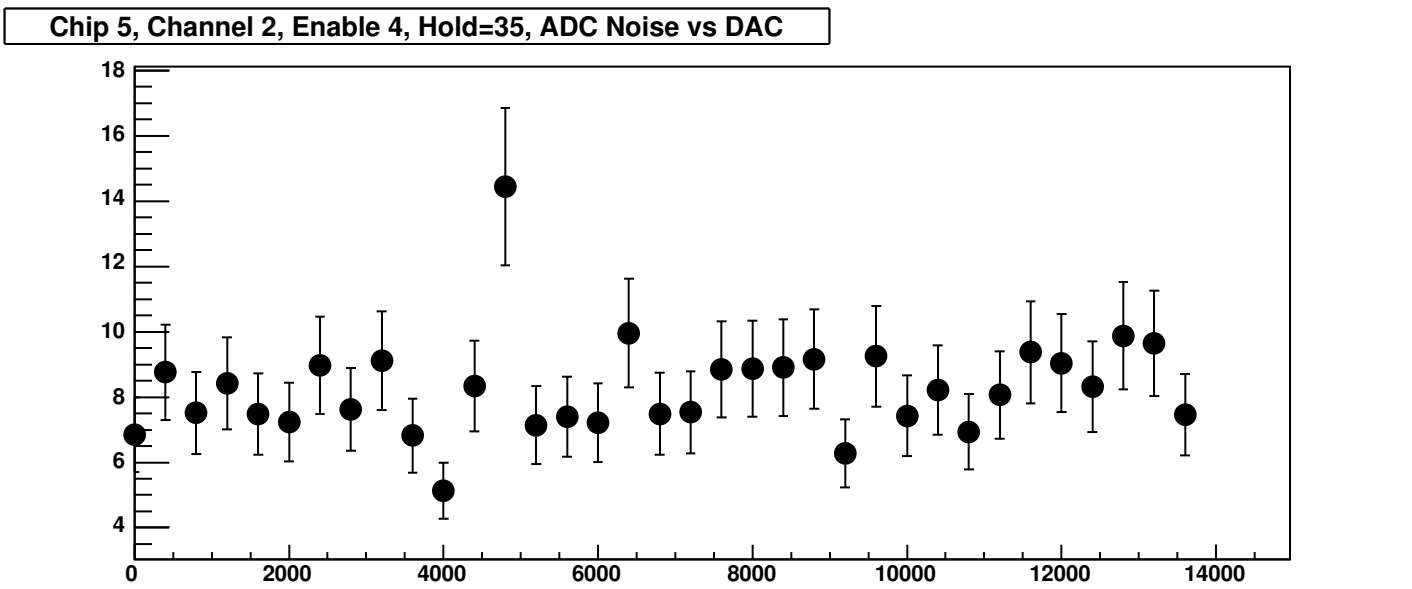

Chip 5, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
-14
0
2000
4000
6000
8000
10000
12000
14000

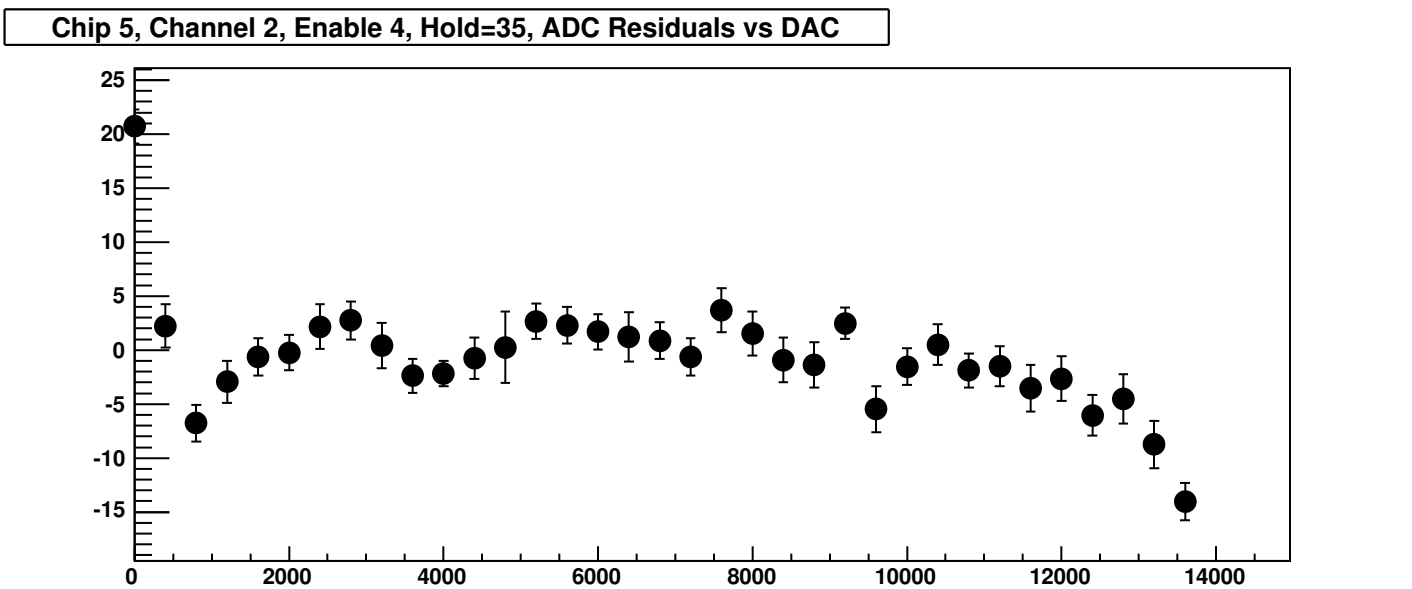

Chip 5, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 115.6 / 23
p0 -256.1 ± 3.75
p1 2.363 ± 0.002491
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

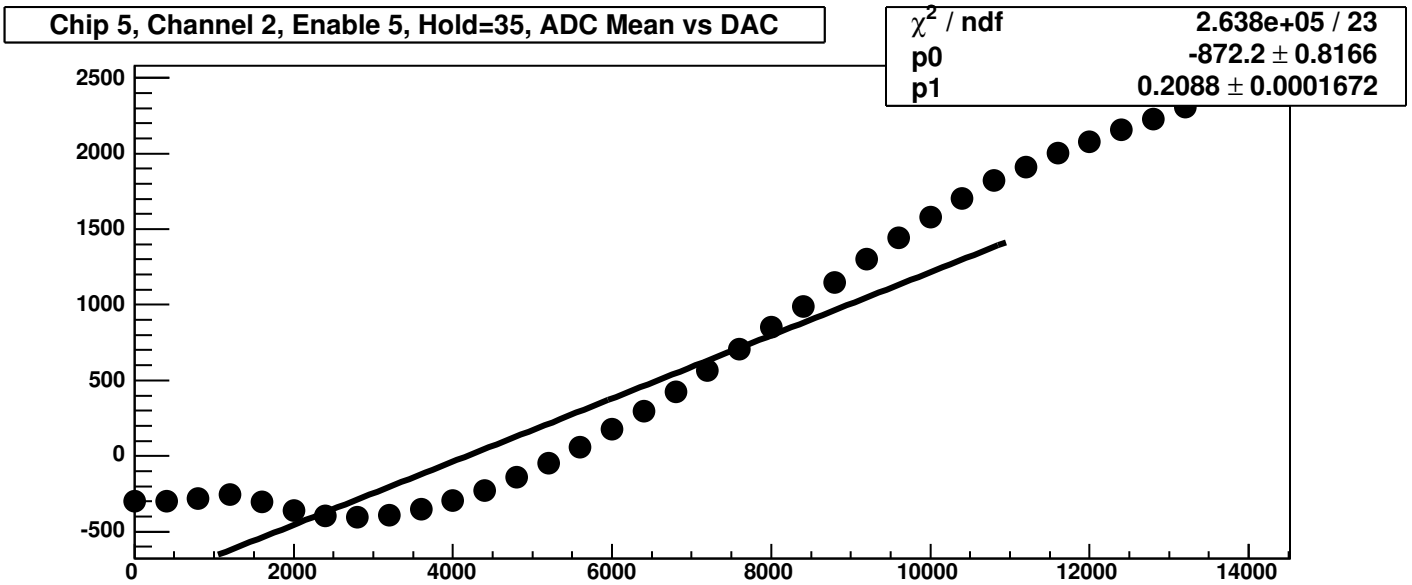

Chip 5, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

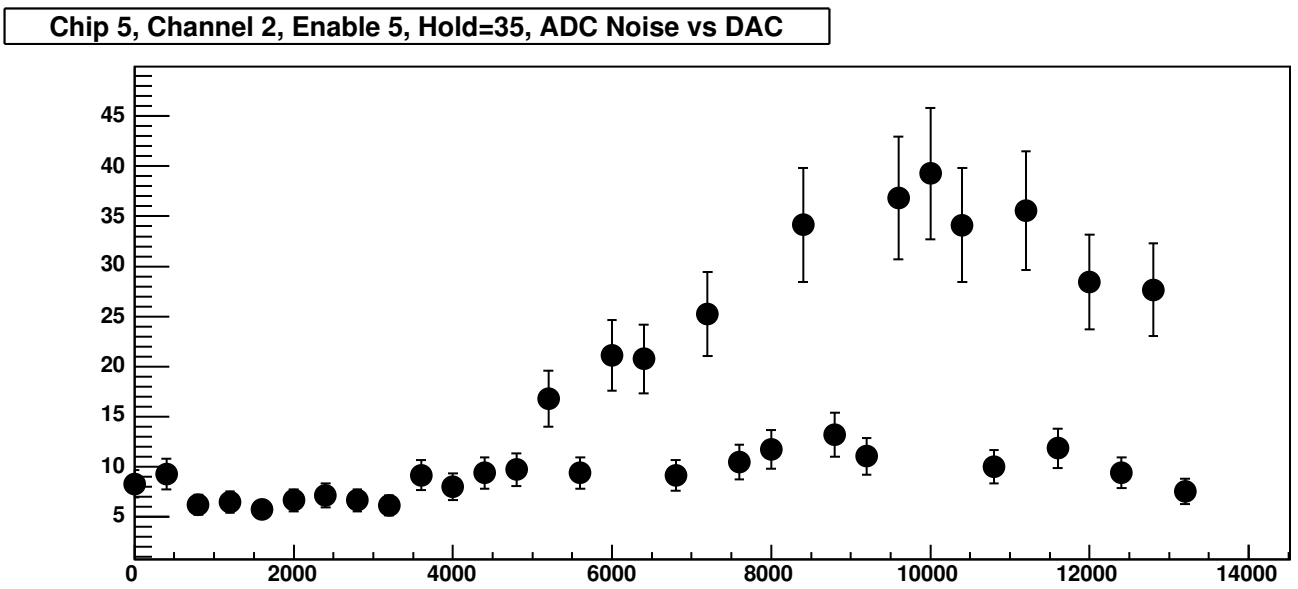

Chip 5, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

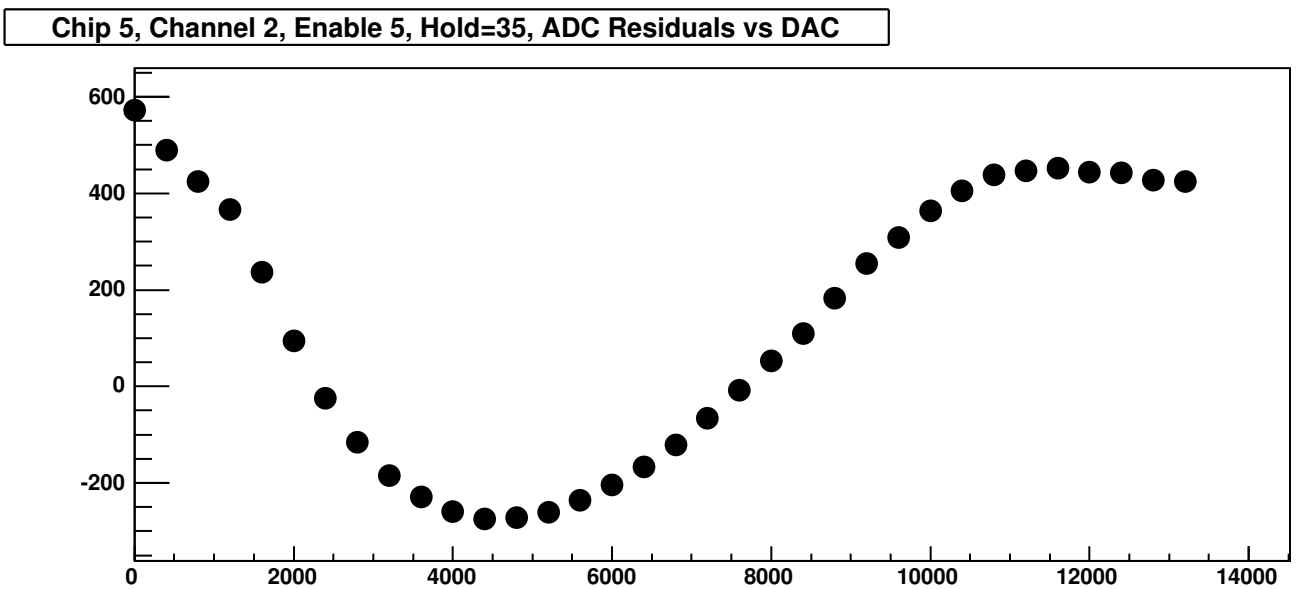

Chip 5, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 34.62 / 23
p0 -383.8 ± 0.811
p1 0.03582 ± 0.0001227
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

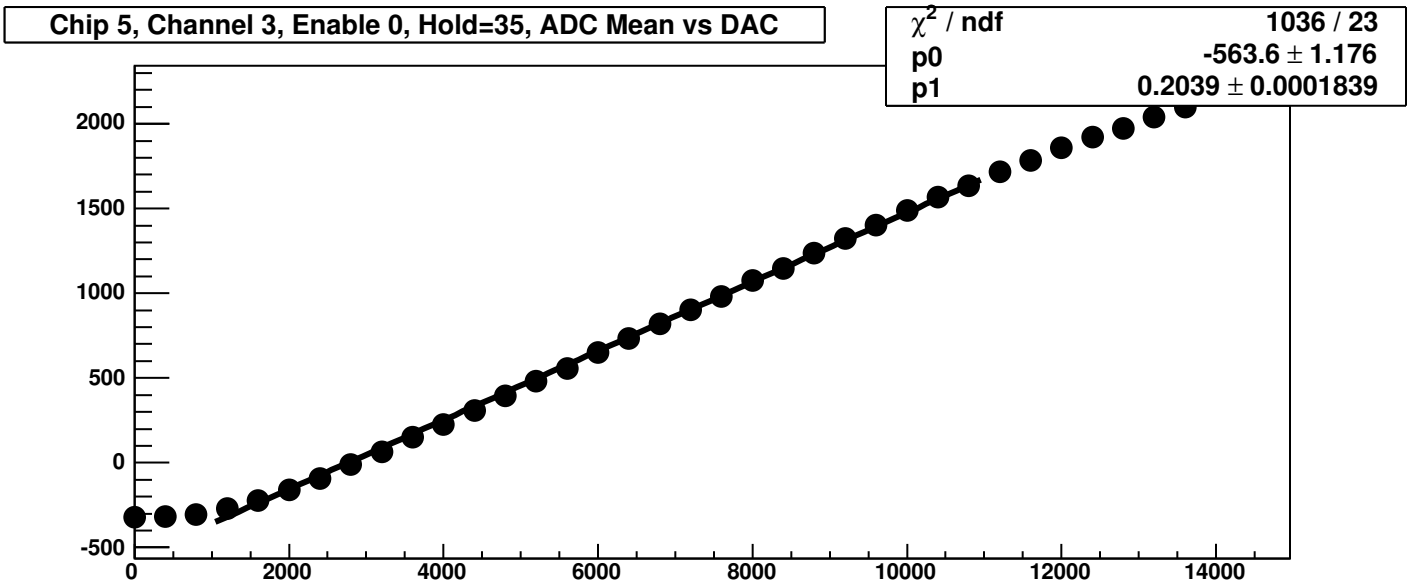

Chip 5, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

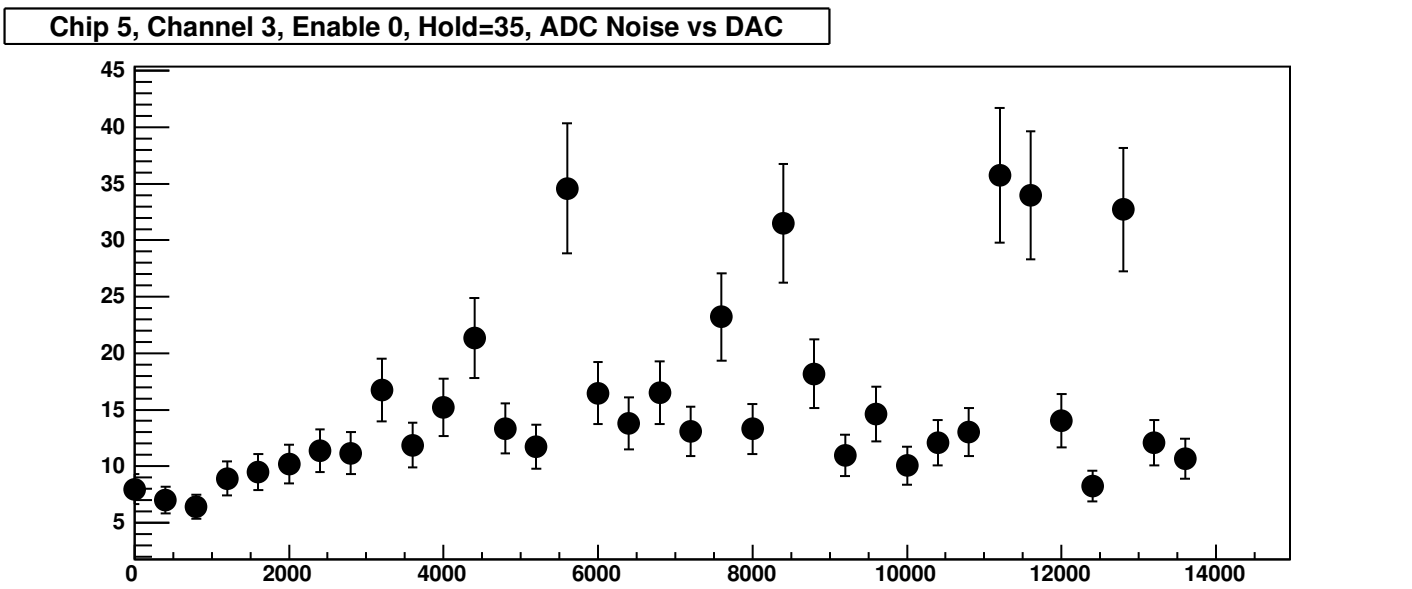

Chip 5, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

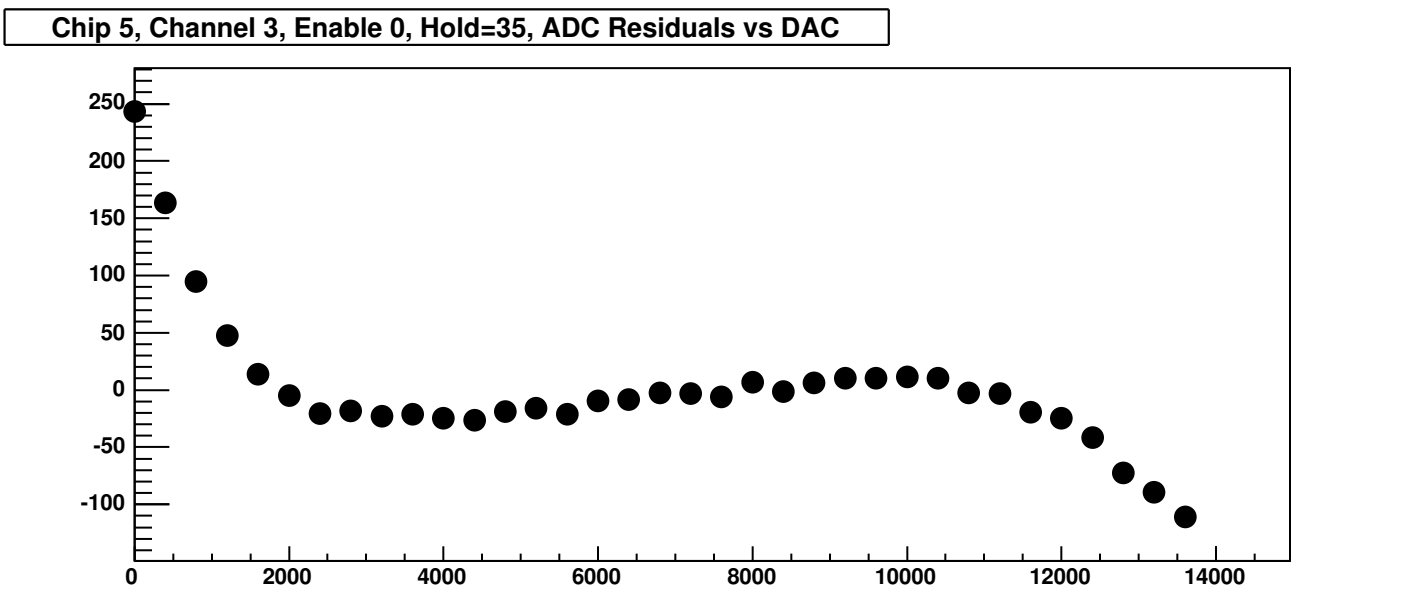

Chip 5, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2.638e+05 / 23
p0 -872.2 ± 0.8166
p1 0.2088 ± 0.0001672
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

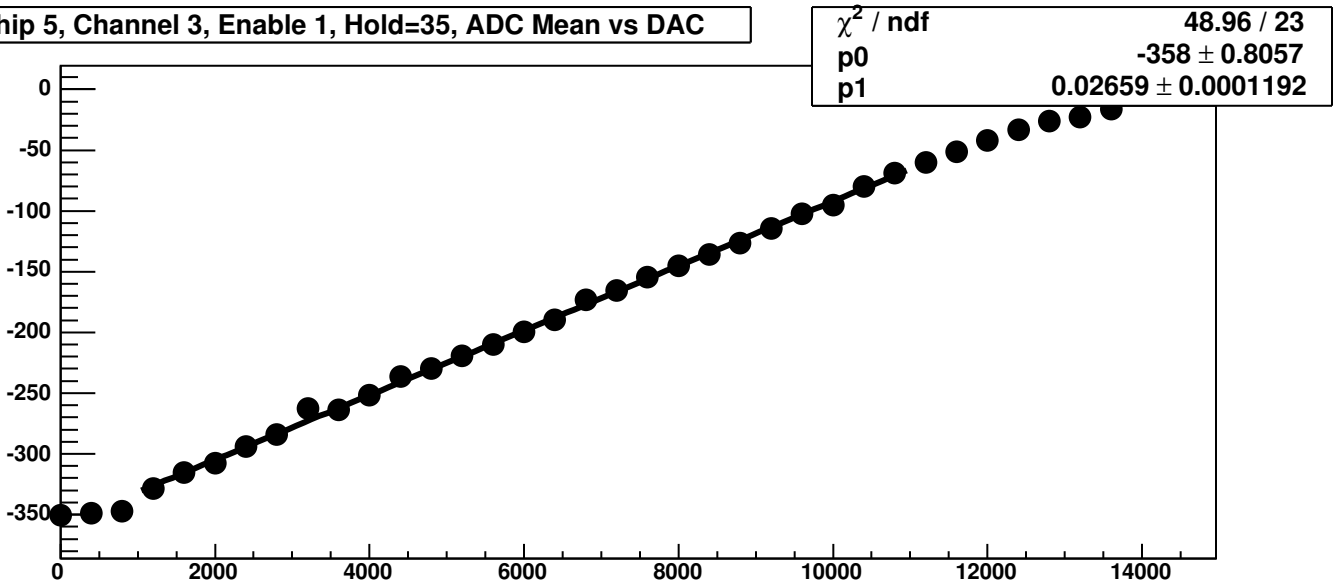

Chip 5, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1036 / 23
p0 -563.6 ± 1.176
p1 0.2039 ± 0.0001839
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

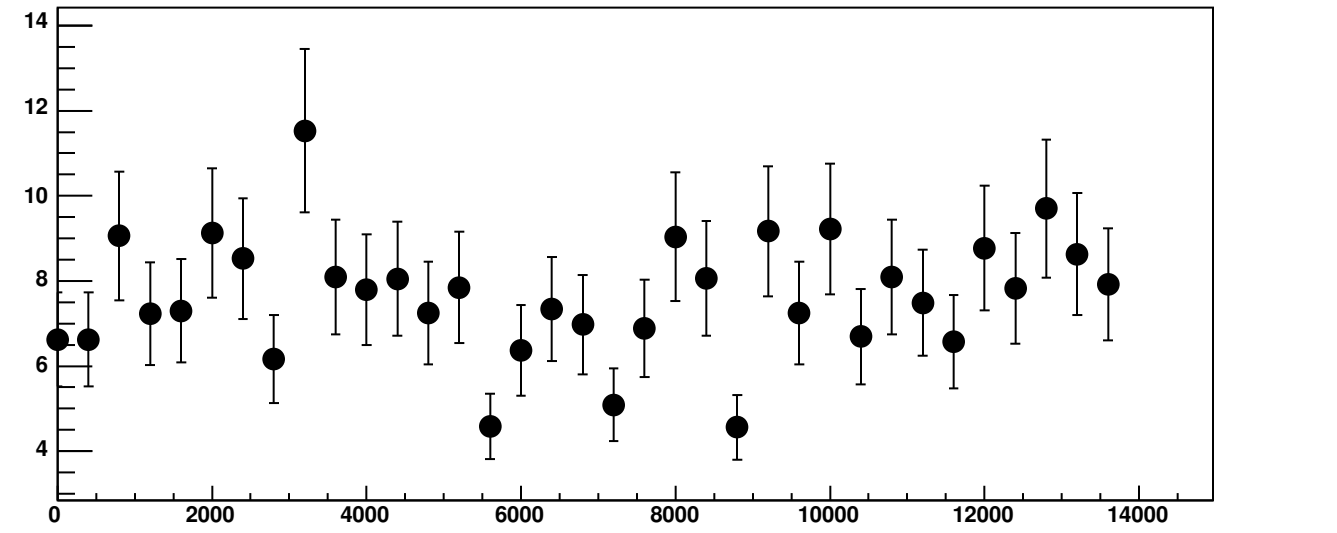

Chip 5, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

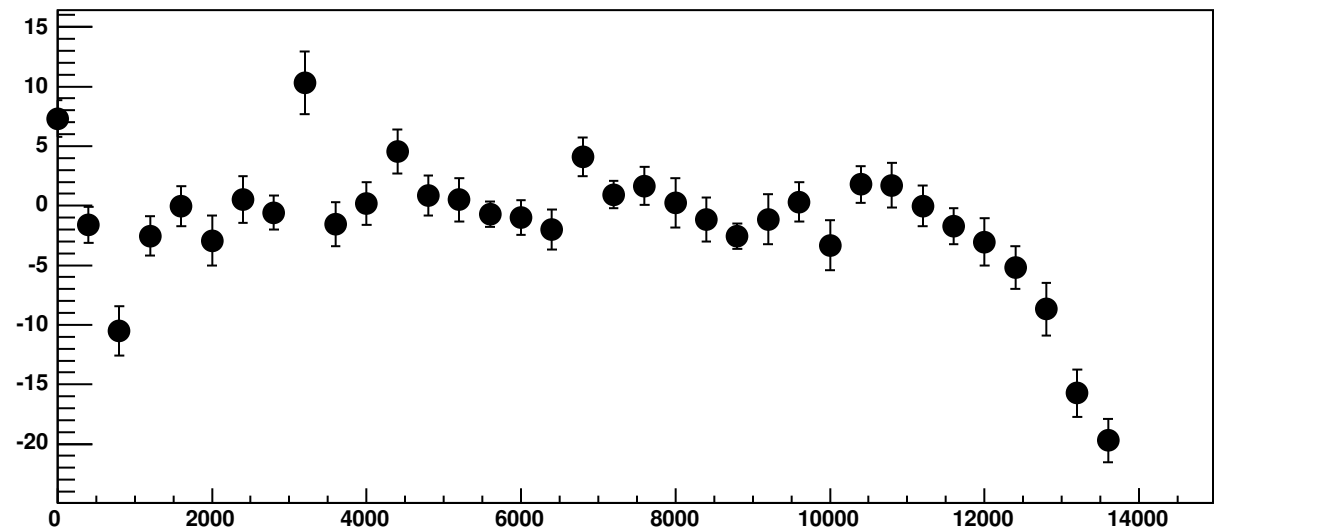
Chip 5, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC

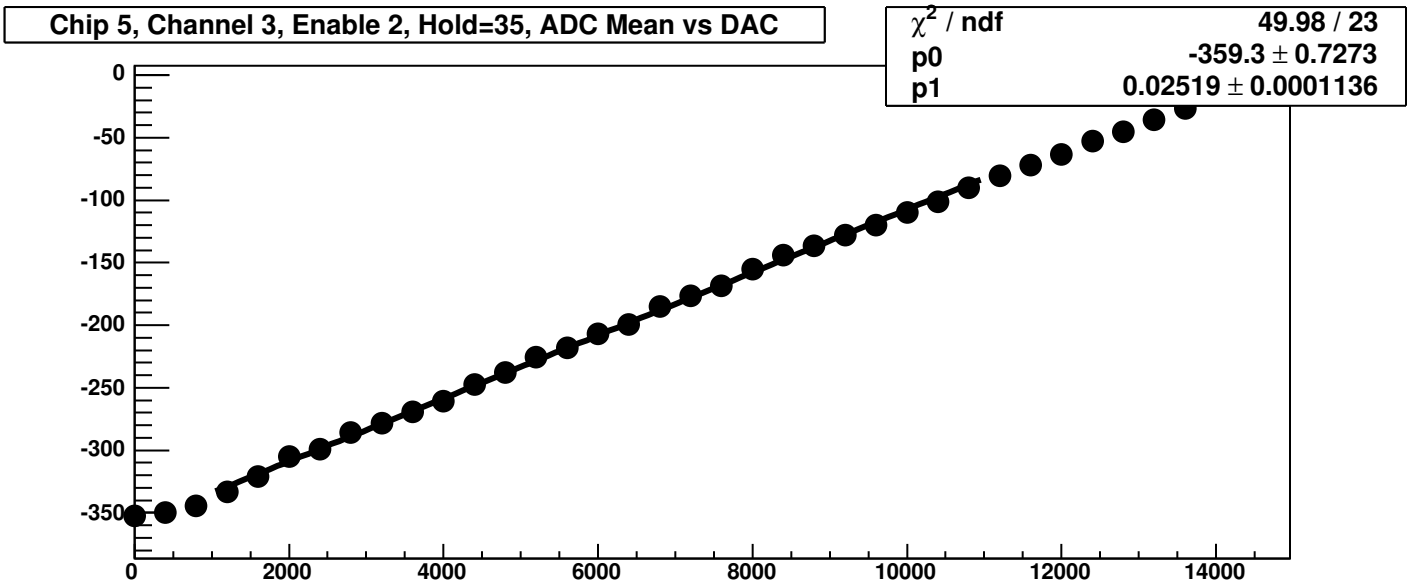


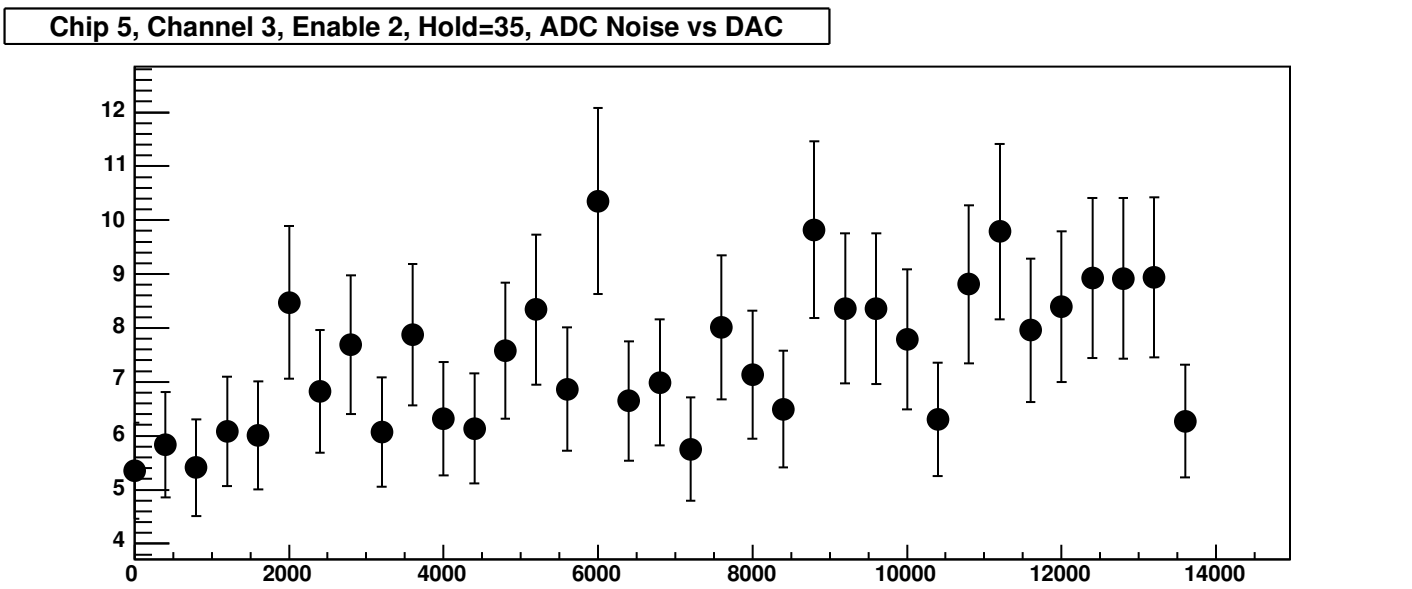
Chip 5, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC

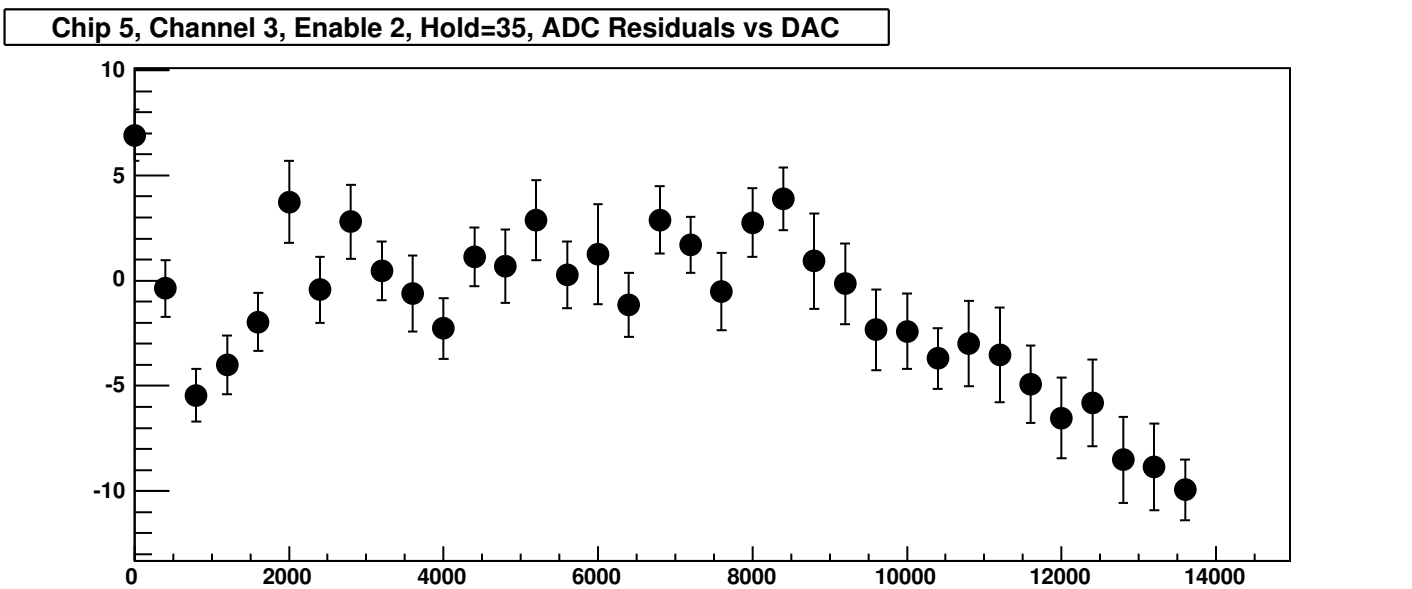


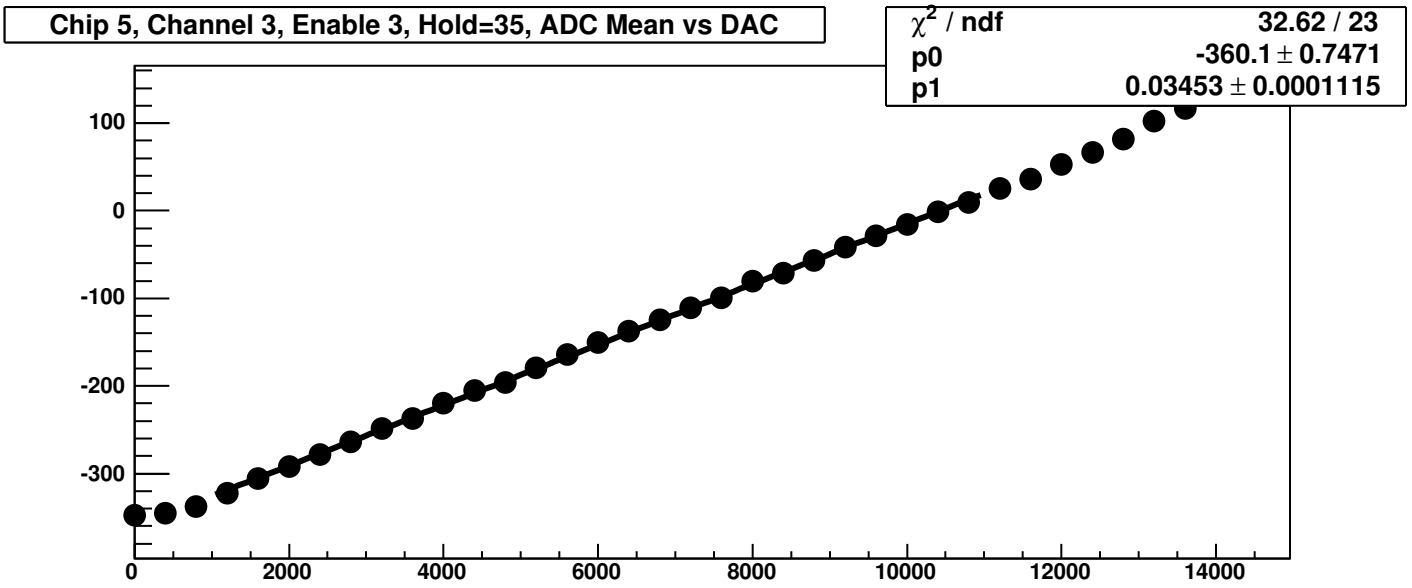
Chip 5, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC

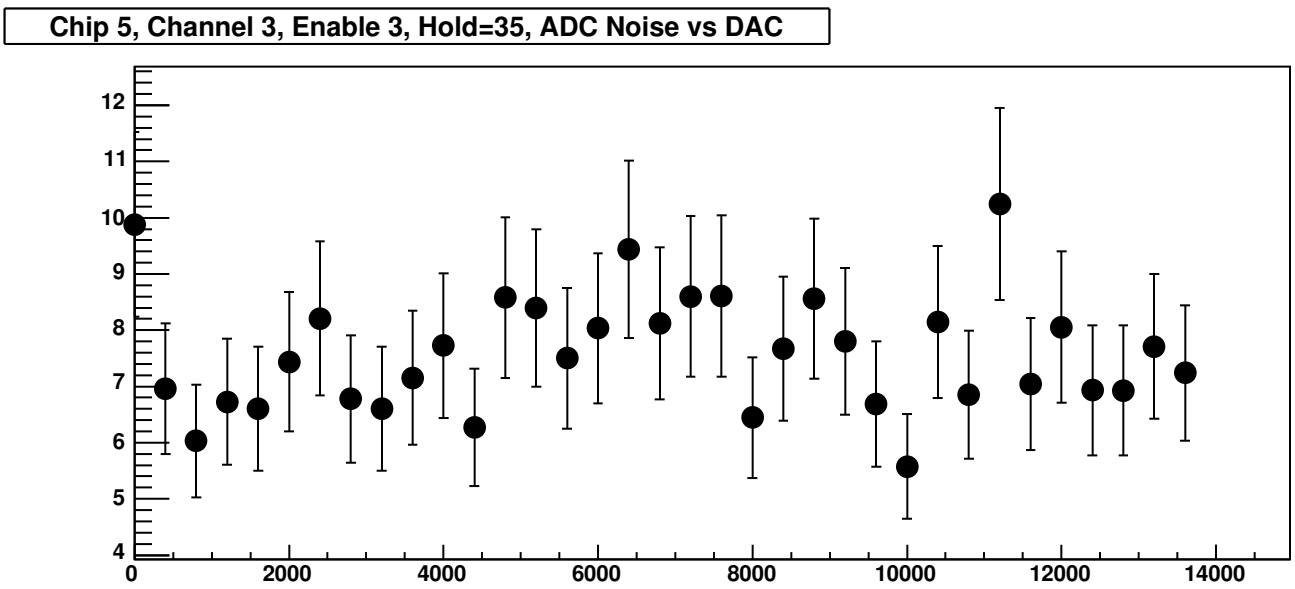


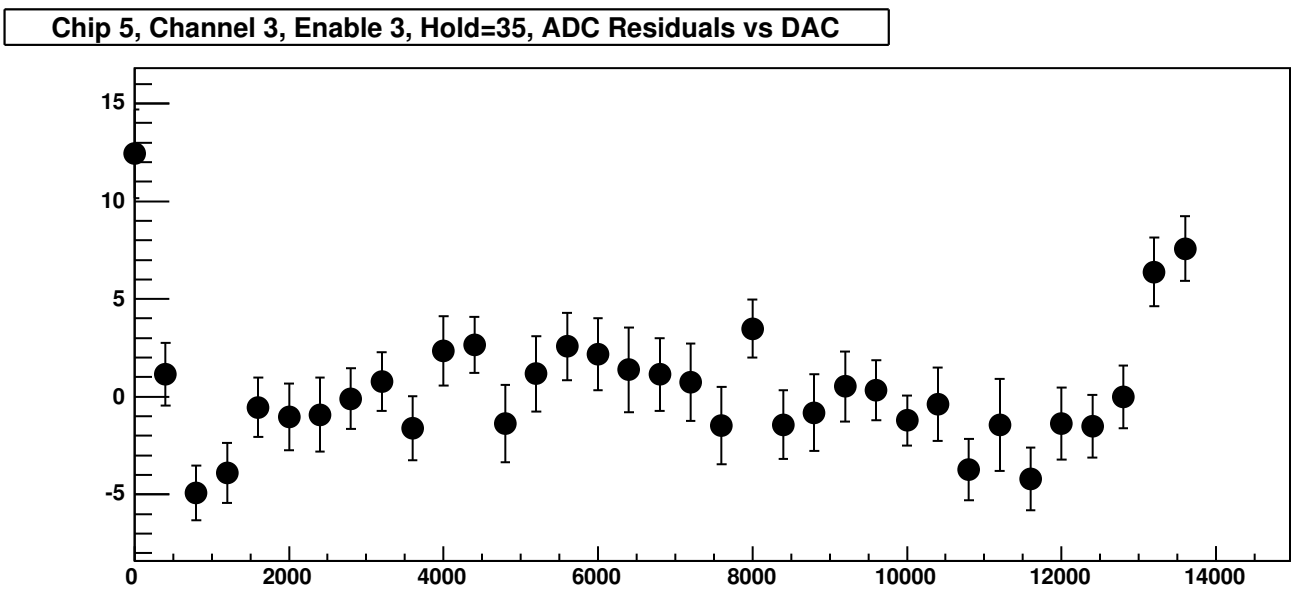

Chip 5, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 49.98 / 23
p0 -359.3 ± 0.7273
p1 0.02519 ± 0.0001136
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

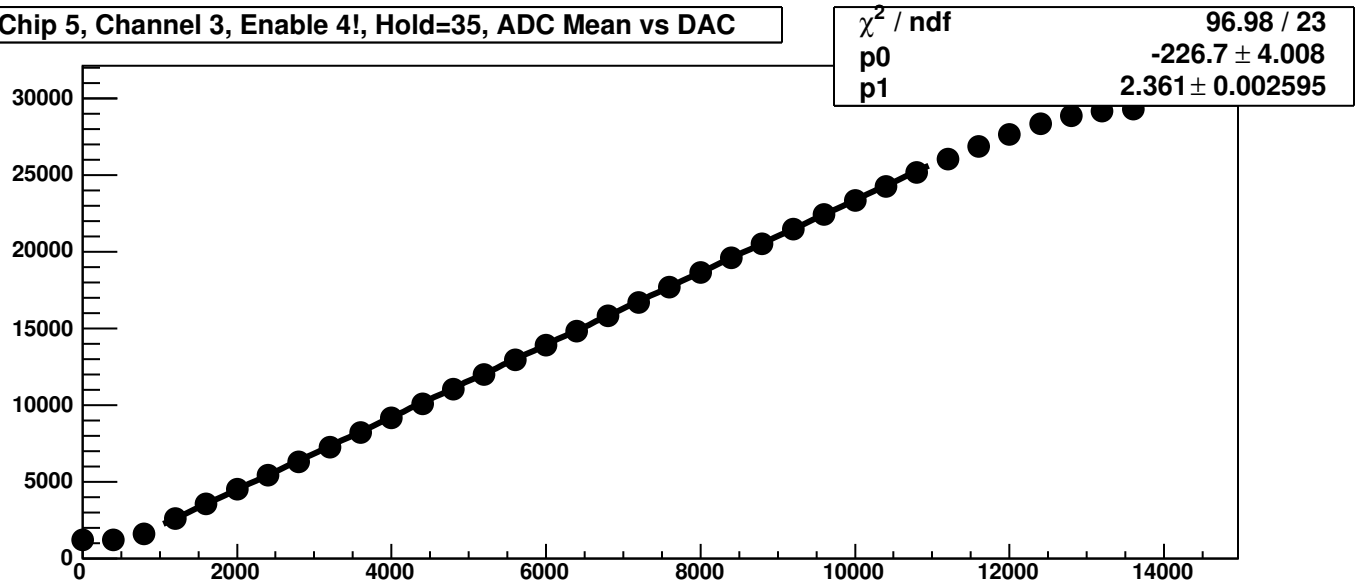

Chip 5, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 32.62 / 23
p0 -360.1 ± 0.7471
p1 0.03453 ± 0.0001115
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

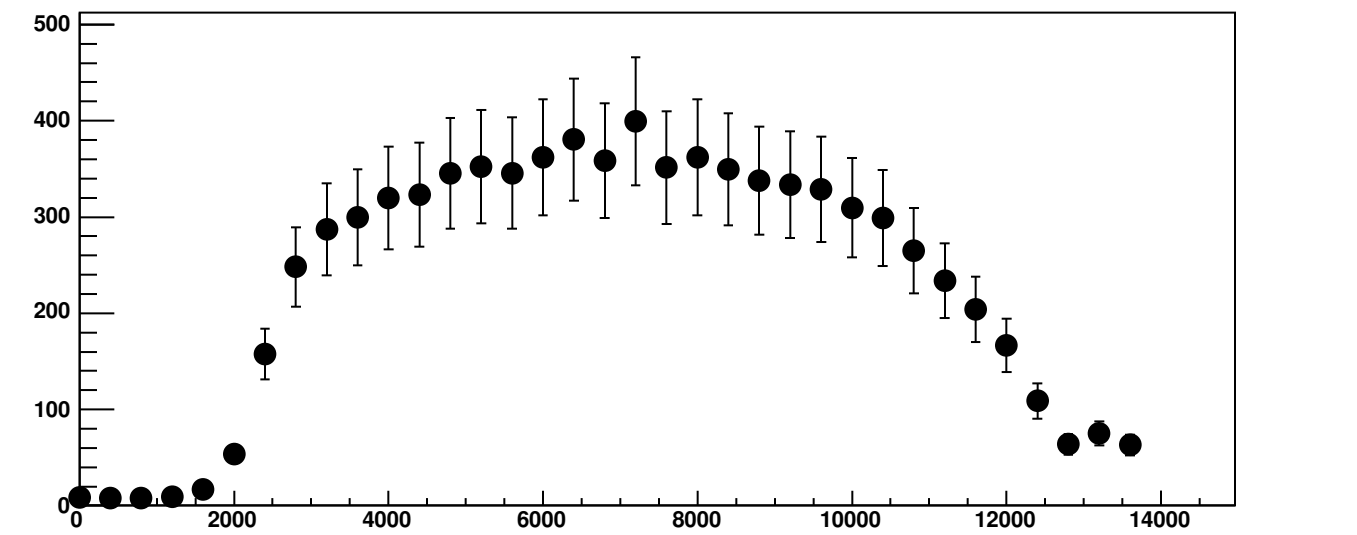

Chip 5, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

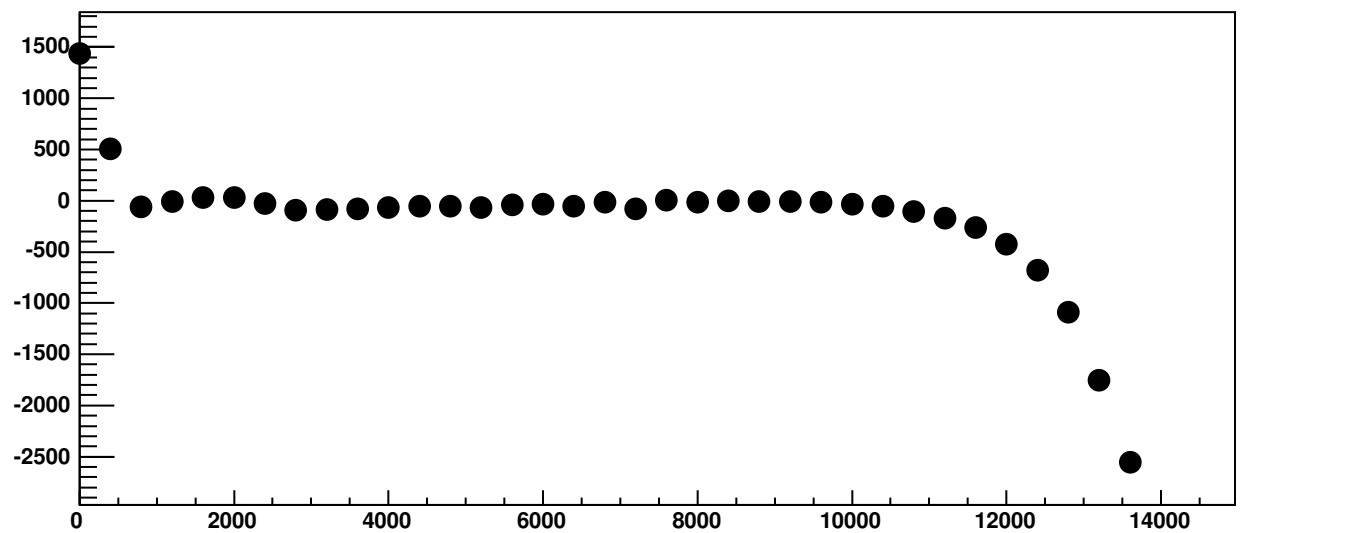
Chip 5, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC

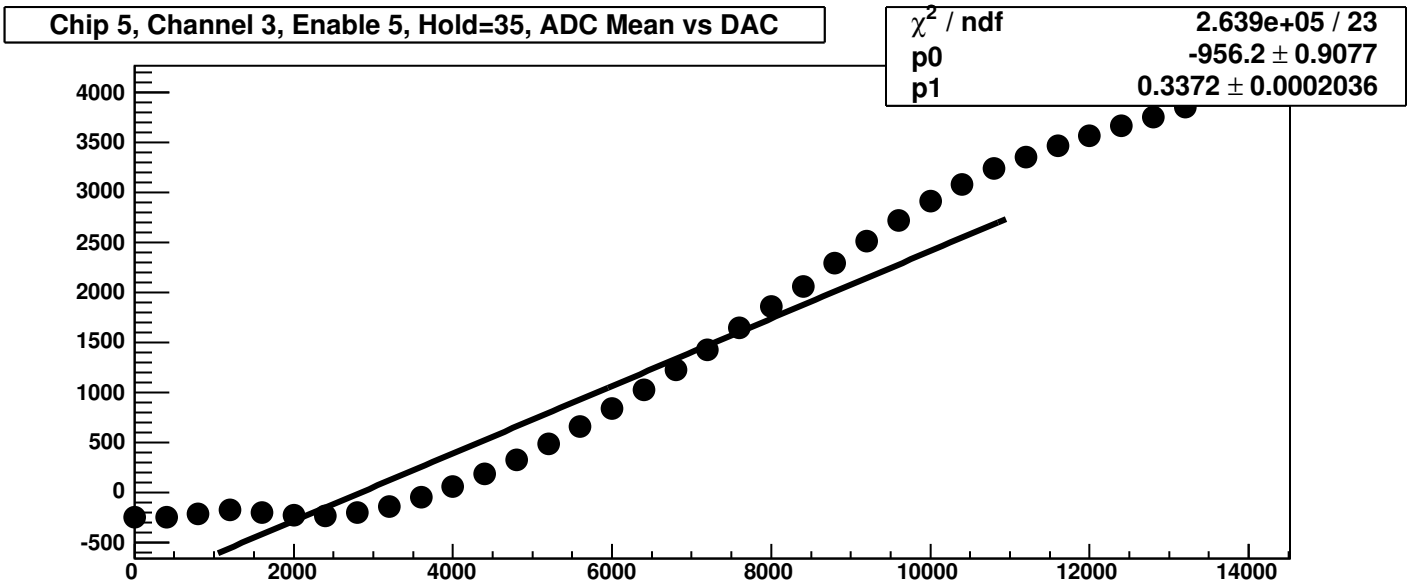


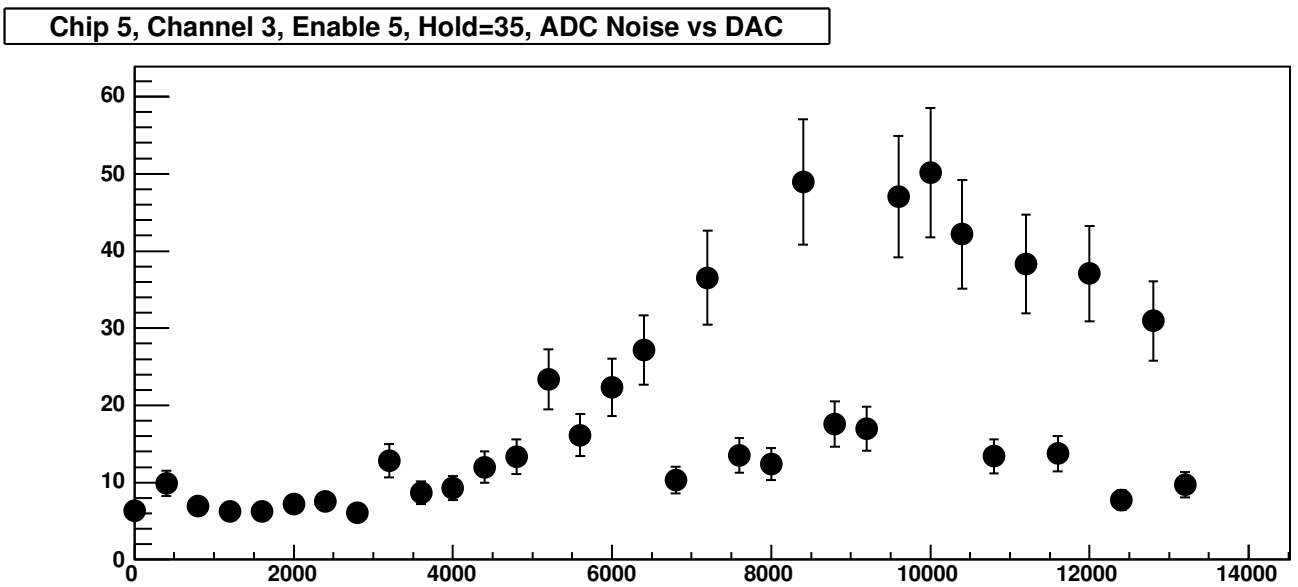
Chip 5, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC

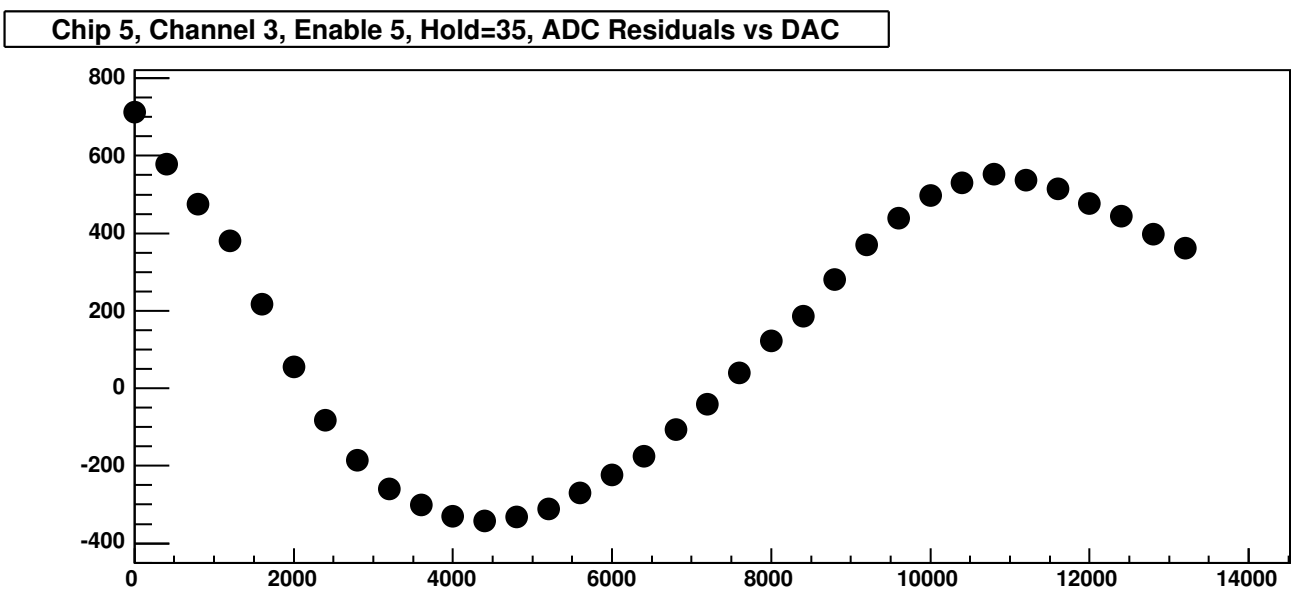


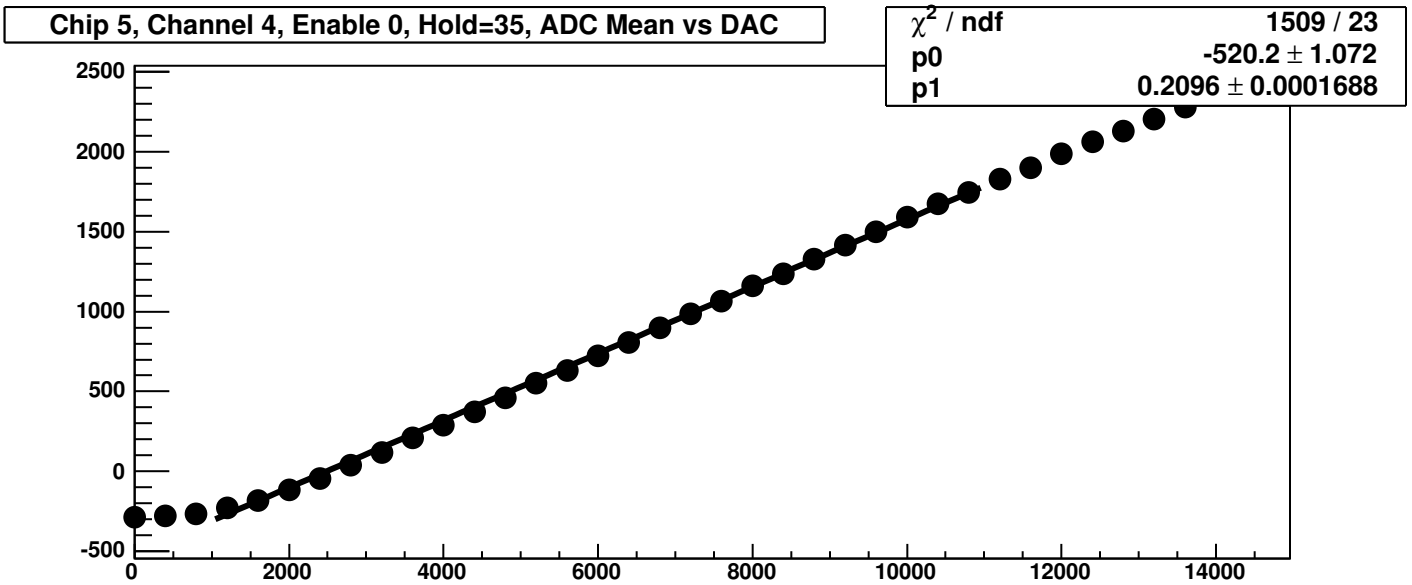
Chip 5, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC

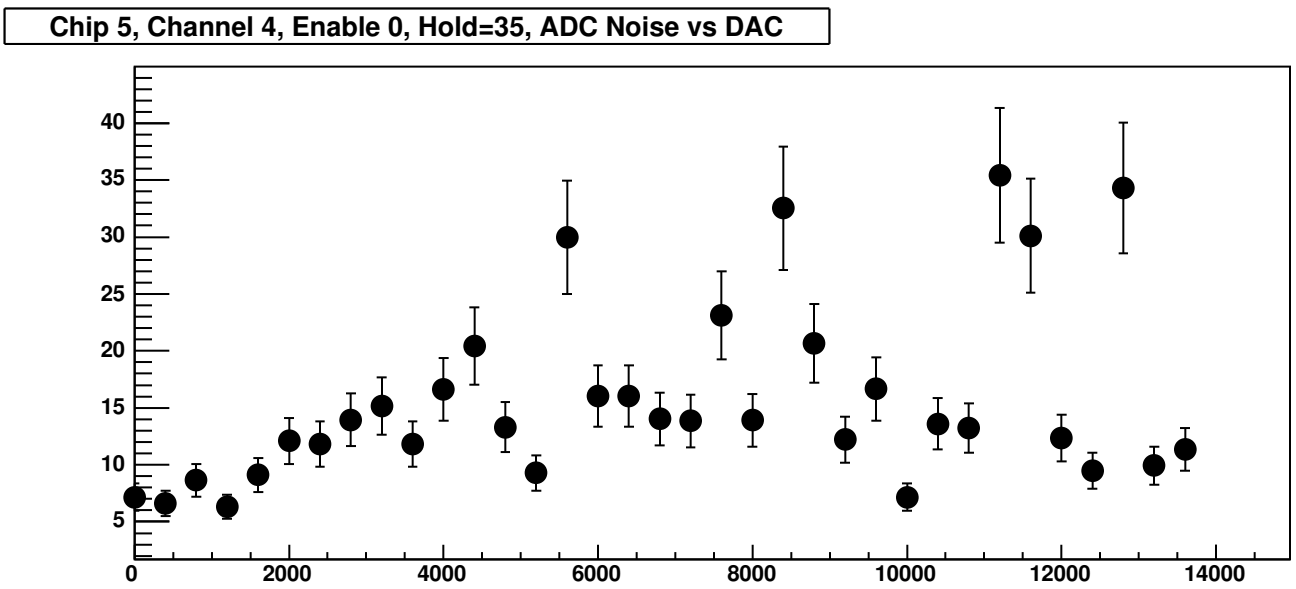


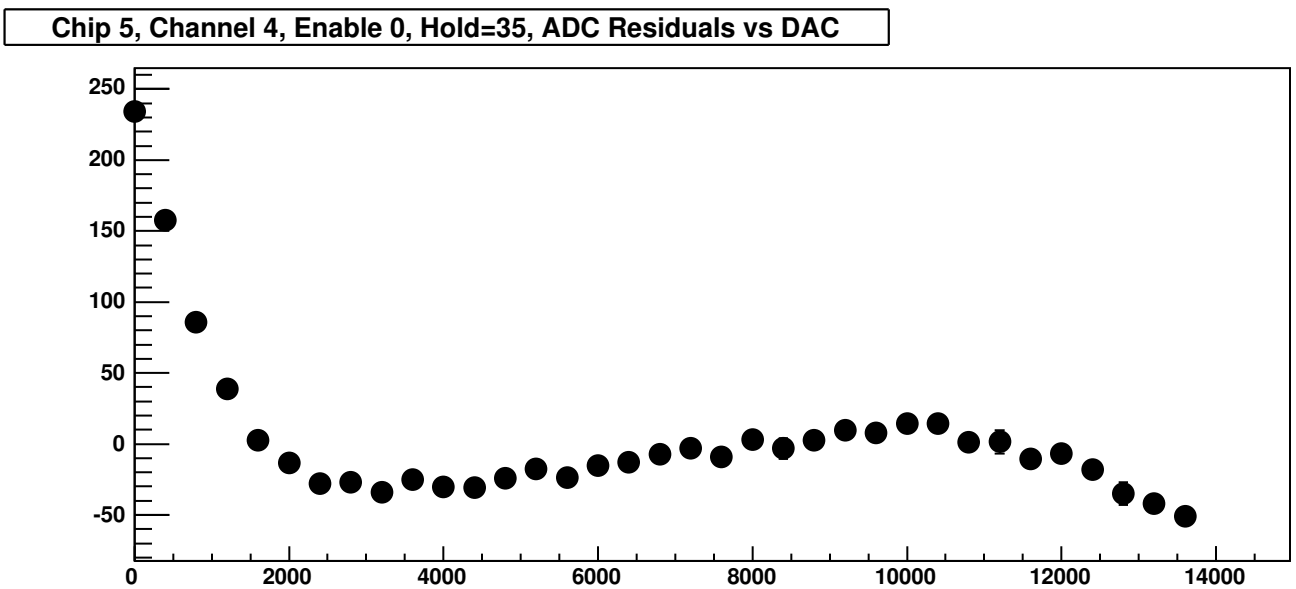

Chip 5, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2.639e+05 / 23
p0 -956.2 ± 0.9077
p1 0.3372 ± 0.0002036
4000
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

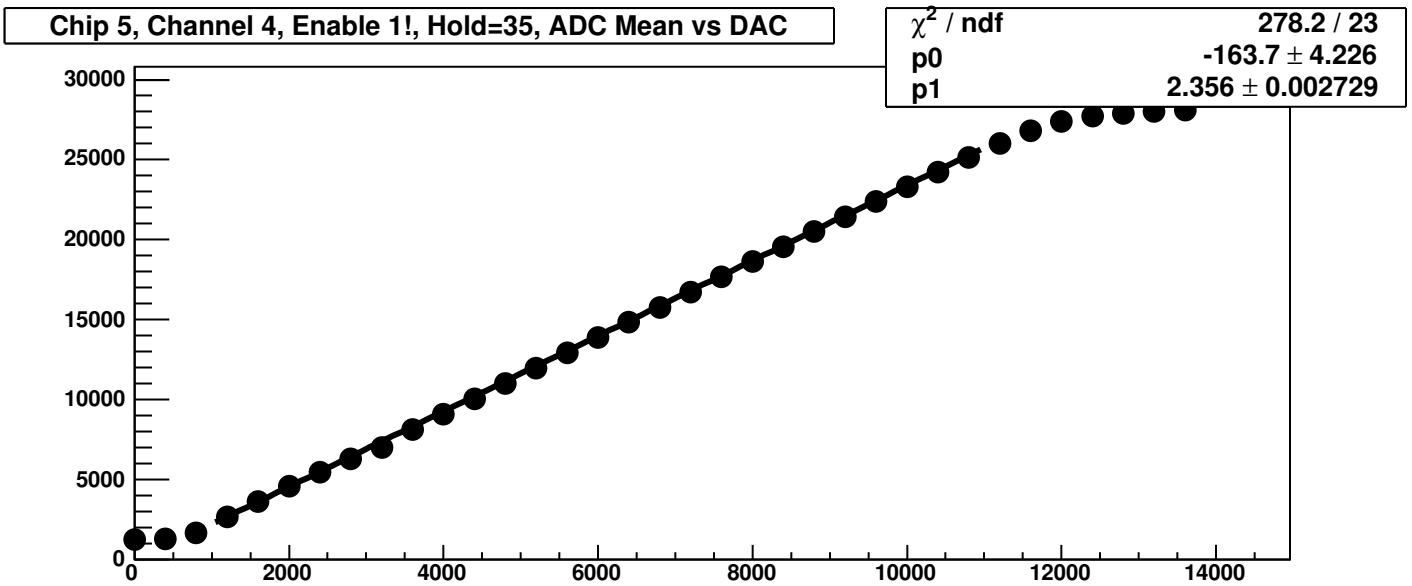

Chip 5, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

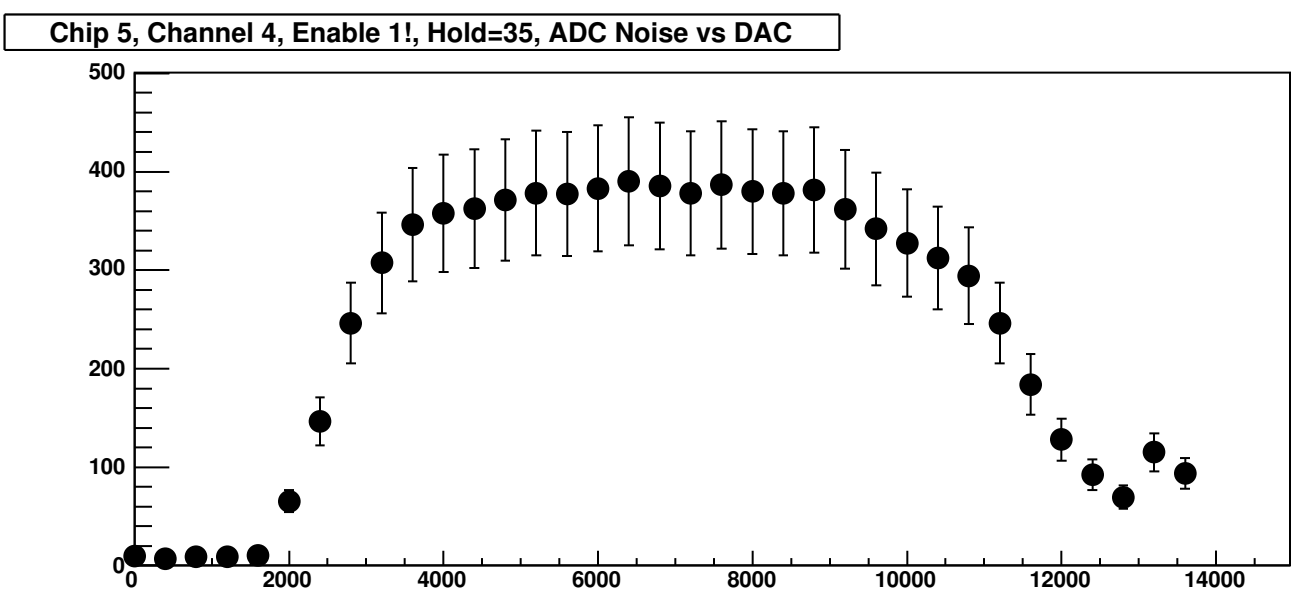

Chip 5, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

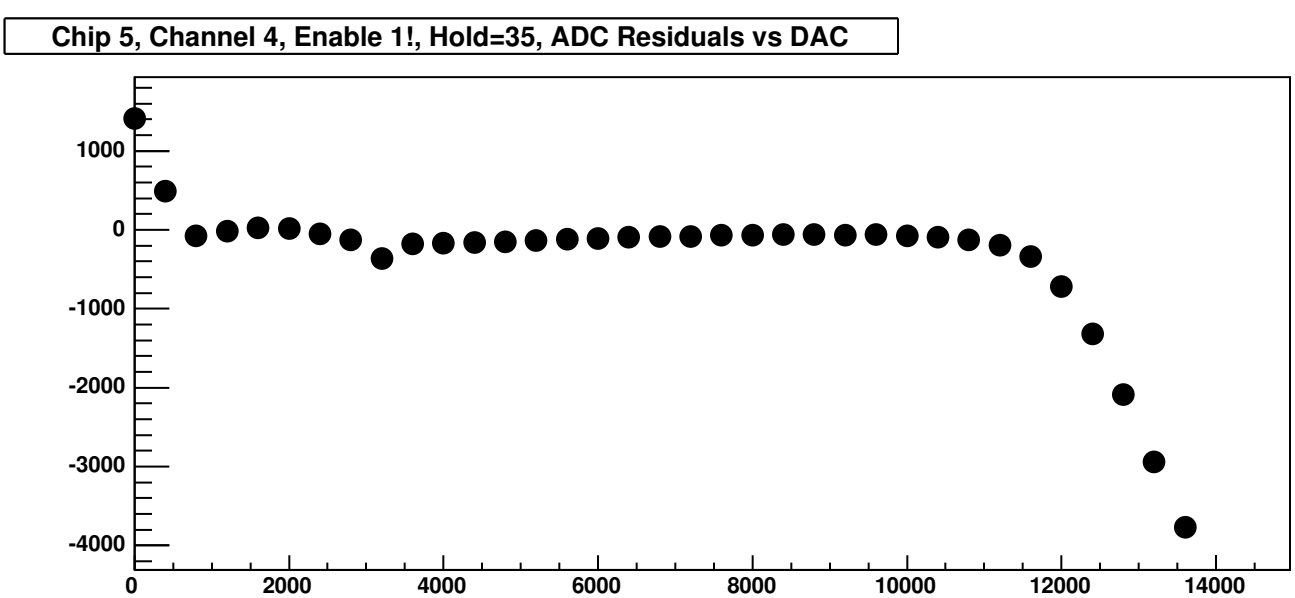

Chip 5, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1509 / 23
p0 -520.2 ± 1.072
p1 0.2096 ± 0.0001688
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

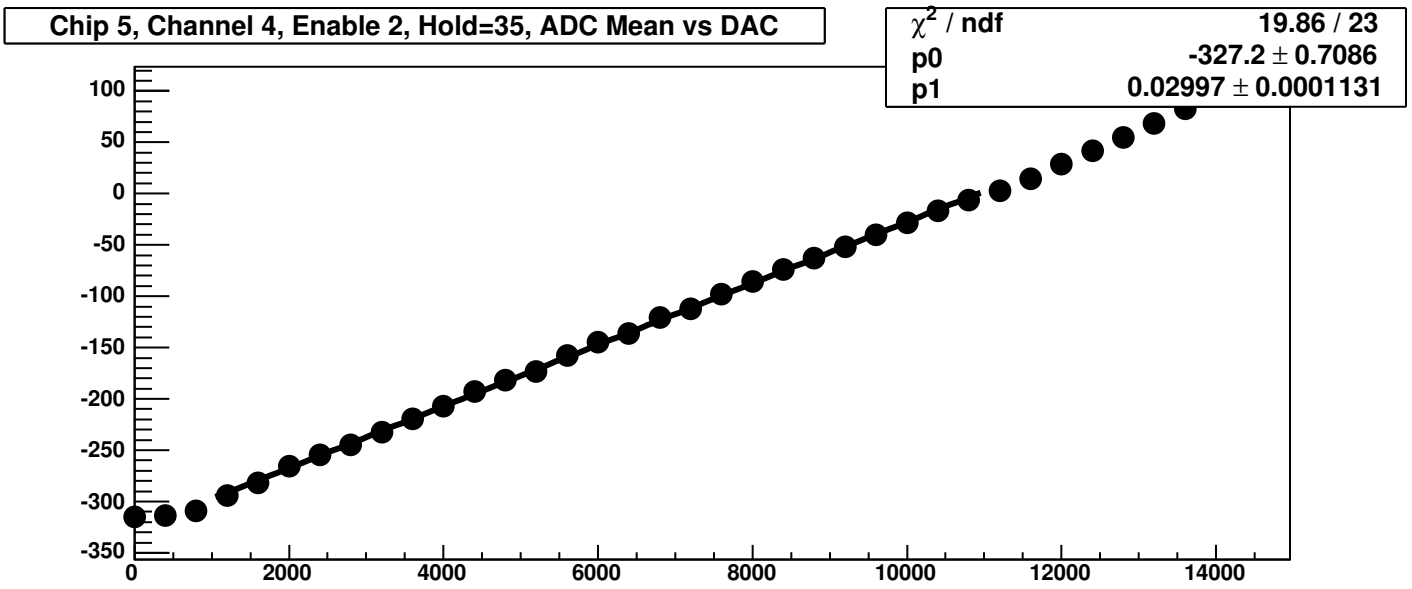

Chip 5, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

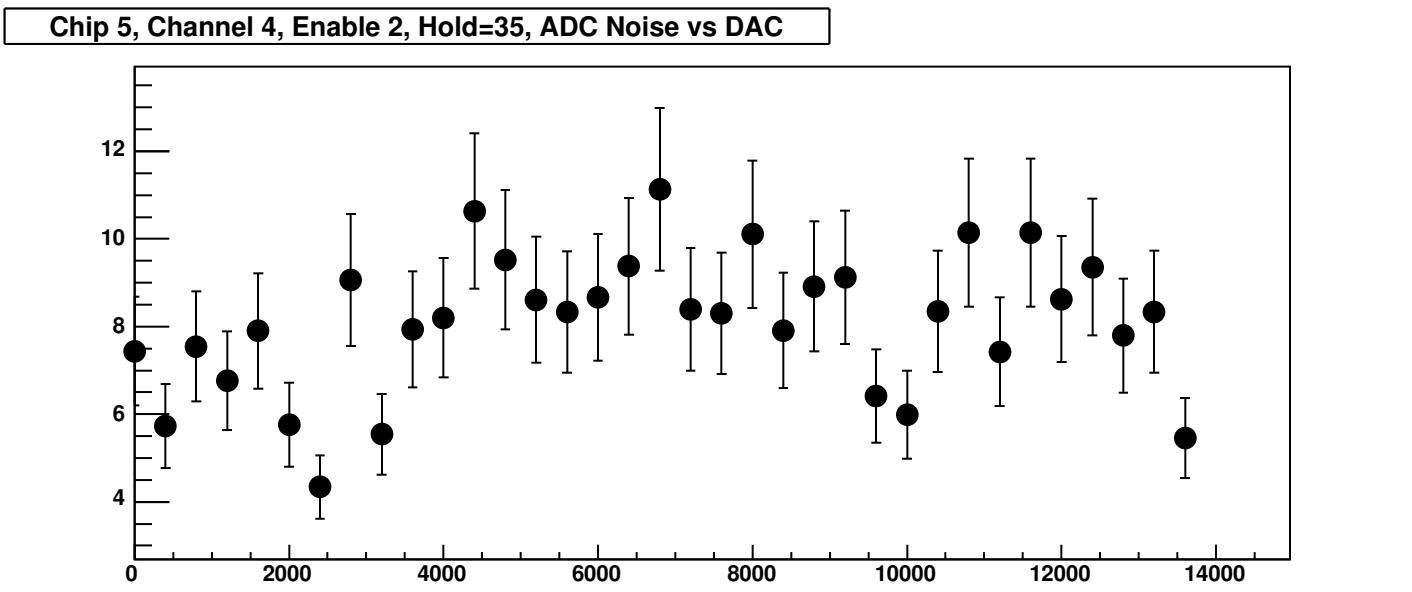

Chip 5, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

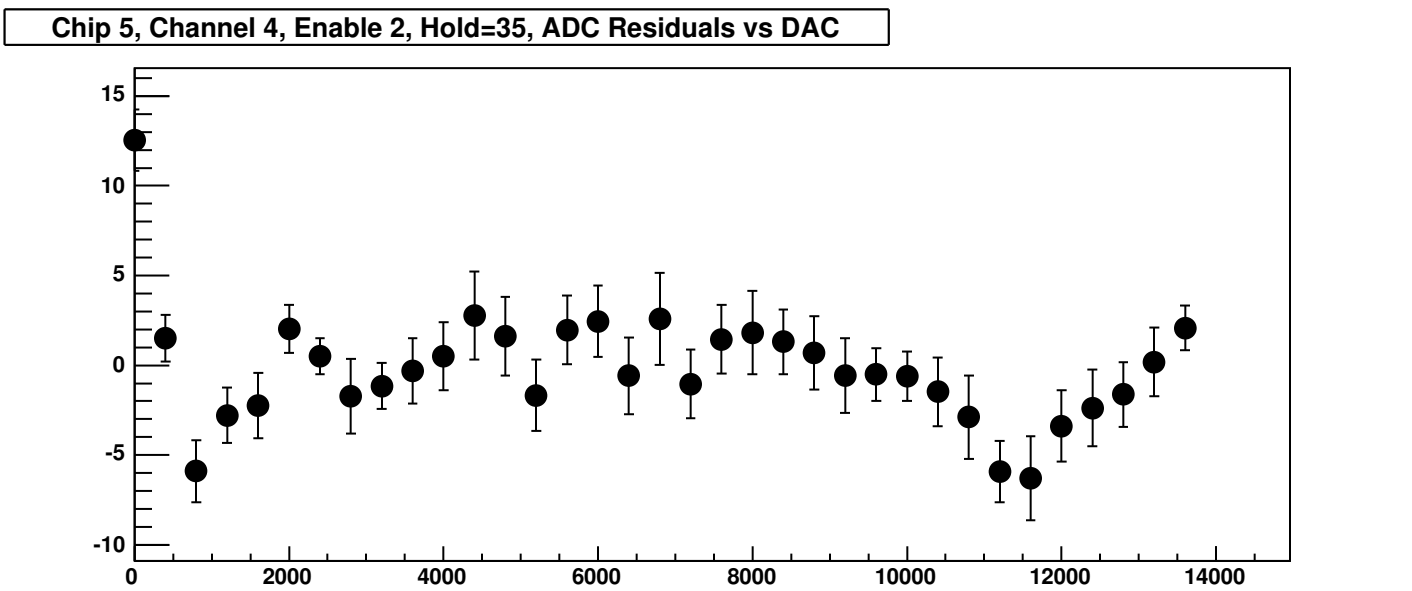

Chip 5, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 278.2 / 23
p0 -163.7 ± 4.226
p1 2.356 ± 0.002729
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

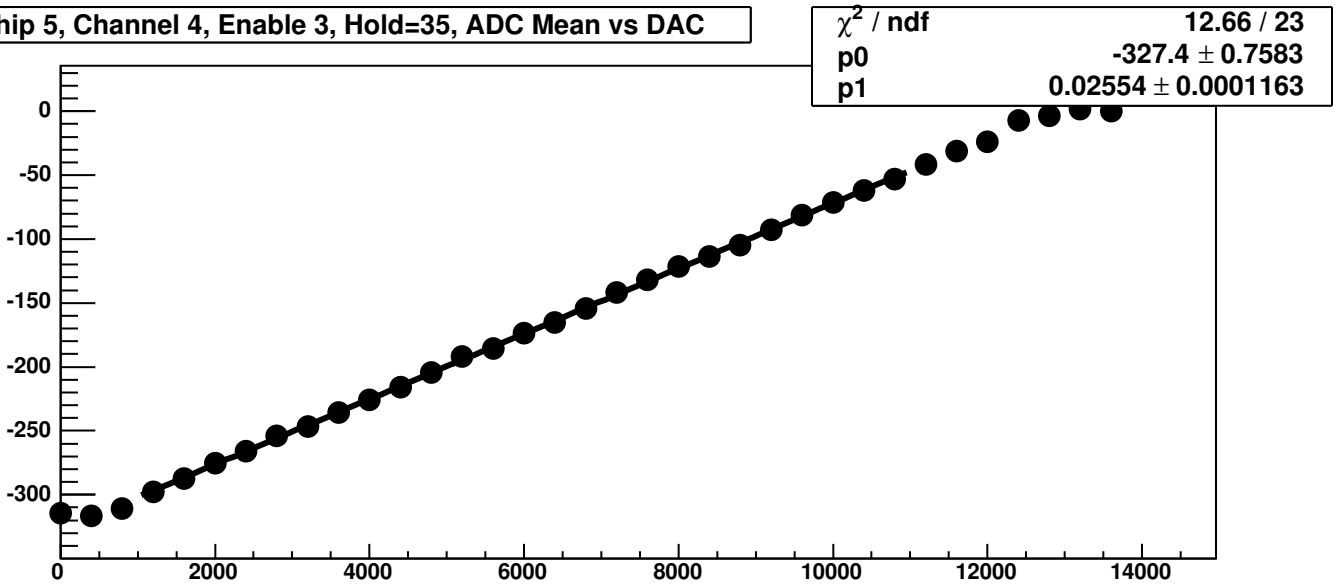

Chip 5, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 19.86 / 23
p0 -327.2 ± 0.7086
p1 0.02997 ± 0.0001131
100
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

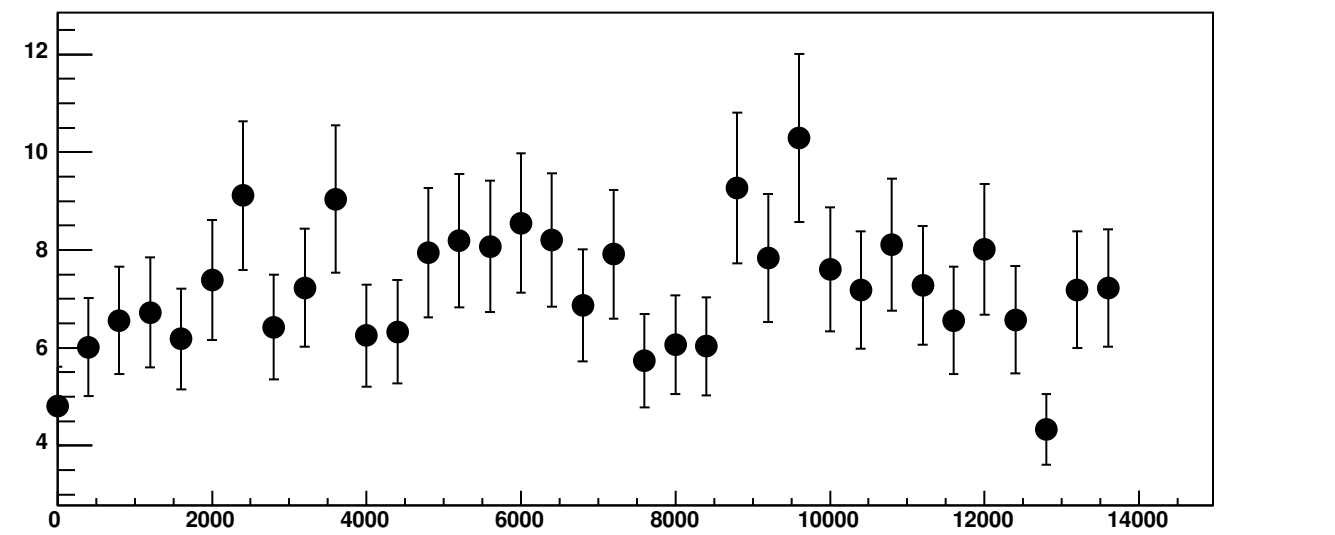

Chip 5, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

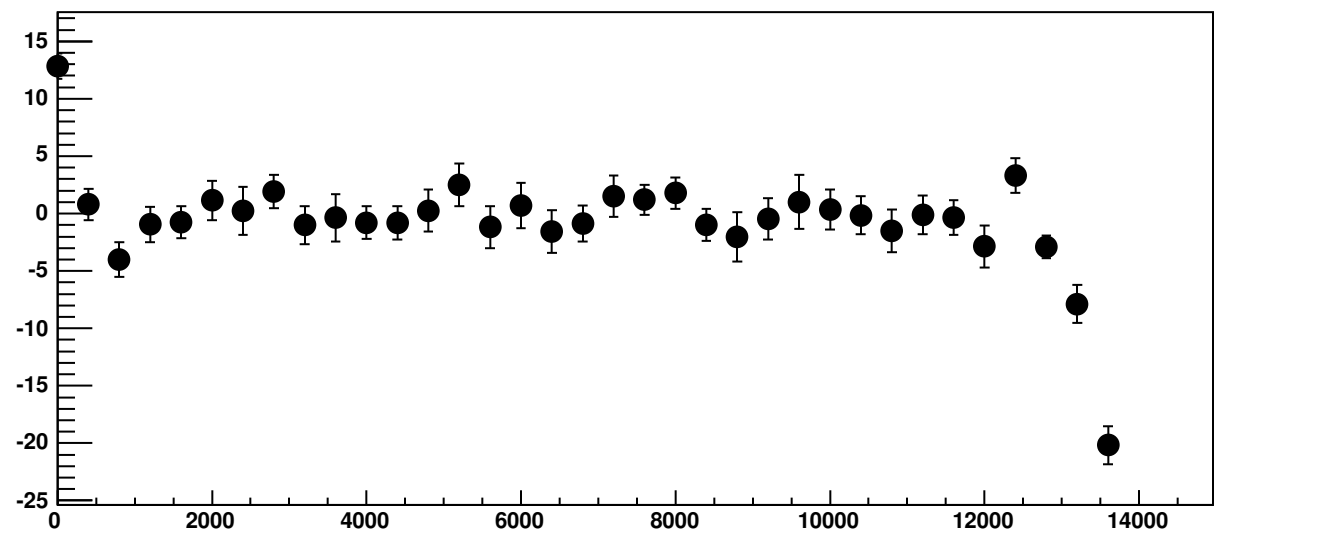
Chip 5, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



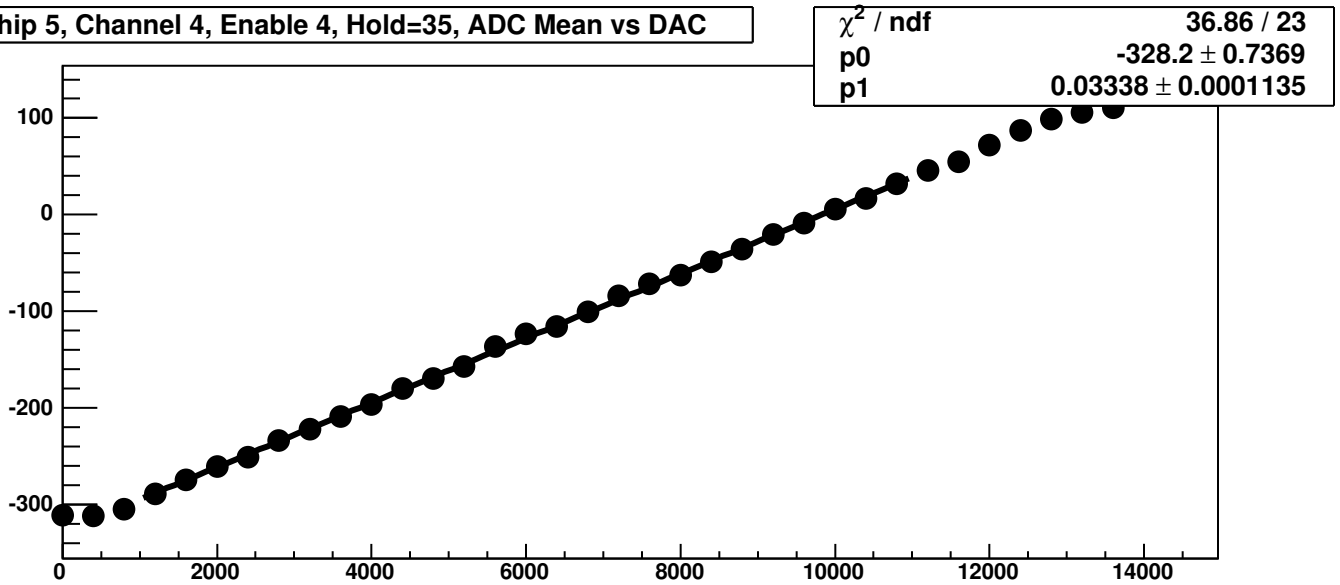
Chip 5, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



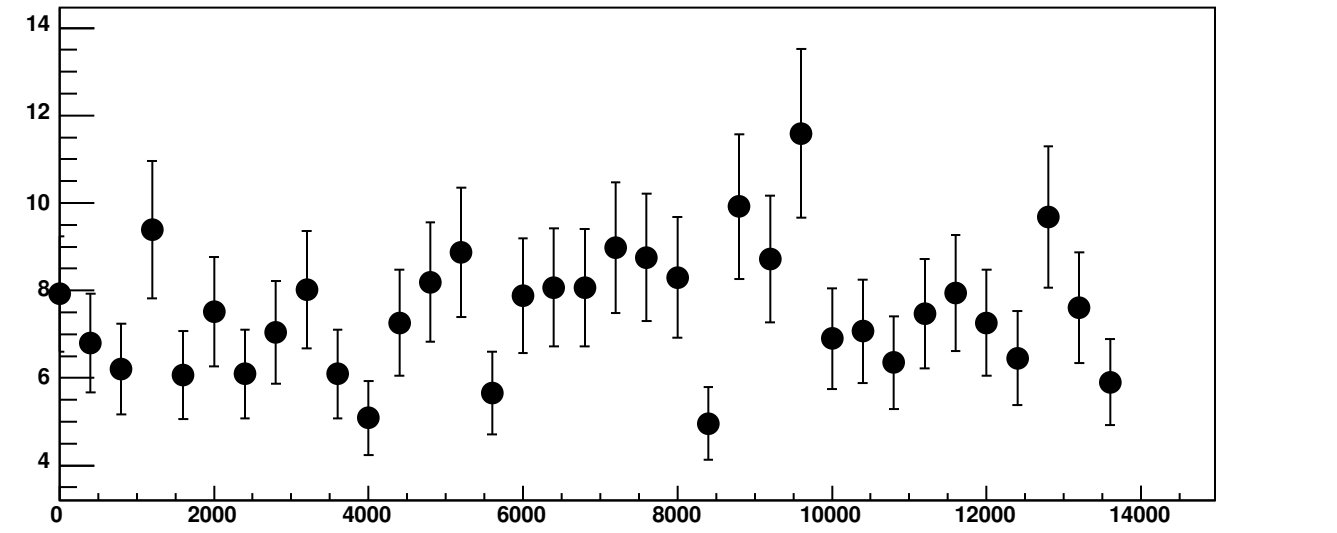
Chip 5, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



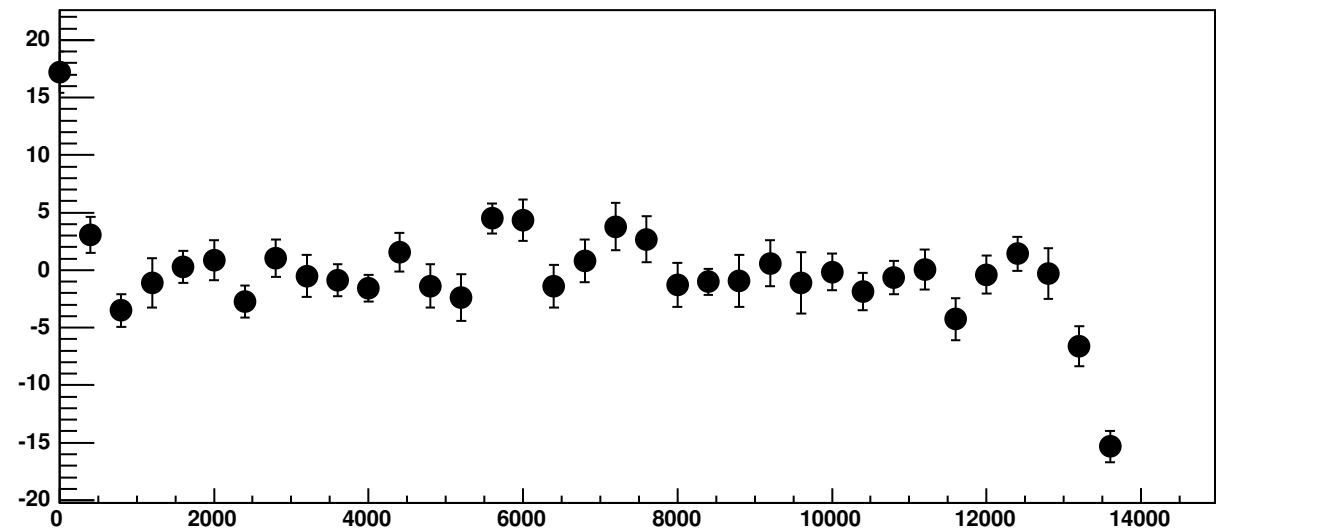
Chip 5, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC



Chip 5, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC

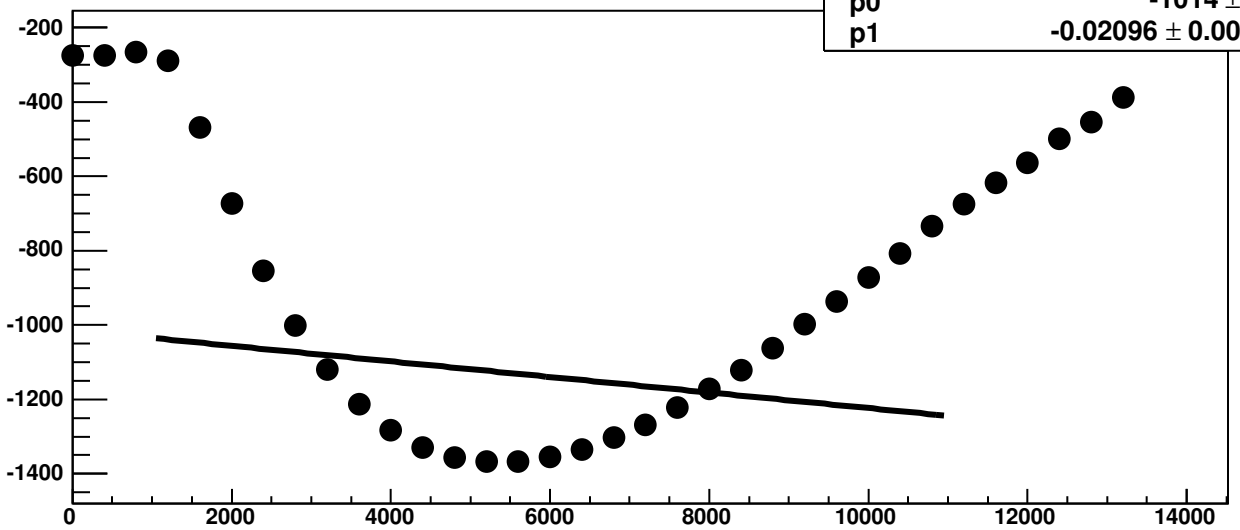


Chip 5, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC

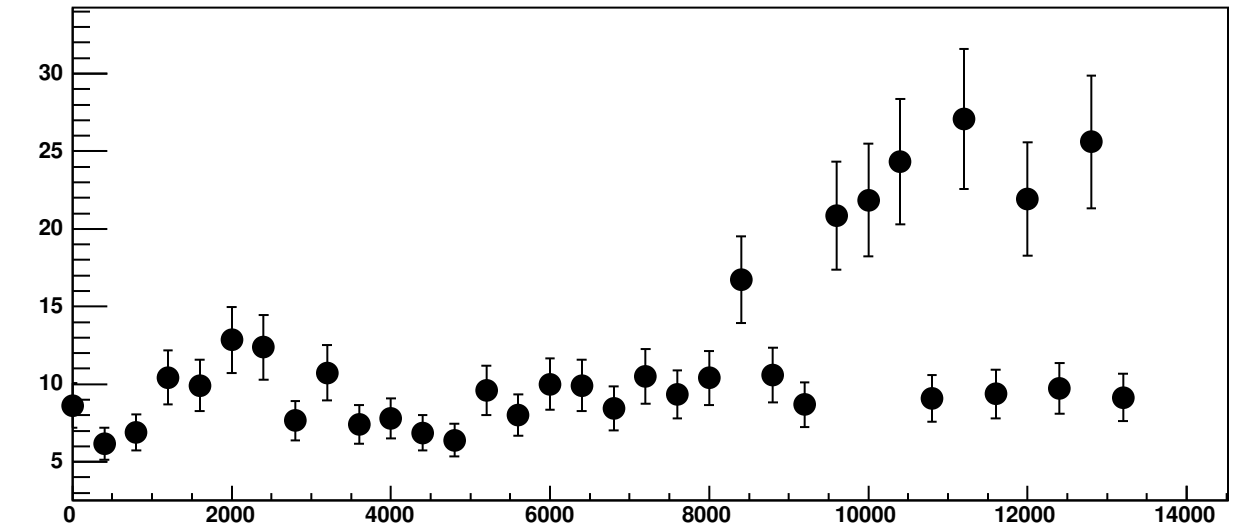


Chip 5, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

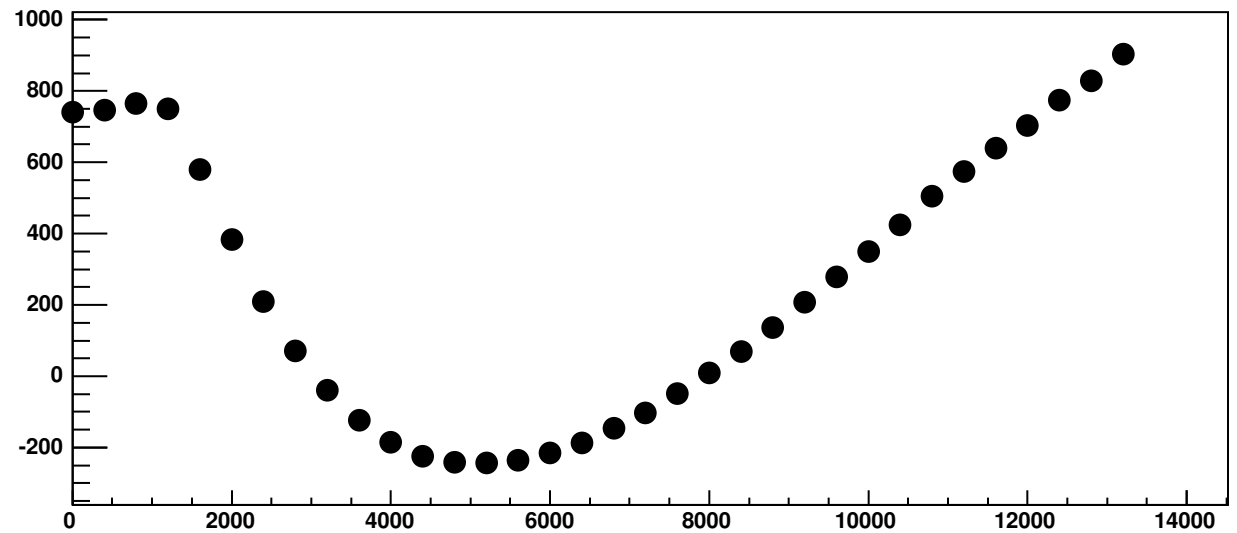
χ^2 / ndf	3.909e+05 / 23
p0	-1014 ± 1.052
p1	-0.02096 ± 0.0001753

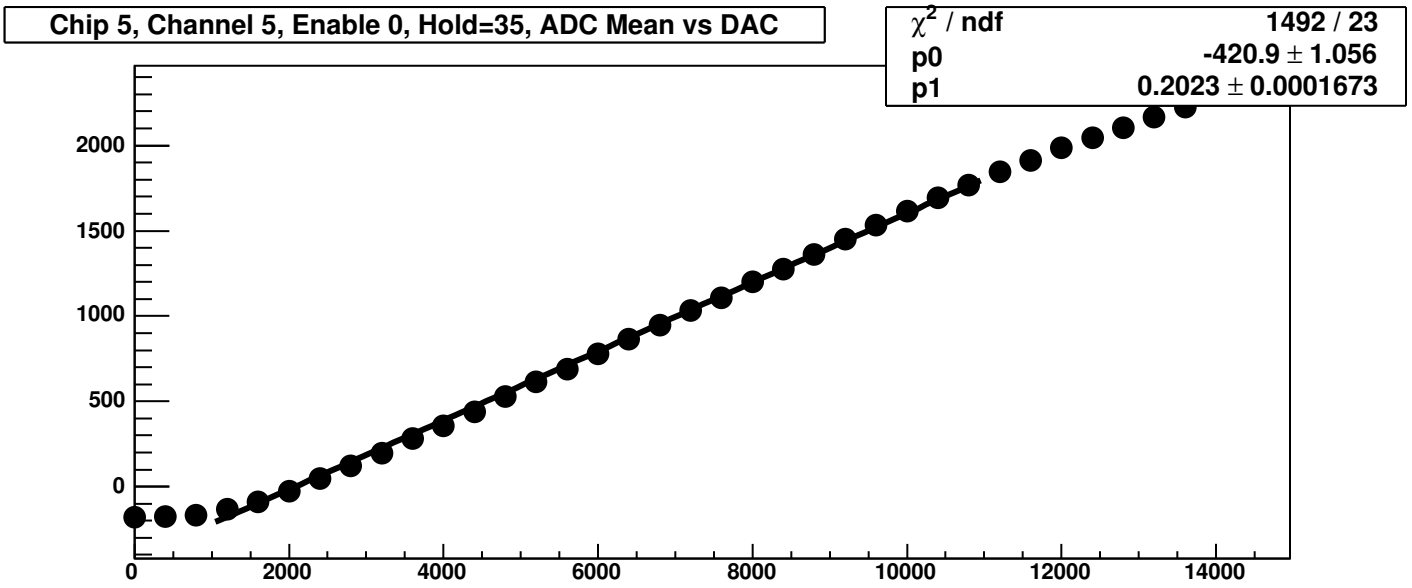


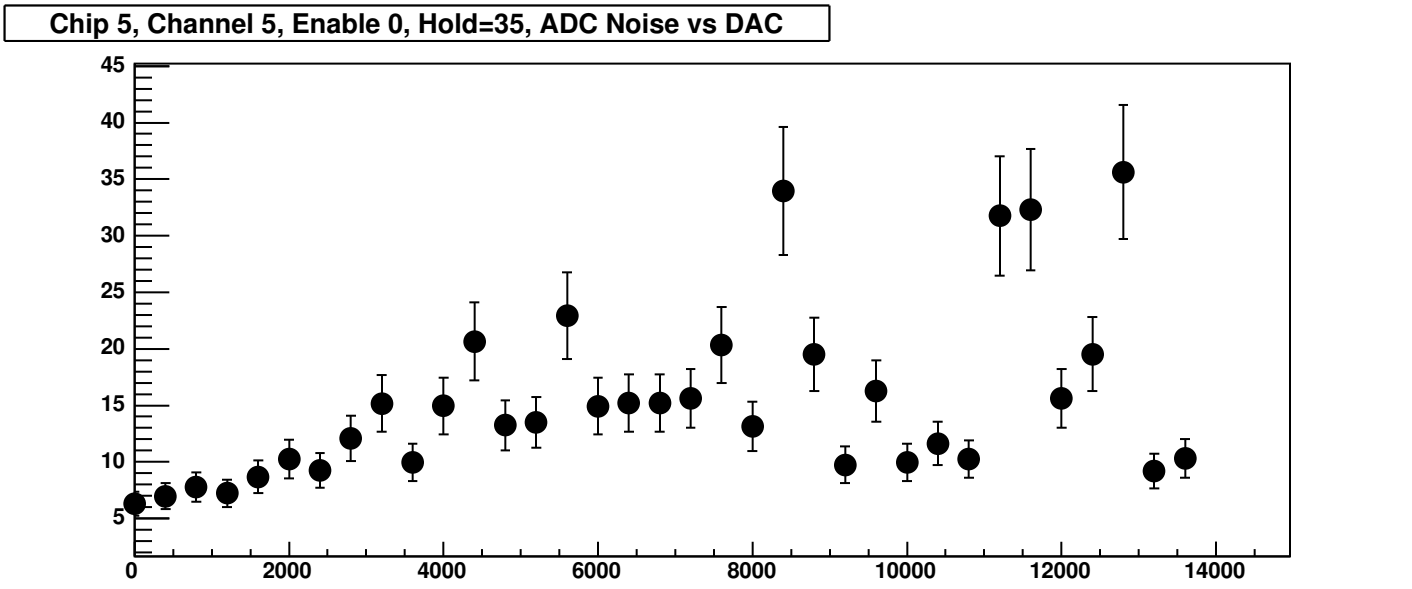
Chip 5, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC

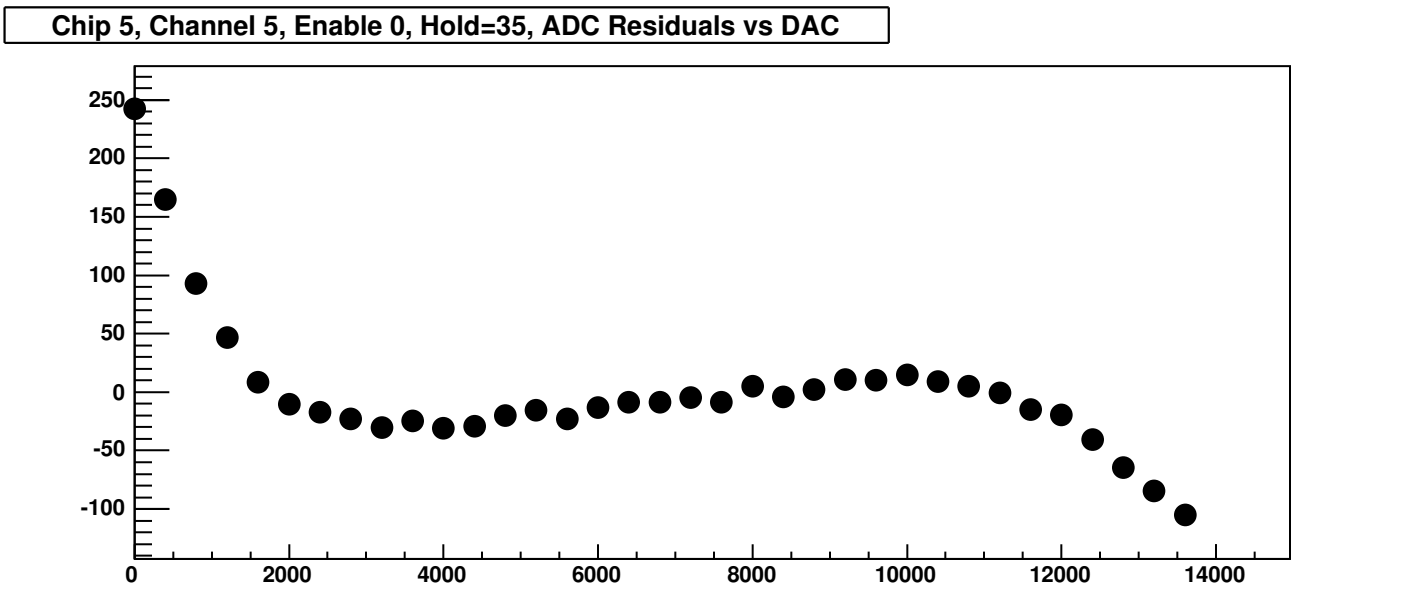


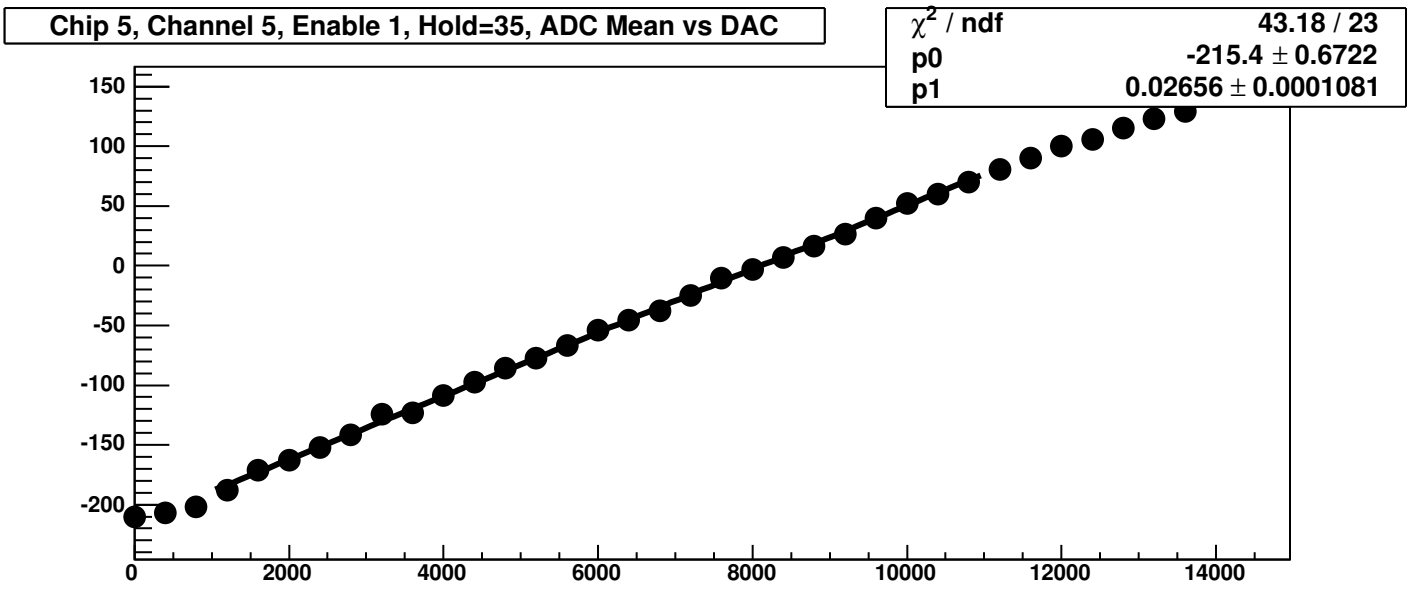
Chip 5, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

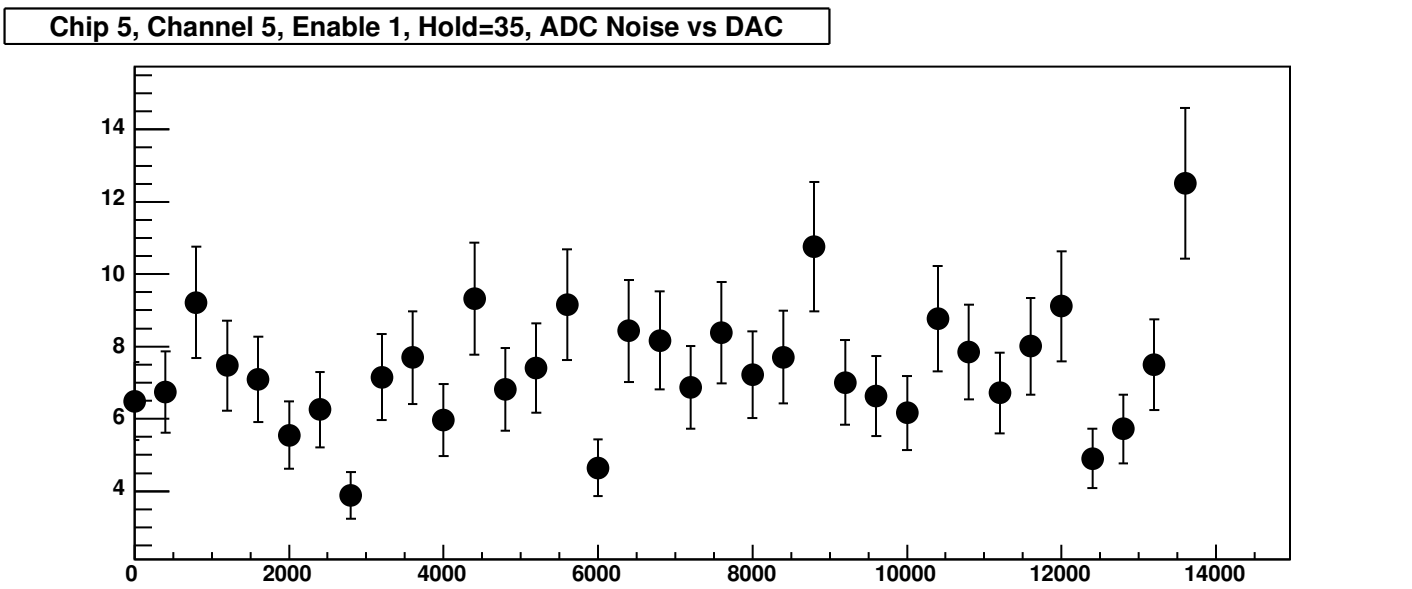


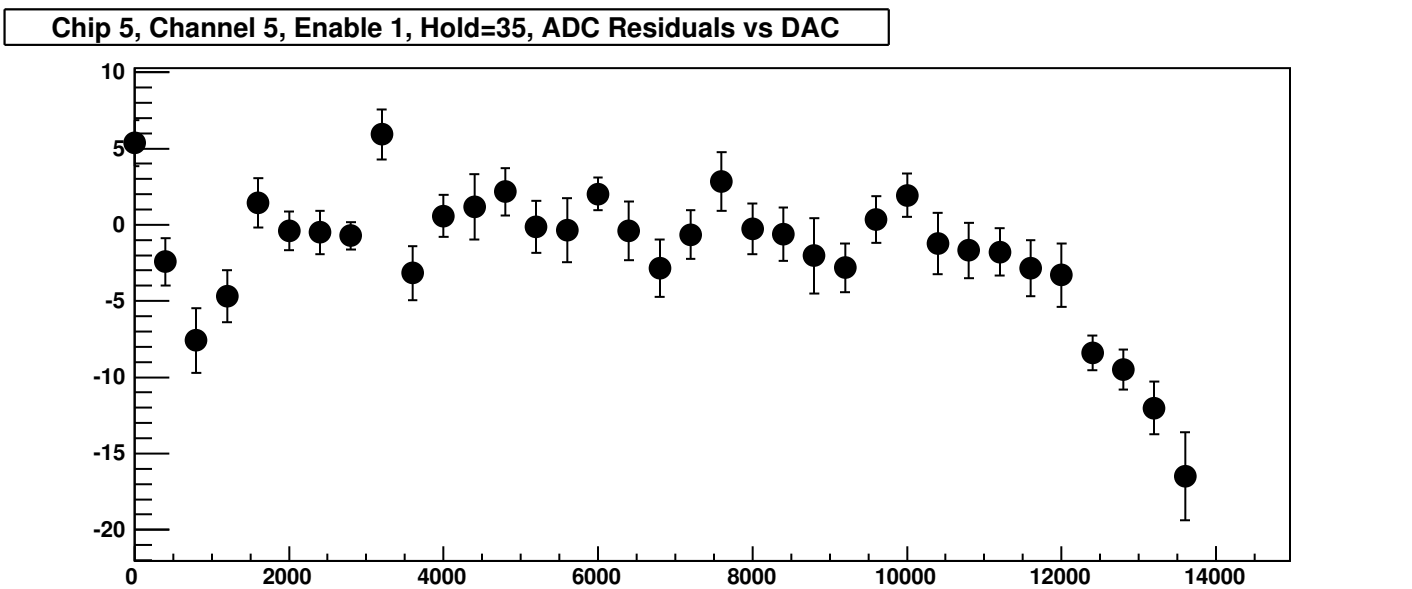

Chip 5, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1492 / 23
p0 -420.9 ± 1.056
p1 0.2023 ± 0.0001673
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

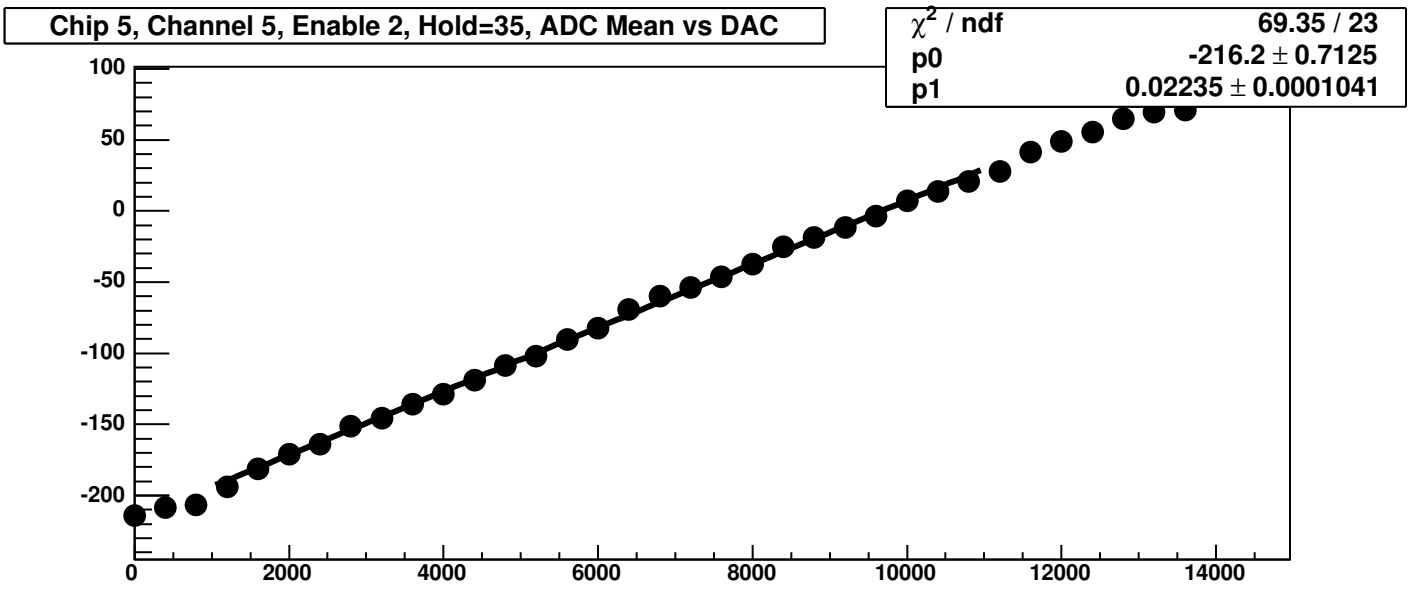

Chip 5, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

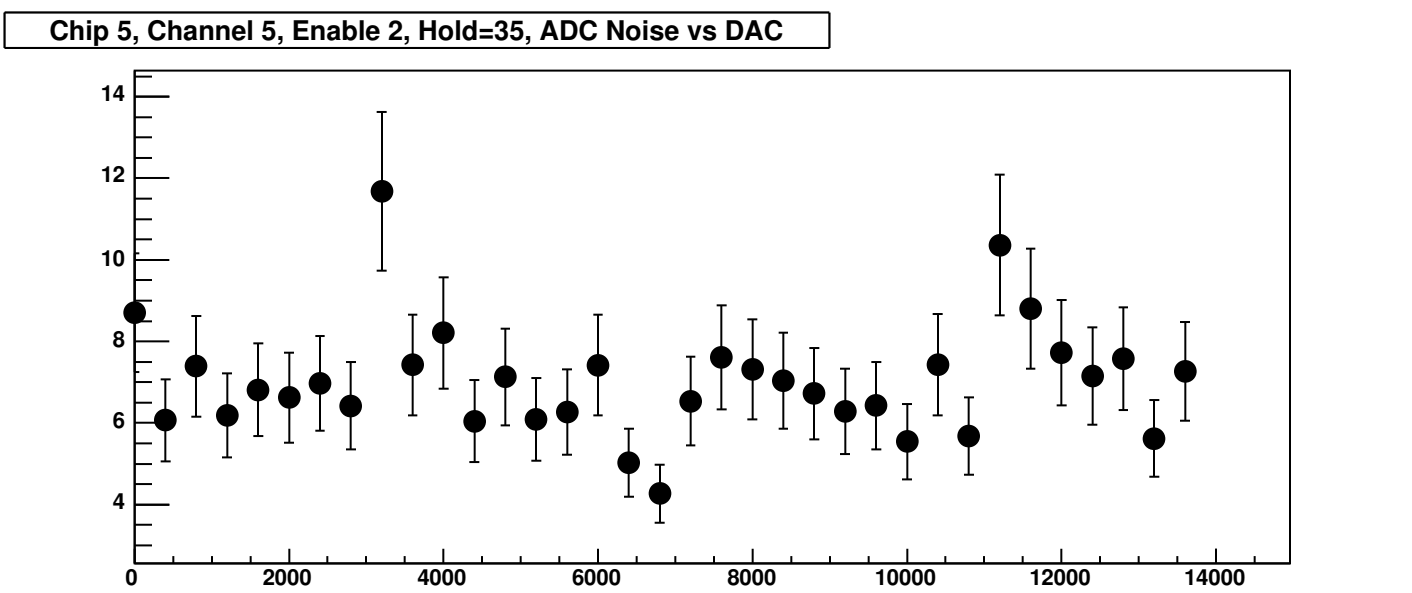

Chip 5, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

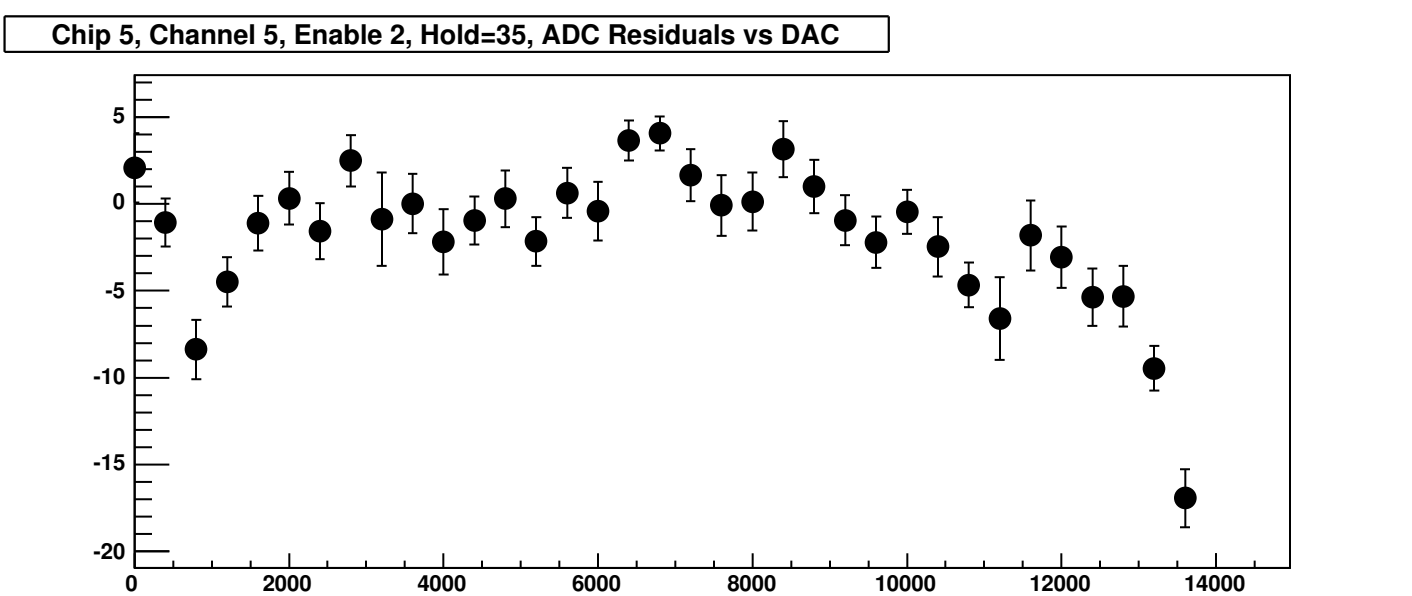

Chip 5, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 43.18 / 23
p0 -215.4 ± 0.6722
p1 0.02656 ± 0.0001081
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

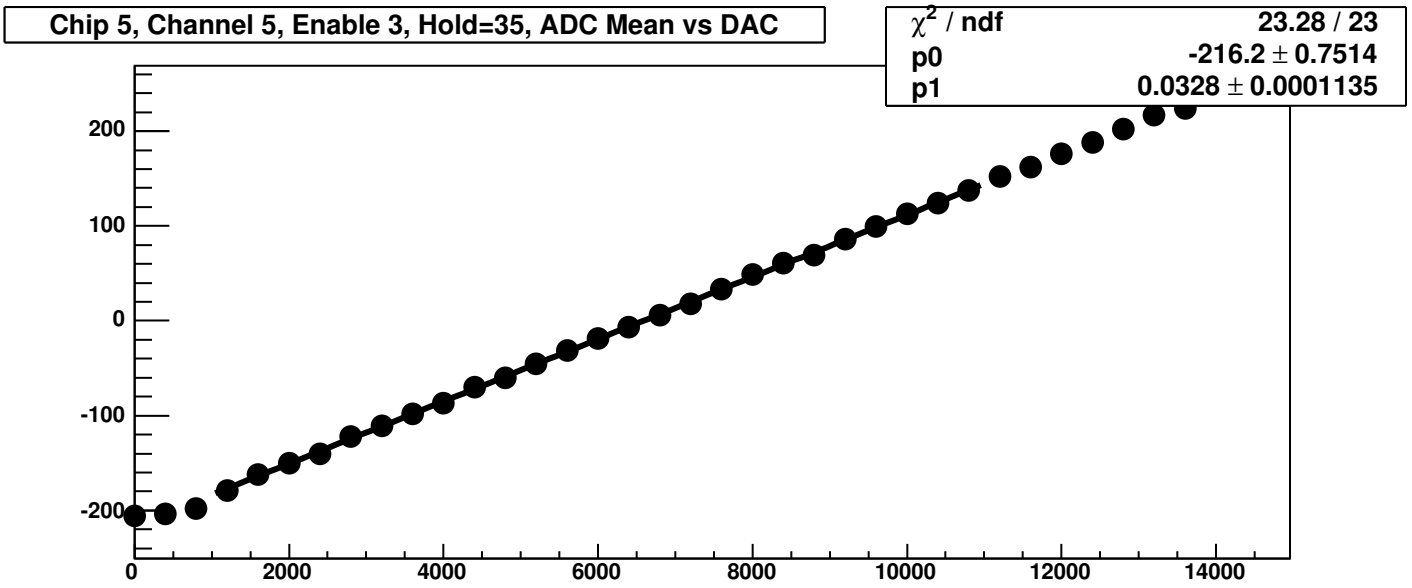

Chip 5, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

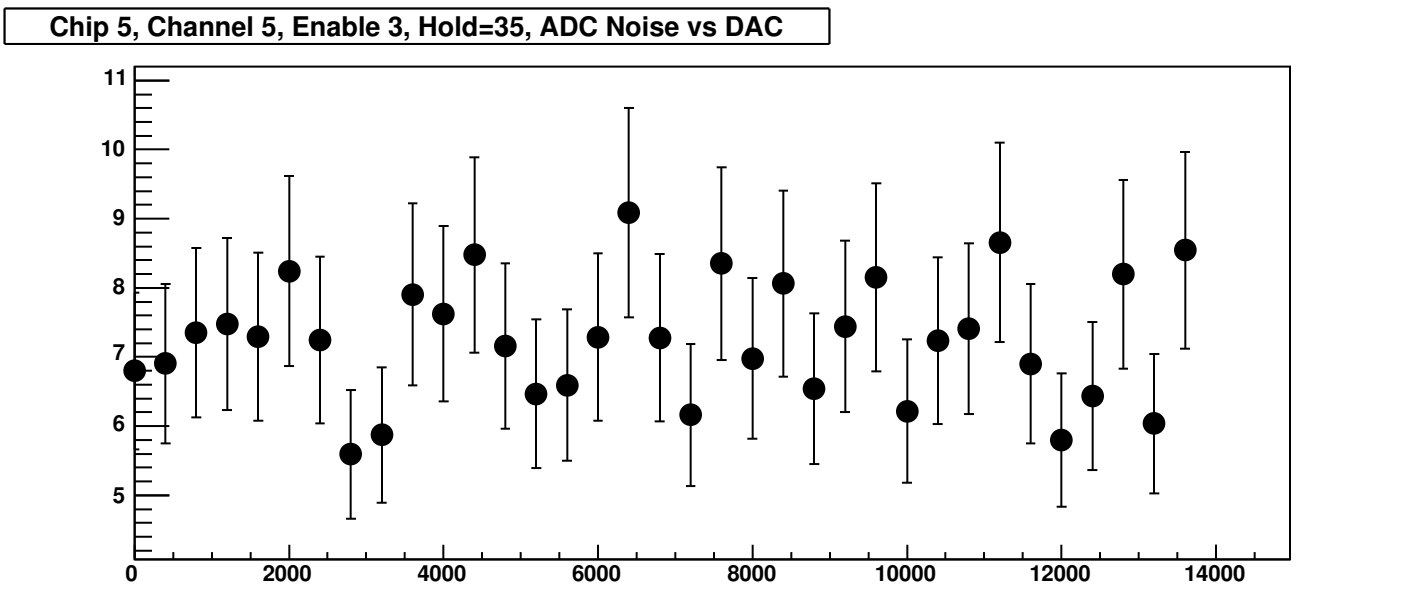

Chip 5, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

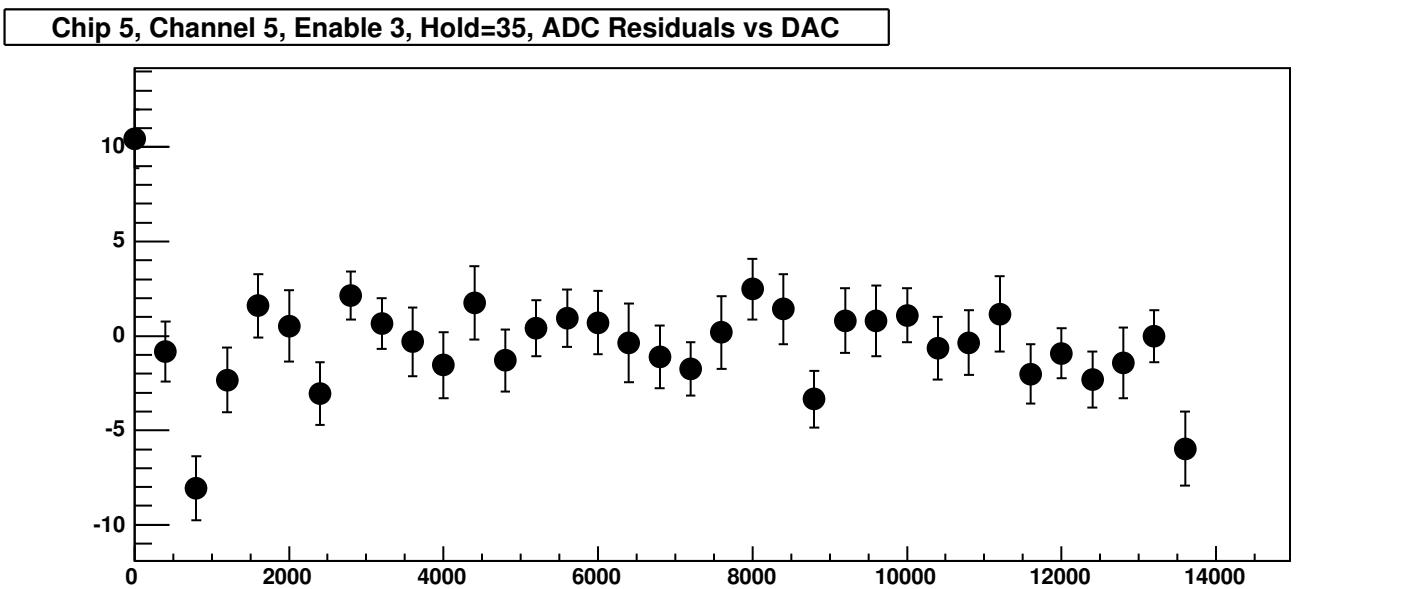

Chip 5, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 69.35 / 23
p0 -216.2 ± 0.7125
p1 0.02235 ± 0.0001041
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

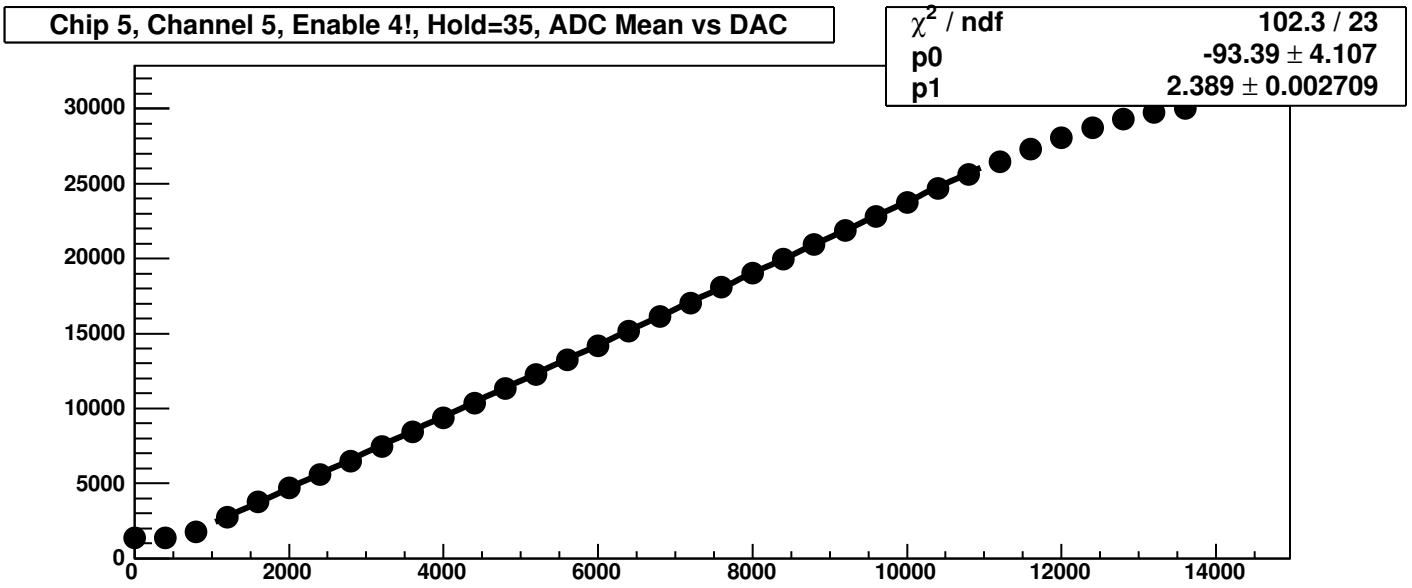

Chip 5, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

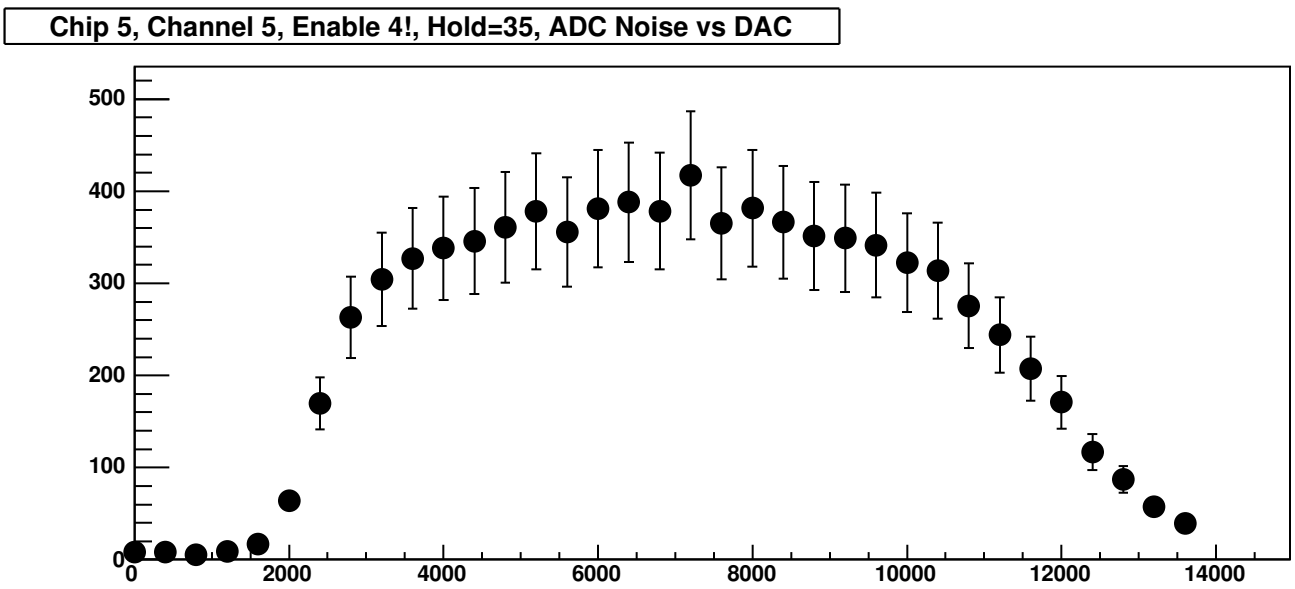

Chip 5, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

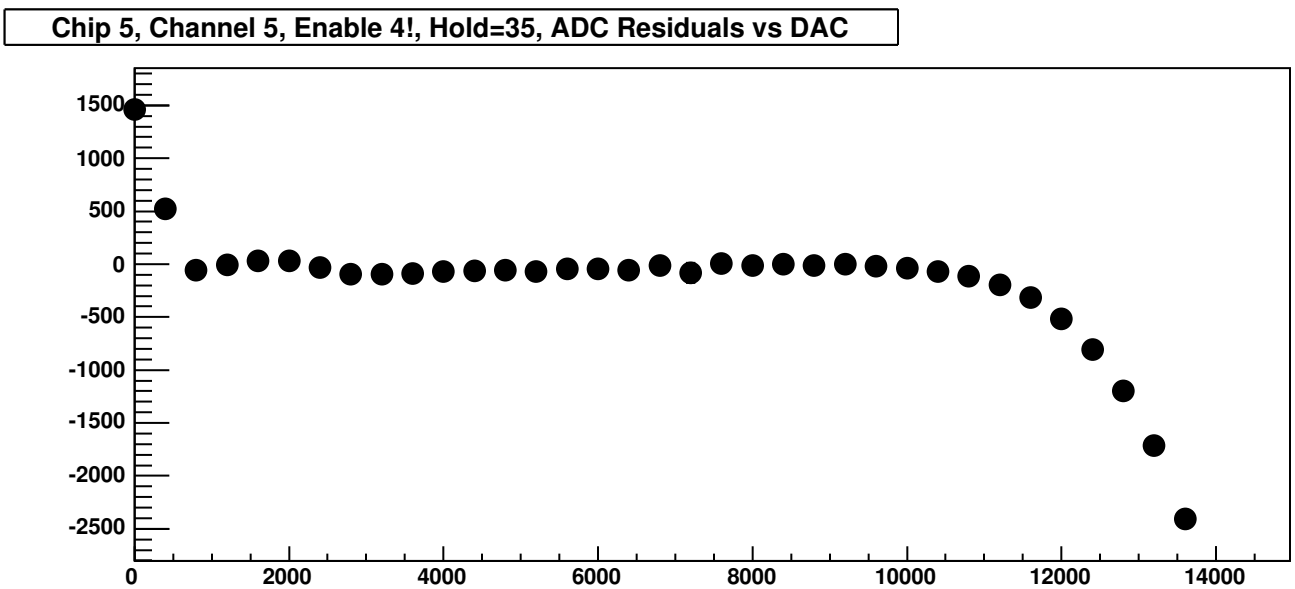

Chip 5, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.28 / 23
p0 -216.2 ± 0.7514
p1 0.0328 ± 0.0001135
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

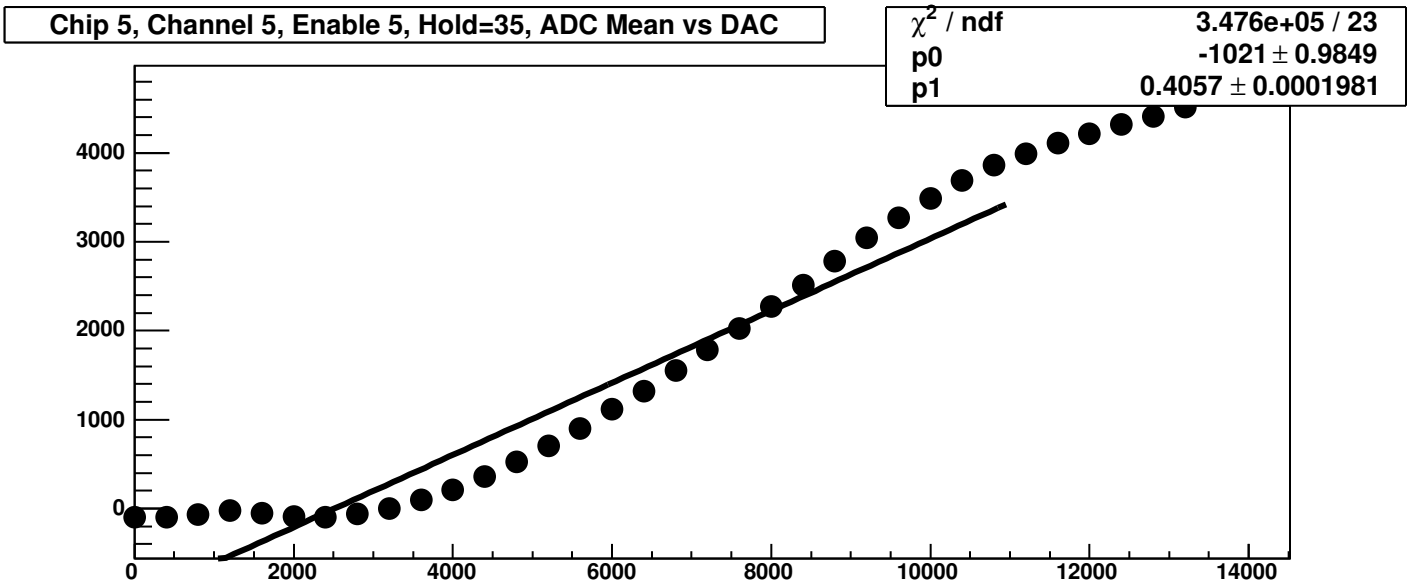

Chip 5, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

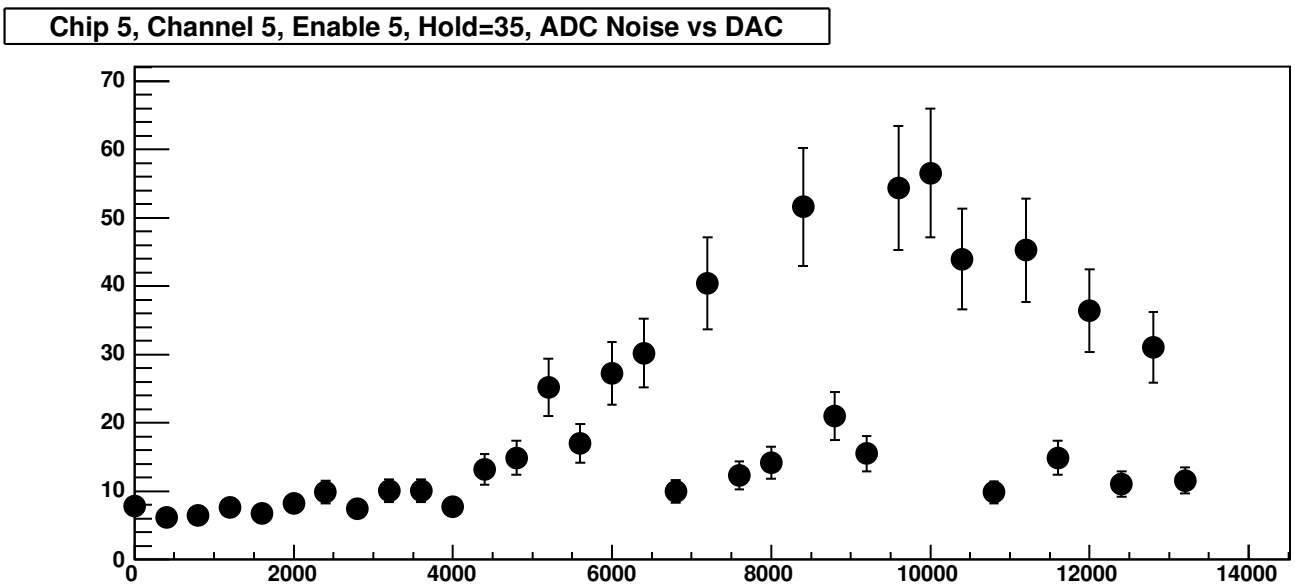

Chip 5, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

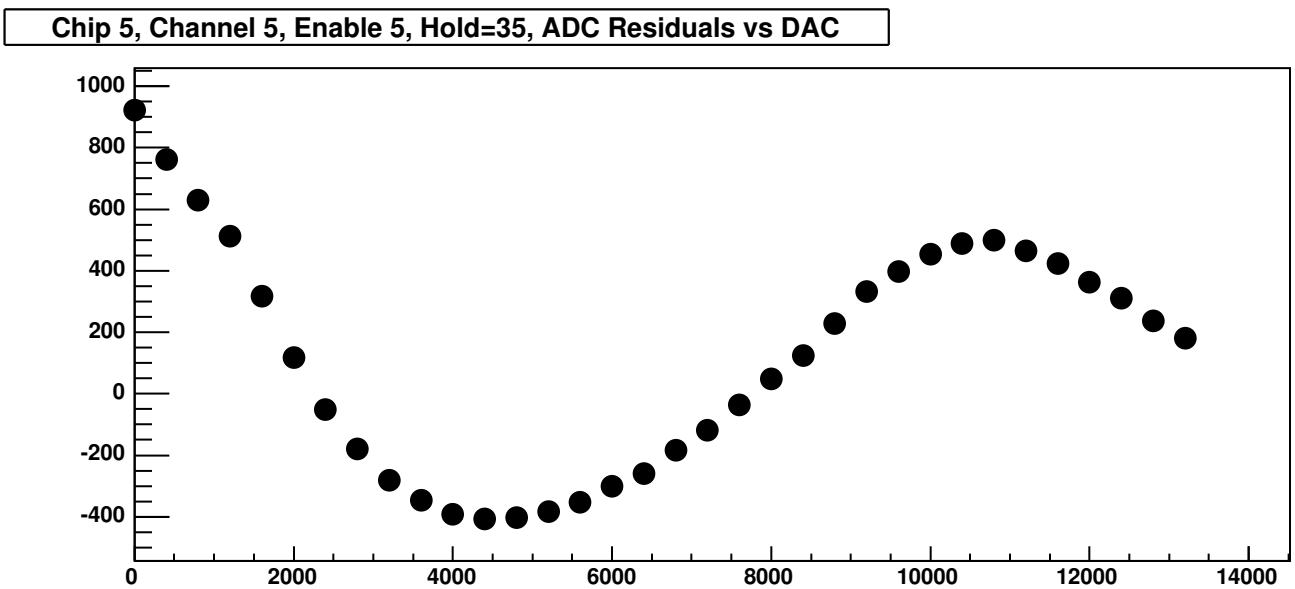

Chip 5, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 102.3 / 23
p0 -93.39 ± 4.107
p1 2.389 ± 0.002709
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

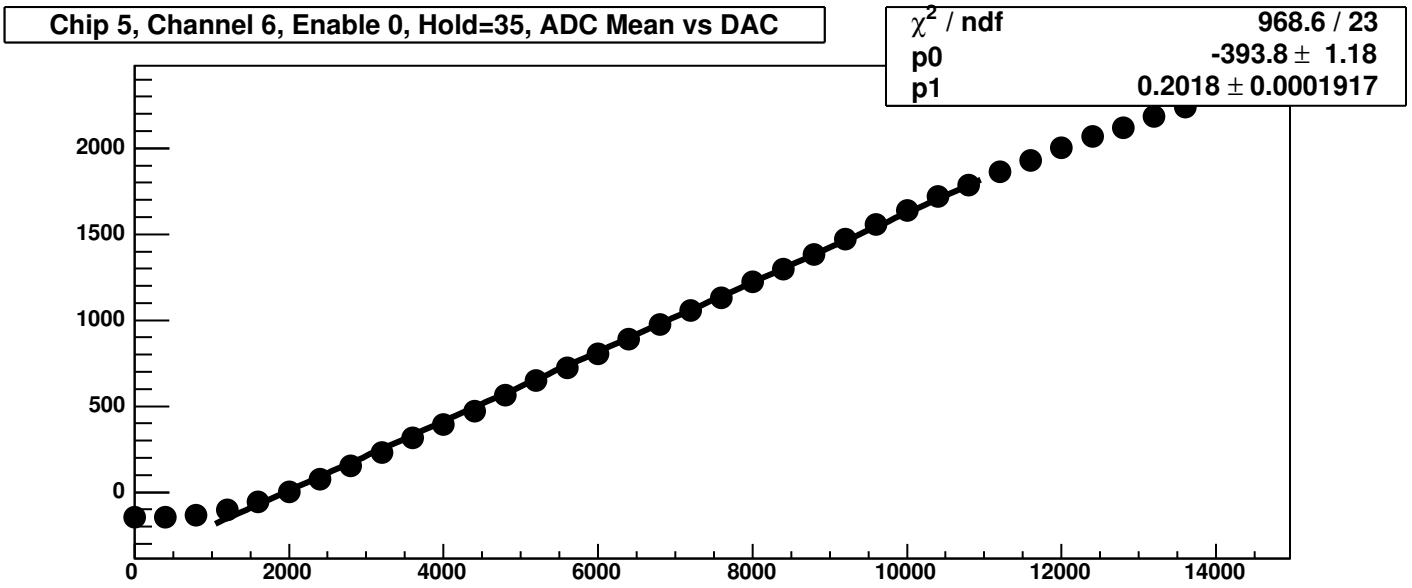

Chip 5, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

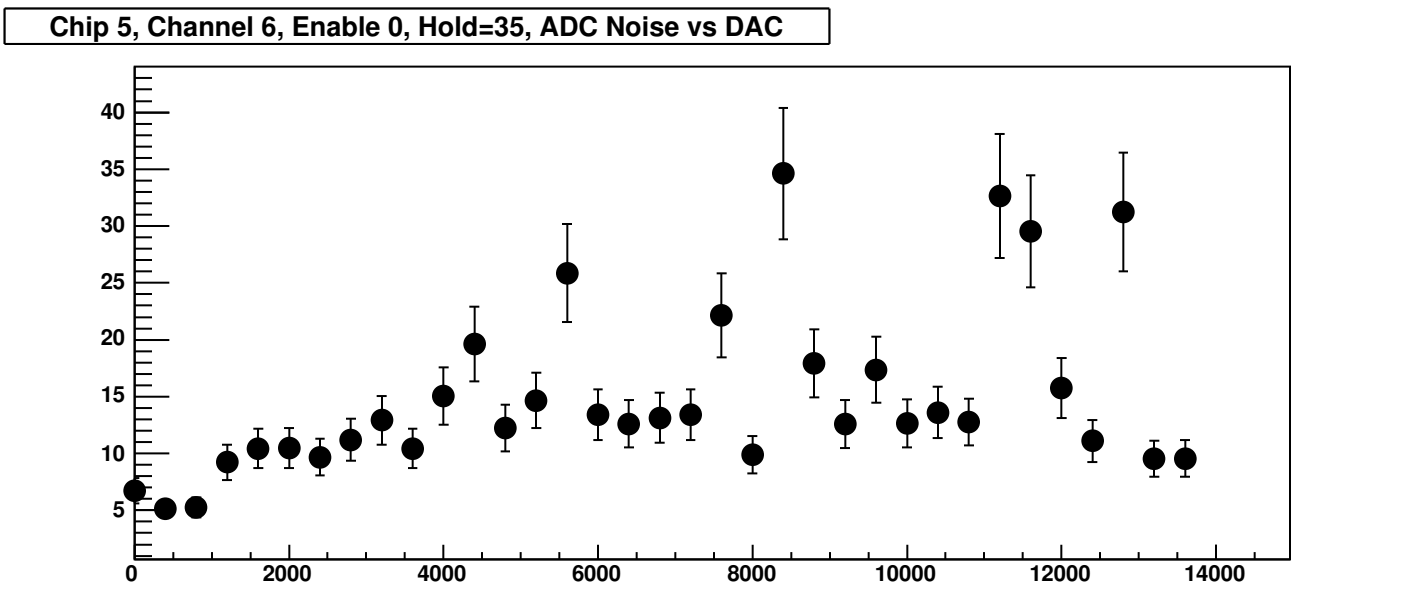

Chip 5, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

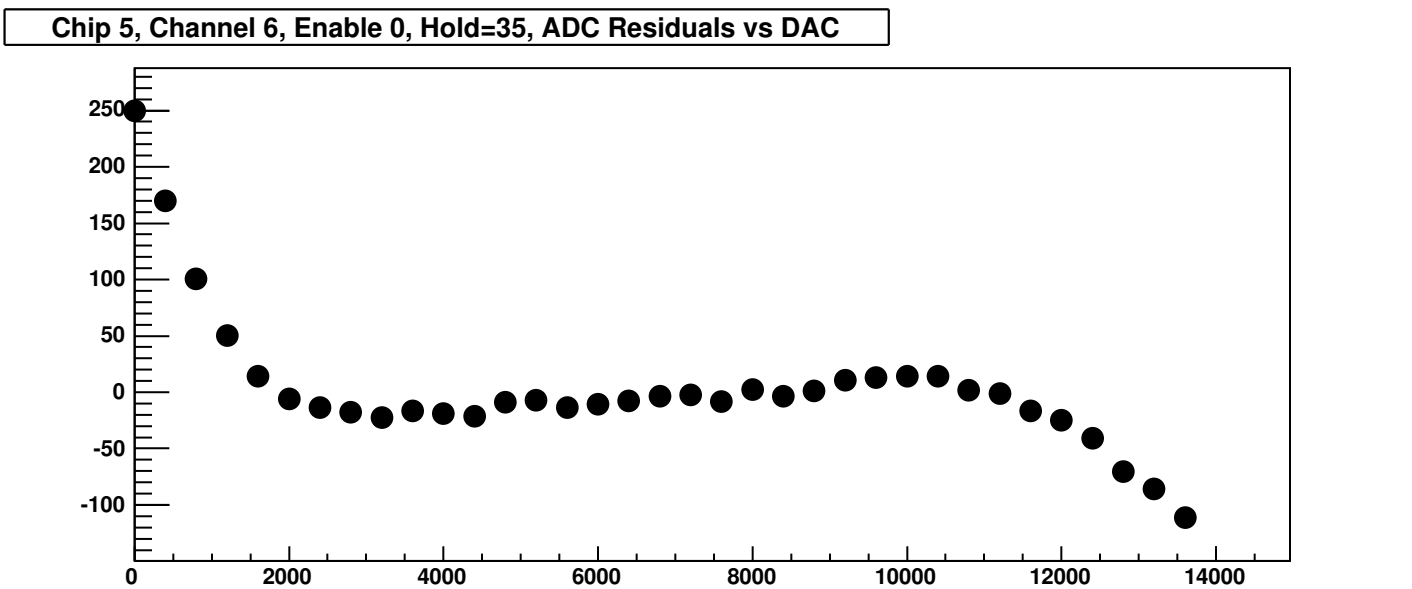

Chip 5, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.476e+05 / 23
p0 -1021 ± 0.9849
p1 0.4057 ± 0.0001981
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

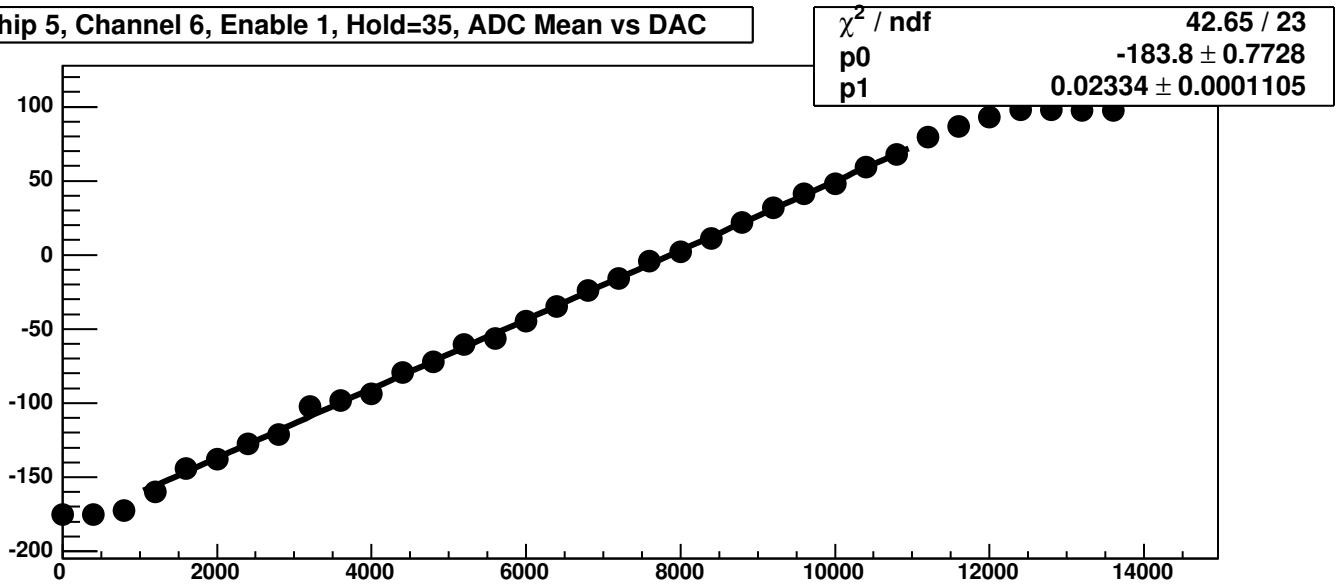

Chip 5, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 968.6 / 23
p0 -393.8 ± 1.18
p1 0.2018 ± 0.0001917
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

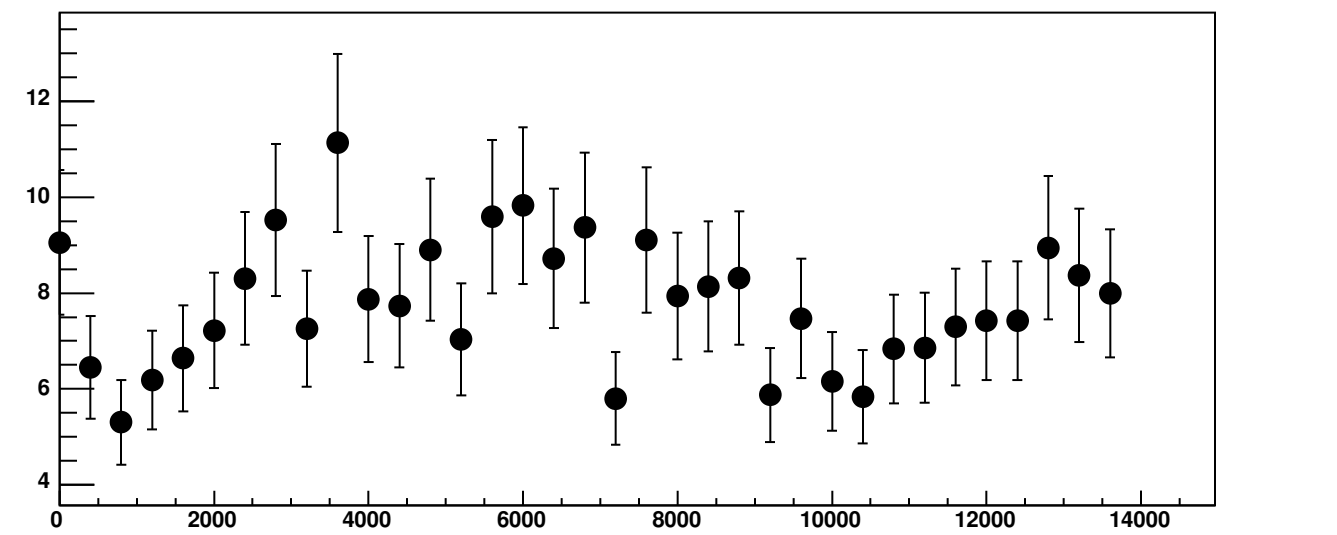

Chip 5, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

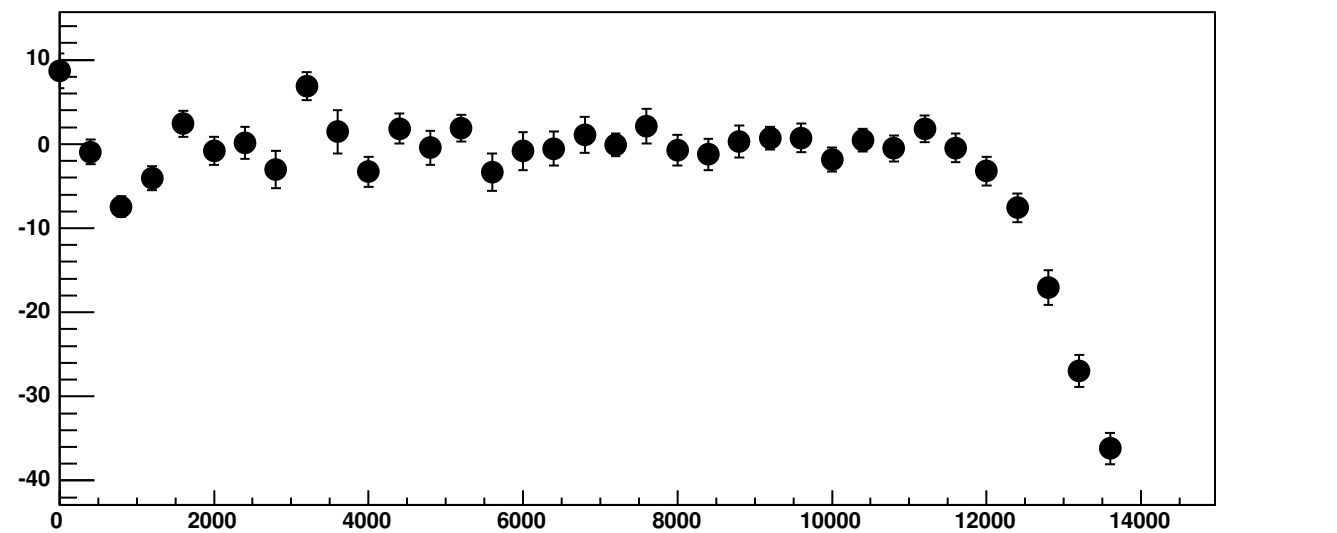
Chip 5, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

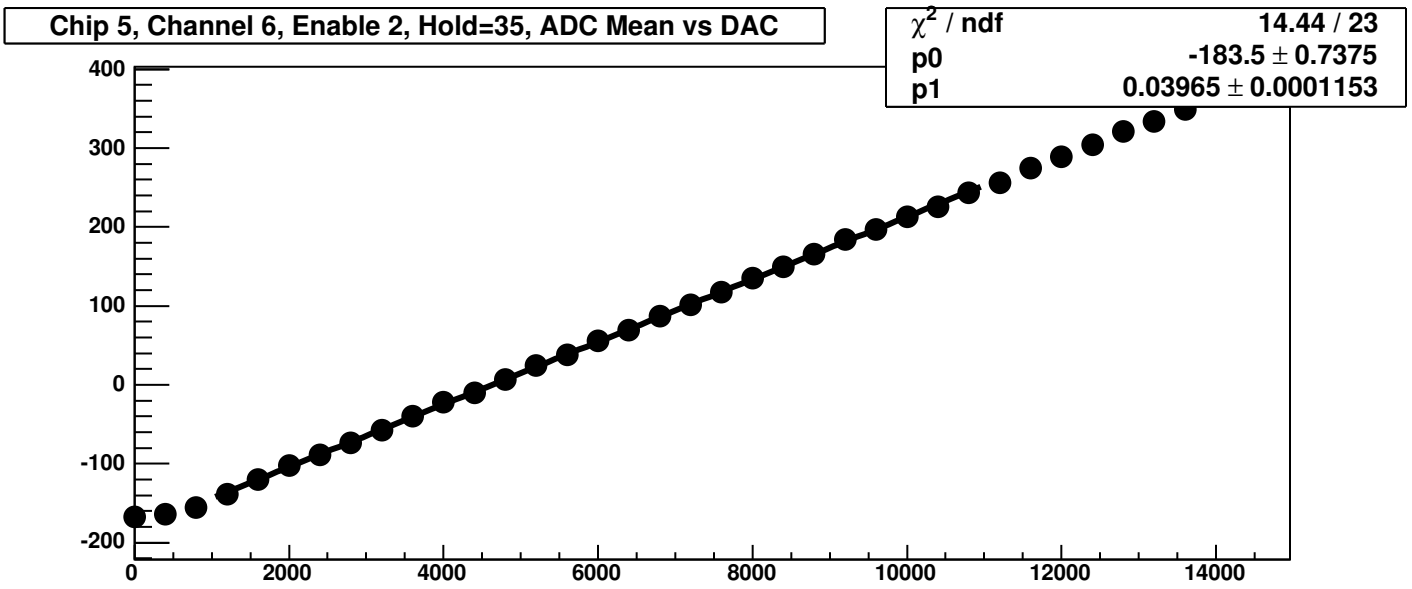


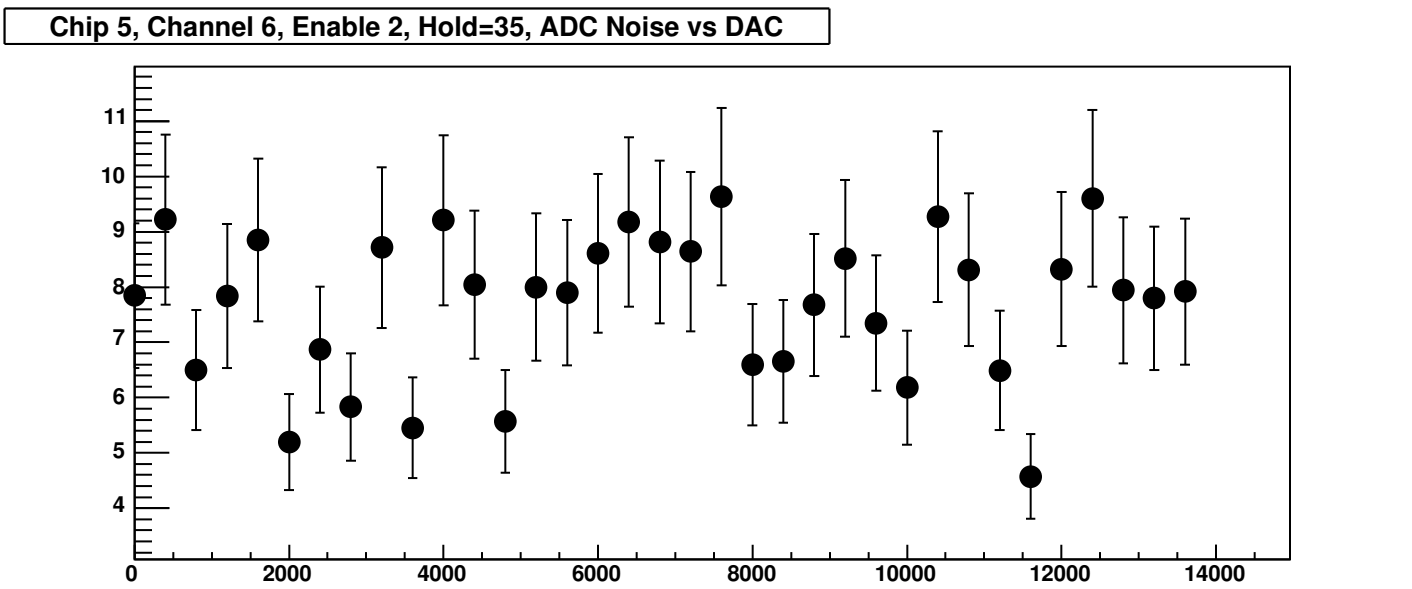
Chip 5, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC

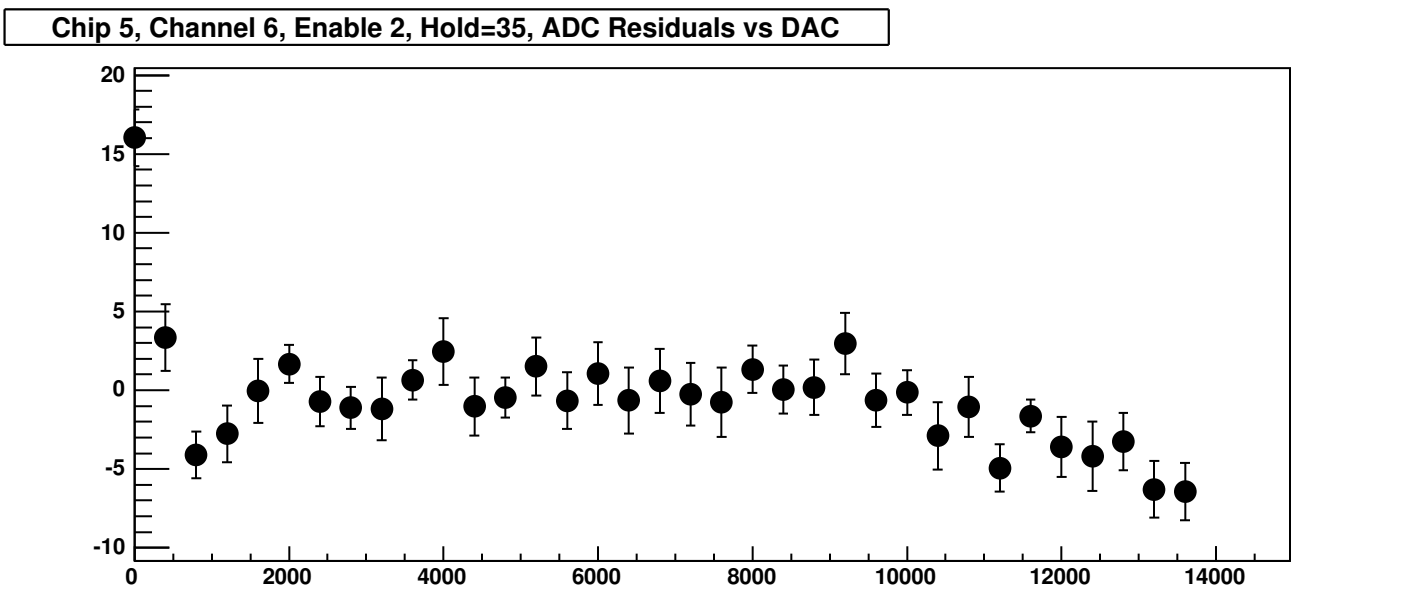


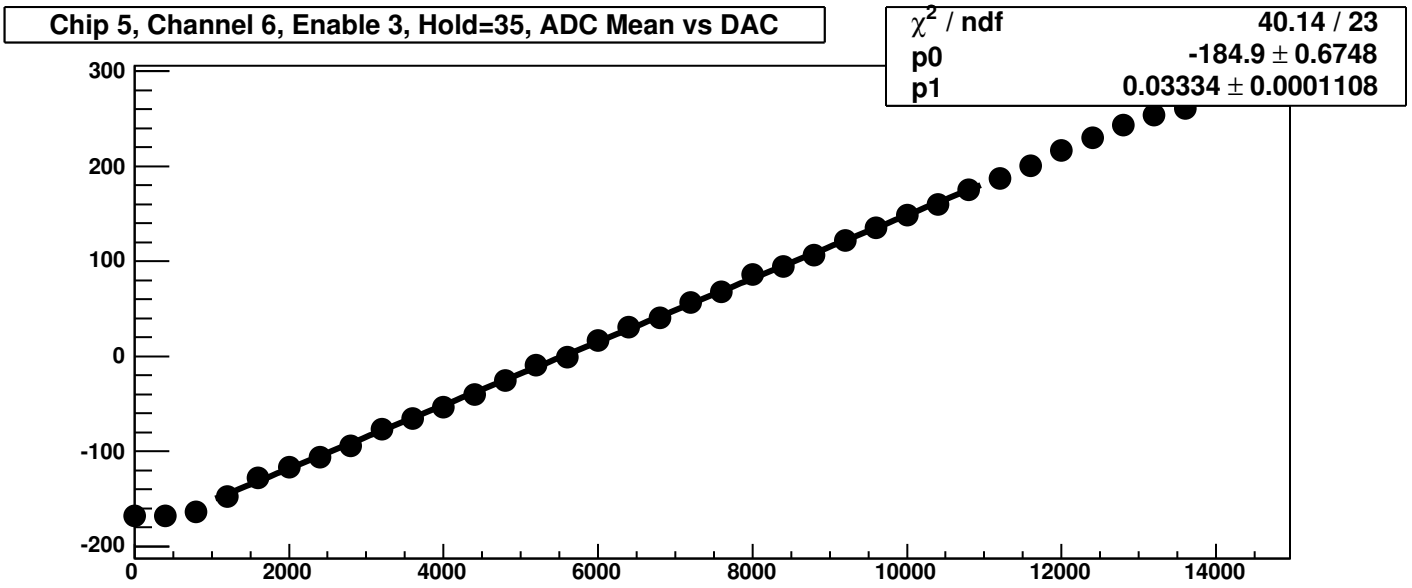
Chip 5, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

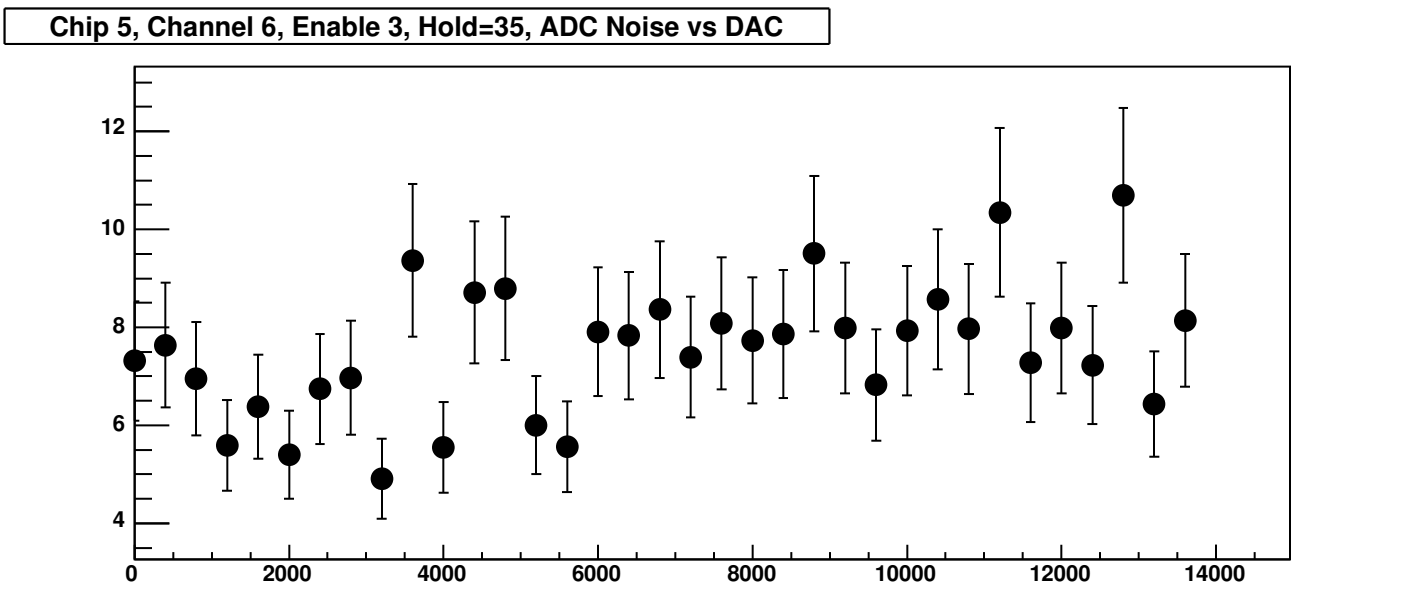


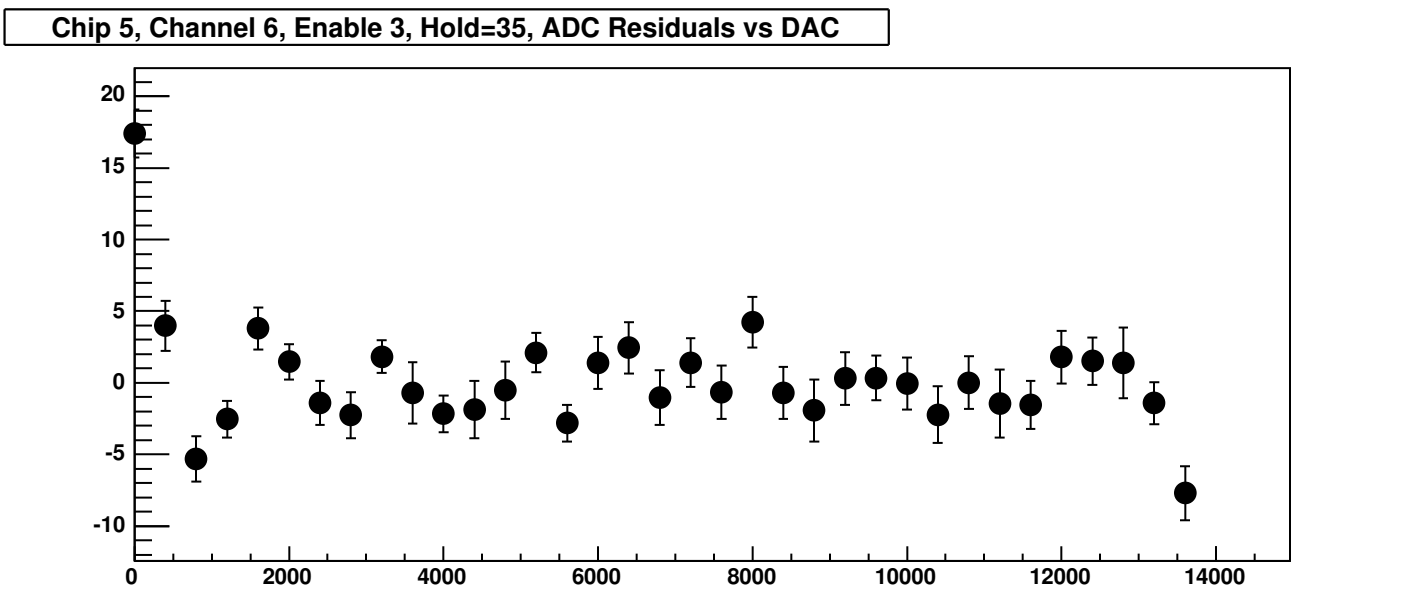

Chip 5, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 14.44 / 23
p0 -183.5 ± 0.7375
p1 0.03965 ± 0.0001153
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

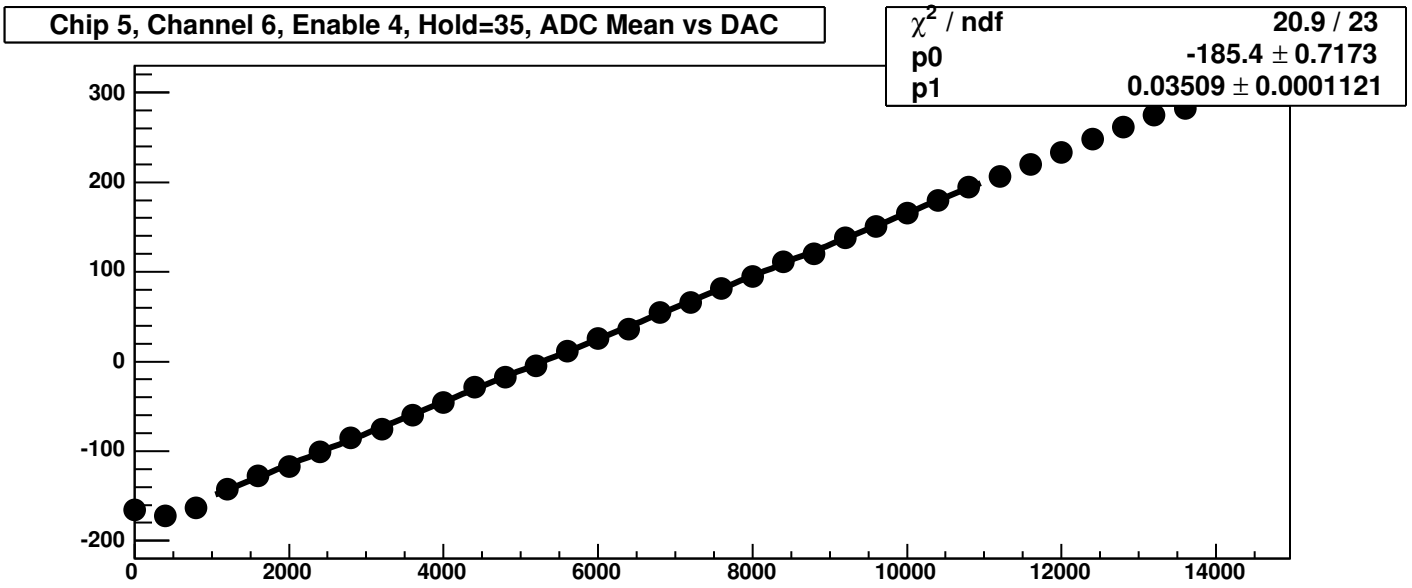

Chip 5, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

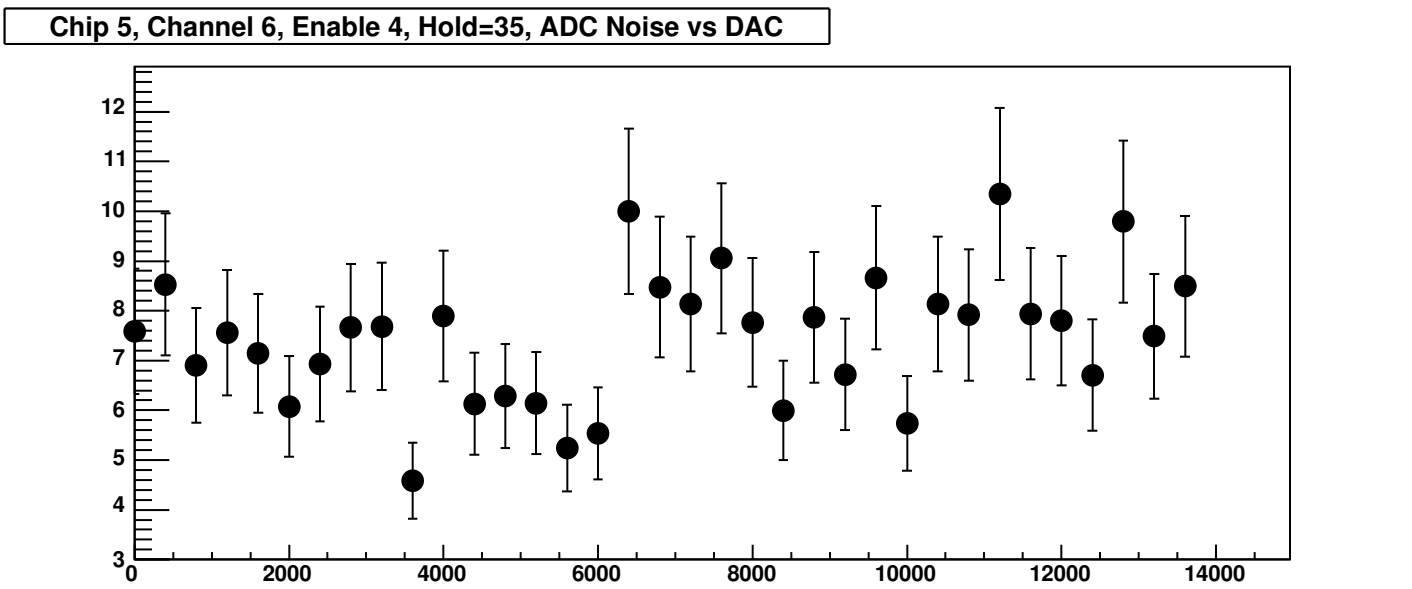

Chip 5, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

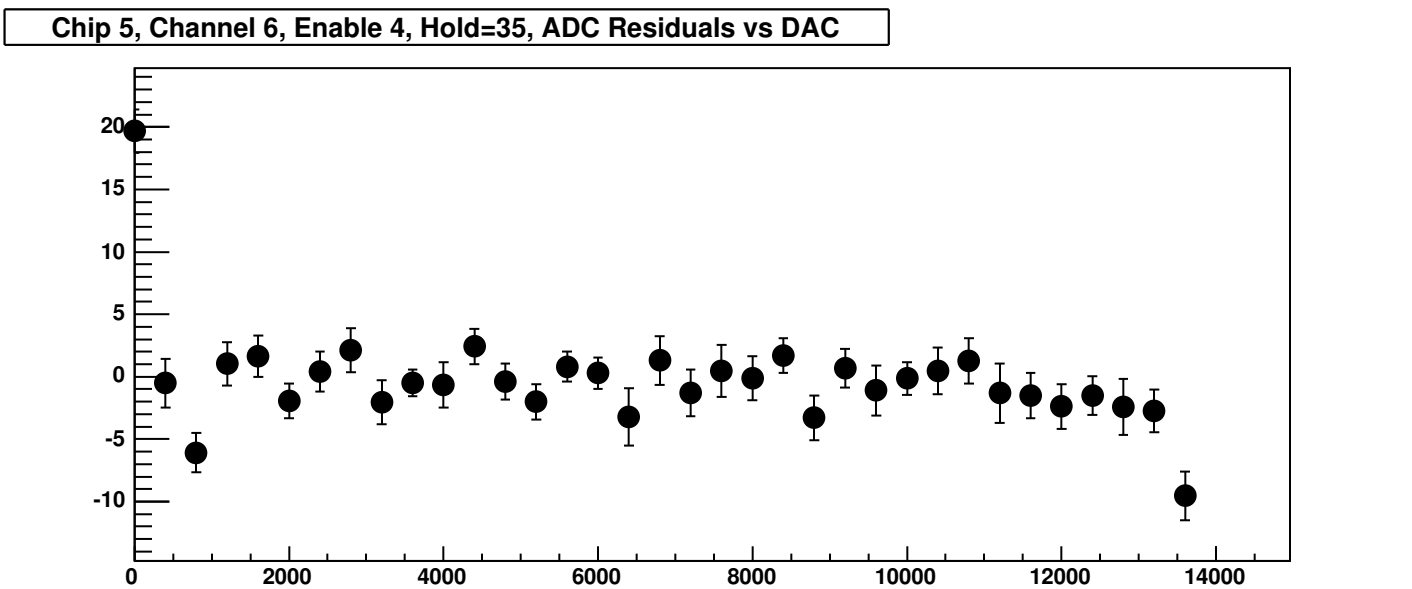

Chip 5, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 40.14 / 23
p0 -184.9 ± 0.6748
p1 0.03334 ± 0.0001108
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

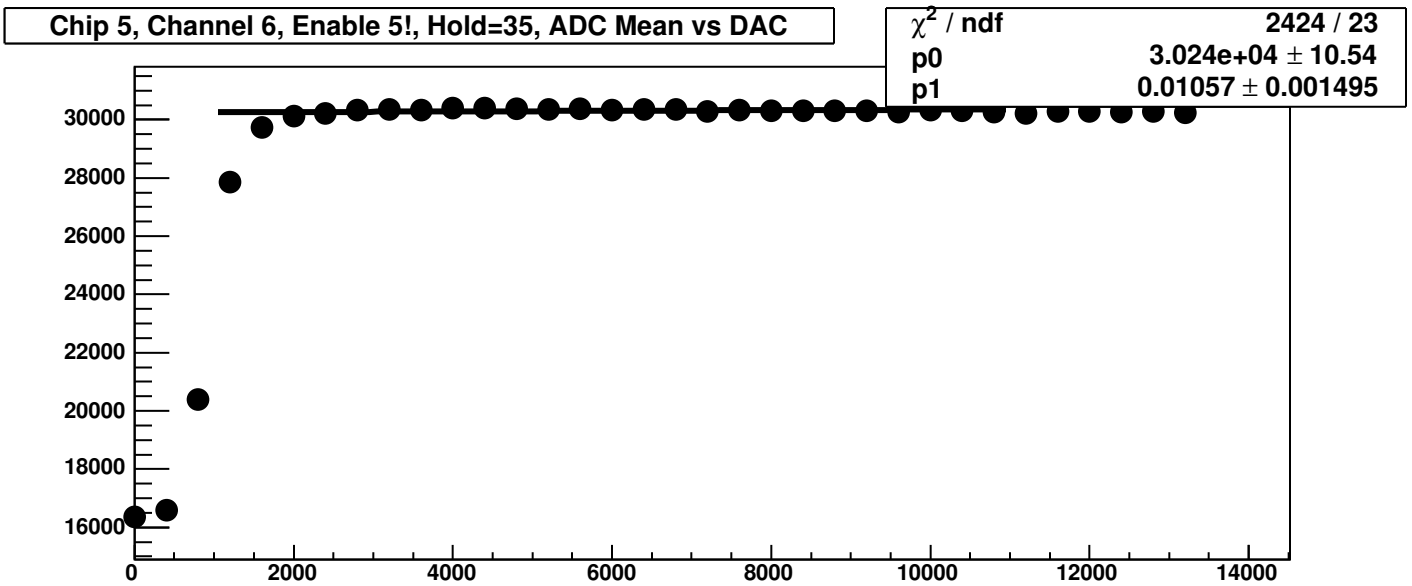

Chip 5, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

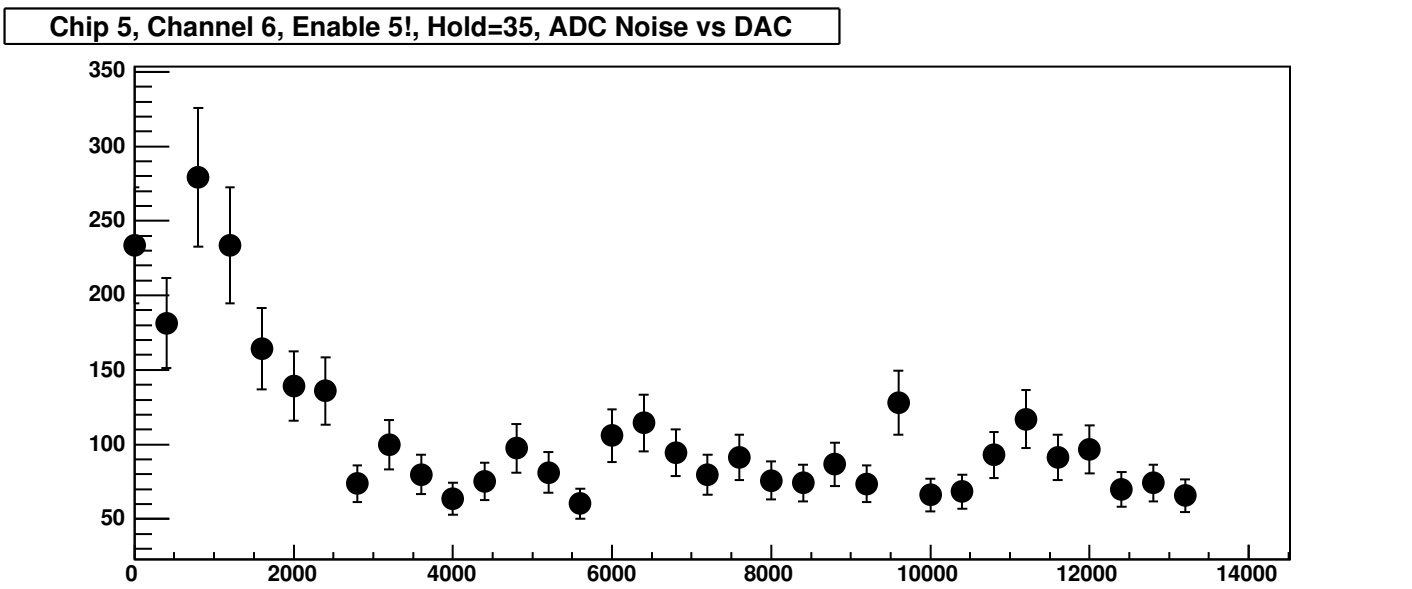

Chip 5, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

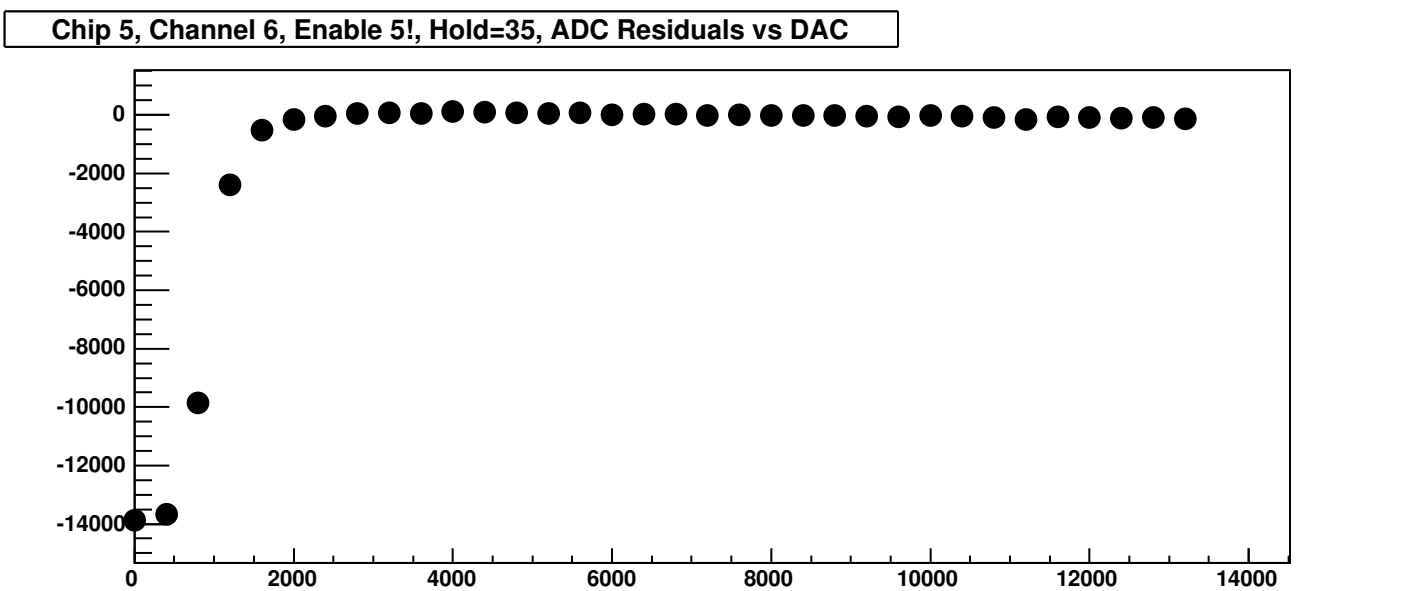

Chip 5, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 20.9 / 23
p0 -185.4 ± 0.7173
p1 0.03509 ± 0.0001121
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

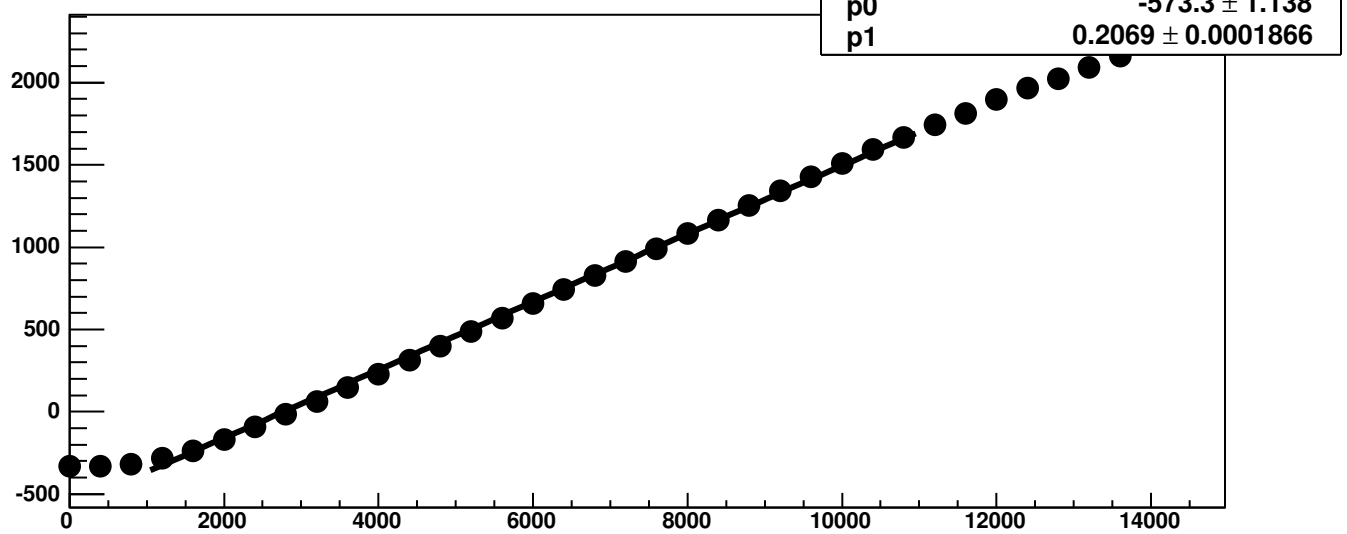

Chip 5, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2424 / 23
p0 3.024e+04 ± 10.54
p1 0.01057 ± 0.001495
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

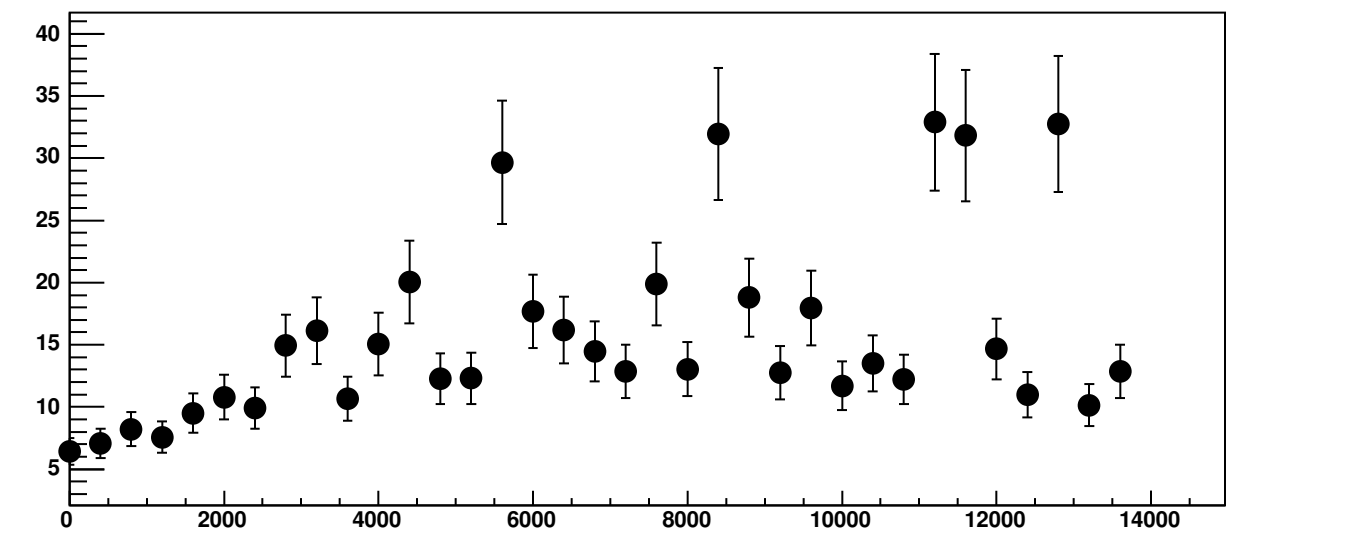

Chip 5, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

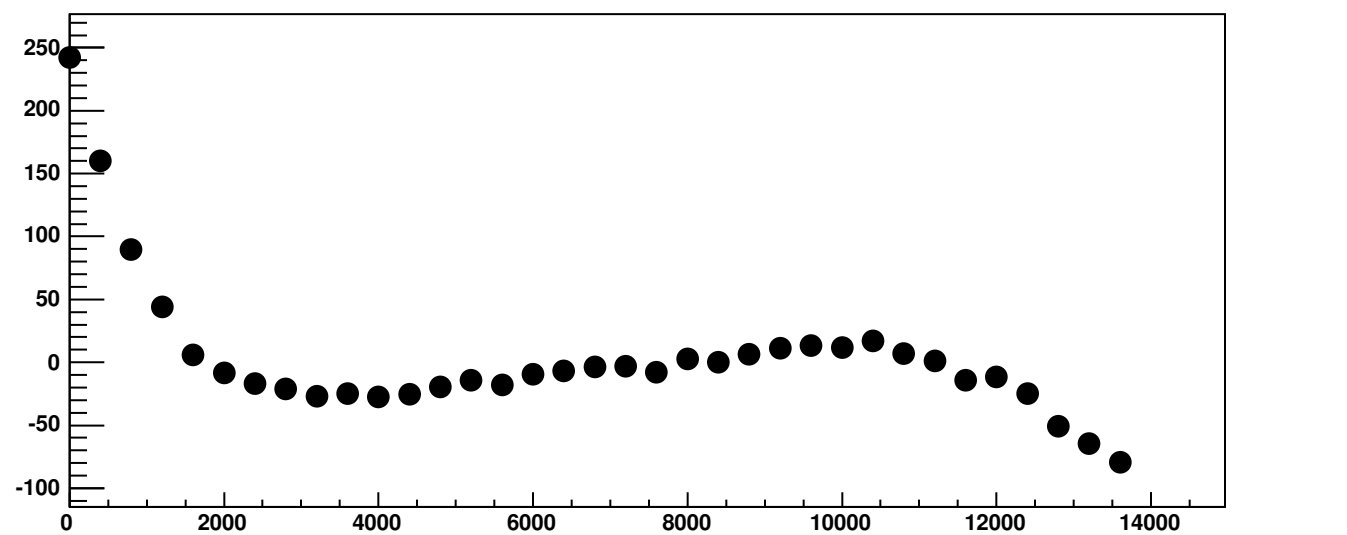
Chip 5, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

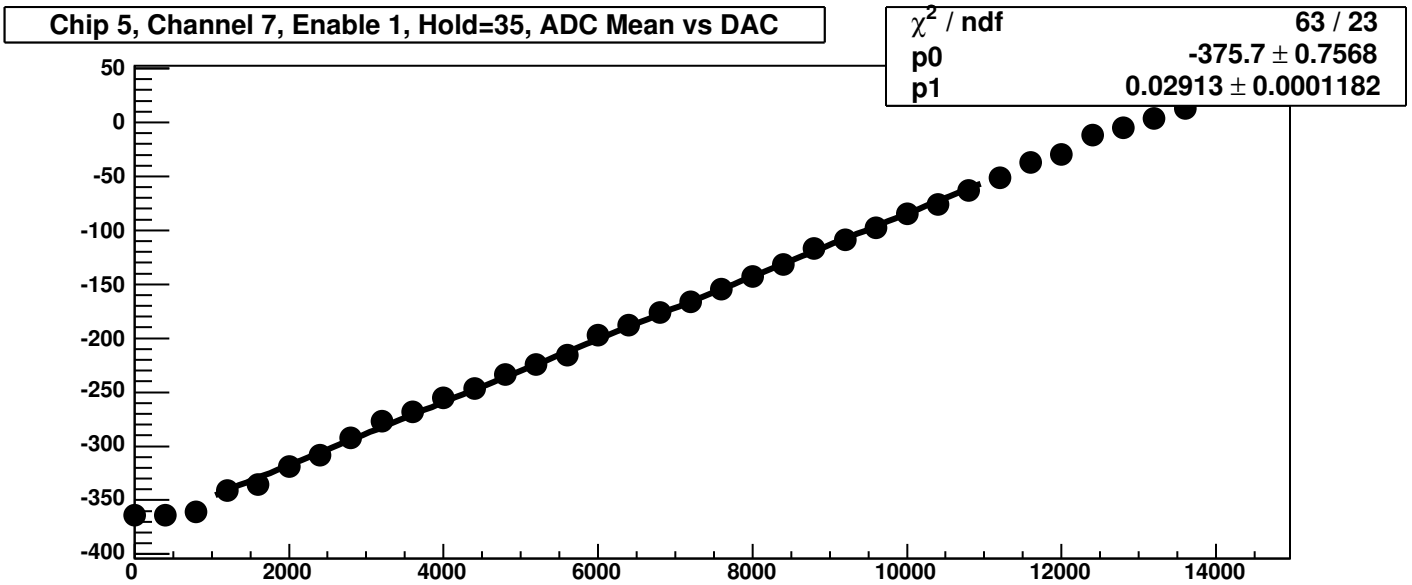


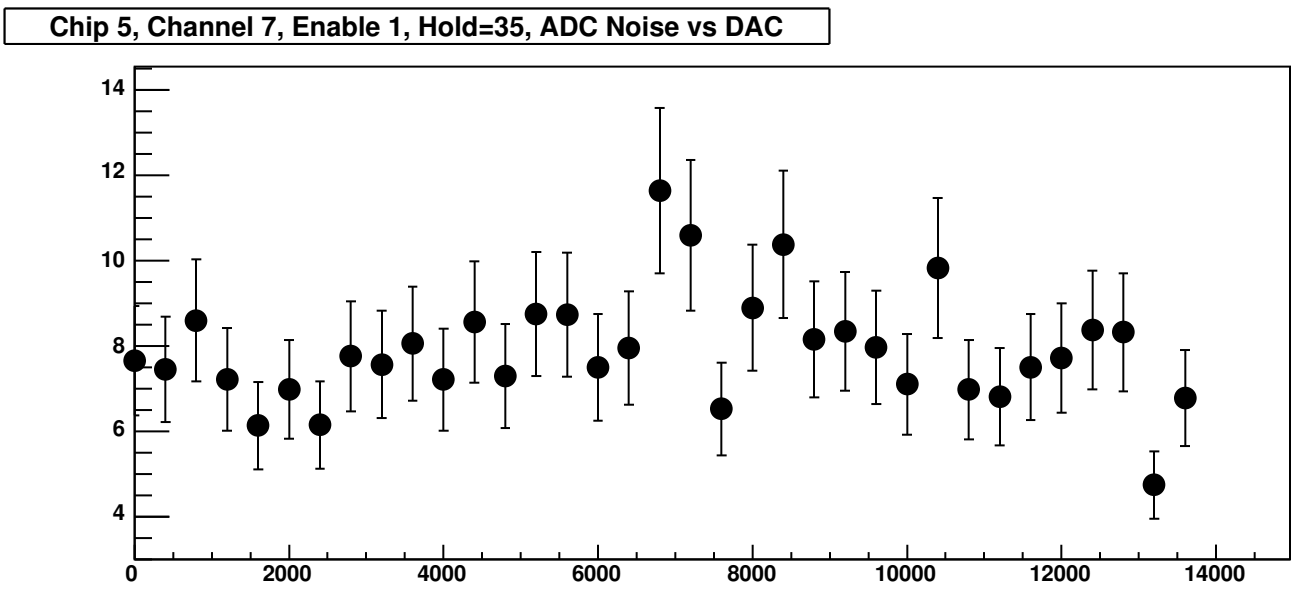
Chip 5, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC

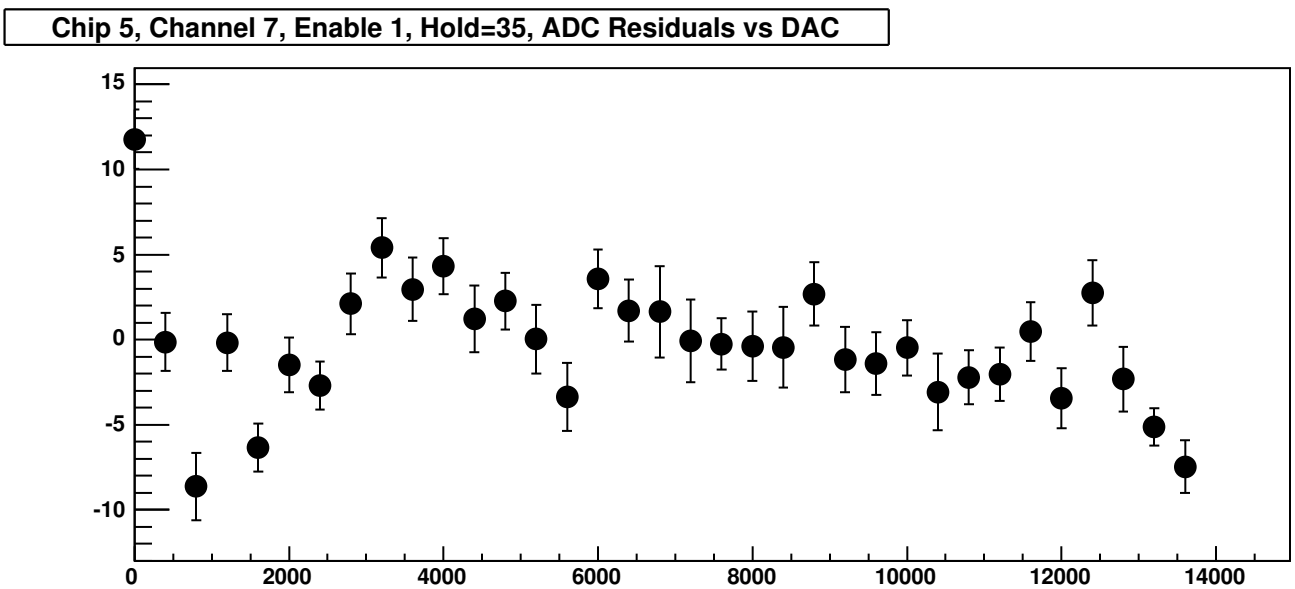


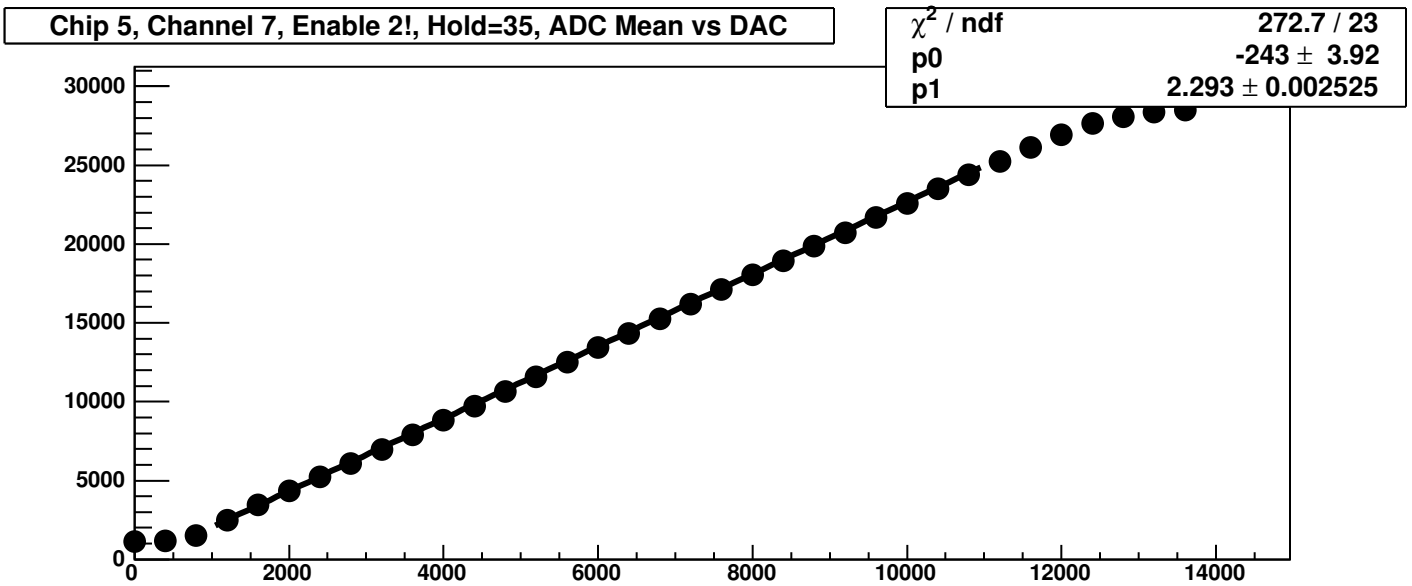
Chip 5, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

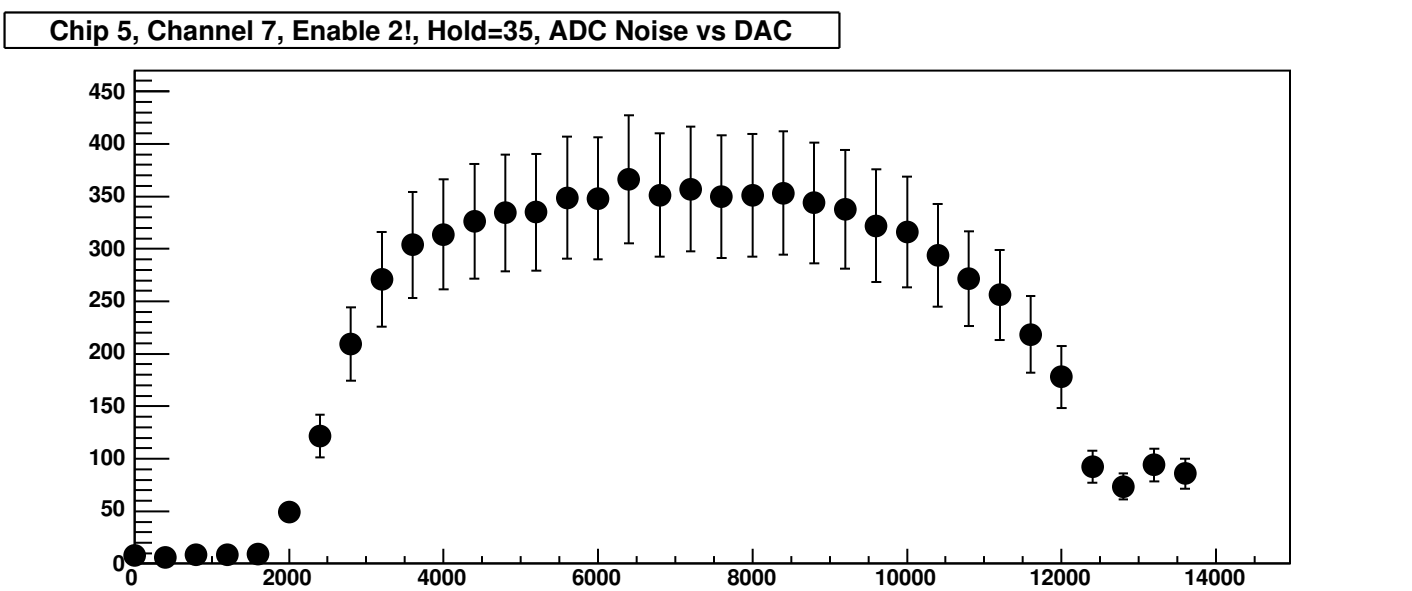


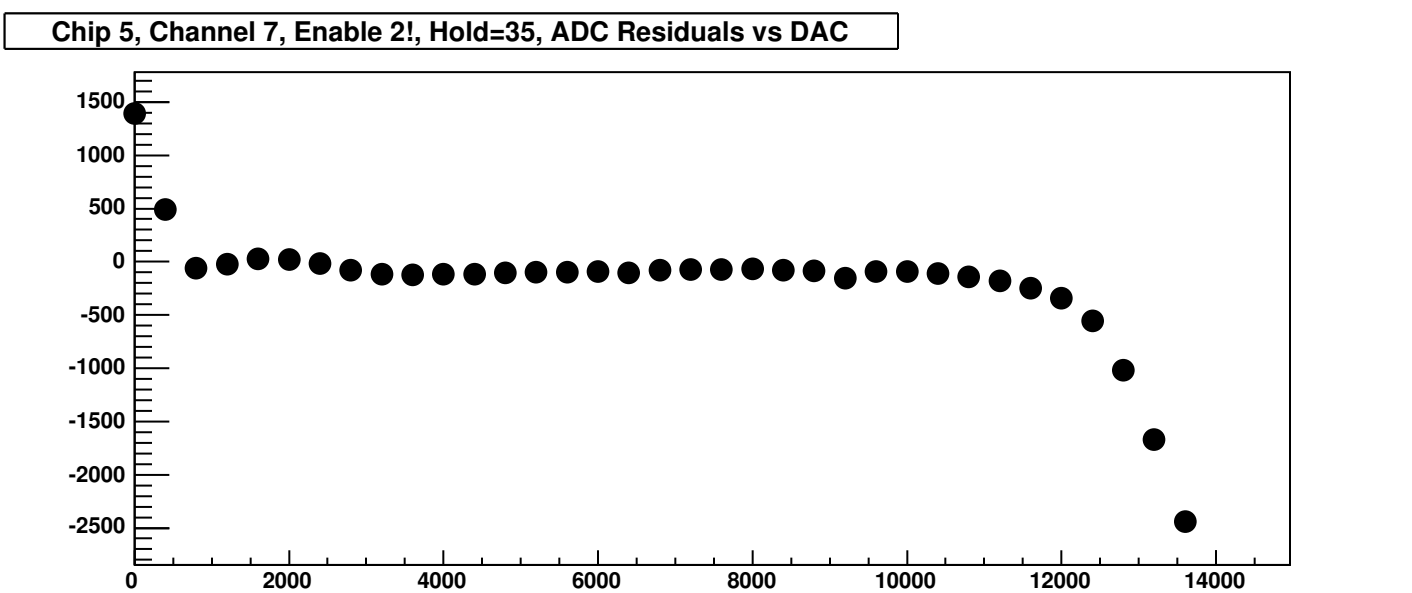

Chip 5, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 63 / 23
p0 -375.7 ± 0.7568
p1 0.02913 ± 0.0001182
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

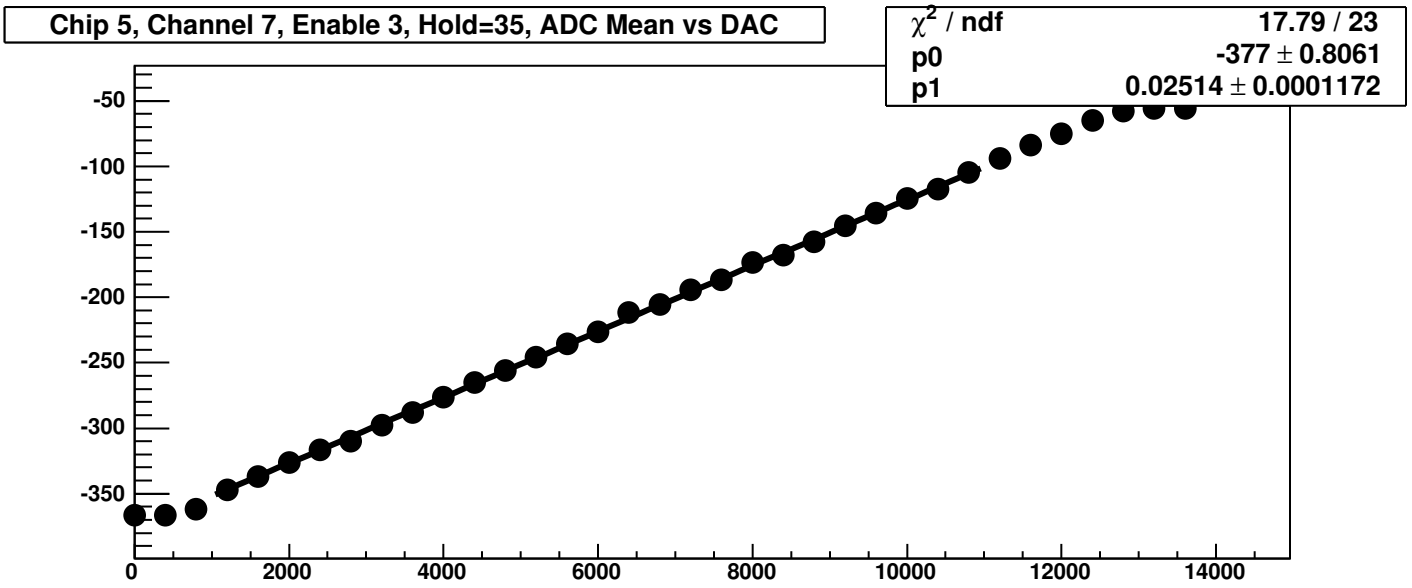

Chip 5, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

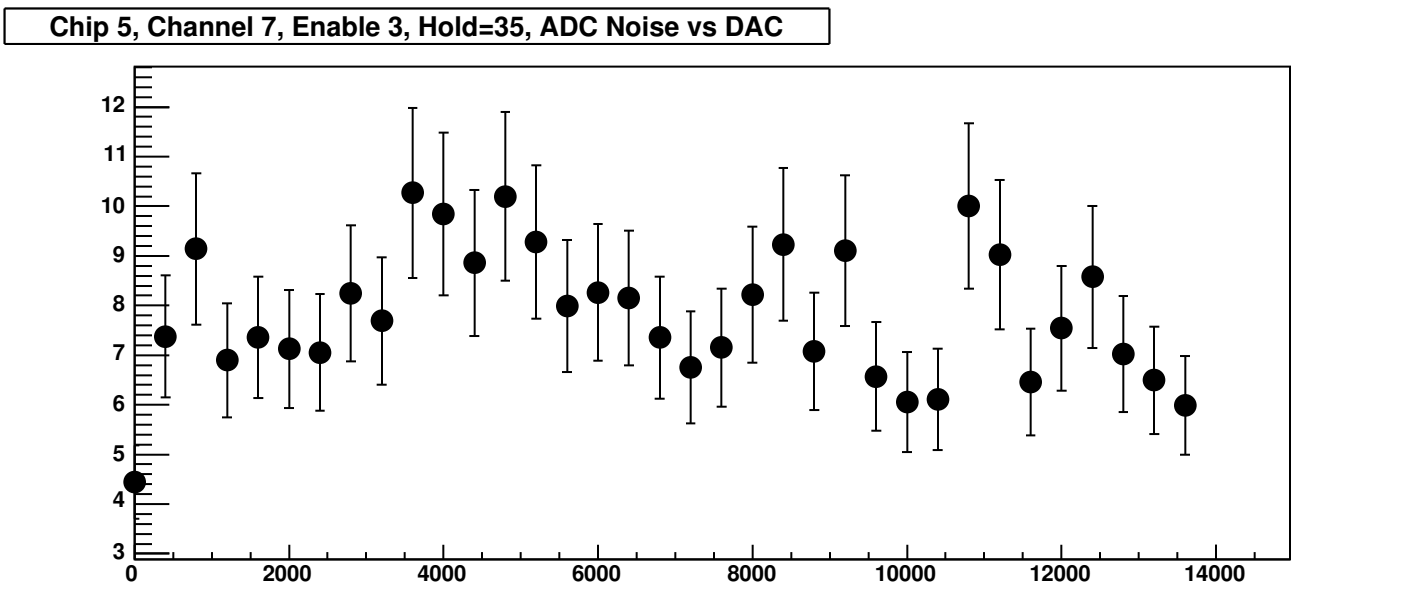

Chip 5, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

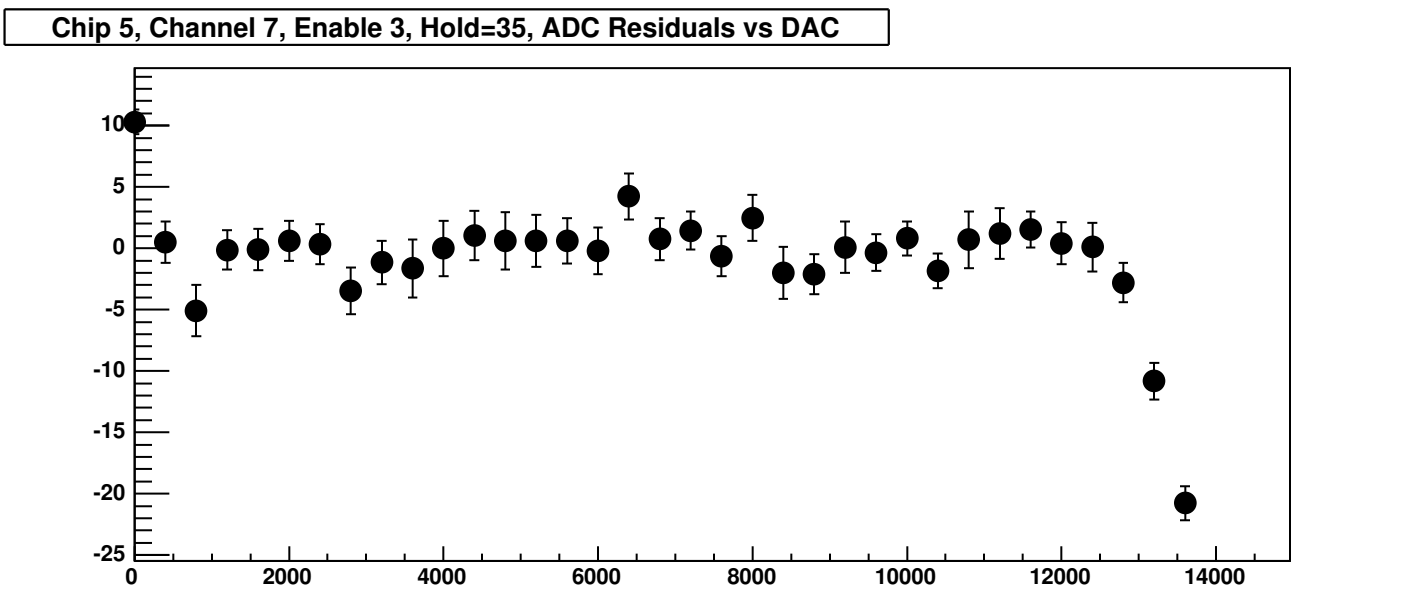

Chip 5, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 272.7 / 23
p0 -243 ± 3.92
p1 2.293 ± 0.002525
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

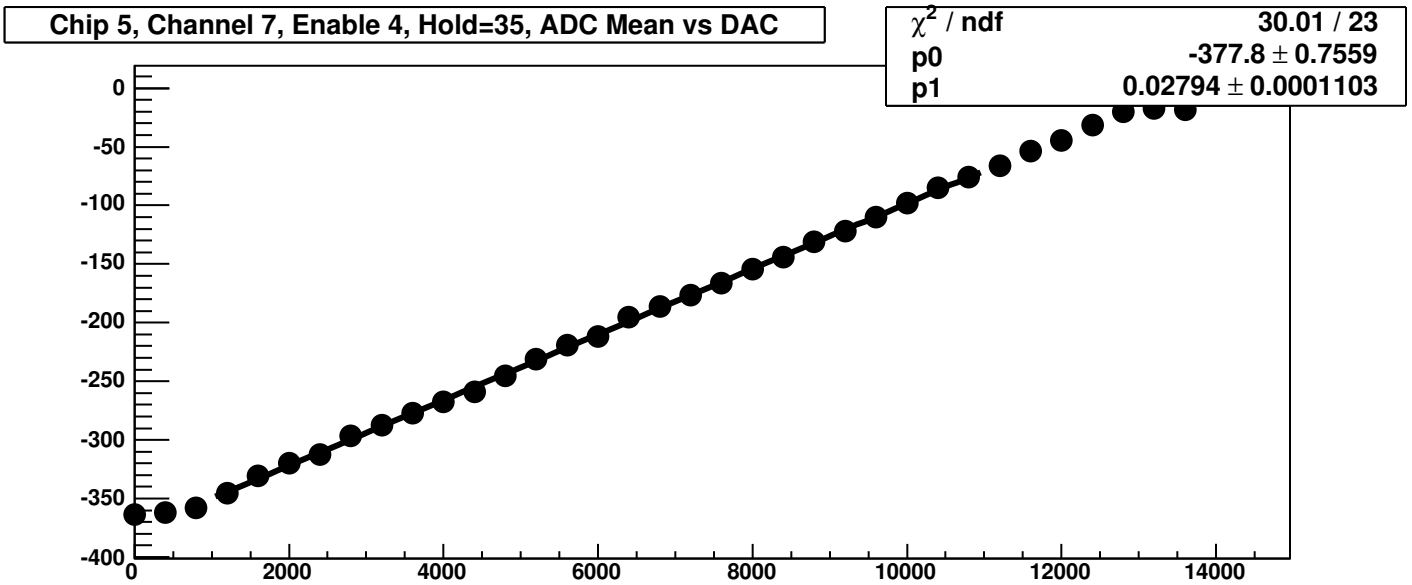

Chip 5, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

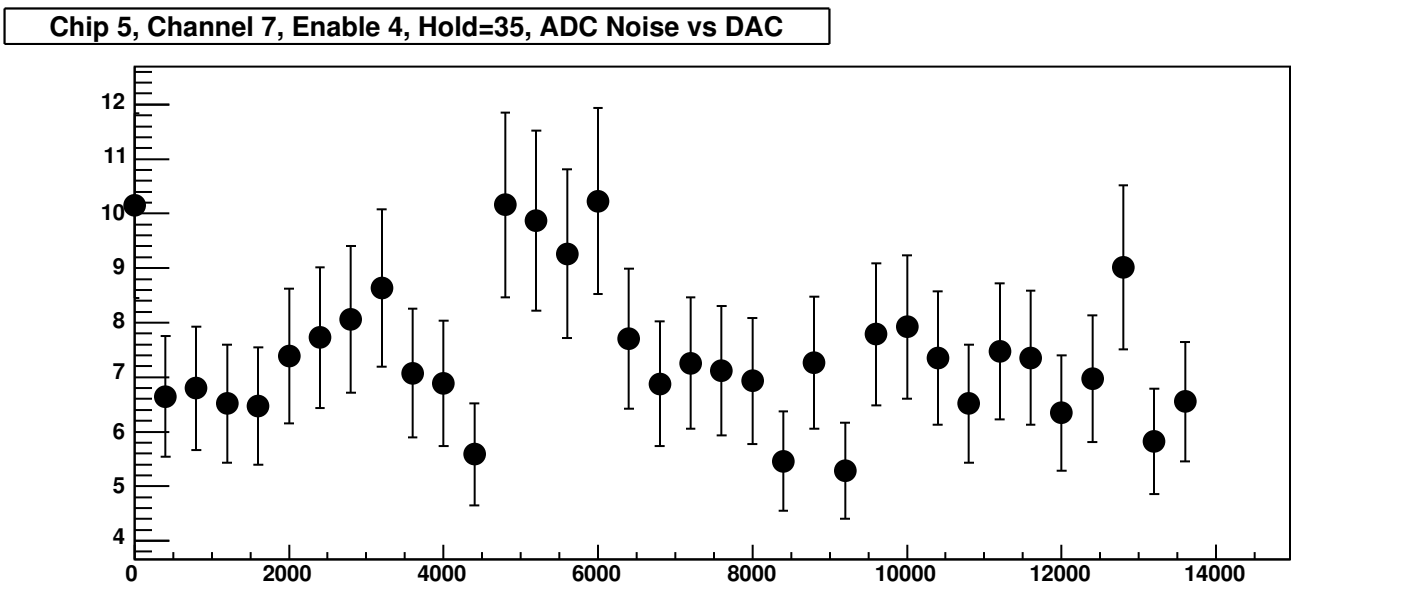

Chip 5, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

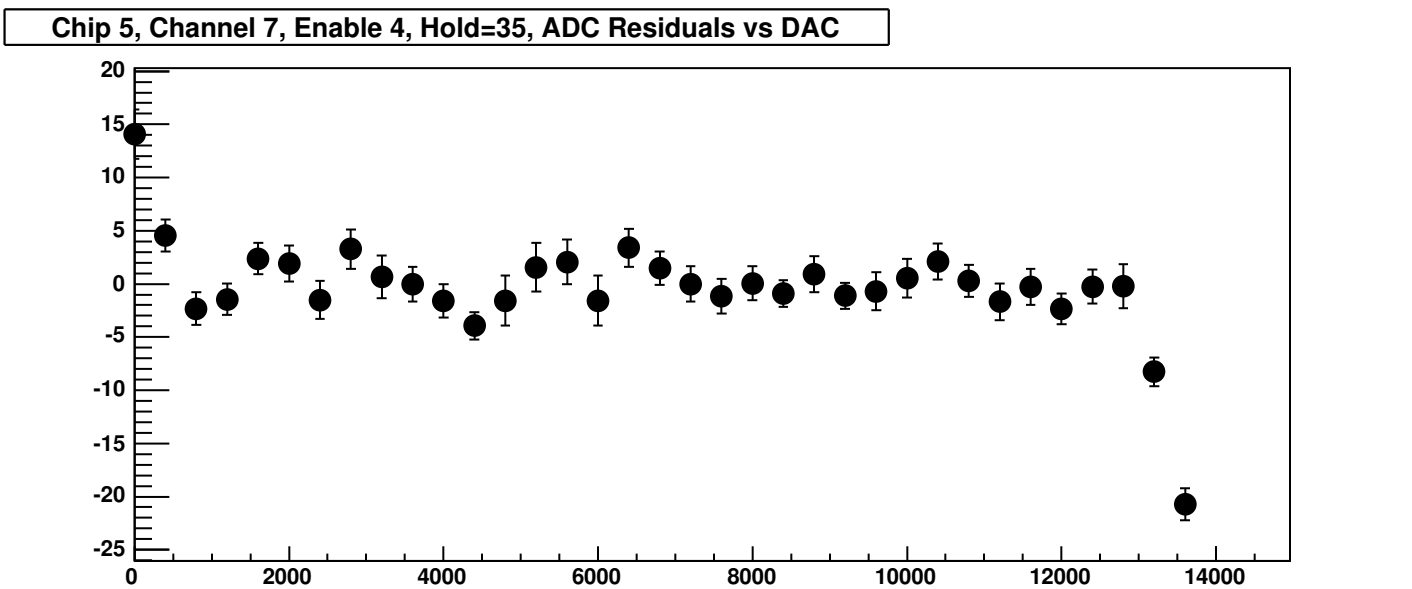

Chip 5, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 17.79 / 23
p0 -377 ± 0.8061
p1 0.02514 ± 0.0001172
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

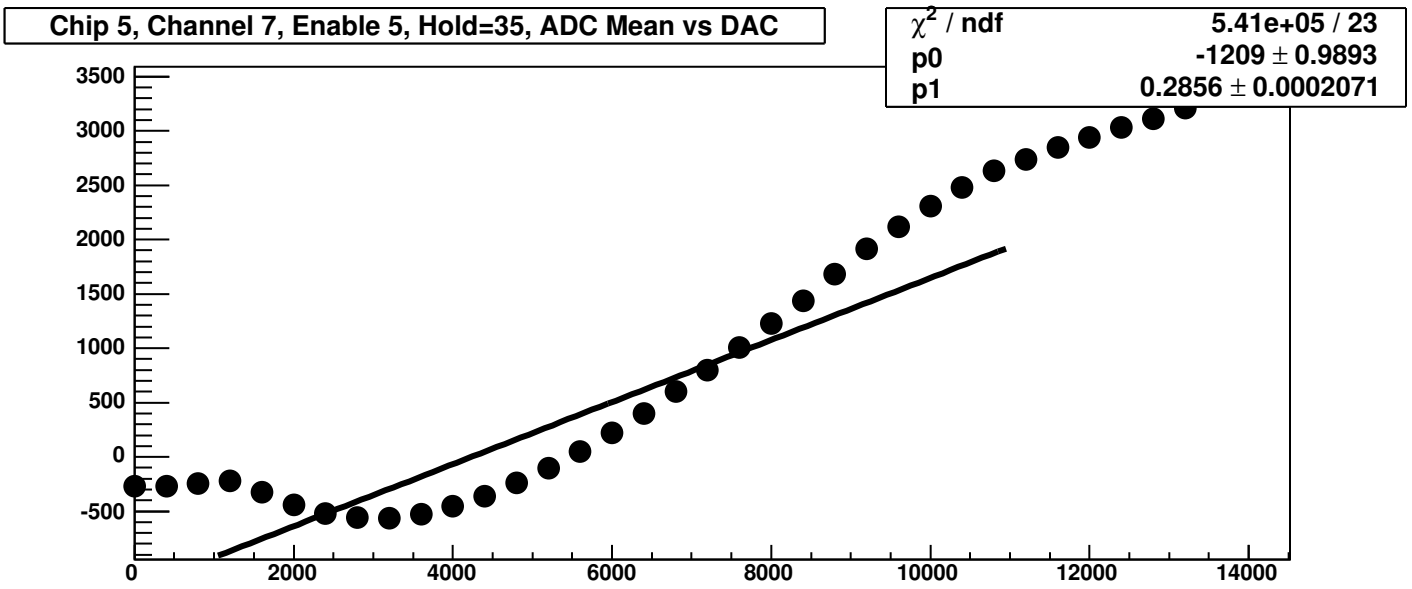

Chip 5, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

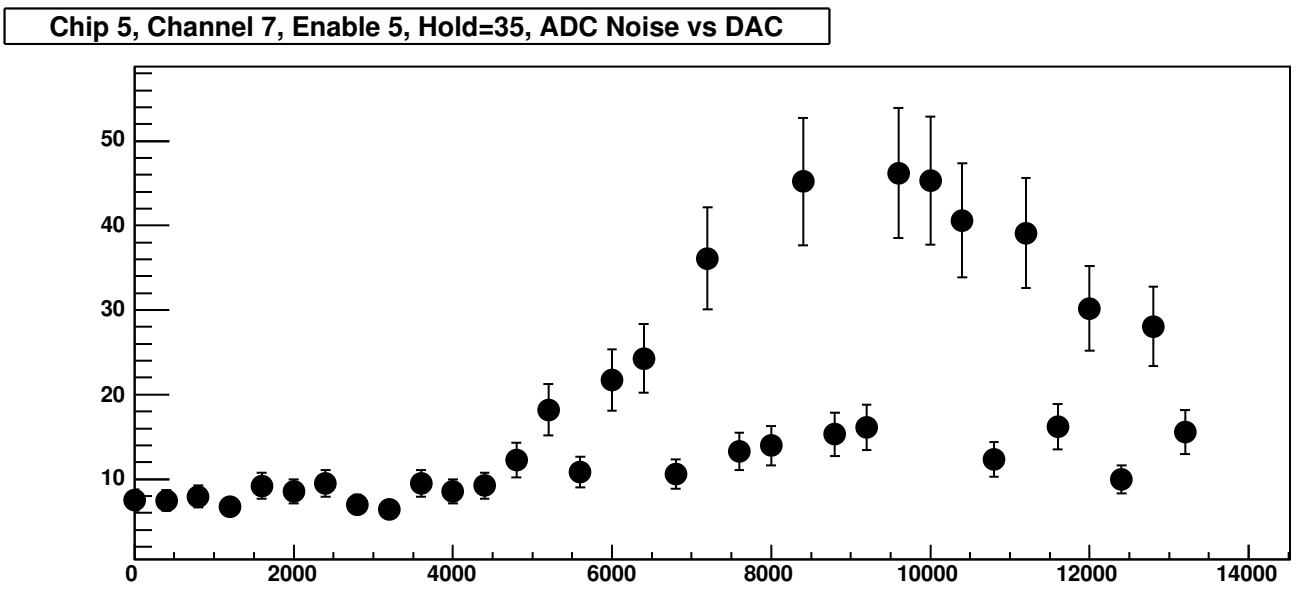

Chip 5, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

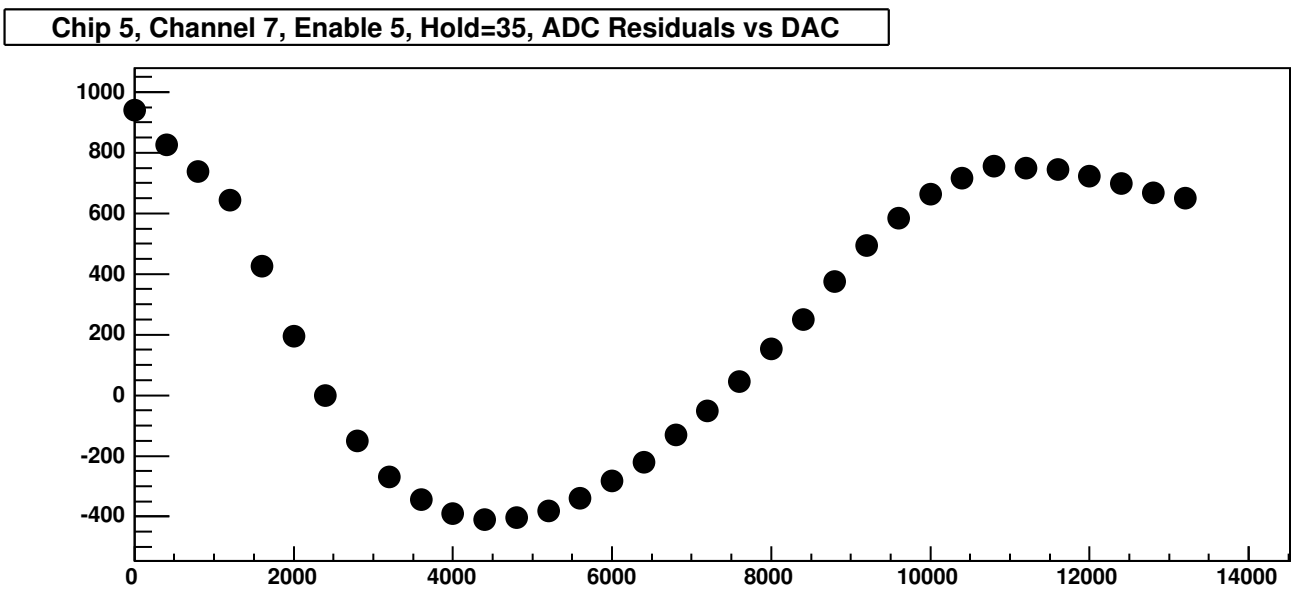

Chip 5, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.01 / 23
p0 -377.8 ± 0.7559
p1 0.02794 ± 0.0001103
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

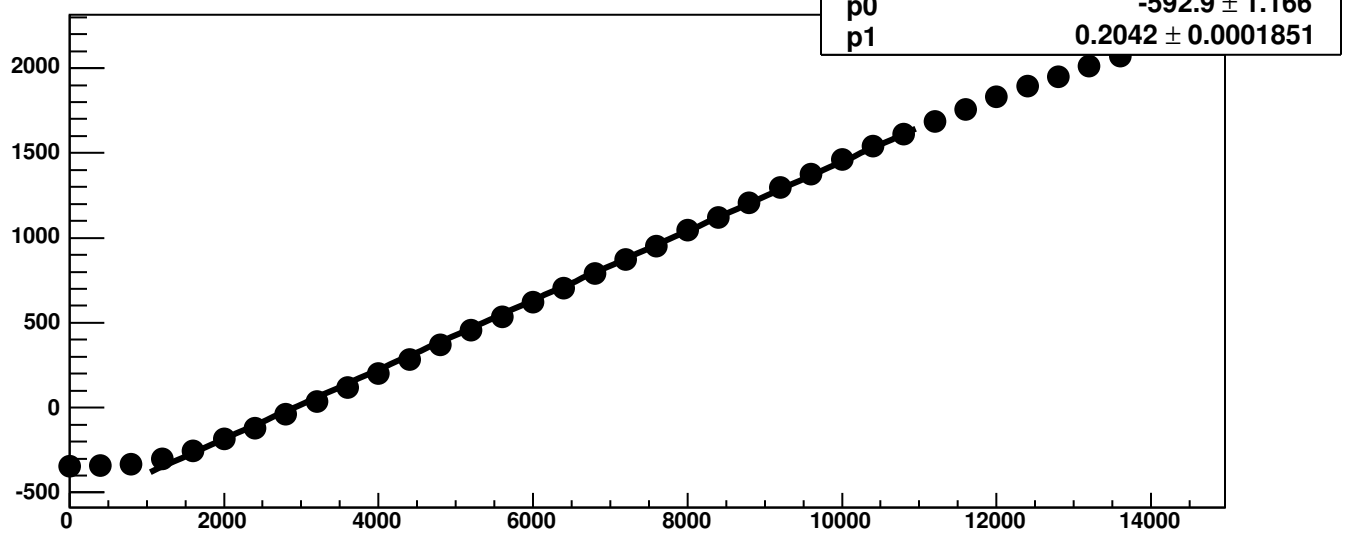

Chip 5, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5.41e+05 / 23
p0 -1209 ± 0.9893
p1 0.2856 ± 0.0002071
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

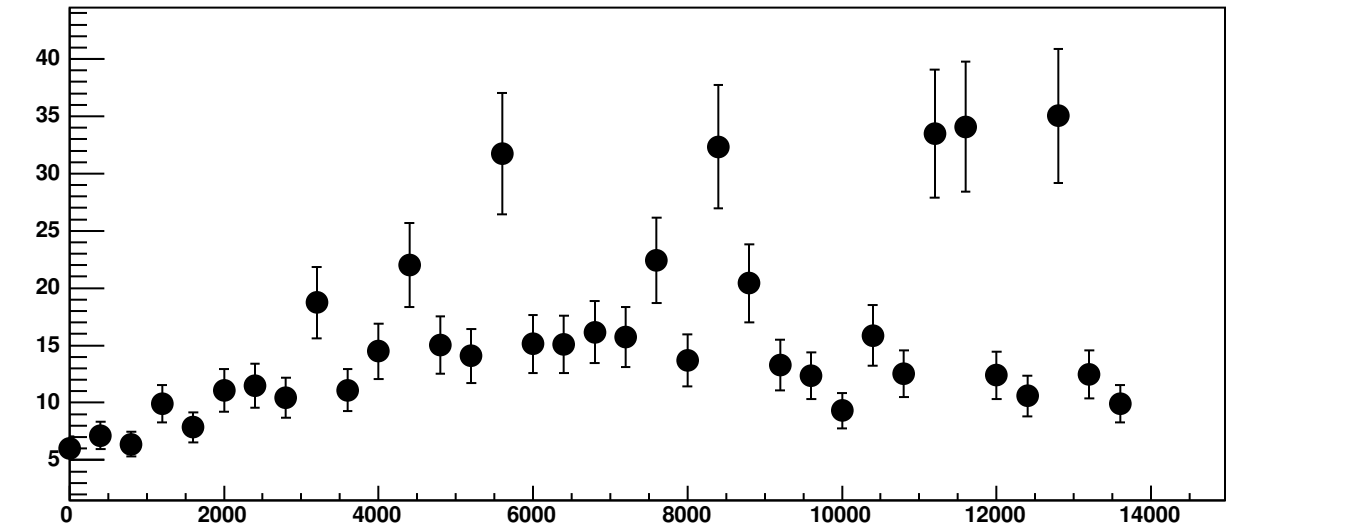

Chip 5, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

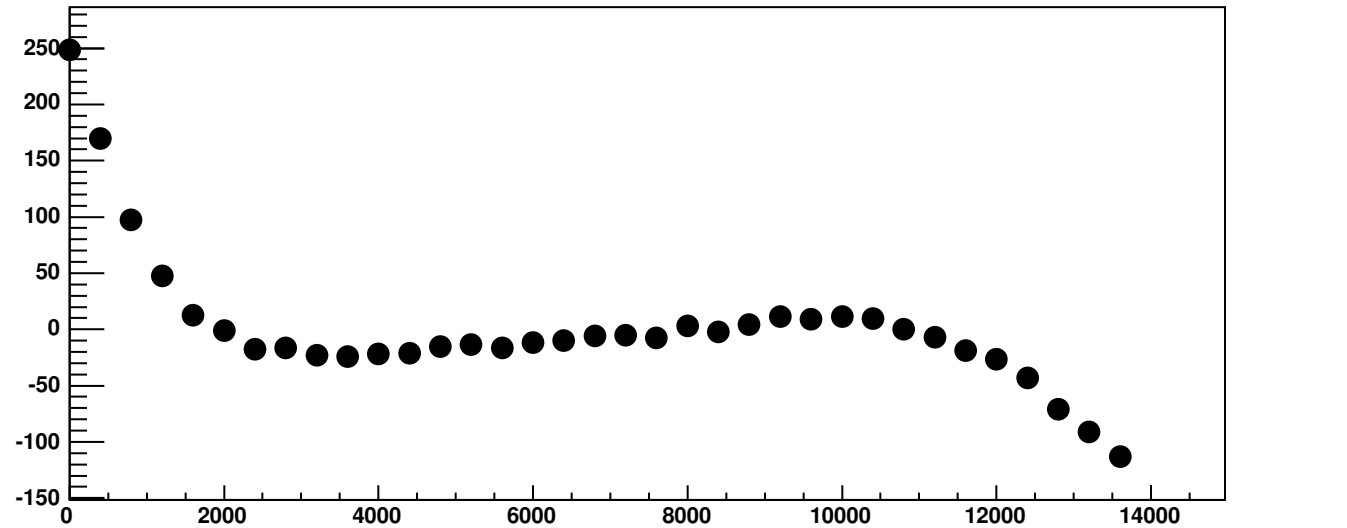
Chip 5, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

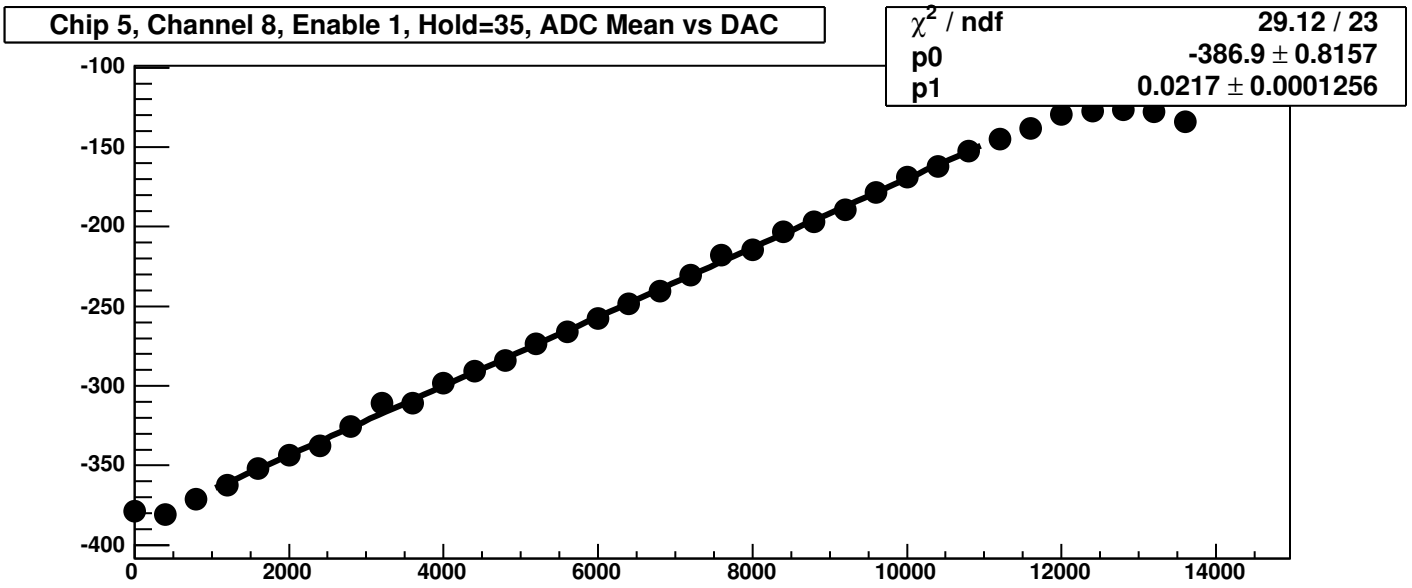


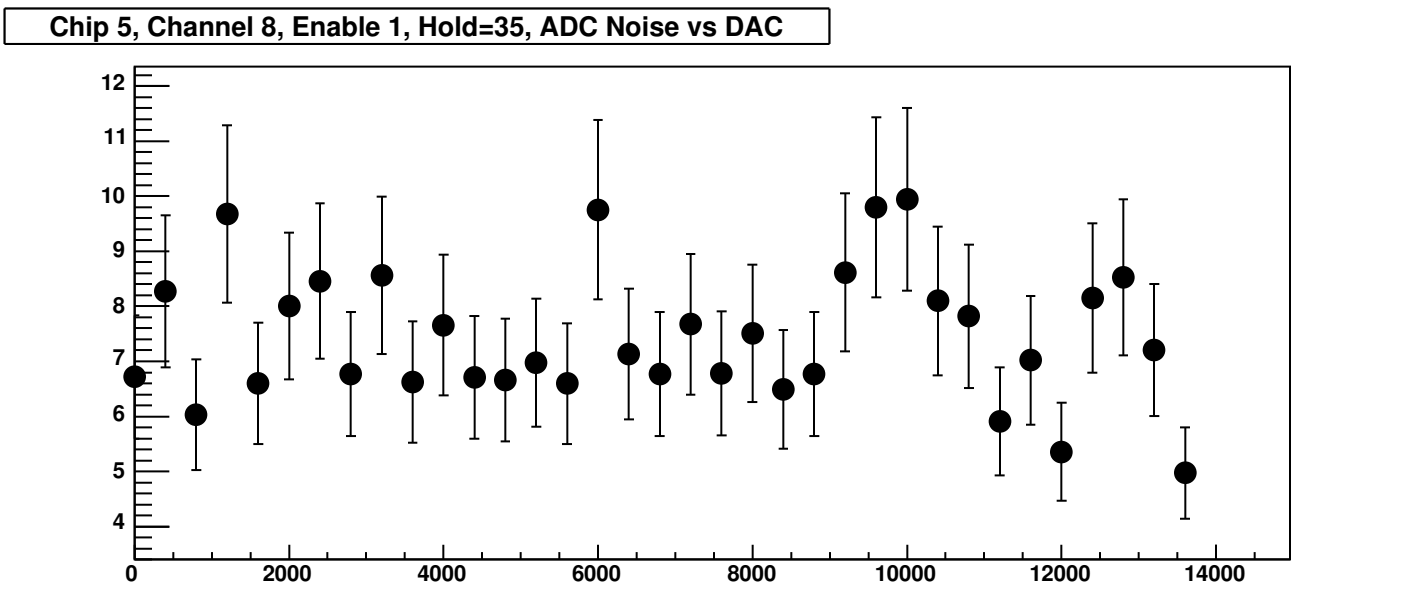
Chip 5, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC

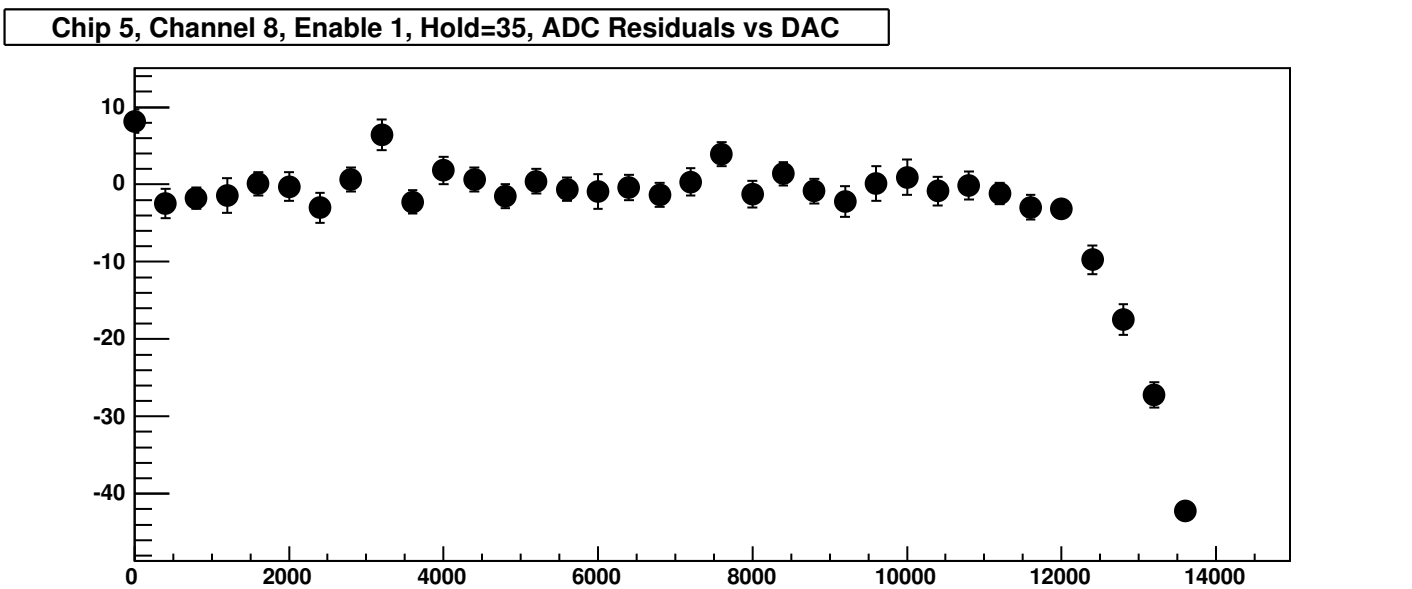


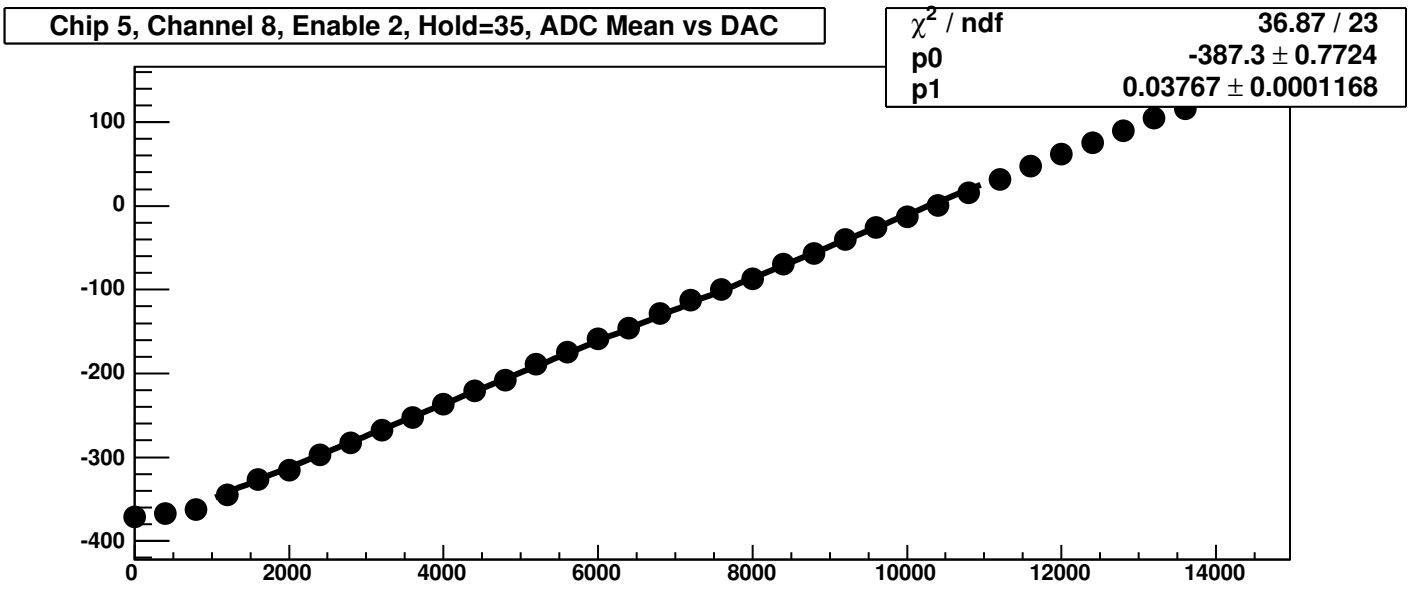
Chip 5, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

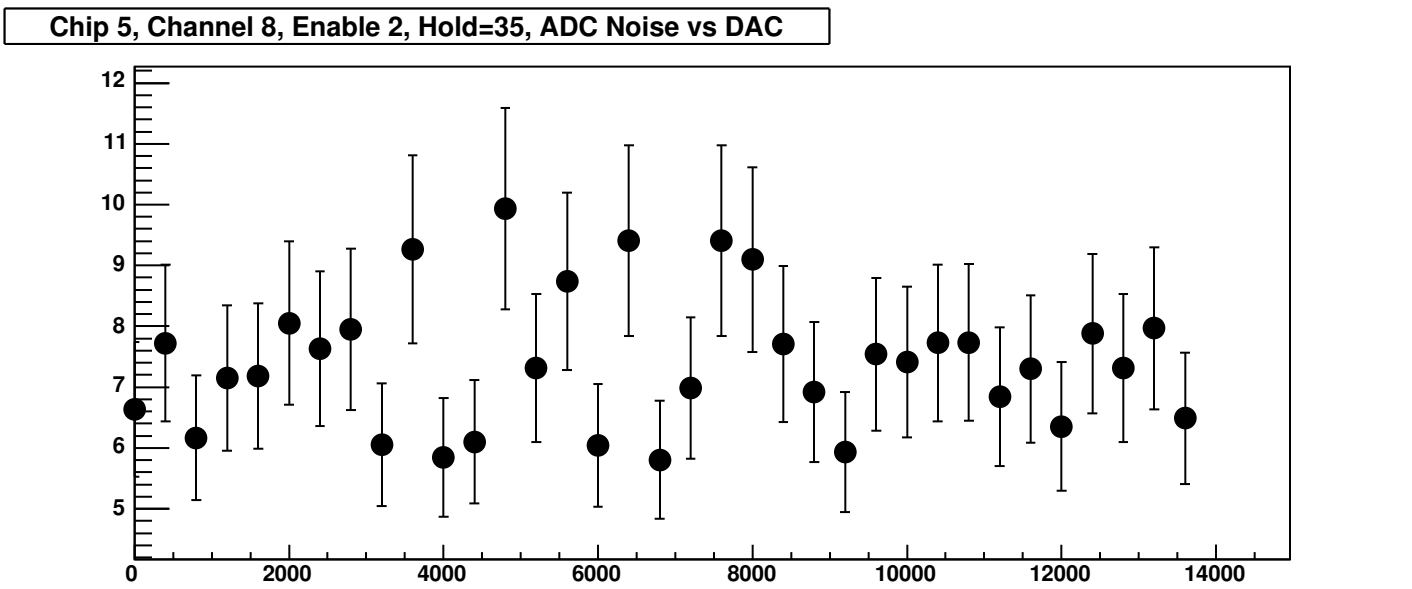


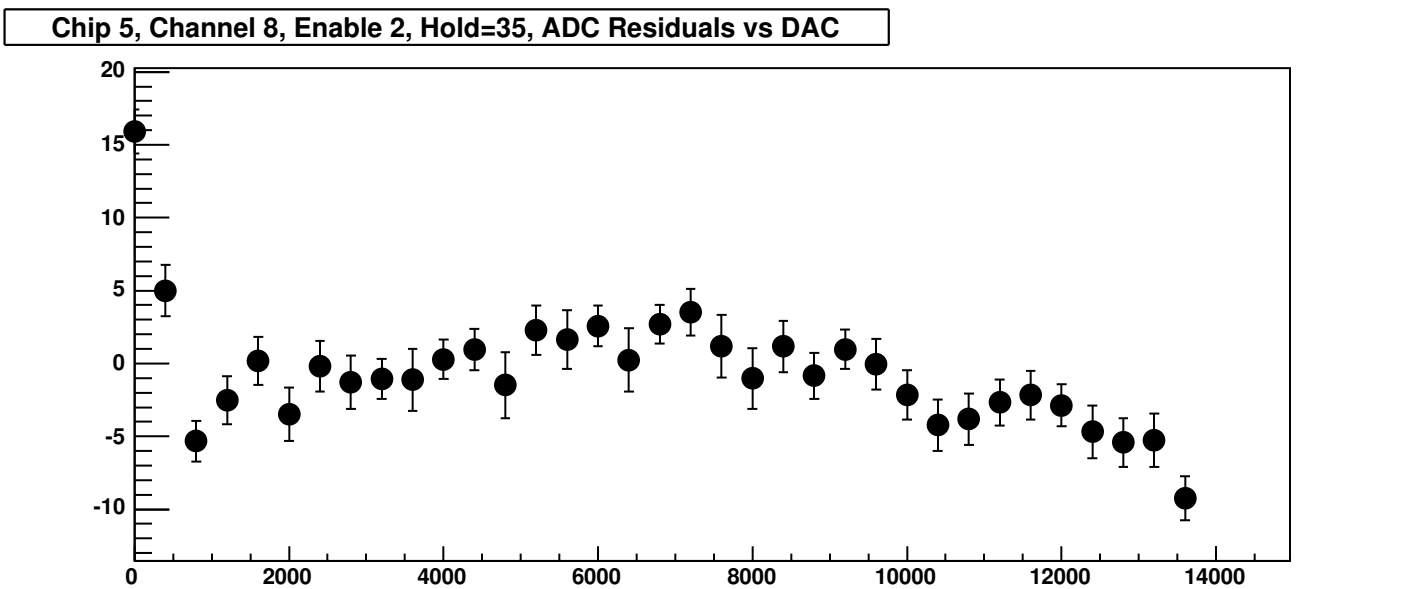

Chip 5, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 29.12 / 23
p0 -386.9 ± 0.8157
p1 0.0217 ± 0.0001256
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

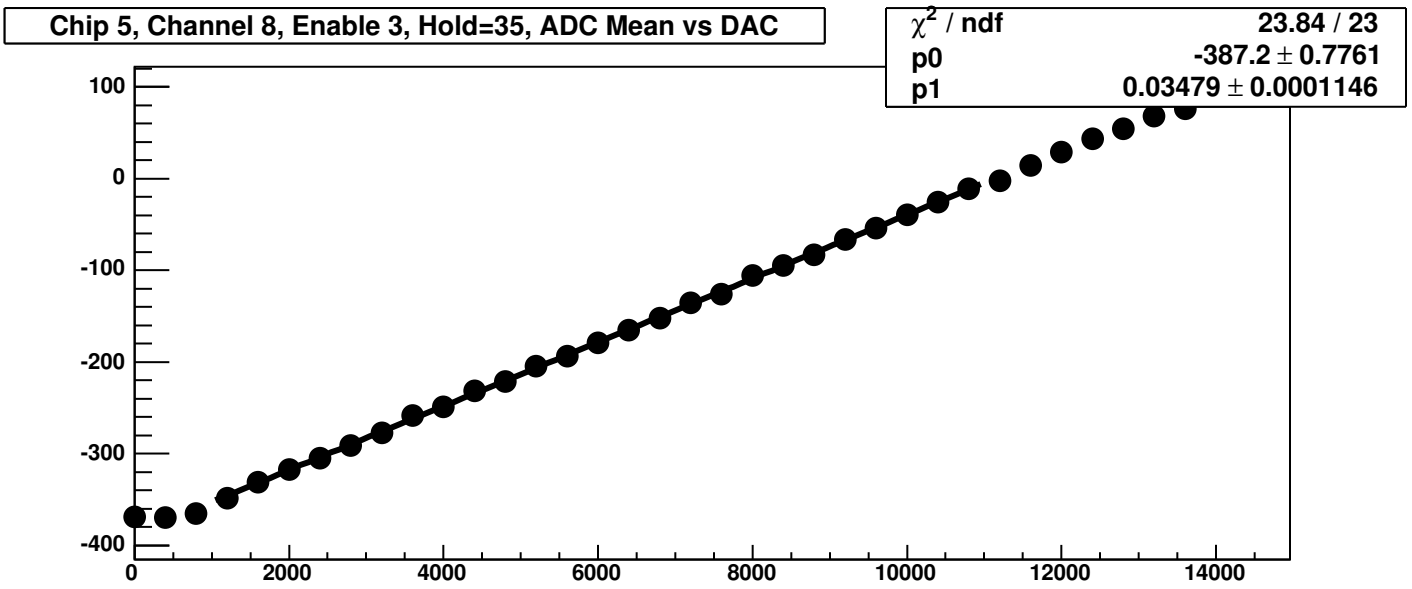

Chip 5, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

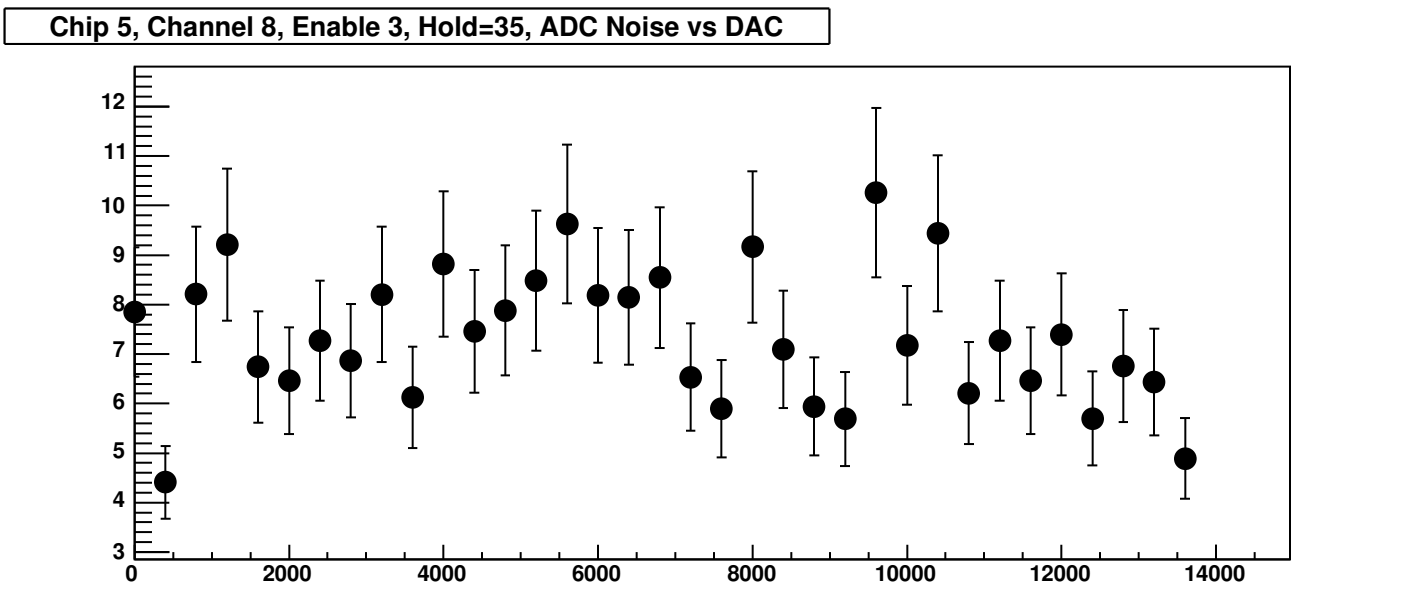

Chip 5, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

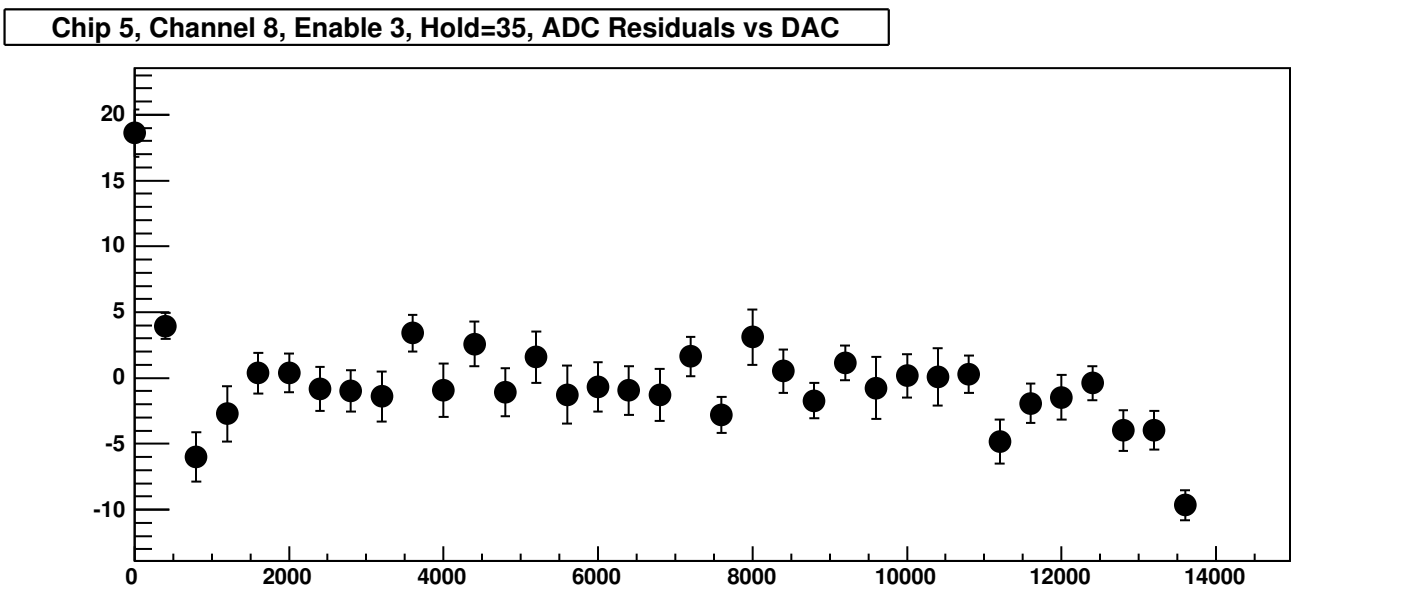

Chip 5, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 36.87 / 23
p0 -387.3 ± 0.7724
p1 0.03767 ± 0.0001168
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

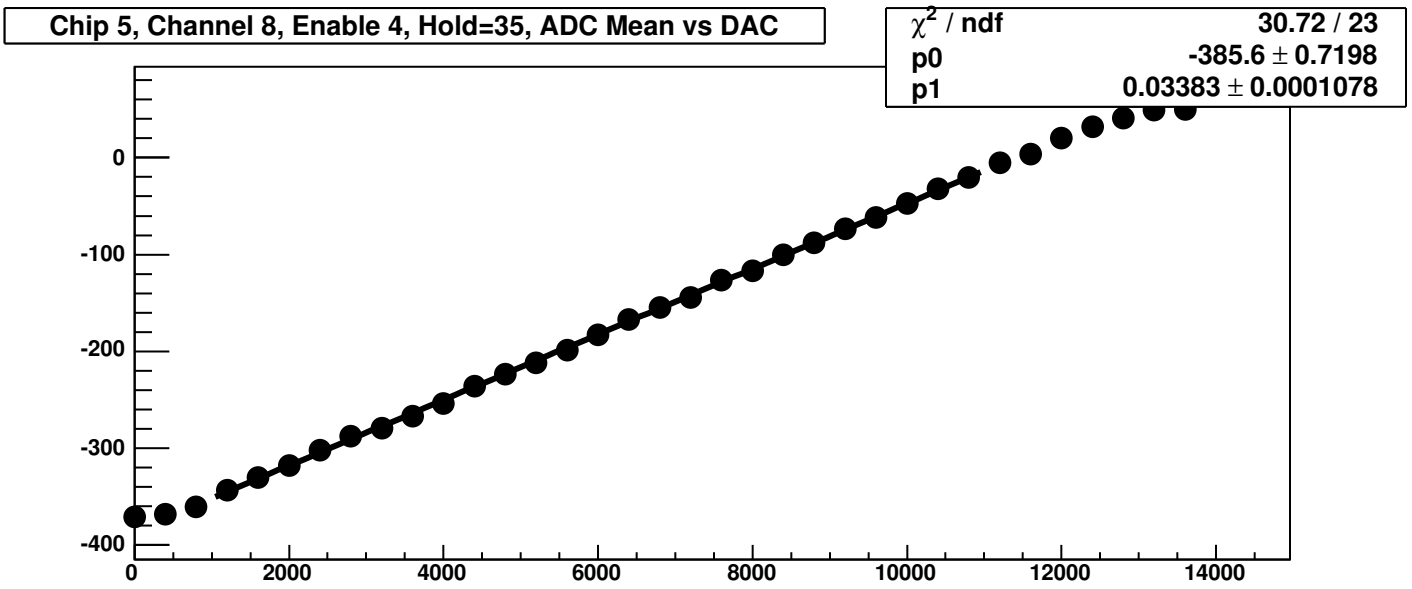

Chip 5, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

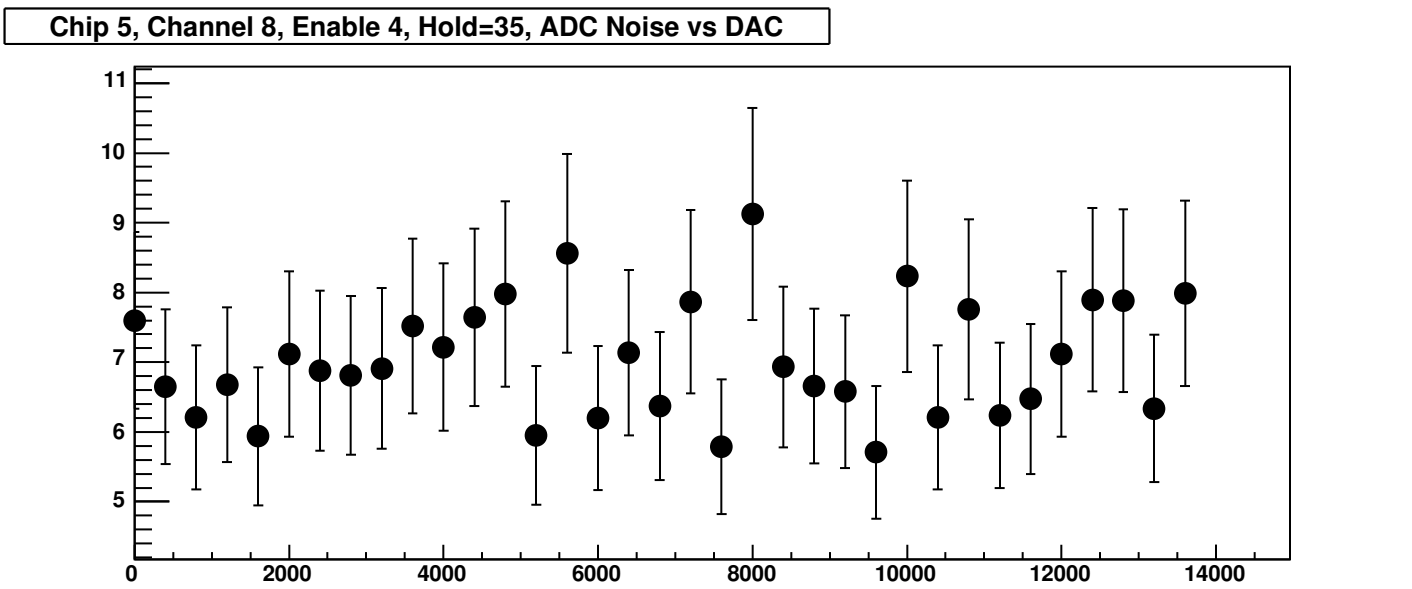

Chip 5, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

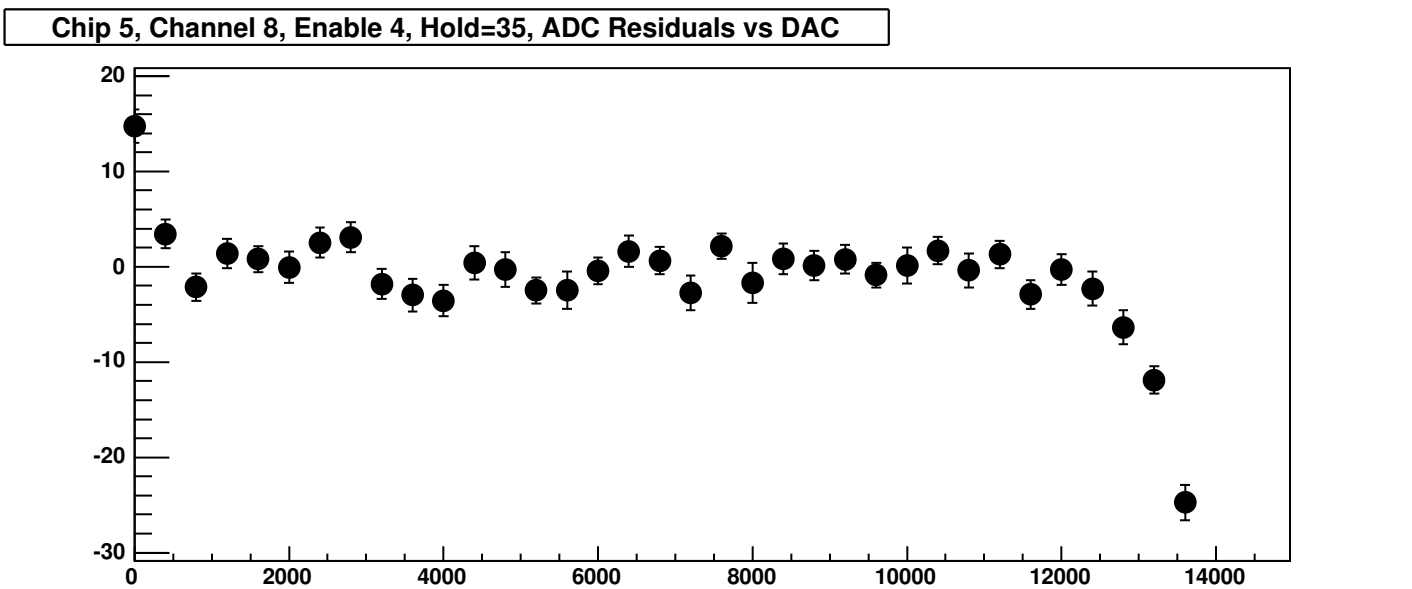

Chip 5, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.84 / 23
p0 -387.2 ± 0.7761
p1 0.03479 ± 0.0001146
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

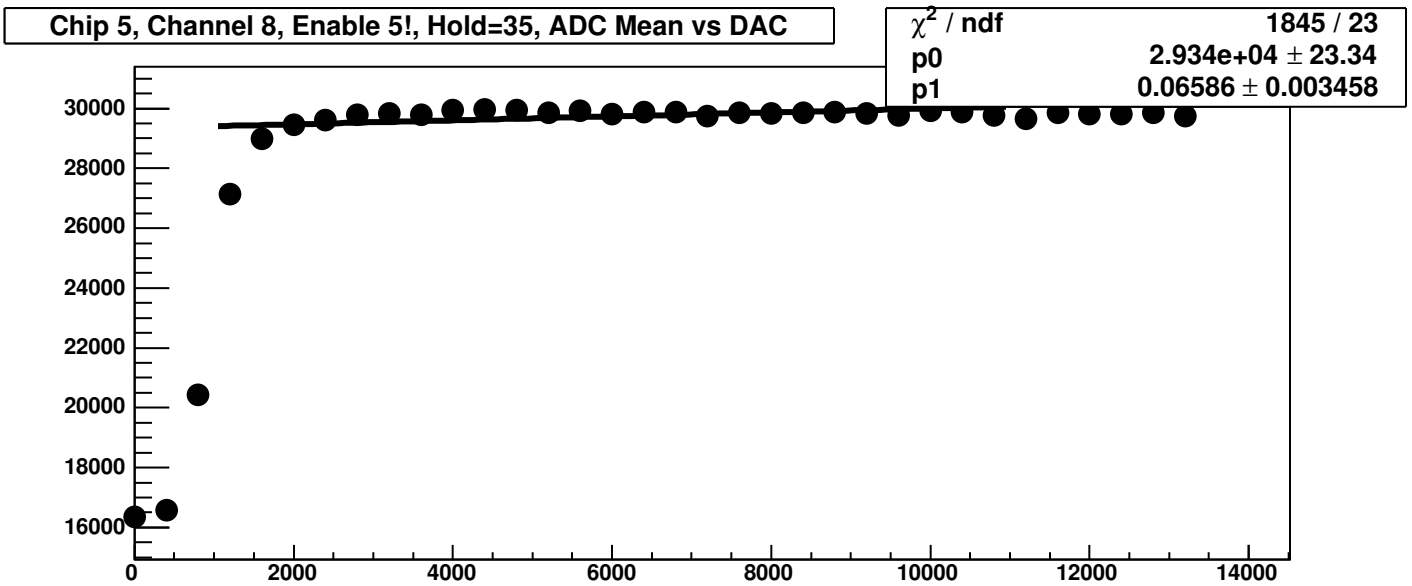

Chip 5, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

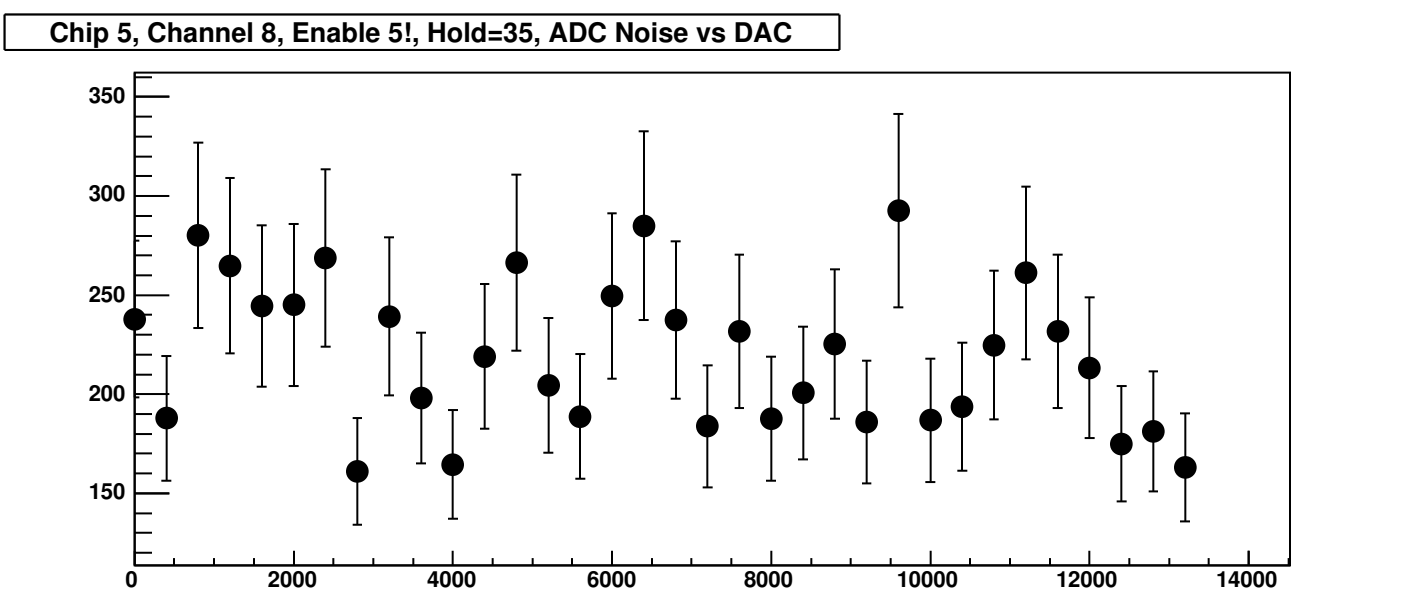

Chip 5, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

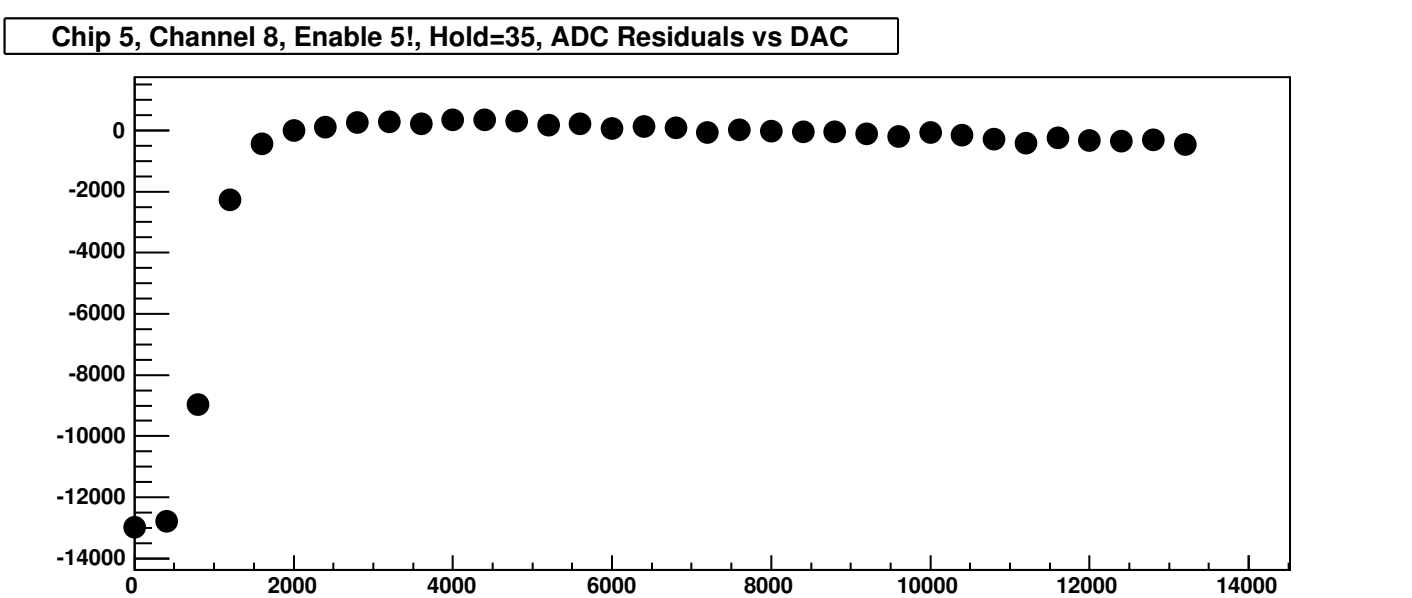

Chip 5, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.72 / 23
p0 -385.6 ± 0.7198
p1 0.03383 ± 0.0001078
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

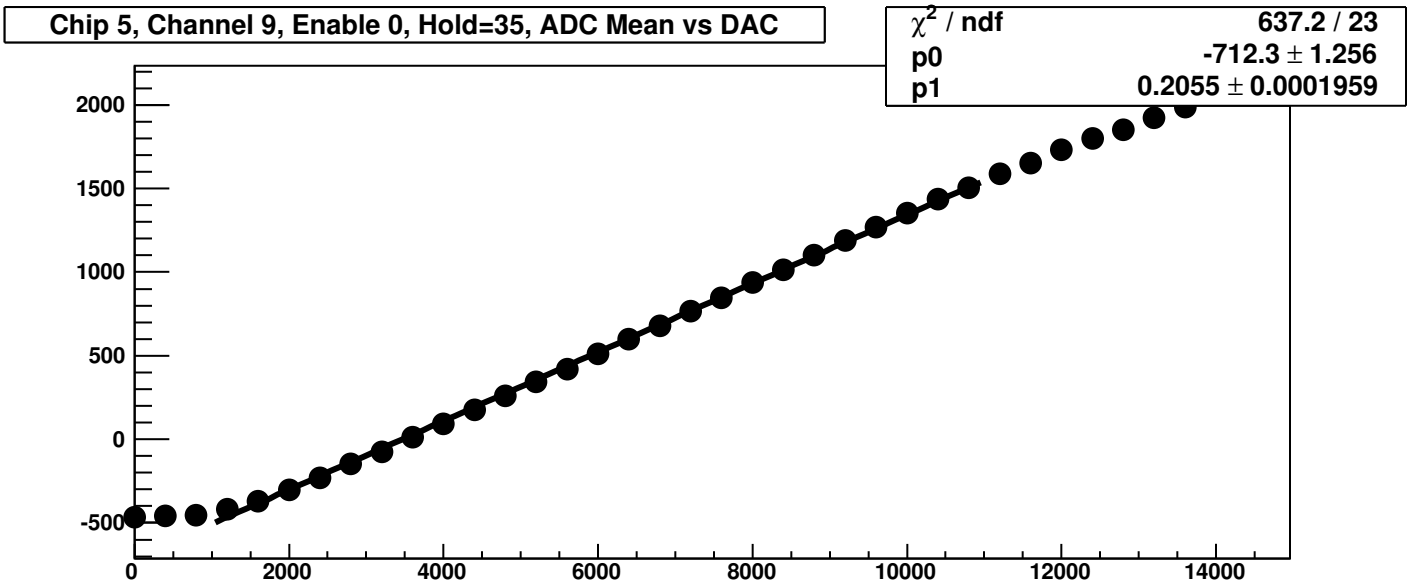

Chip 5, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

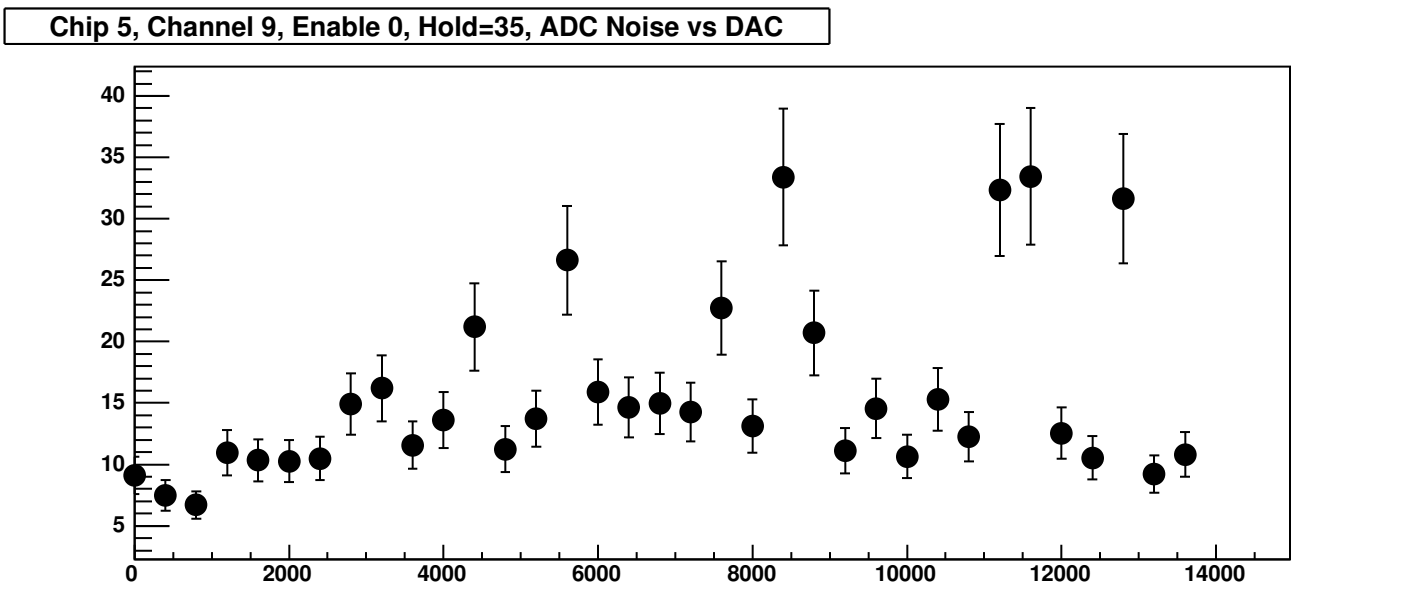

Chip 5, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

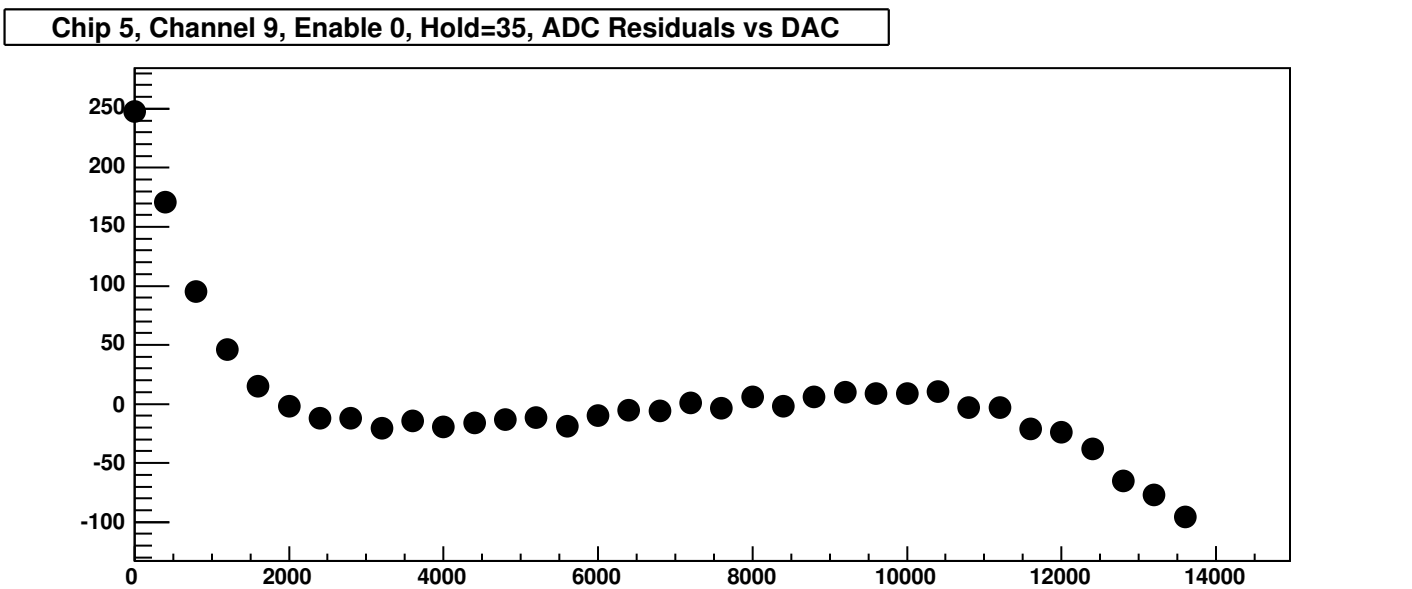

Chip 5, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1845 / 23
p0 2.934e+04 ± 23.34
p1 0.06586 ± 0.003458
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

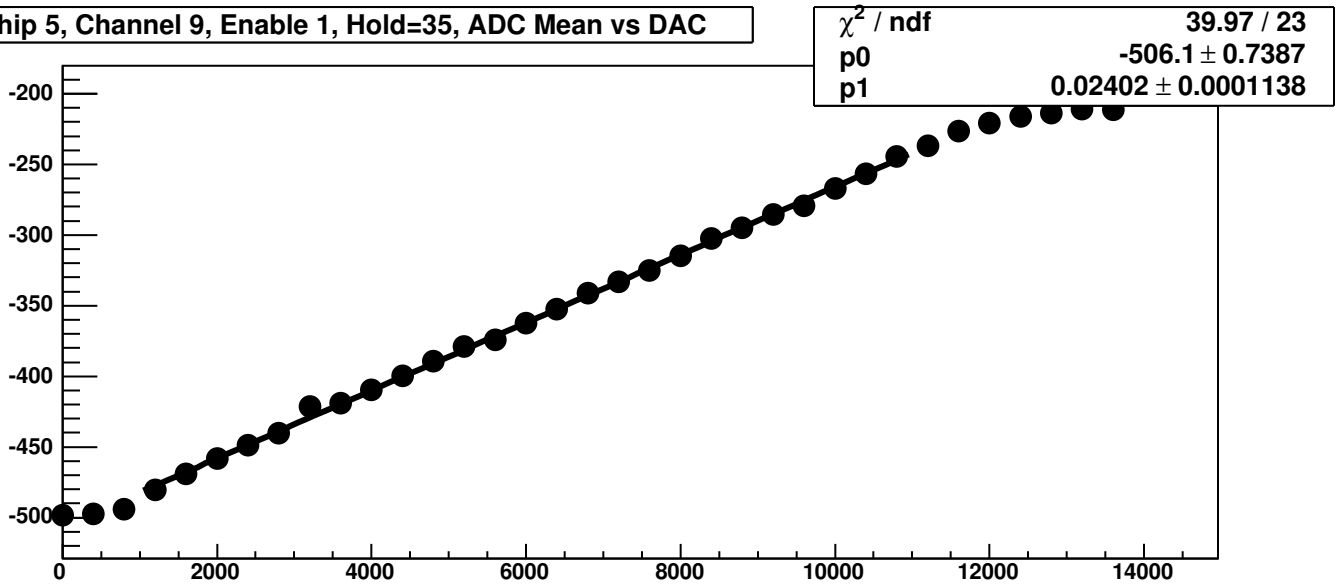

Chip 5, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 637.2 / 23
p0 -712.3 ± 1.256
p1 0.2055 ± 0.0001959
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

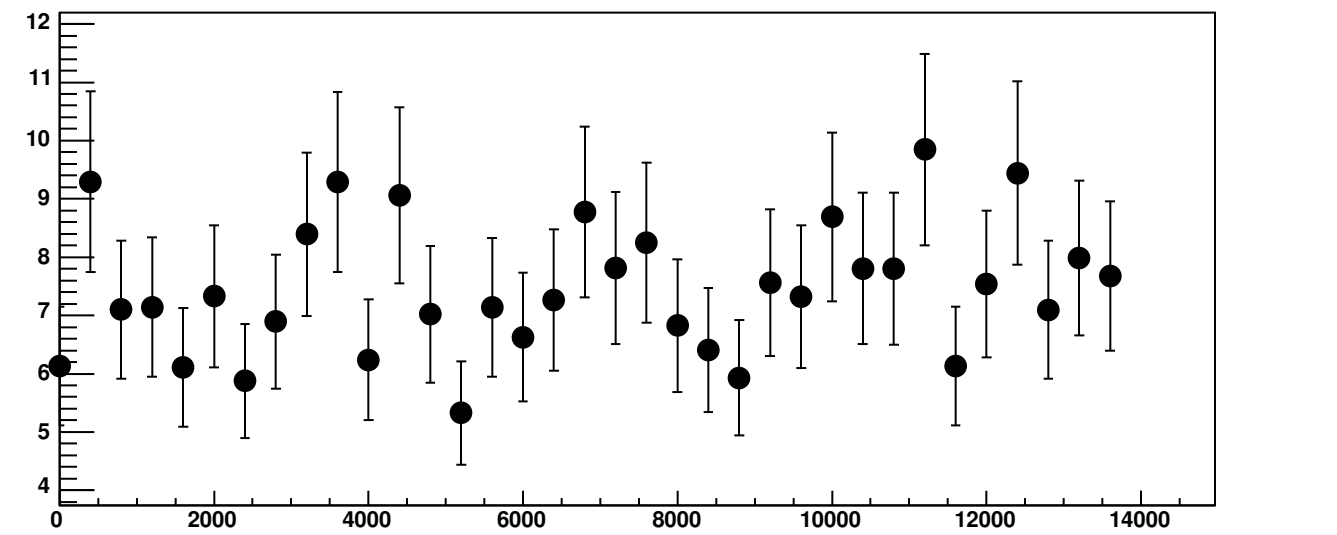

Chip 5, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

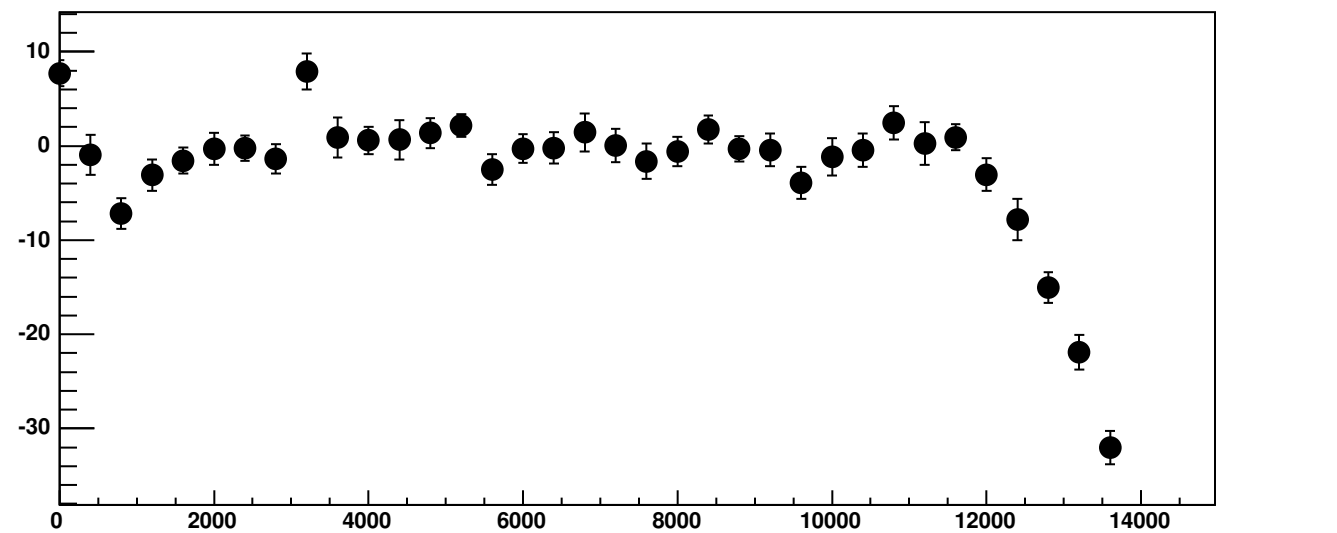
Chip 5, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



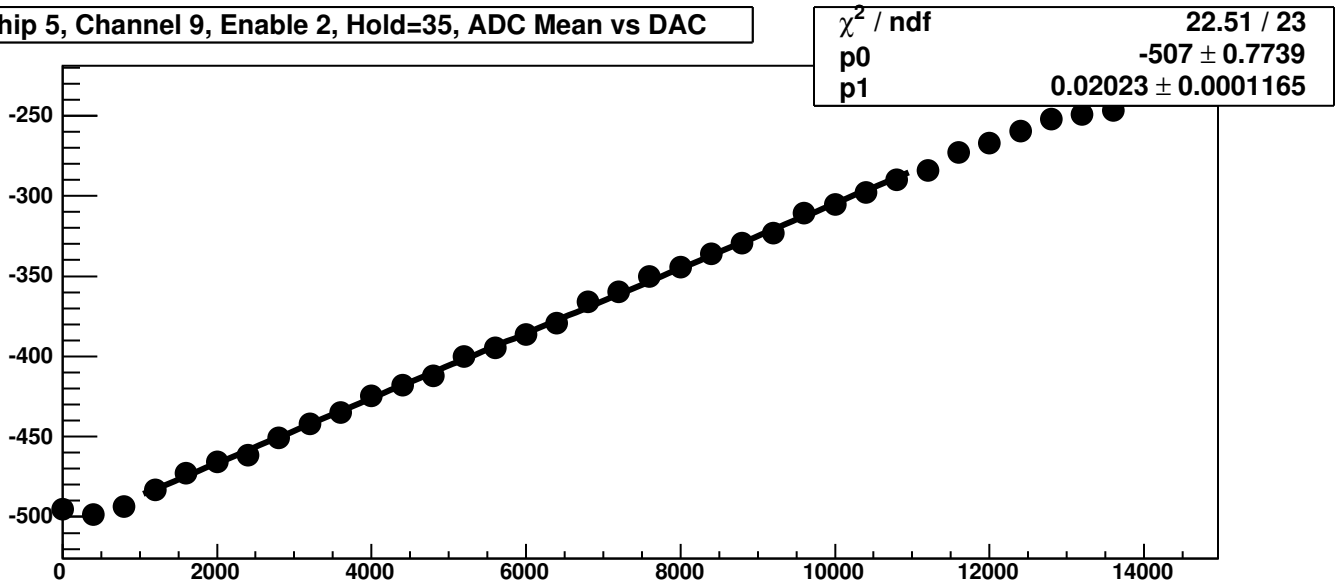
Chip 5, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



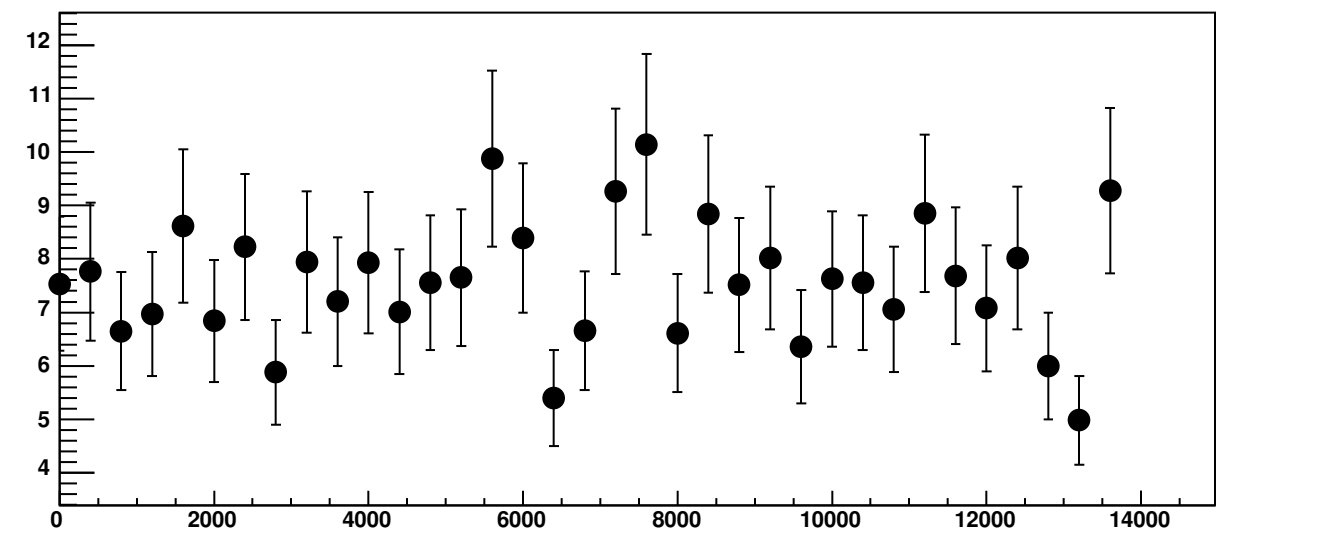
Chip 5, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



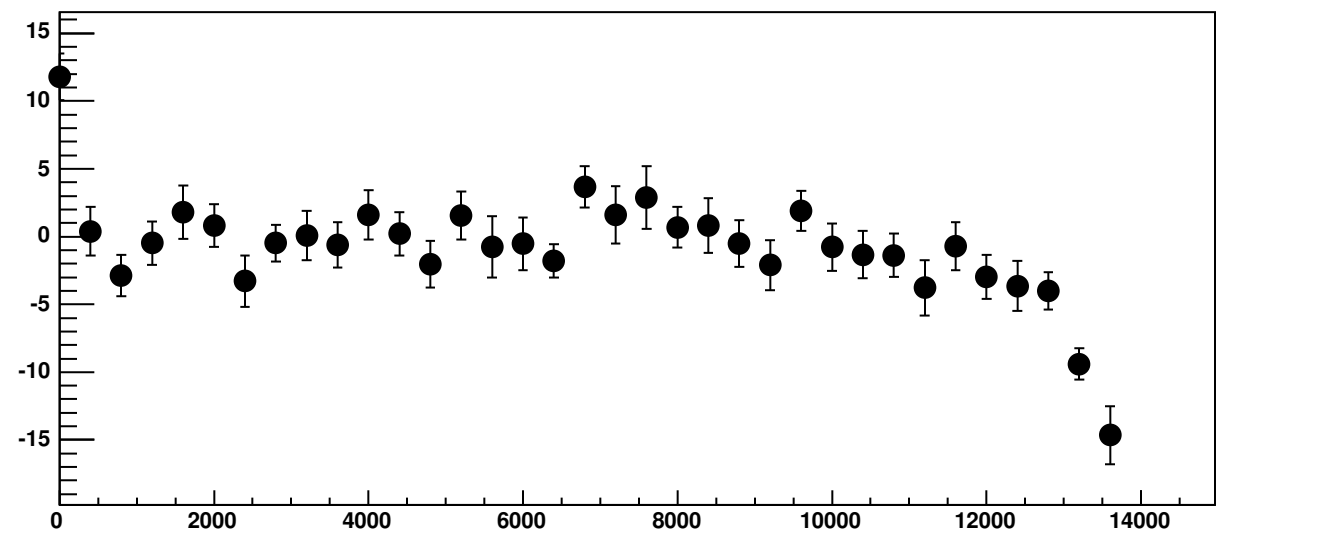
Chip 5, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

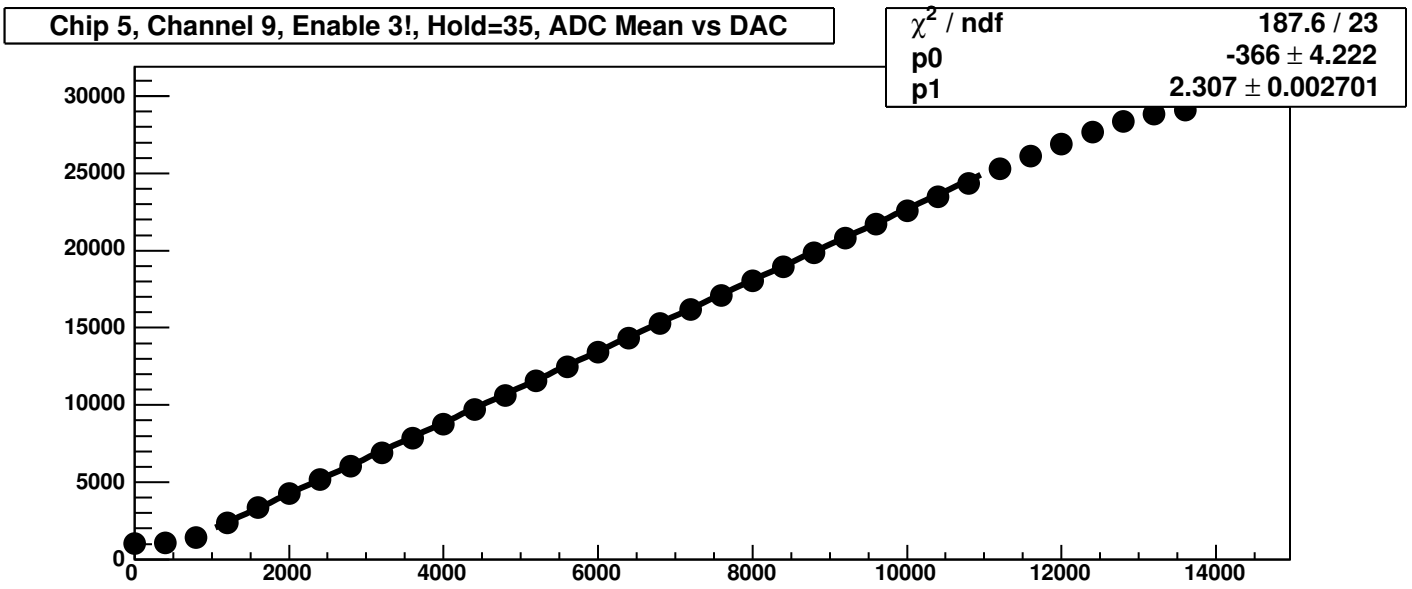


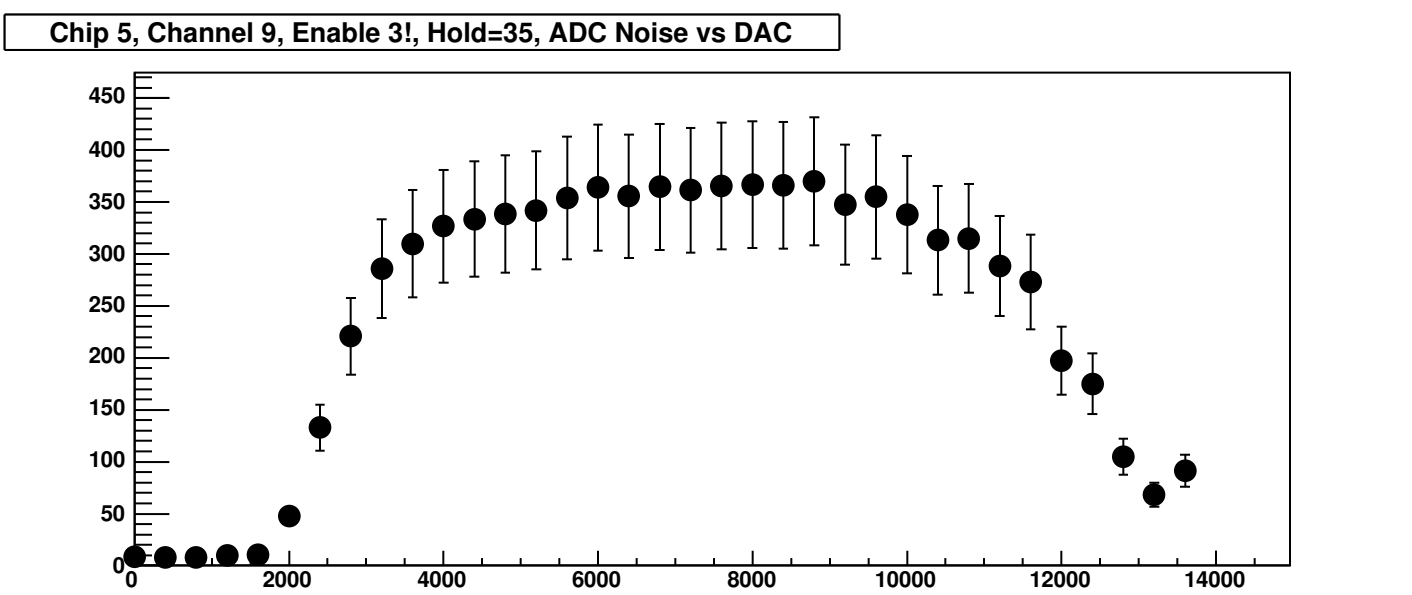
Chip 5, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC

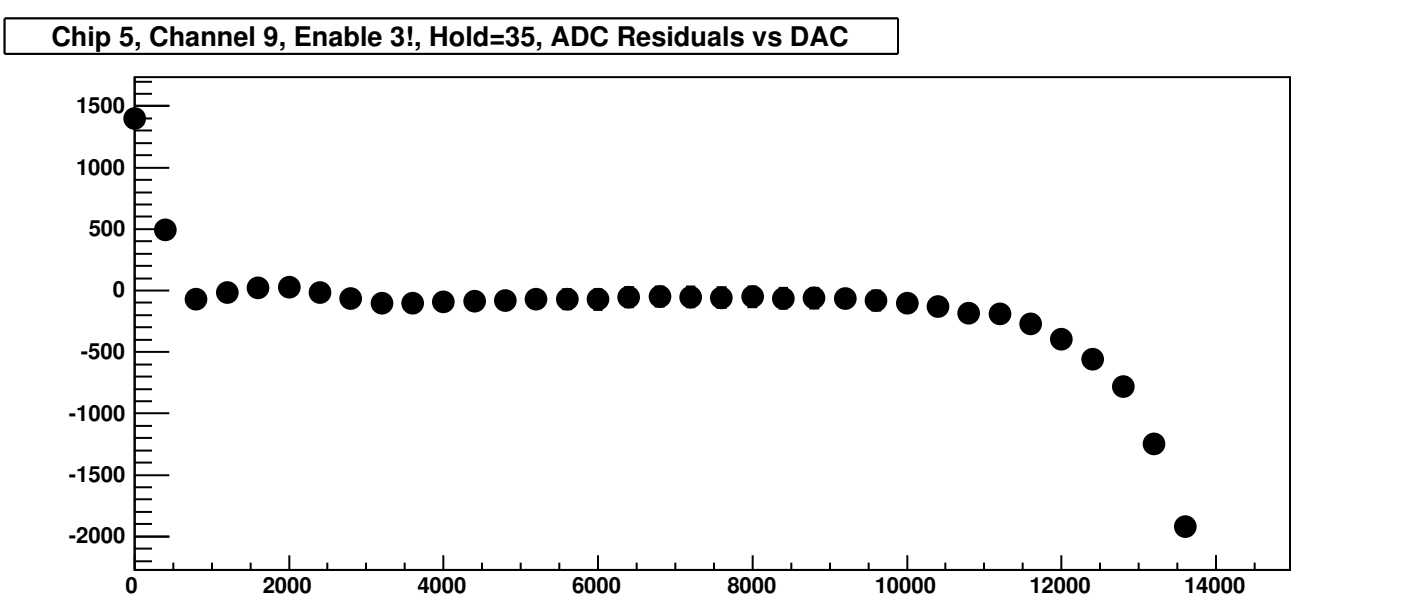


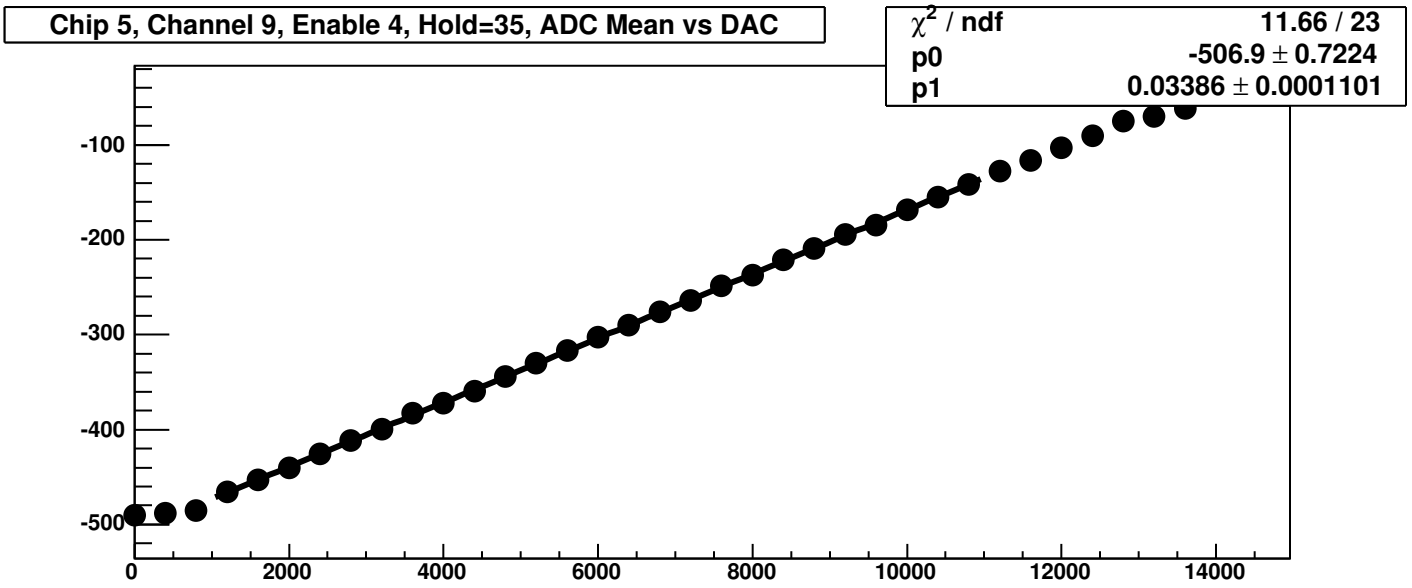
Chip 5, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

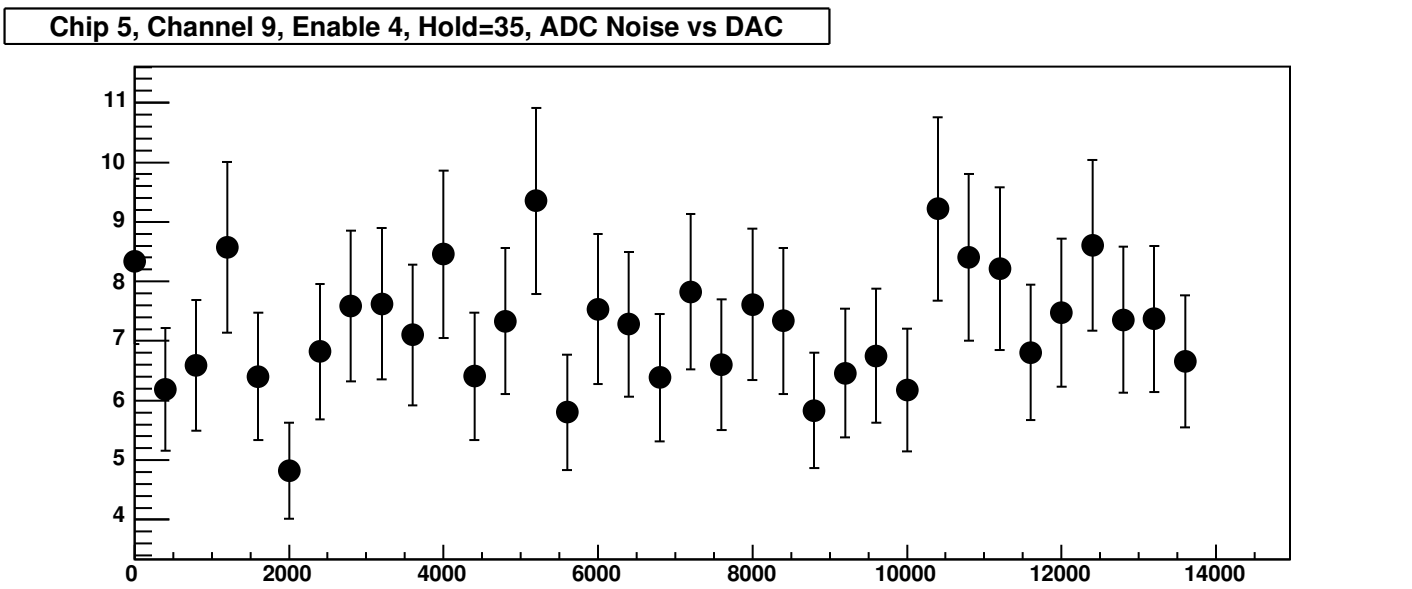


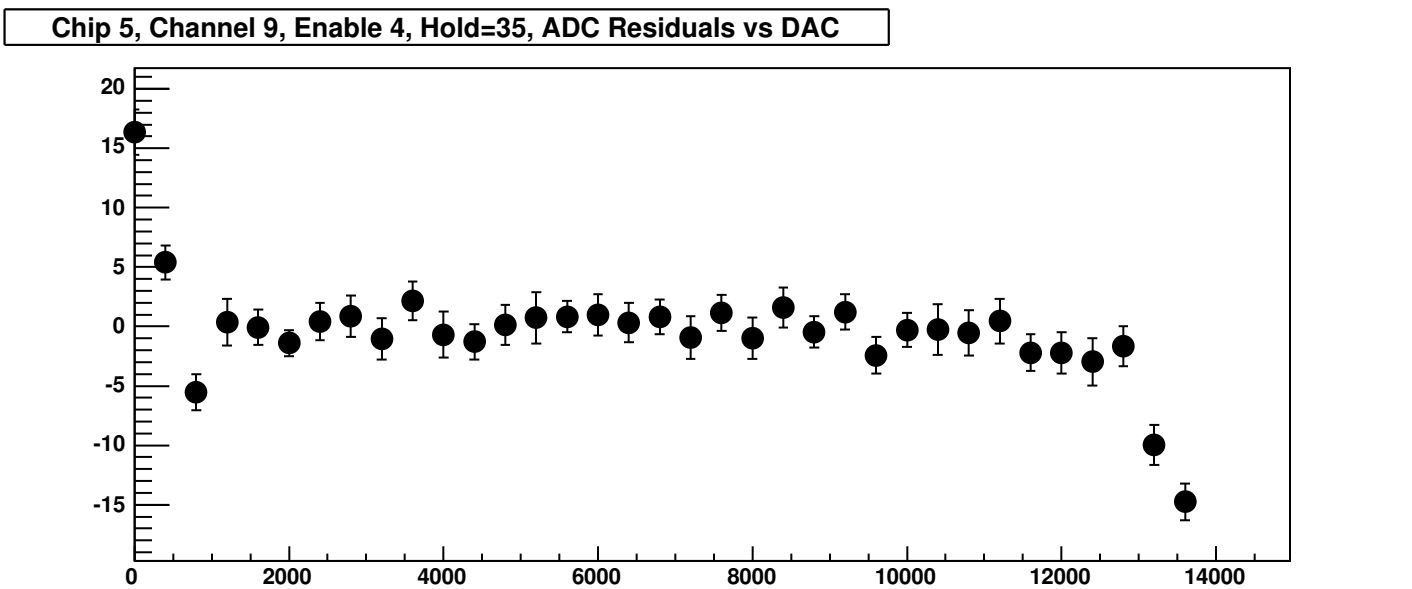

Chip 5, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 187.6 / 23
p0 -366 ± 4.222
p1 2.307 ± 0.002701
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

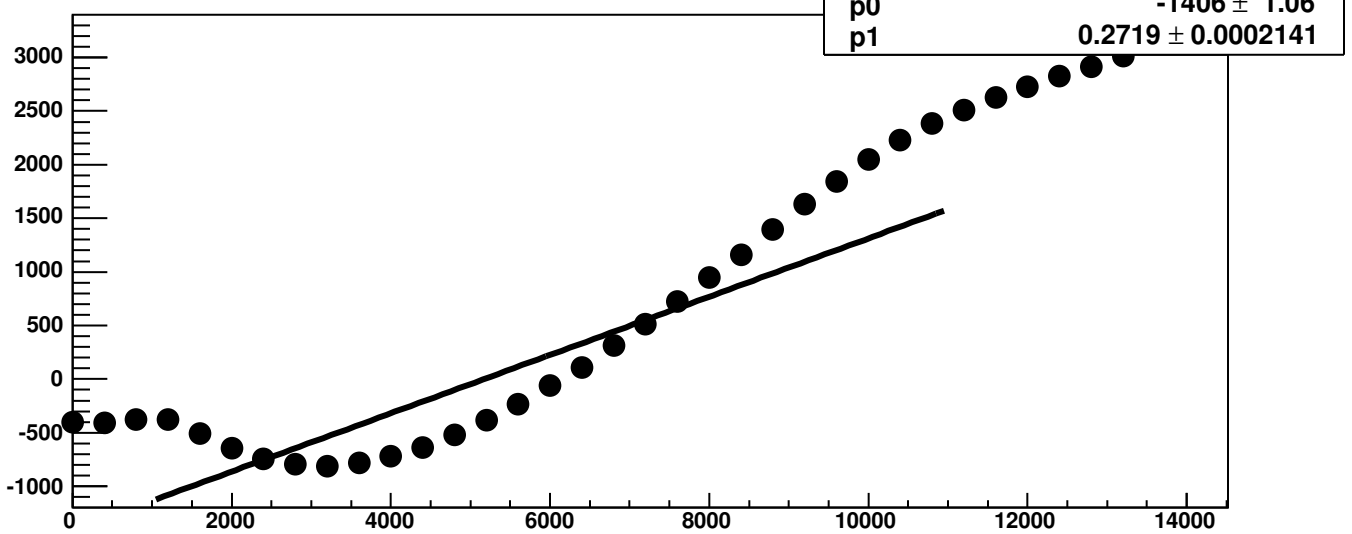

Chip 5, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 11.66 / 23
p0 -506.9 ± 0.7224
p1 0.03386 ± 0.0001101
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000

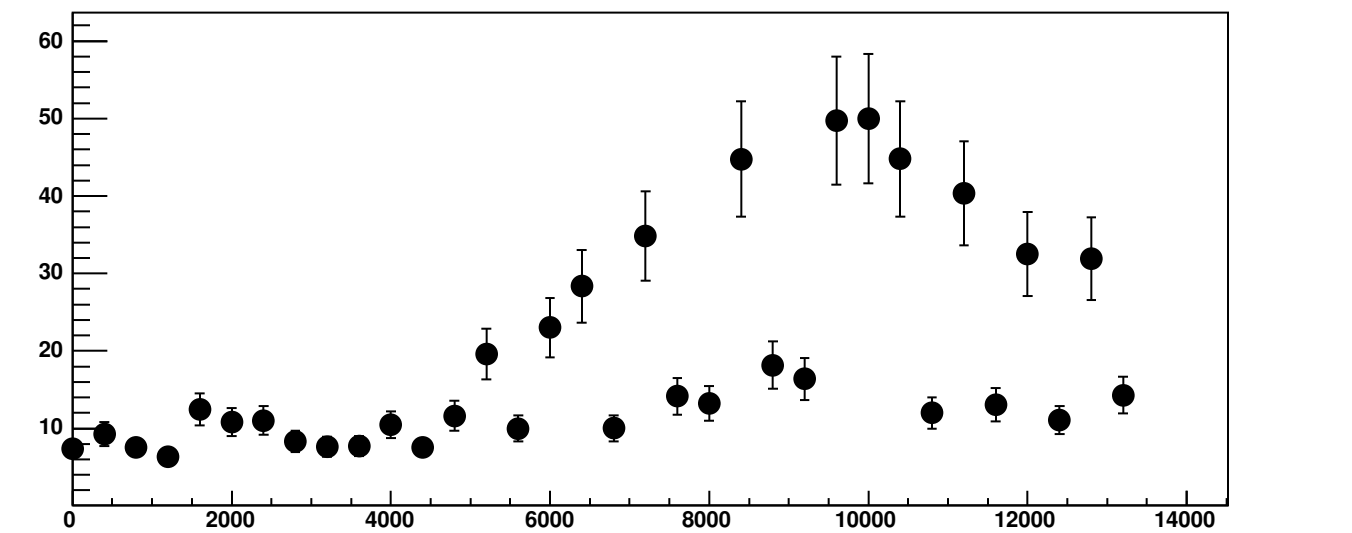

Chip 5, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

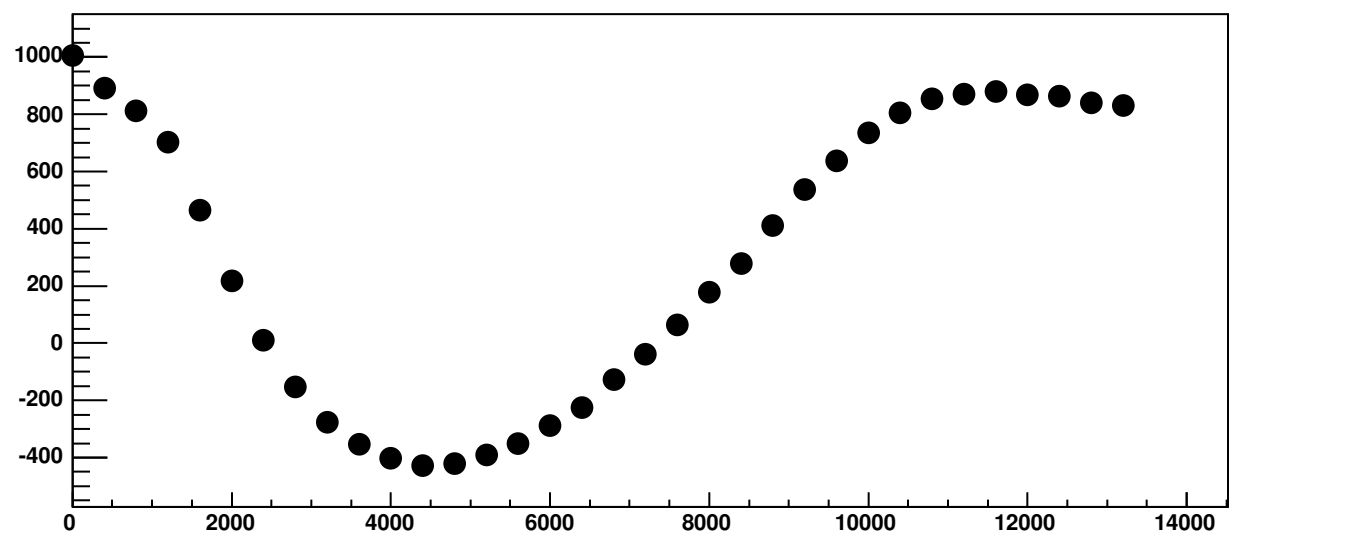
Chip 5, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

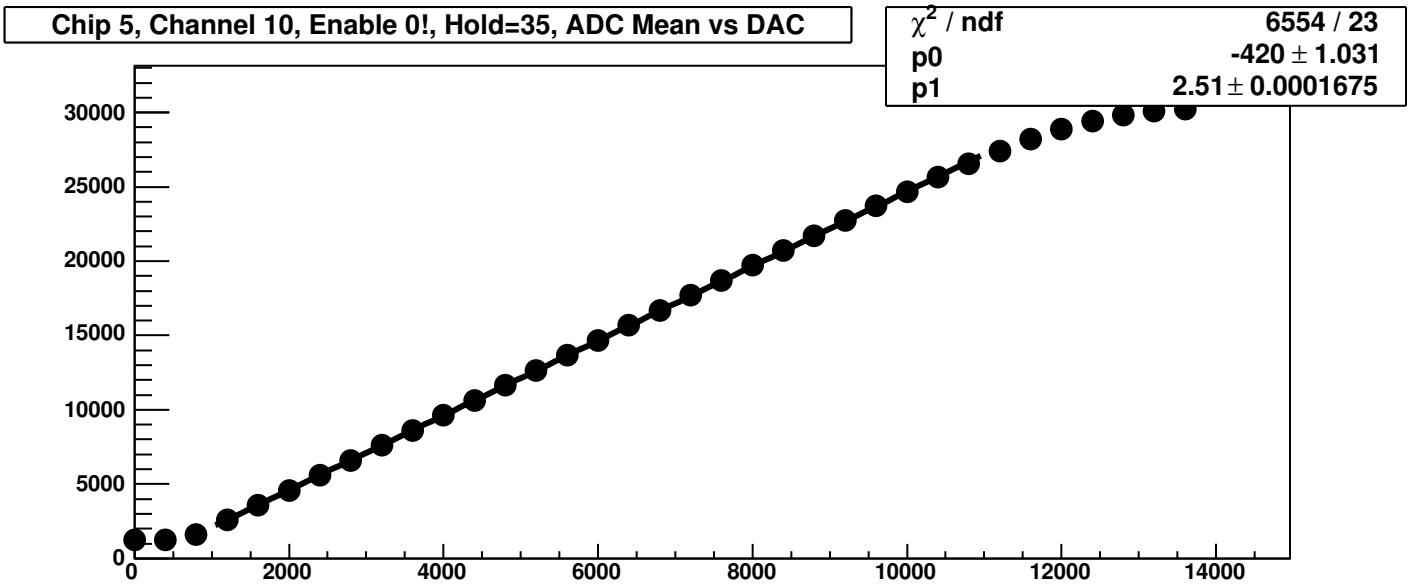


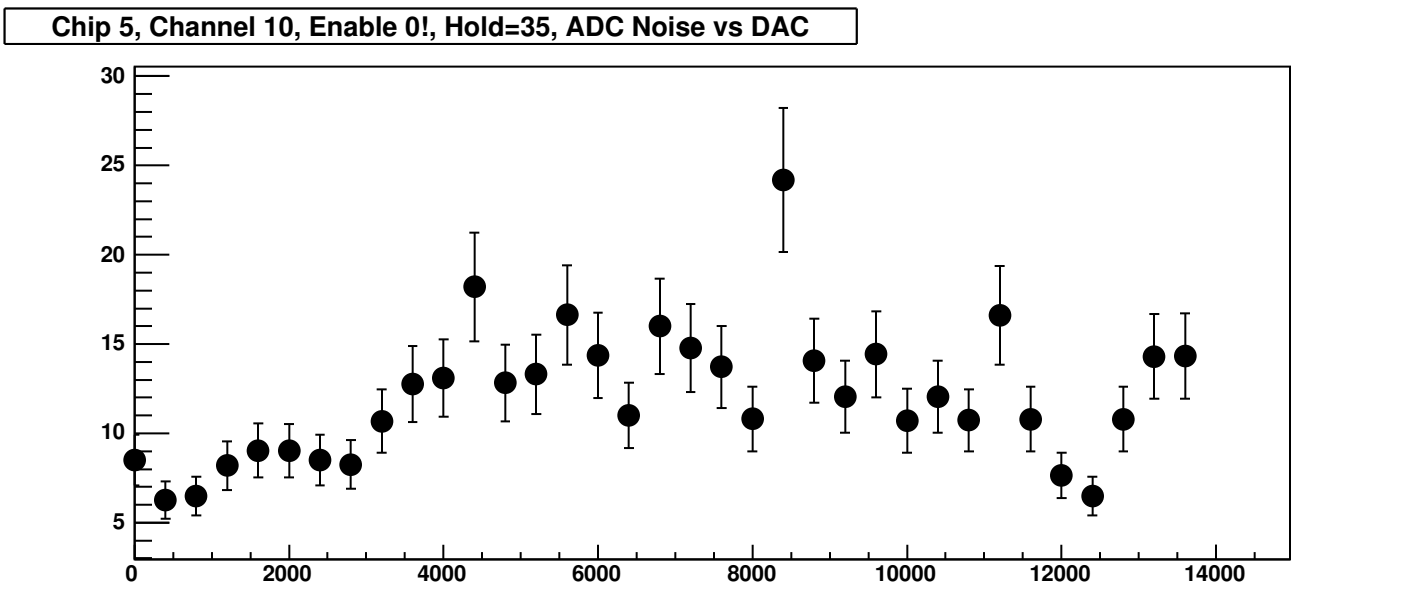
Chip 5, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

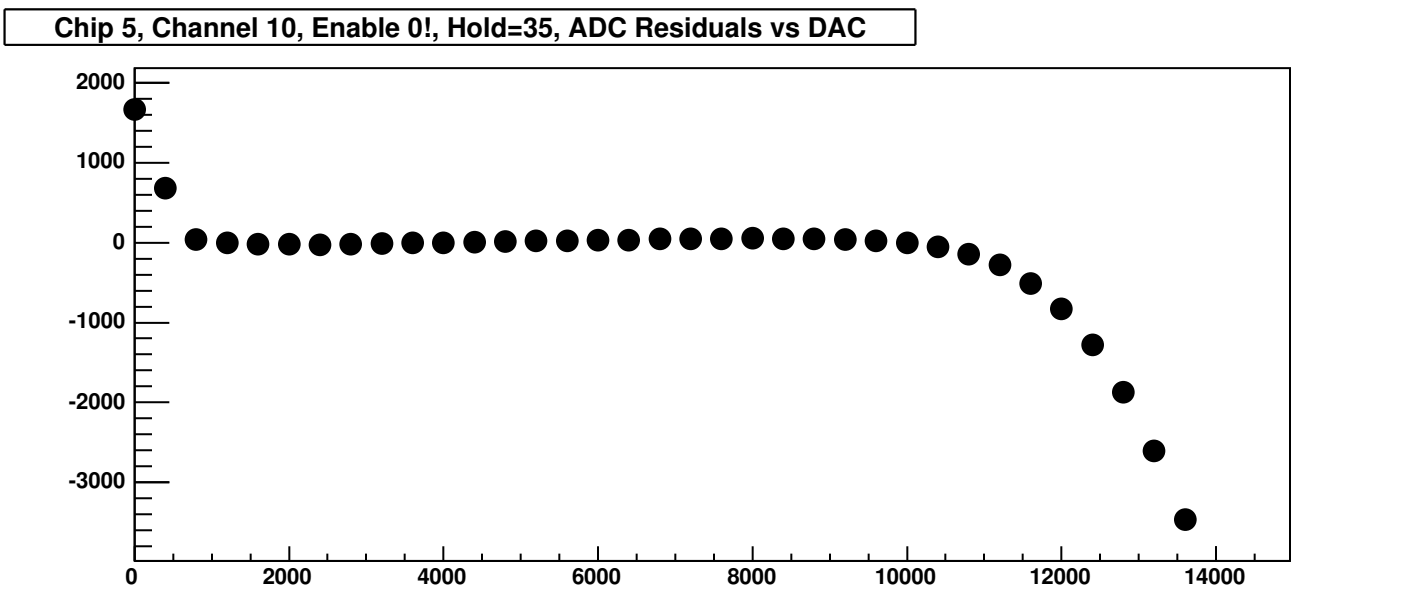


Chip 5, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

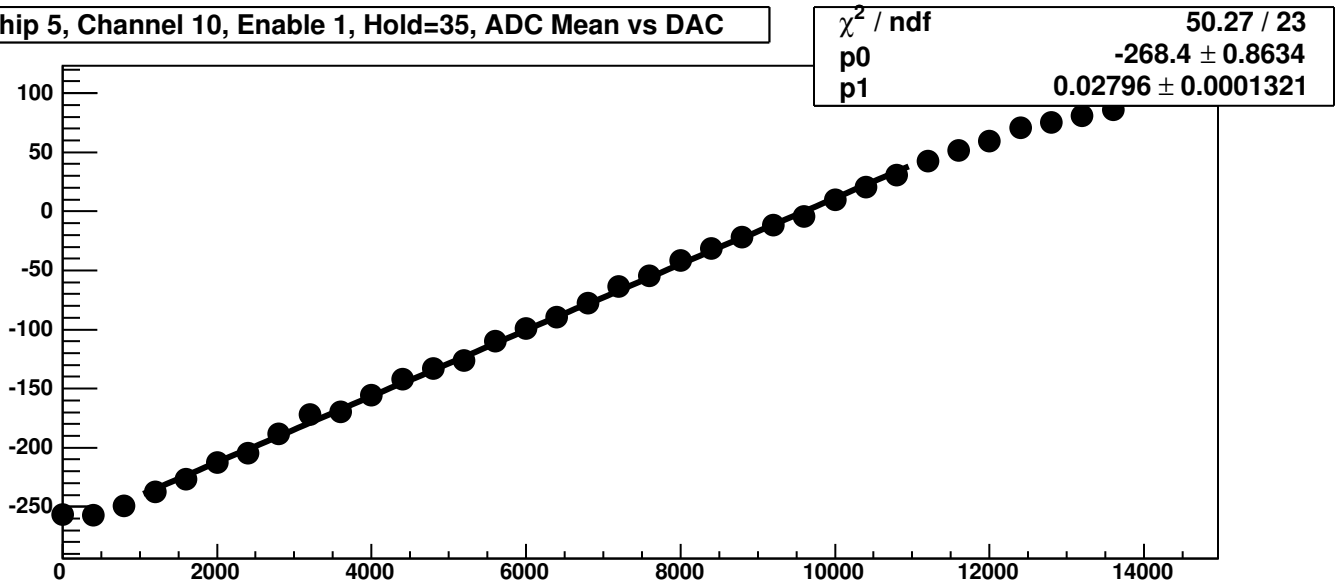



Chip 5, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 6554 / 23
p0 -420 ± 1.031
p1 2.51 ± 0.0001675
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

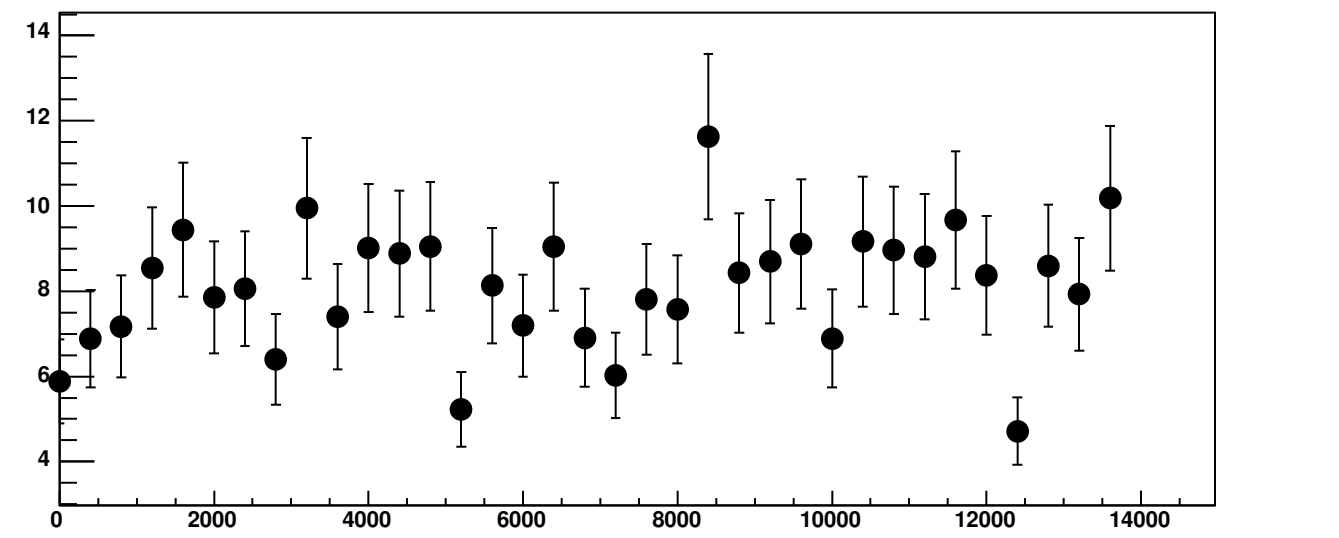

Chip 5, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

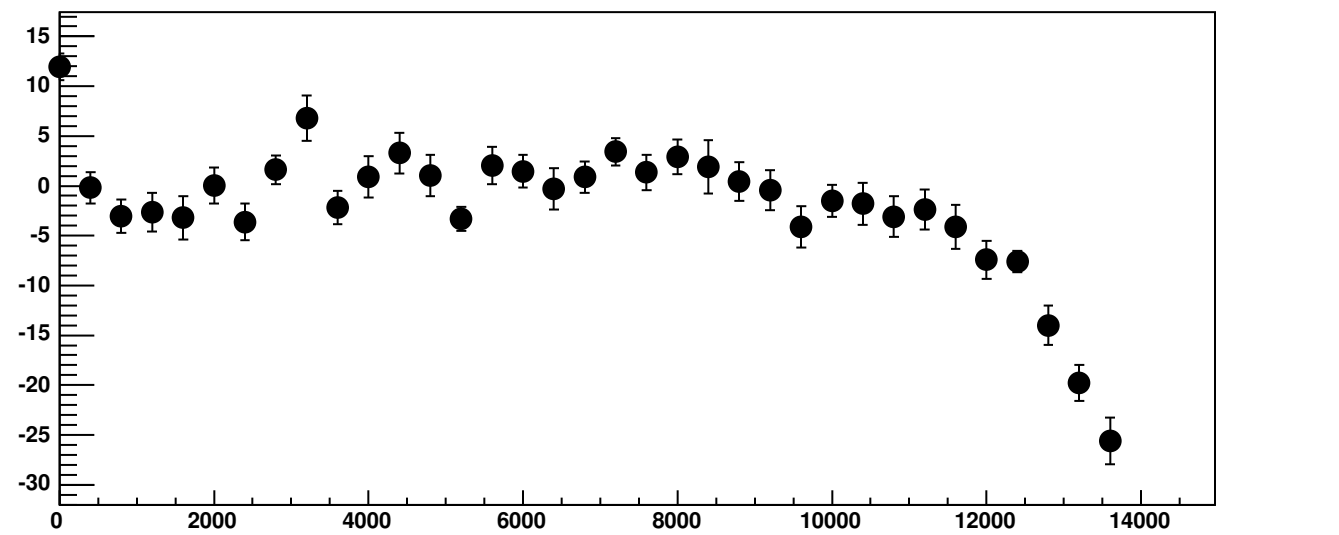
Chip 5, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC



Chip 5, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC

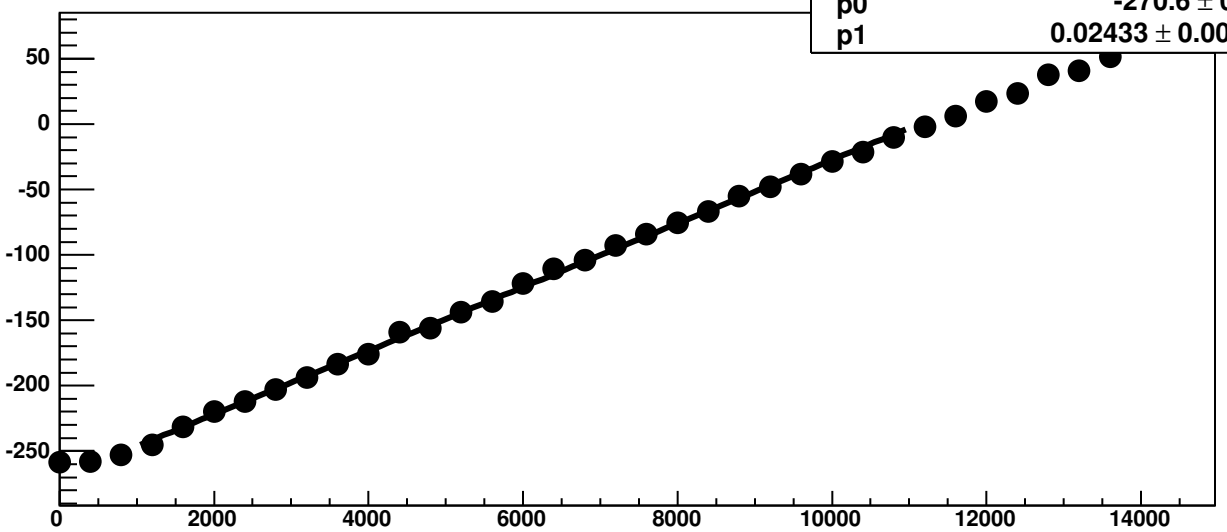


Chip 5, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

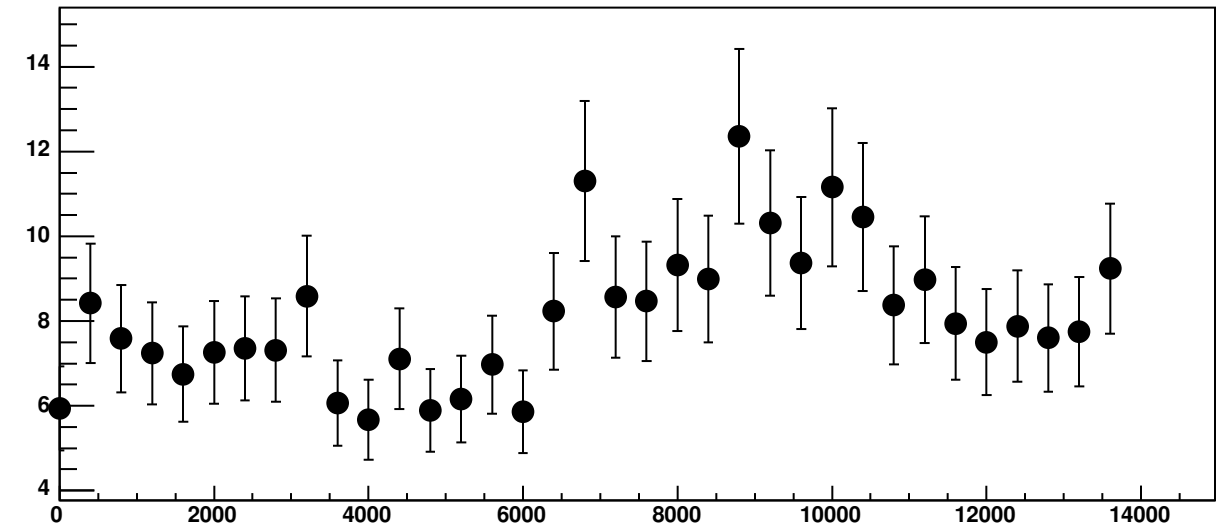


Chip 5, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

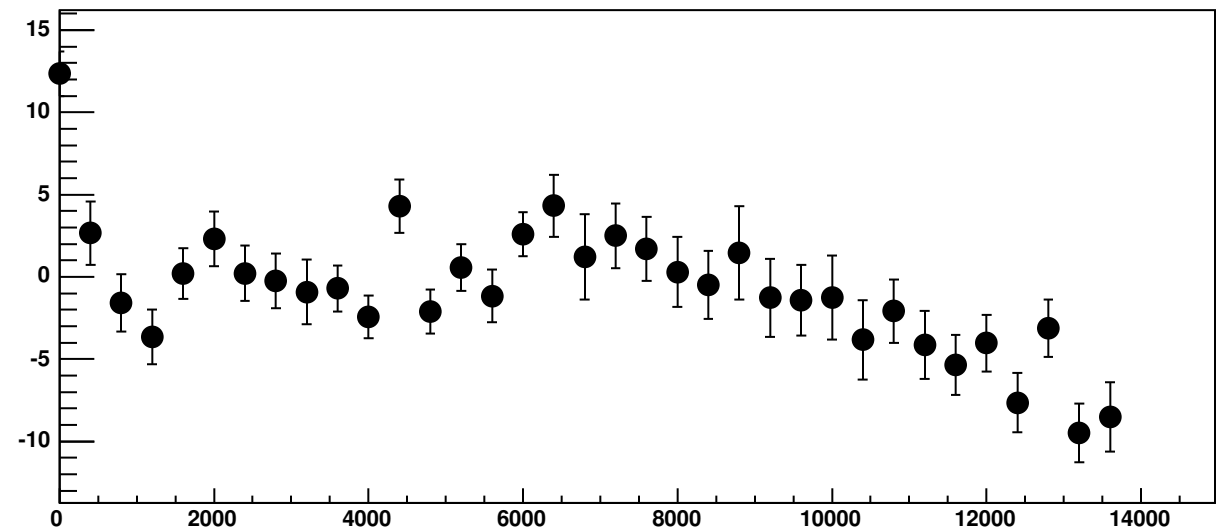
χ^2 / ndf	37.49 / 23
p0	-270.6 ± 0.7825
p1	0.02433 ± 0.0001331

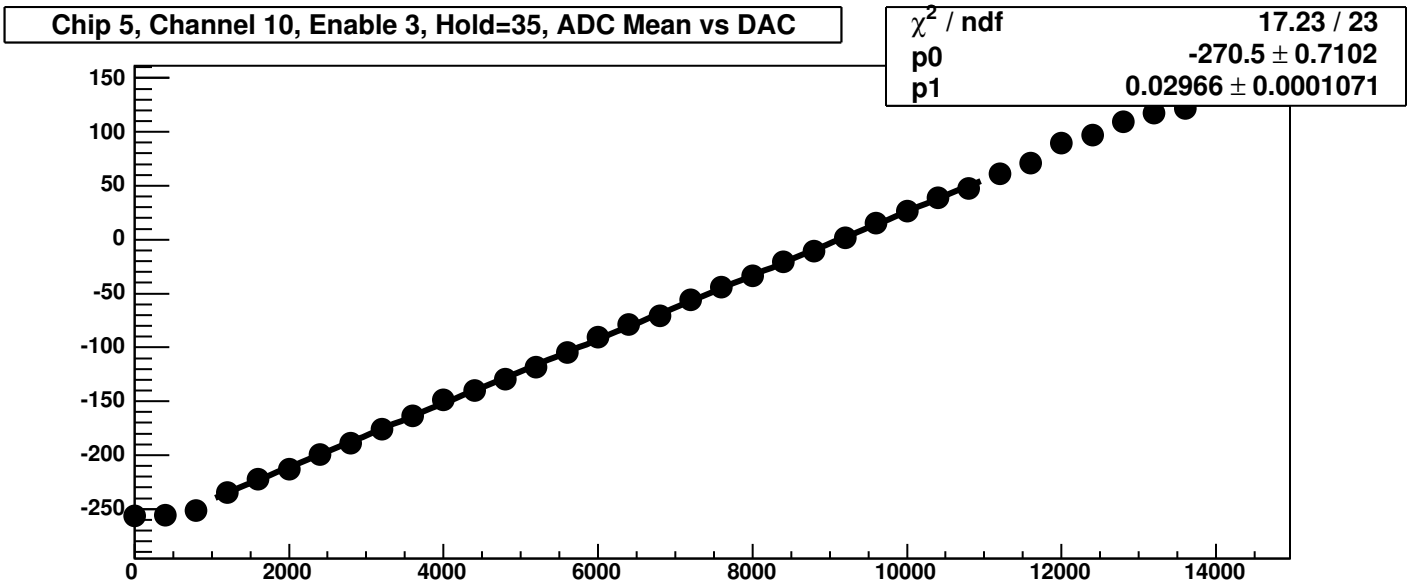


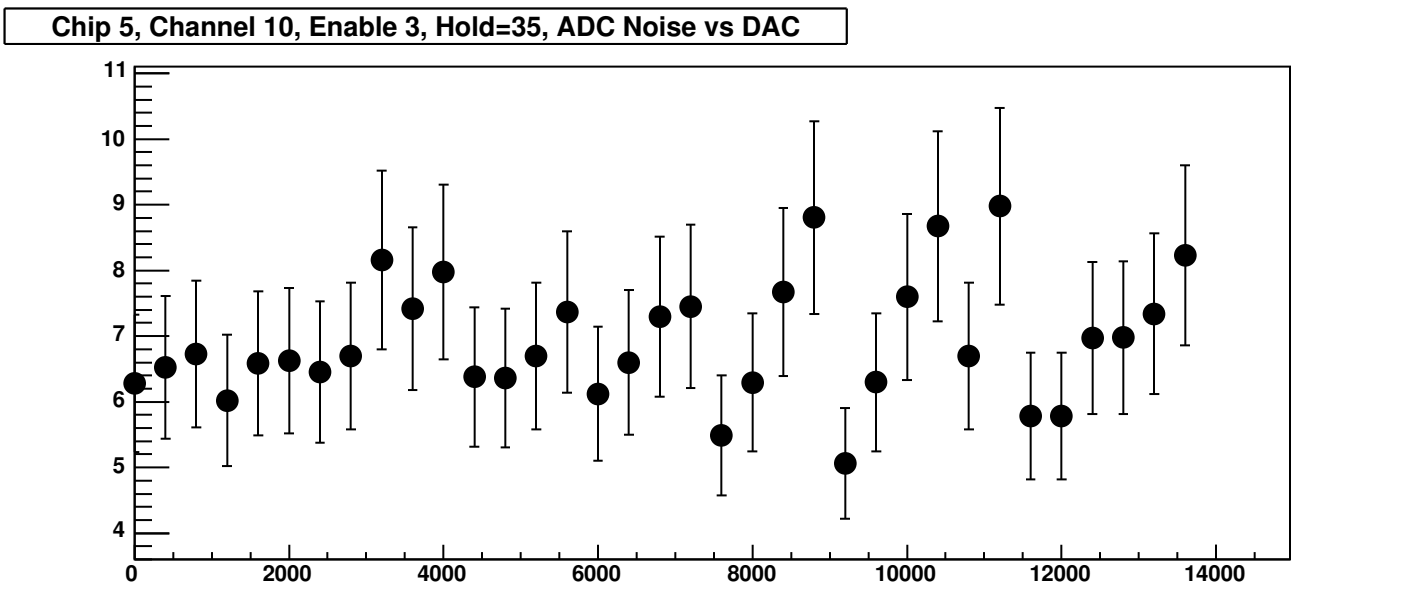
Chip 5, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

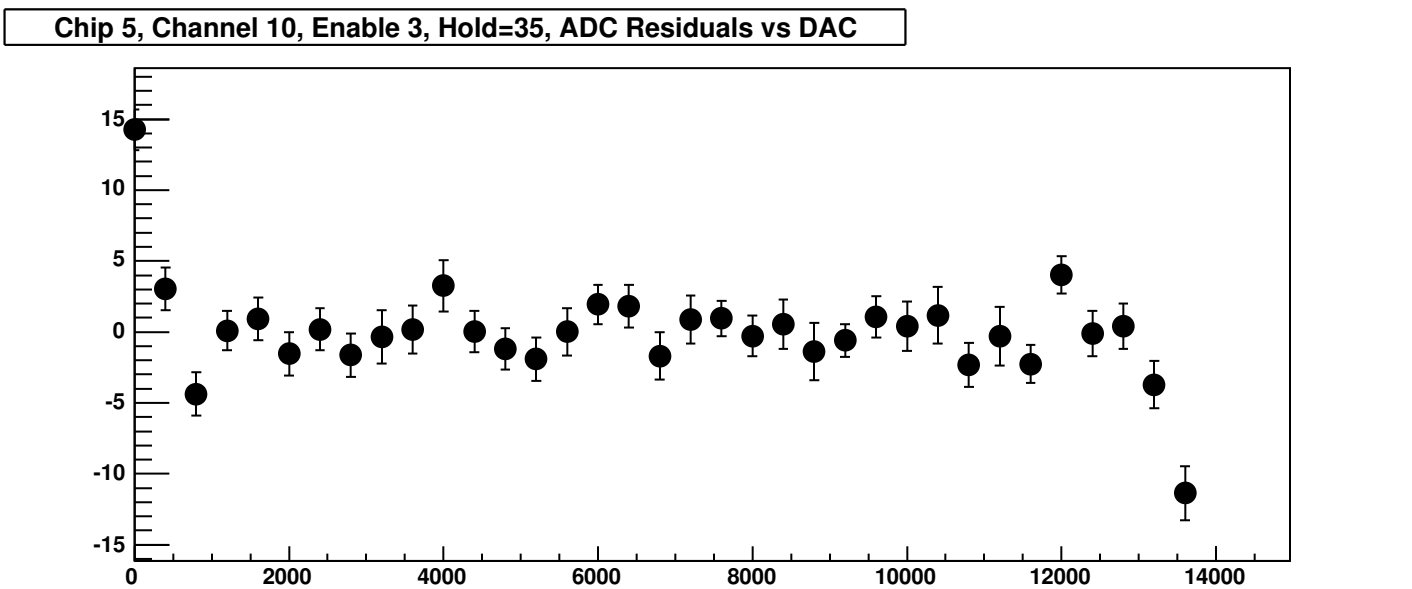


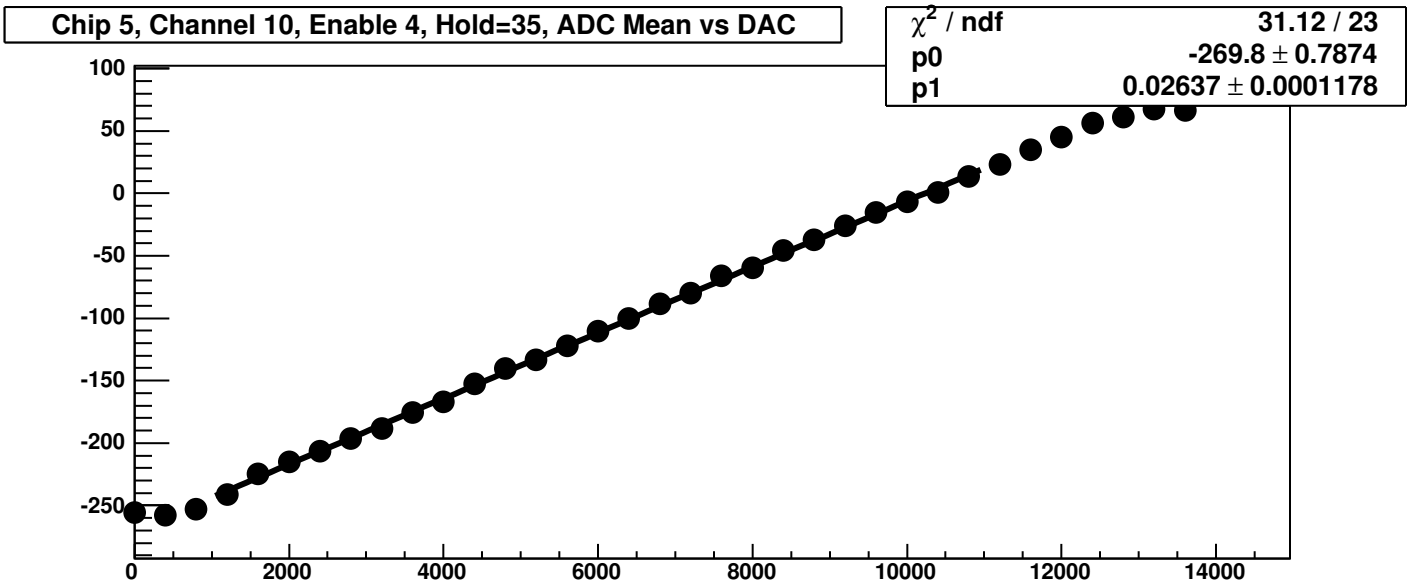
Chip 5, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

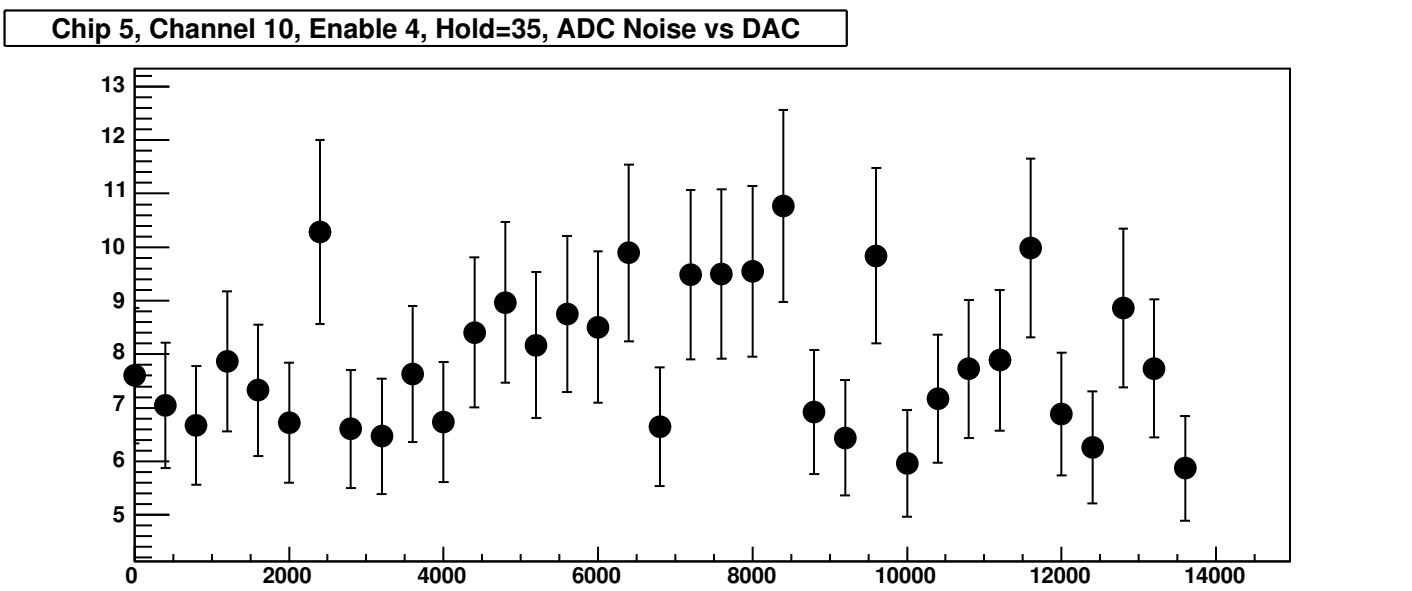


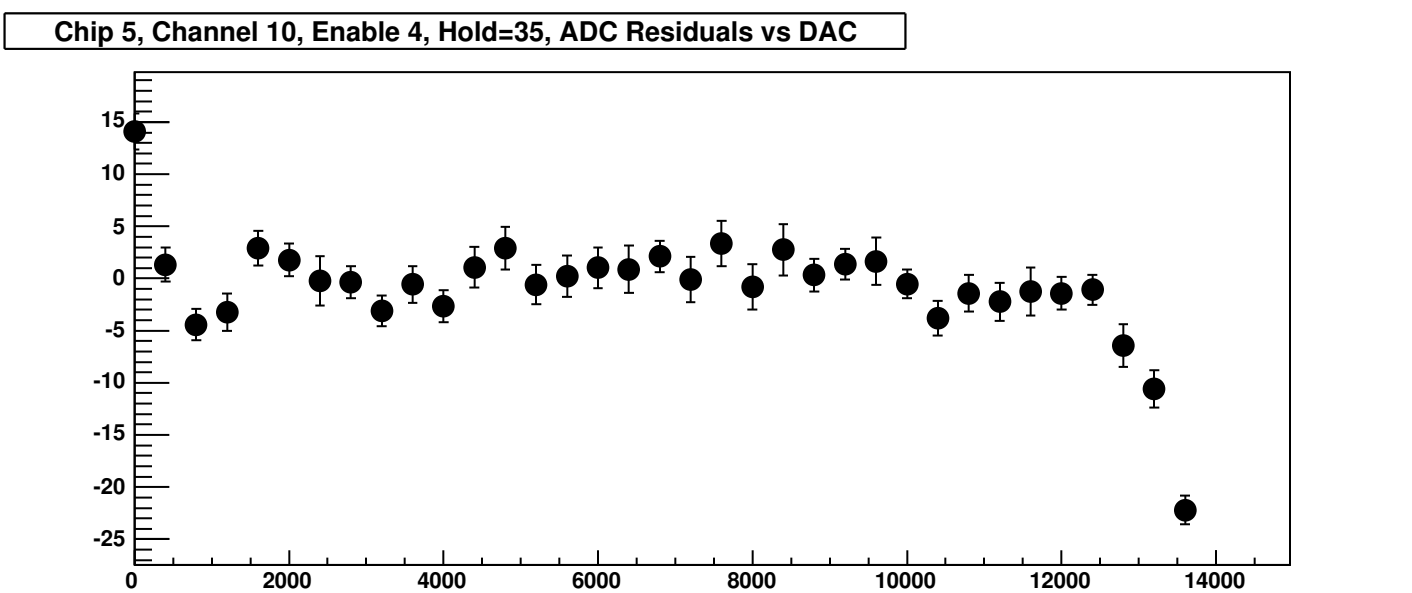

Chip 5, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 17.23 / 23
p0 -270.5 ± 0.7102
p1 0.02966 ± 0.0001071
150
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

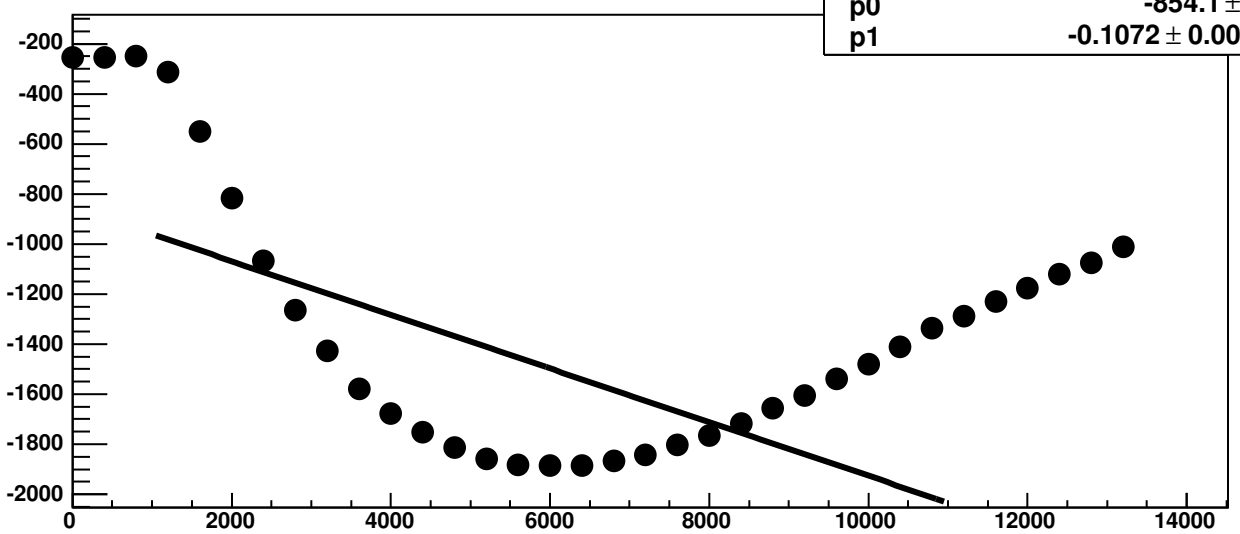

Chip 5, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 31.12 / 23
p0 -269.8 ± 0.7874
p1 0.02637 ± 0.0001178
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

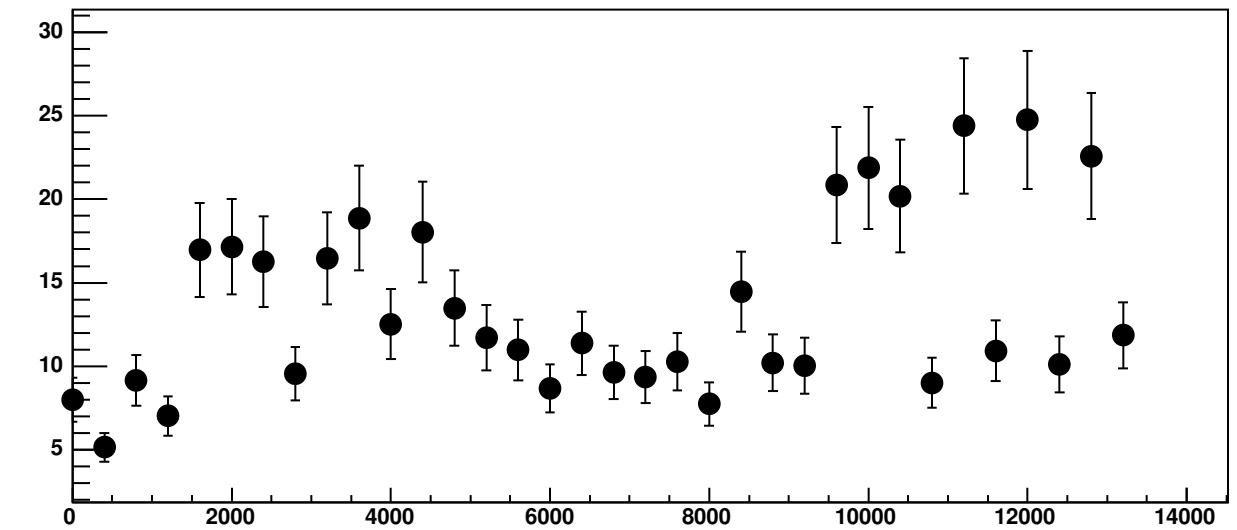

Chip 5, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

Chip 5, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

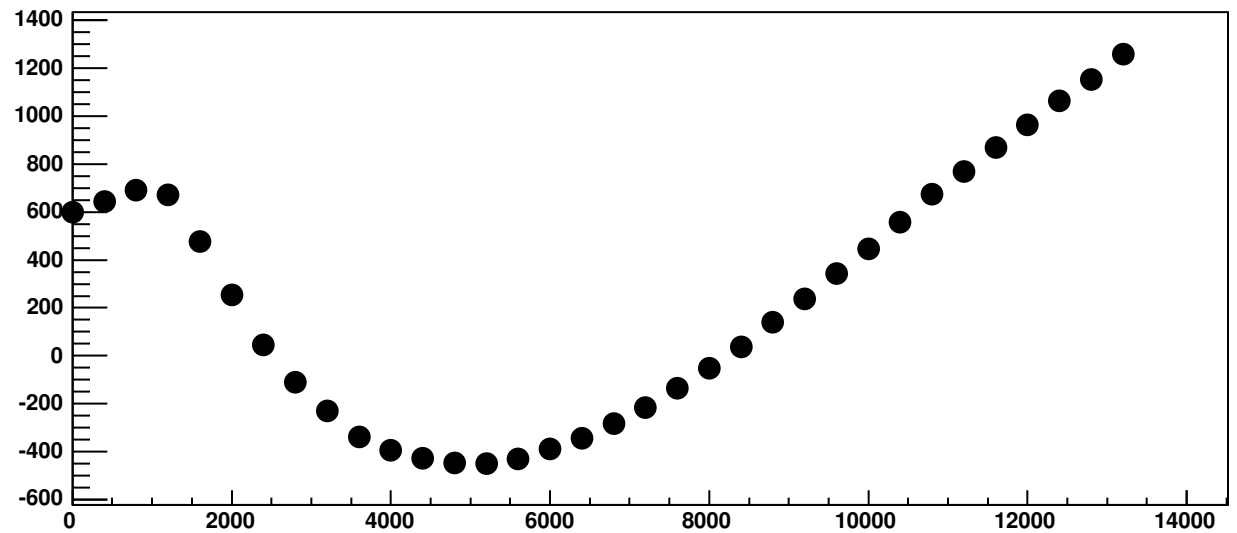
χ^2 / ndf	5.469e+05 / 23
p0	-854.1 ± 1.225
p1	-0.1072 ± 0.0001838

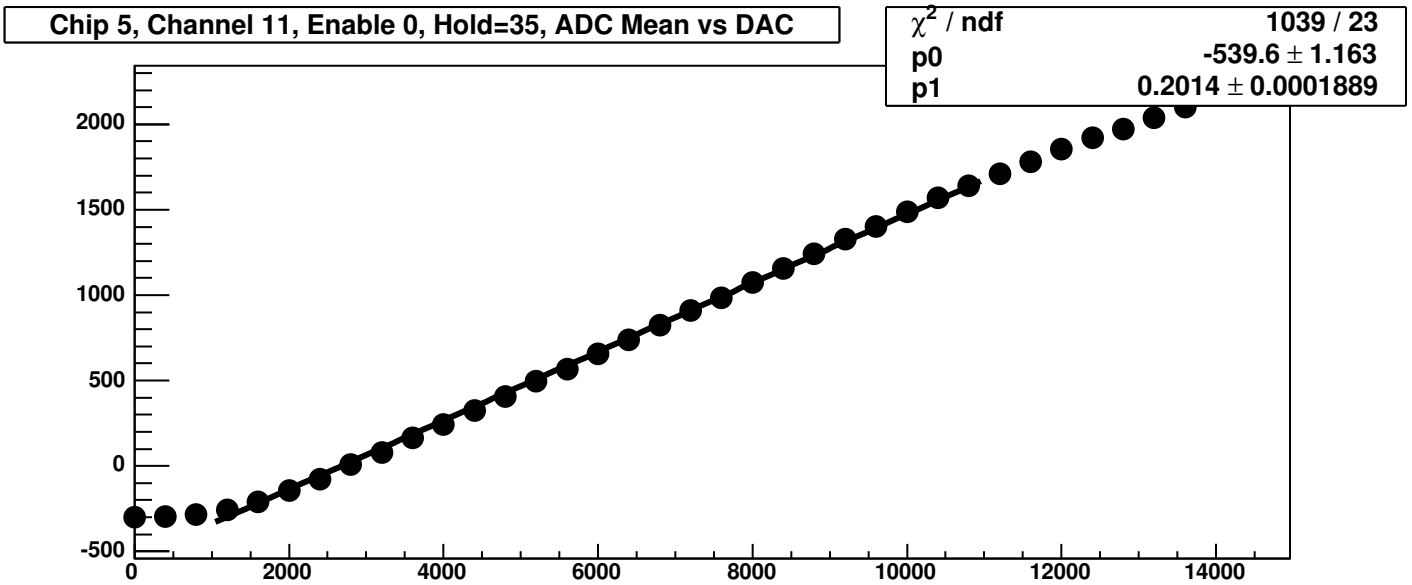


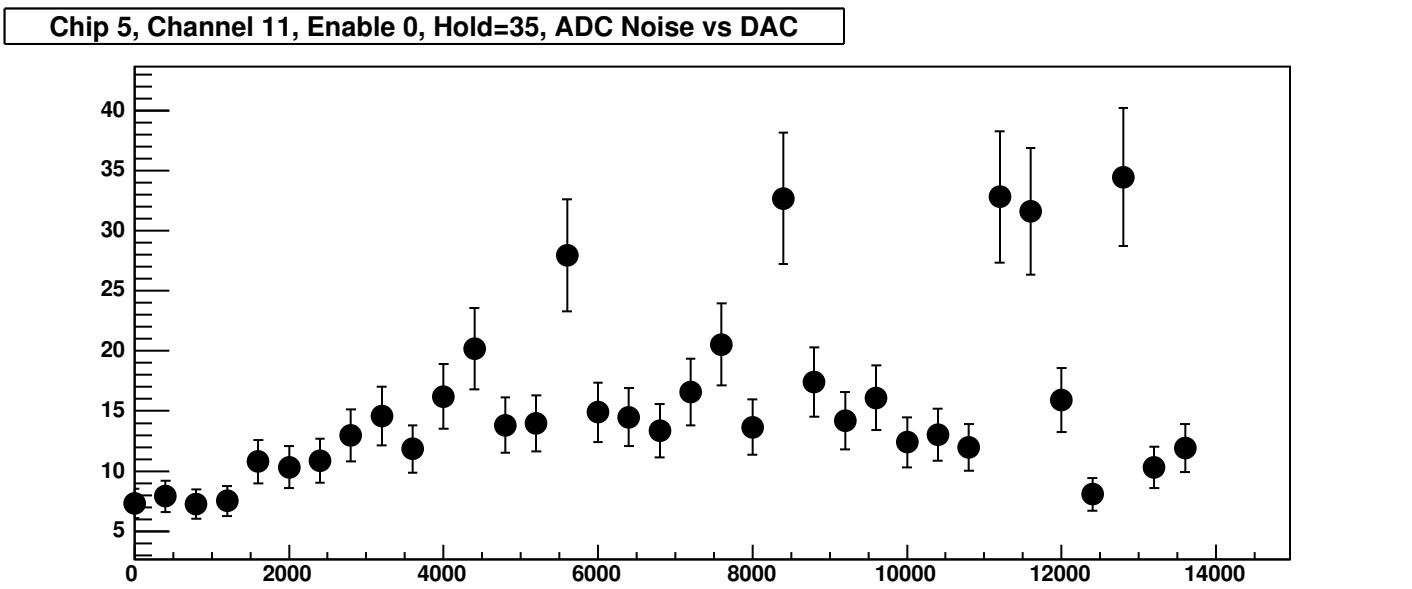
Chip 5, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

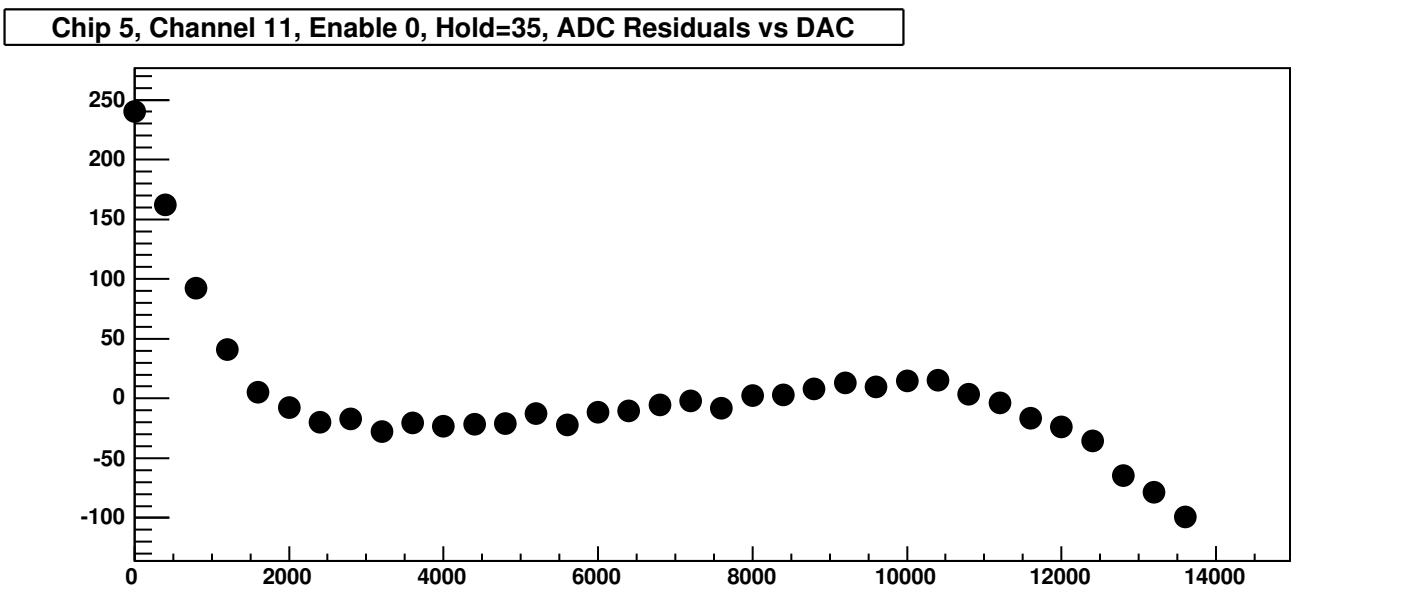


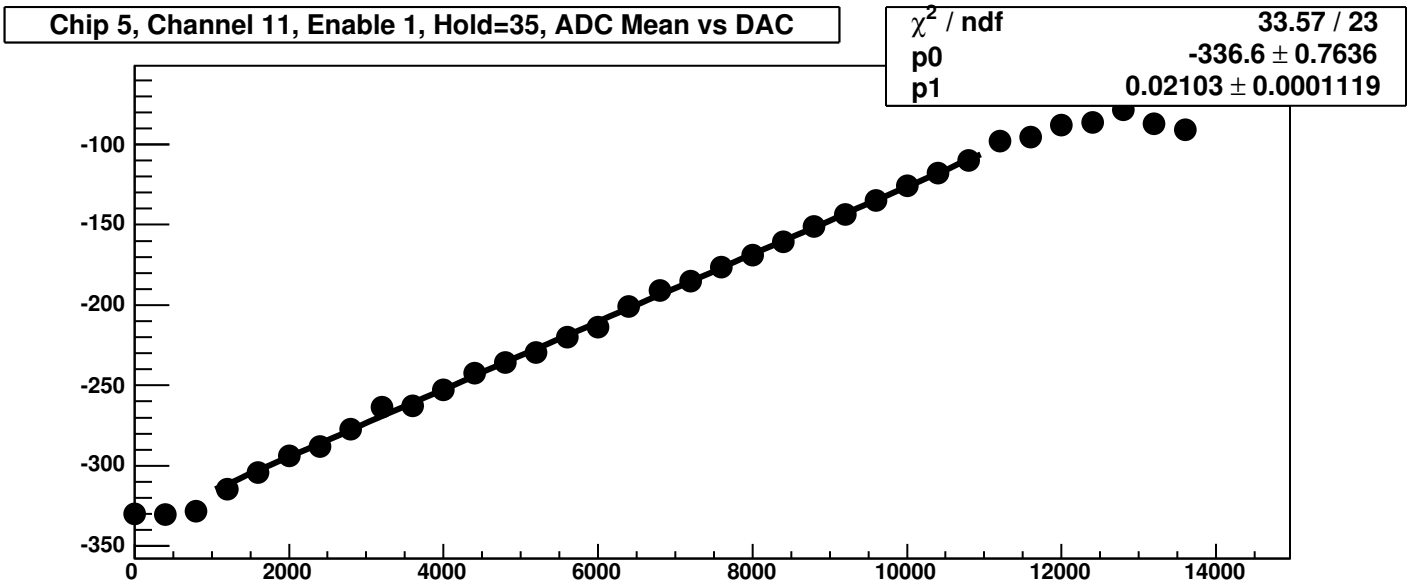
Chip 5, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

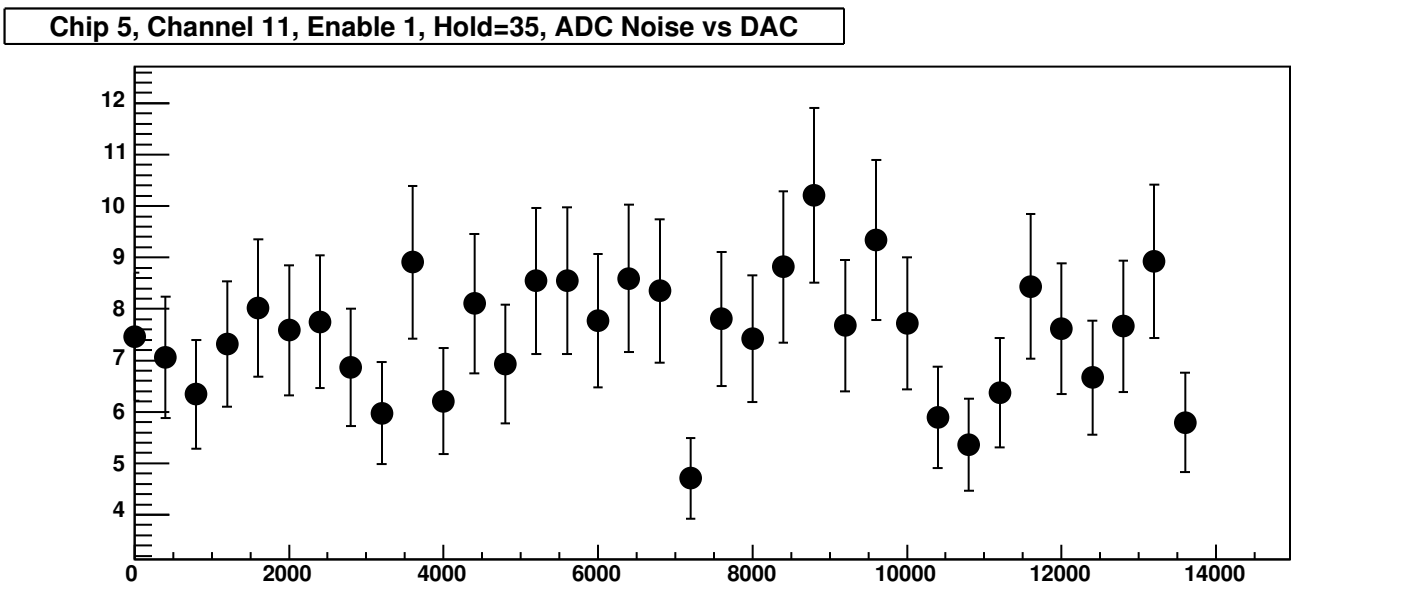


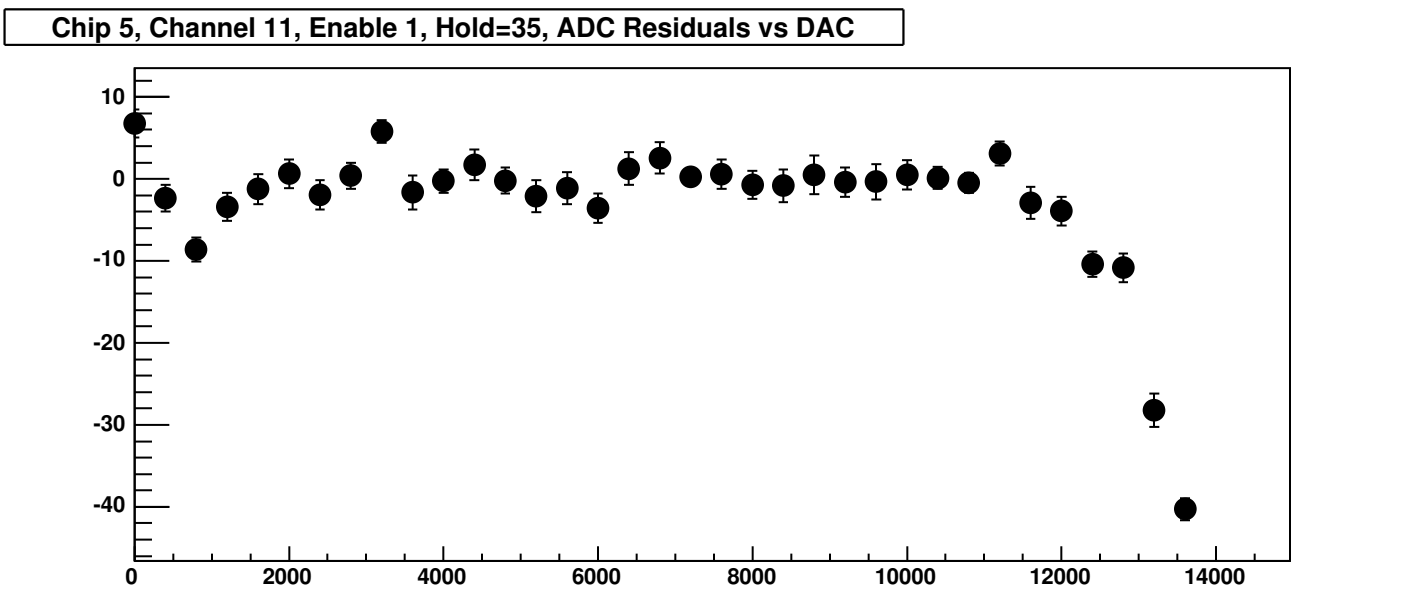

Chip 5, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1039 / 23
p0 -539.6 ± 1.163
p1 0.2014 ± 0.0001889
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

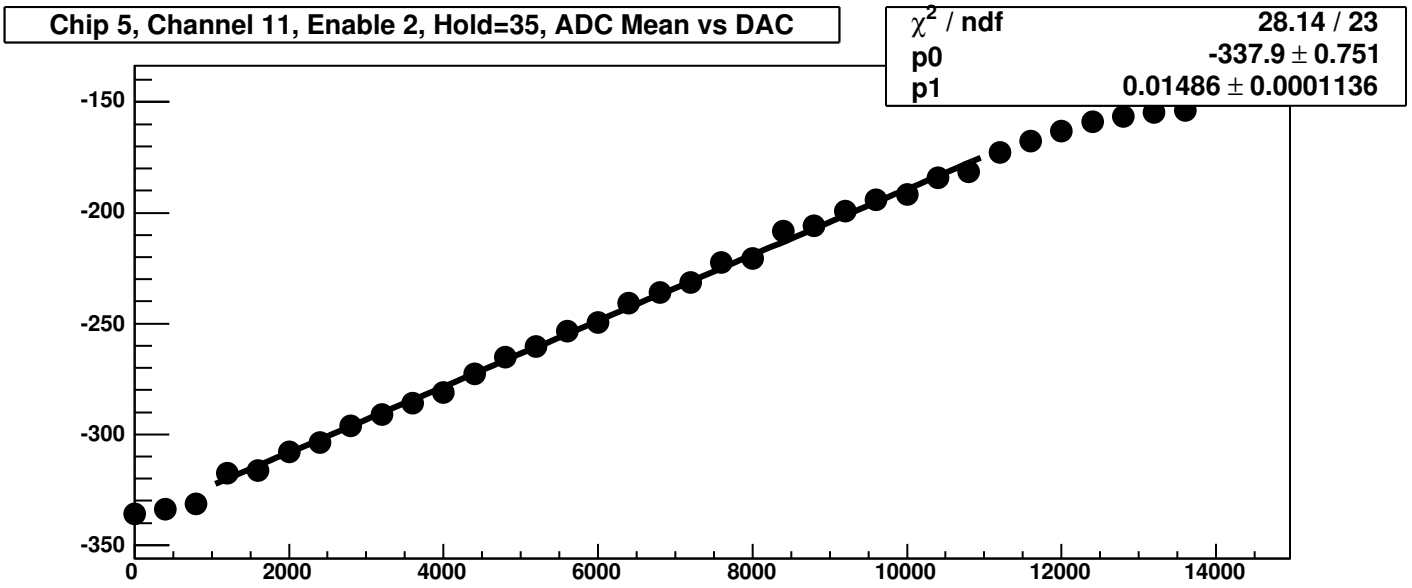

Chip 5, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

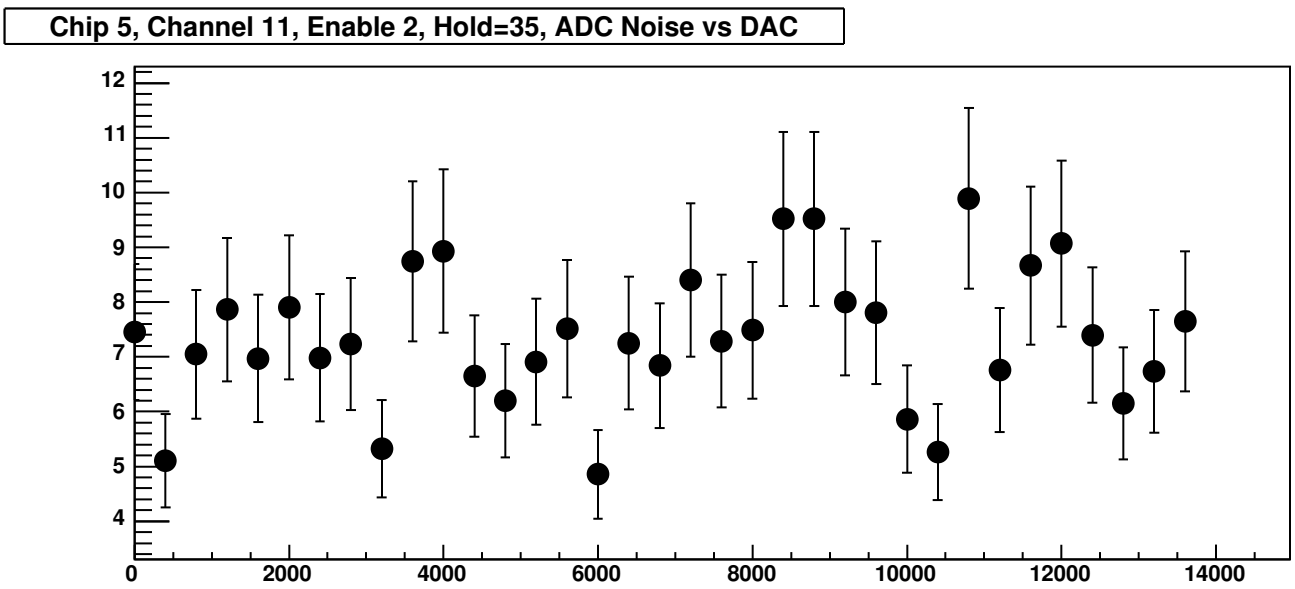

Chip 5, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

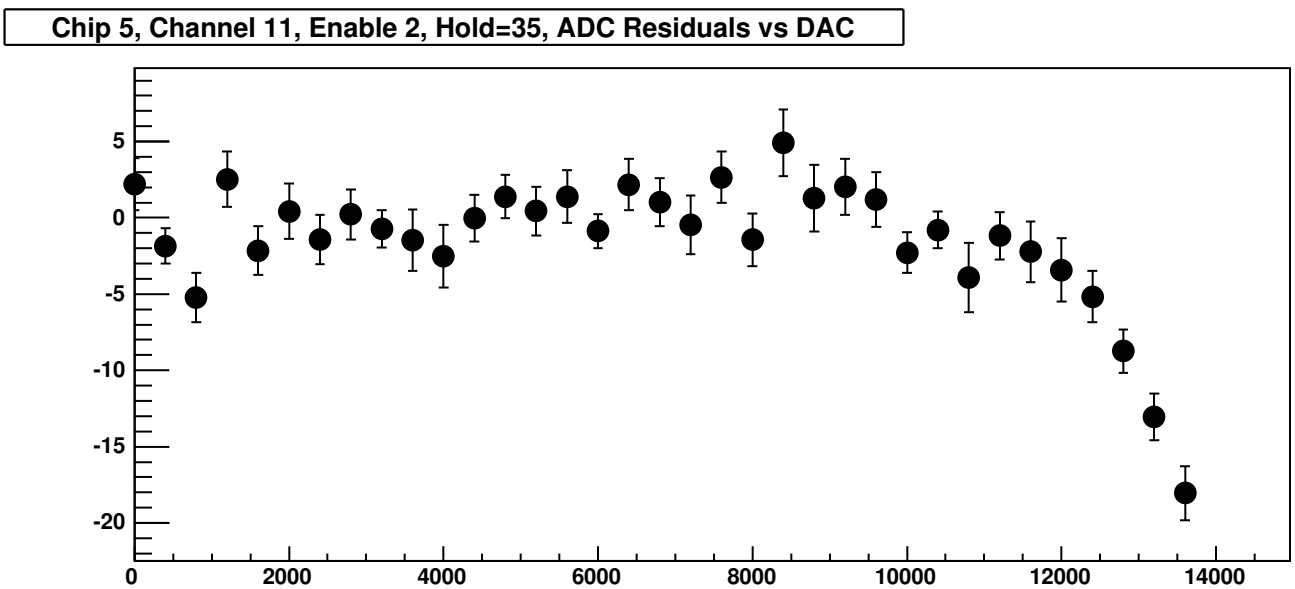

Chip 5, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.57 / 23
p0 -336.6 ± 0.7636
p1 0.02103 ± 0.0001119
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

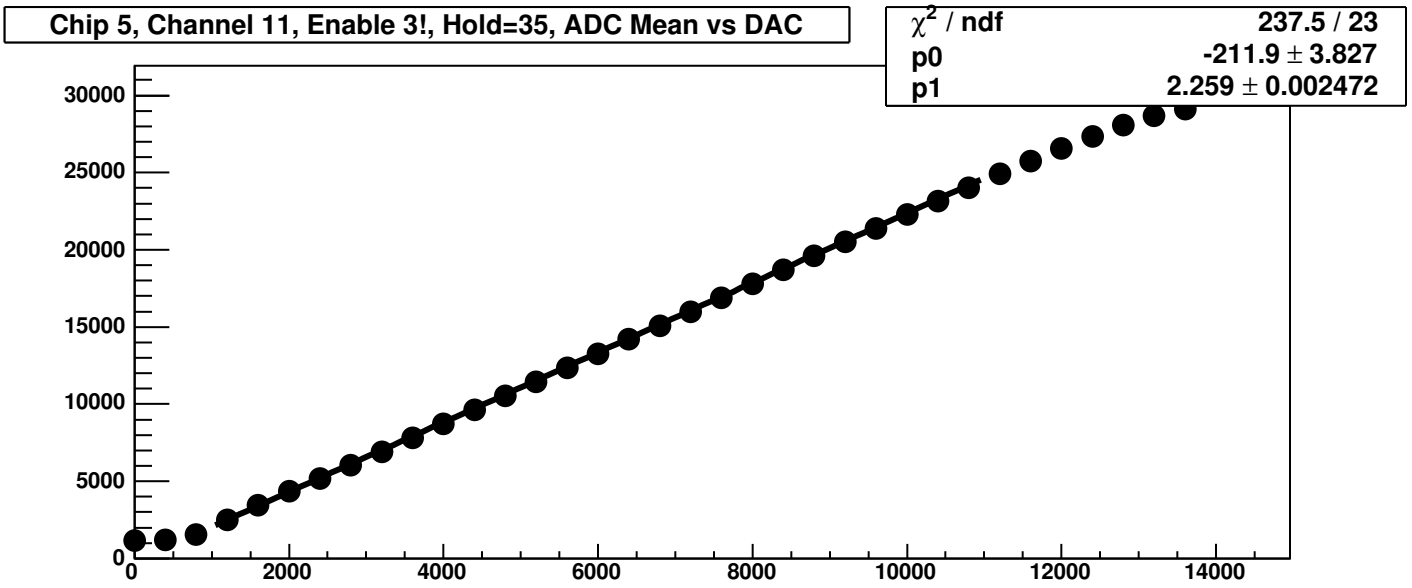

Chip 5, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

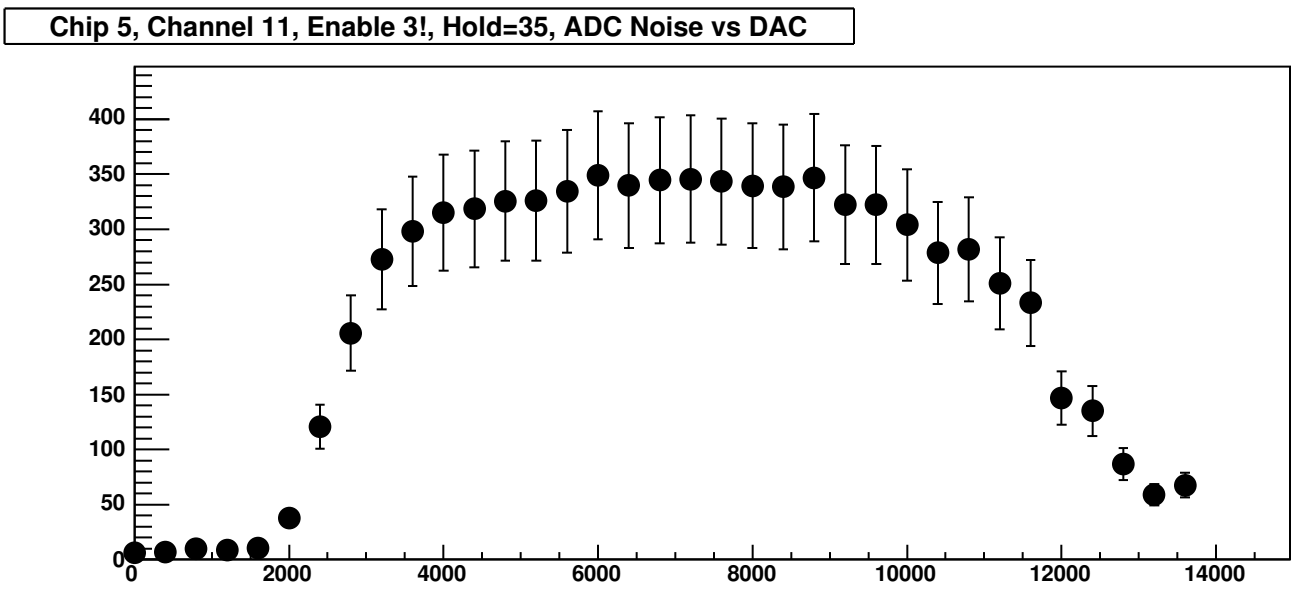

Chip 5, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

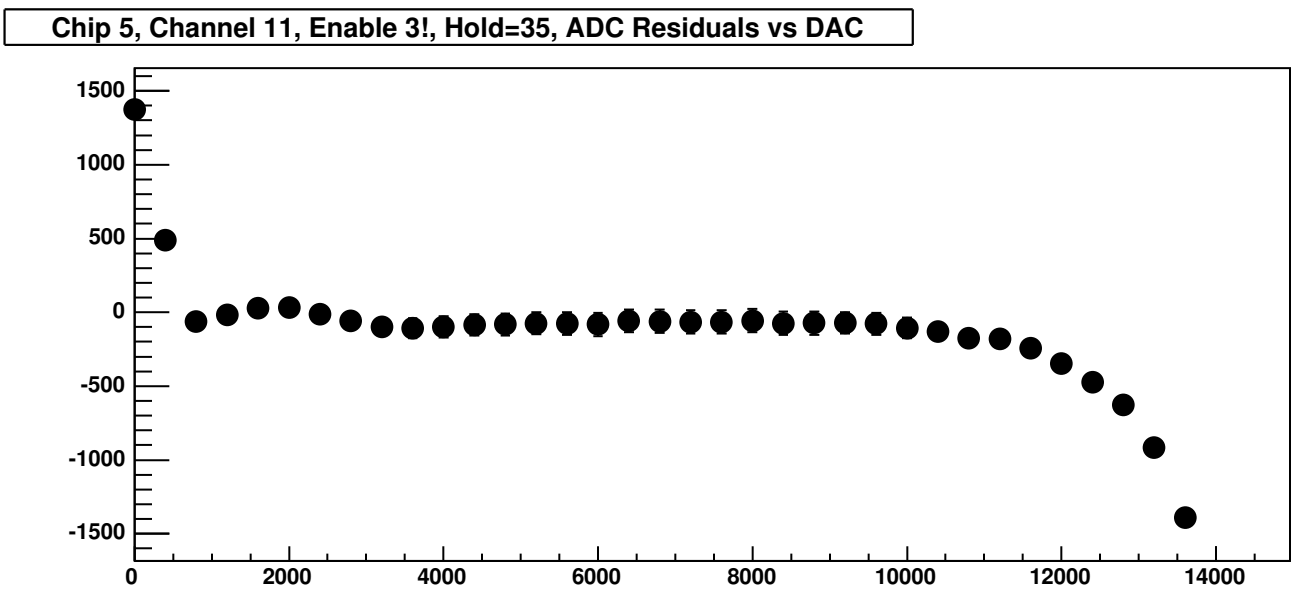

Chip 5, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 28.14 / 23
p0 -337.9 ± 0.751
p1 0.01486 ± 0.0001136
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

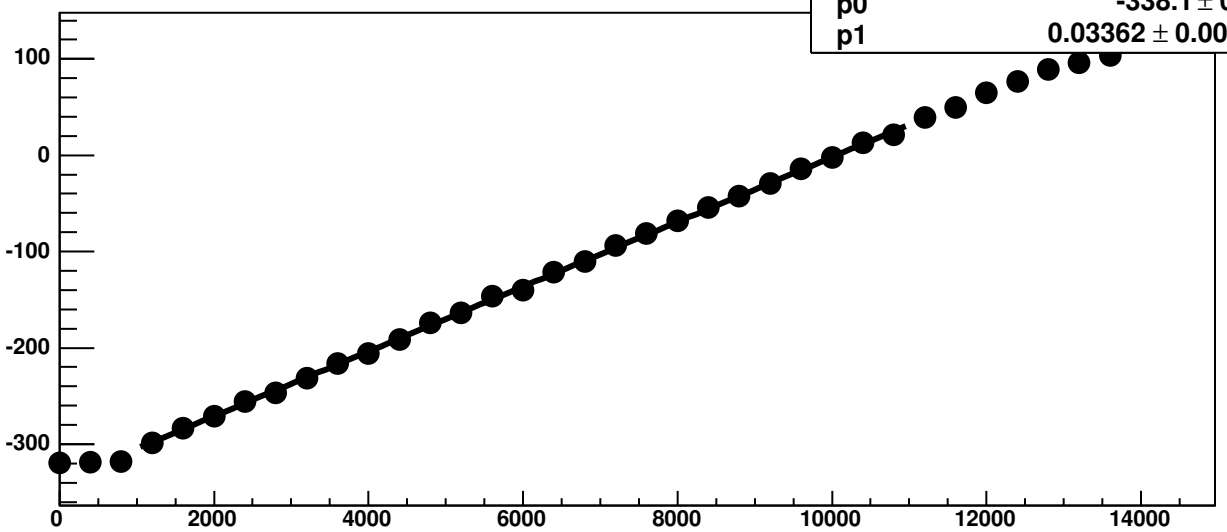

Chip 5, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 237.5 / 23
p0 -211.9 ± 3.827
p1 2.259 ± 0.002472
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

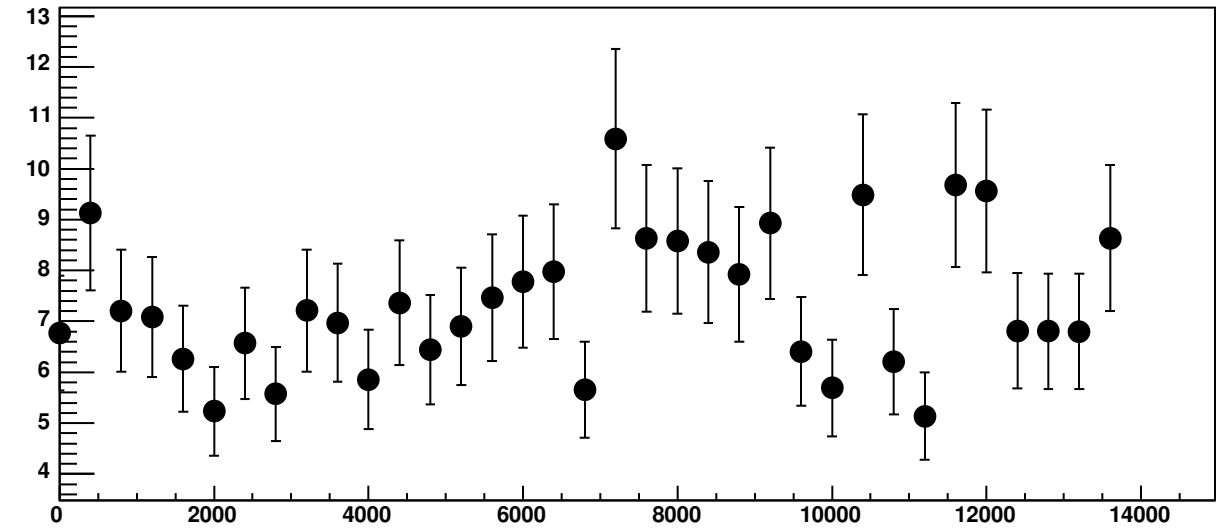

Chip 5, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

Chip 5, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

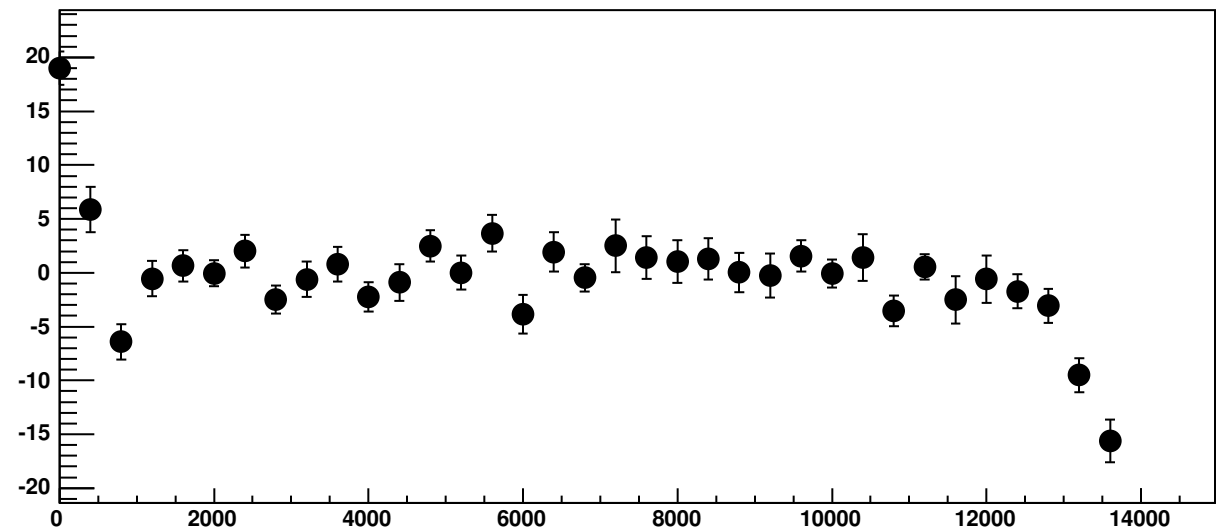
χ^2 / ndf	32.76 / 23
p0	-338.1 ± 0.6776
p1	0.03362 ± 0.0001062

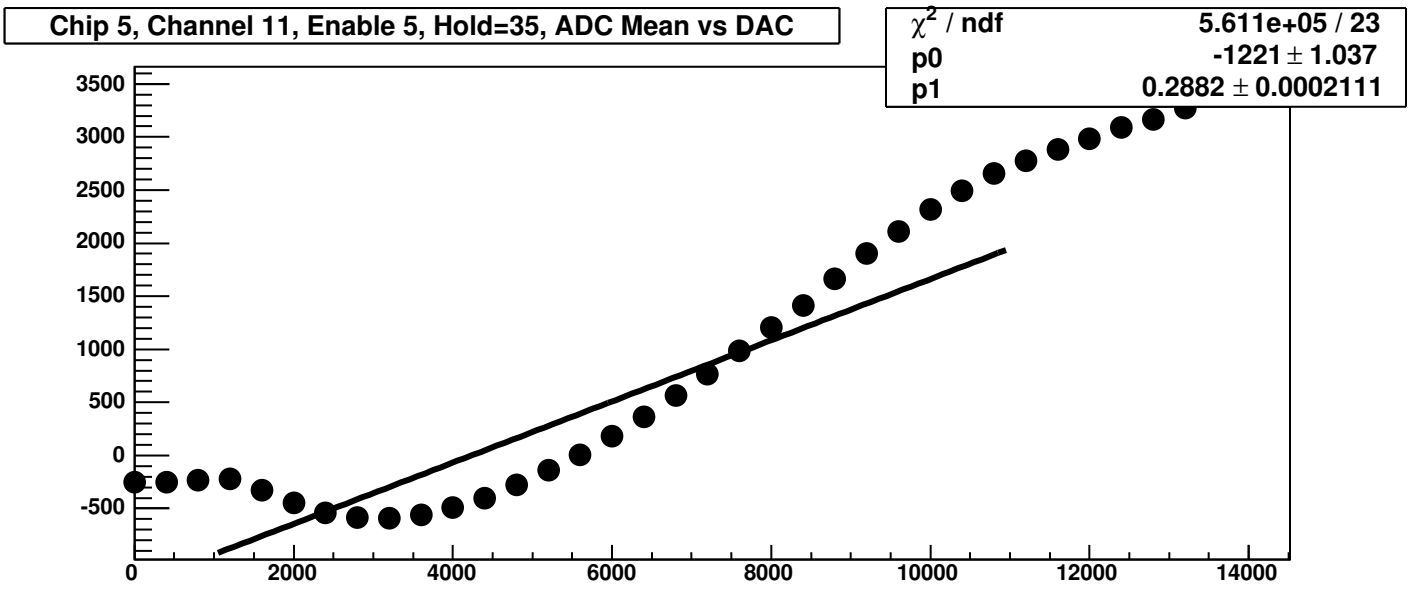


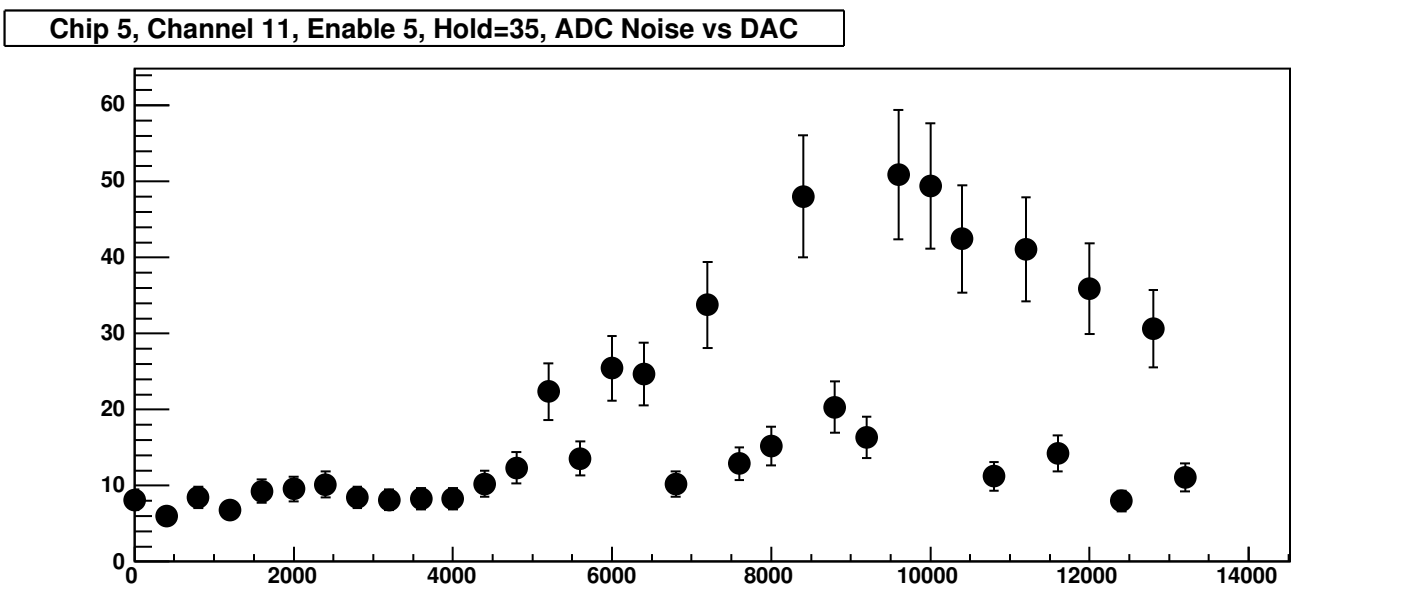
Chip 5, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

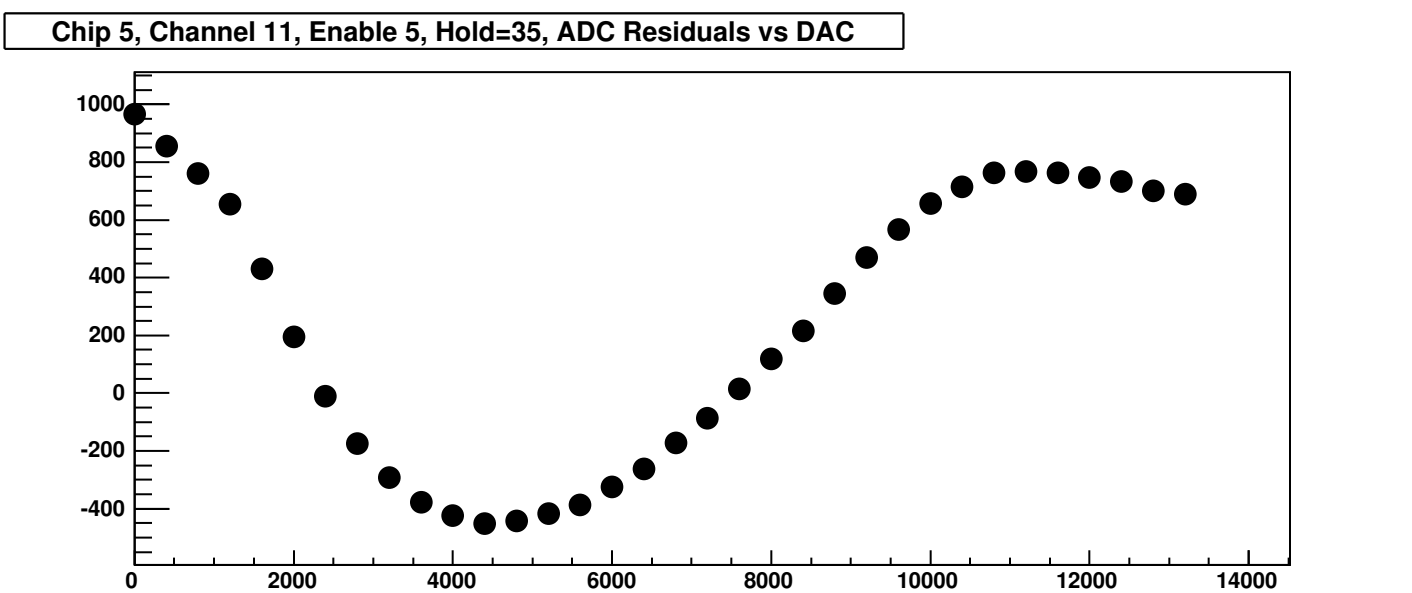


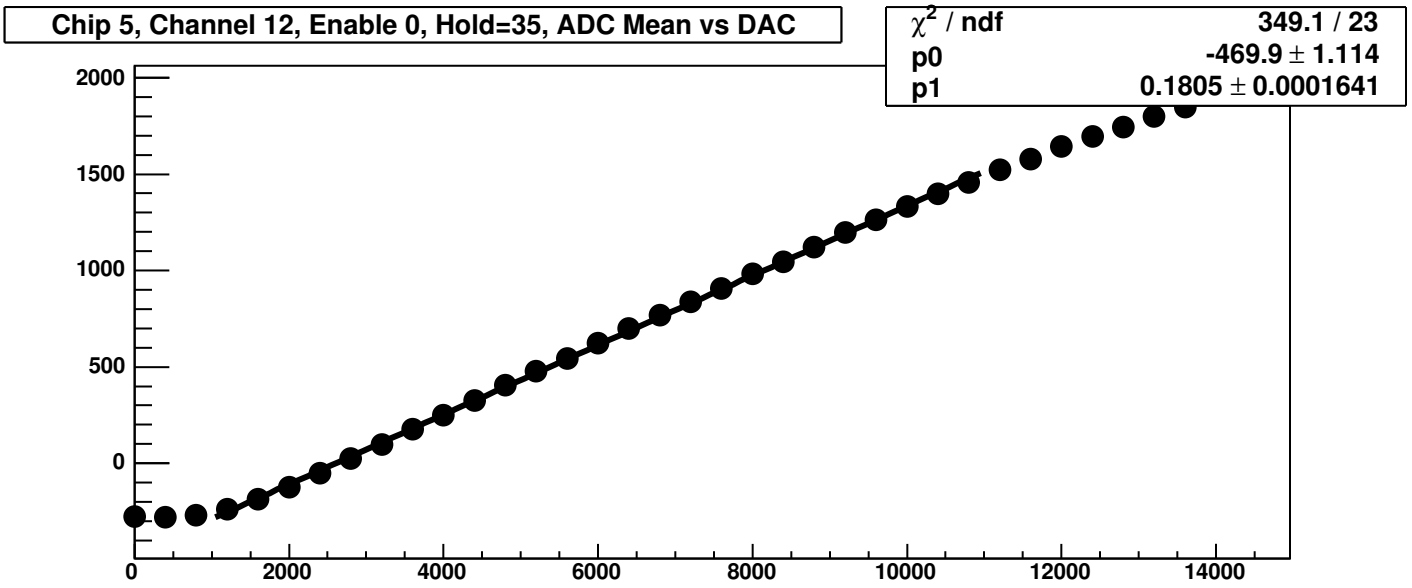
Chip 5, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

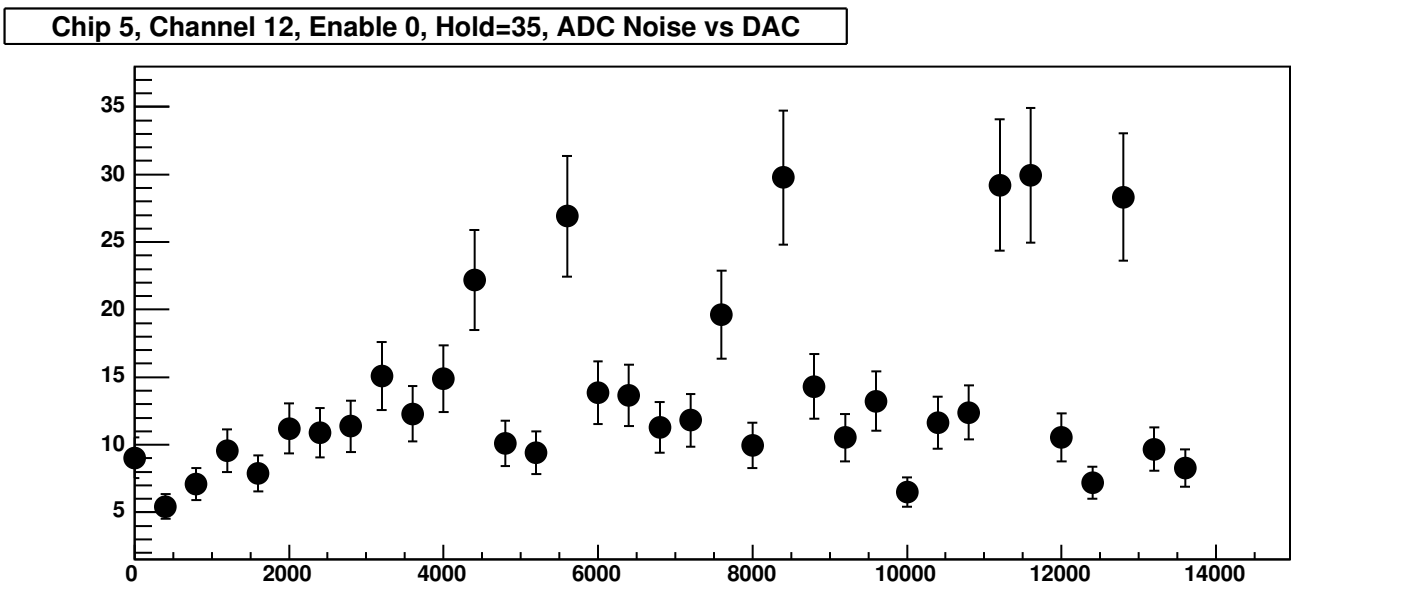


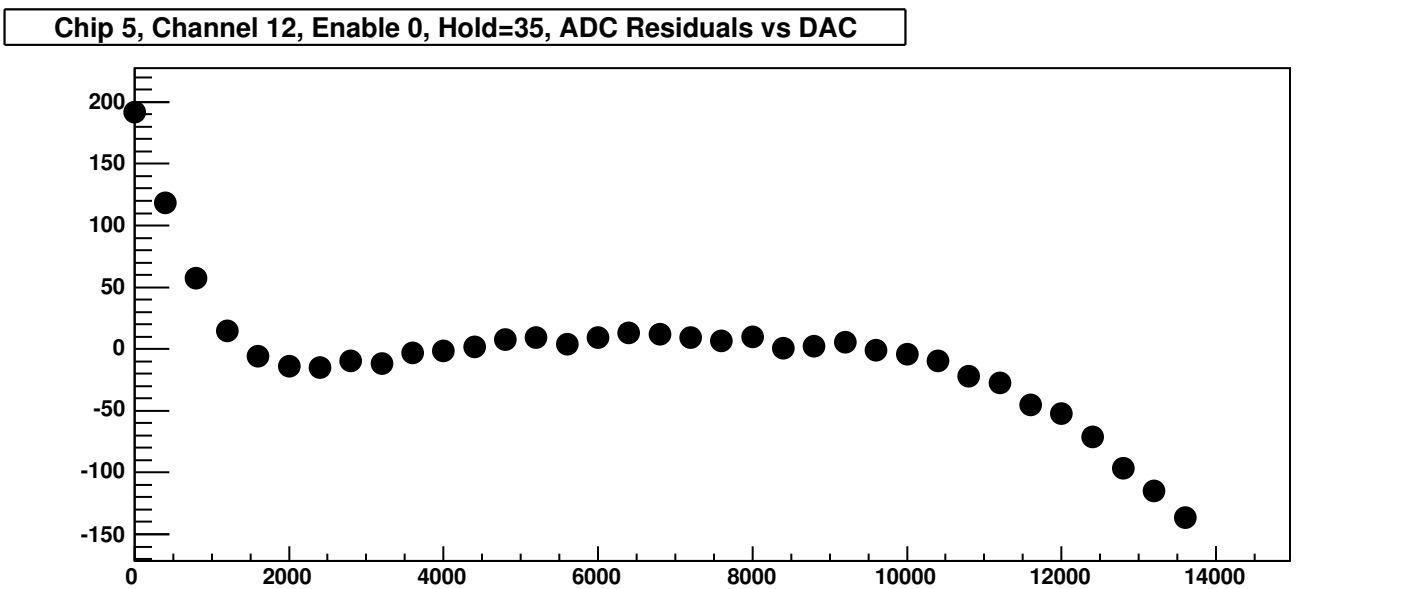

Chip 5, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5.611e+05 / 23
p0 -1221 ± 1.037
p1 0.2882 ± 0.0002111
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

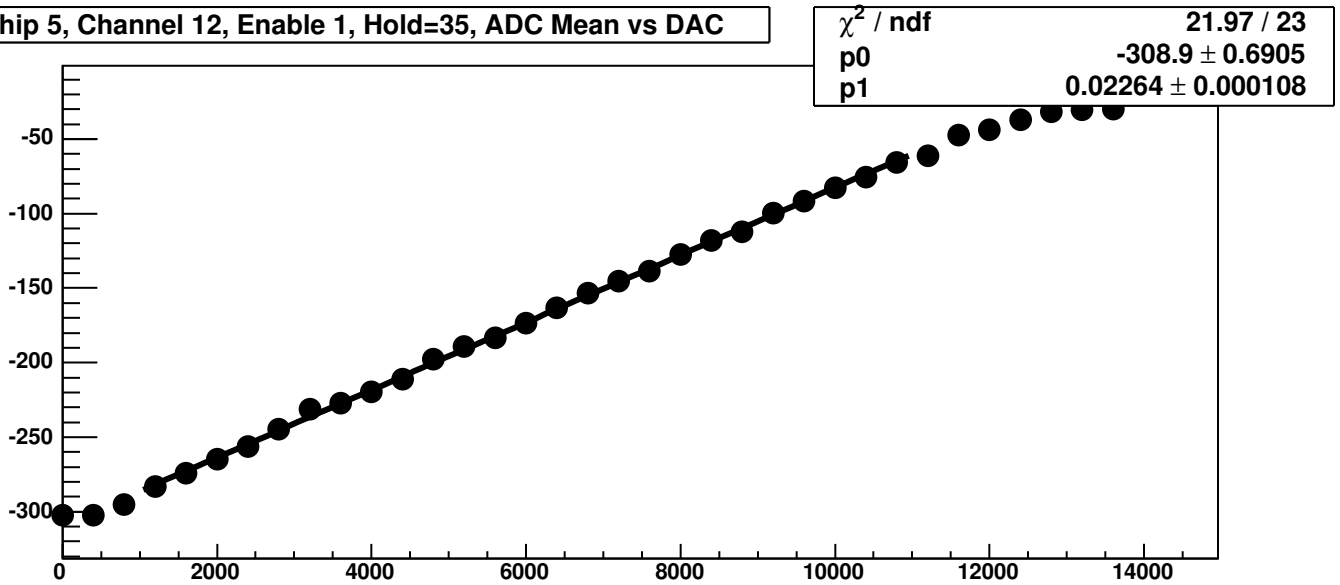

Chip 5, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 349.1 / 23
p0 -469.9 ± 1.114
p1 0.1805 ± 0.0001641
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

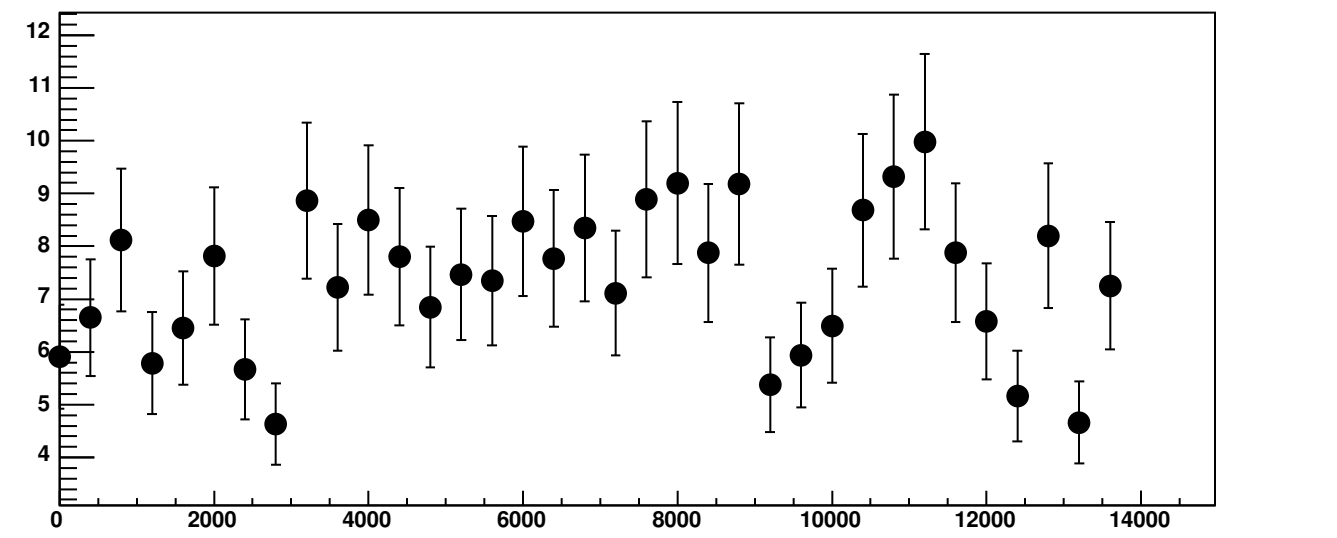

Chip 5, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

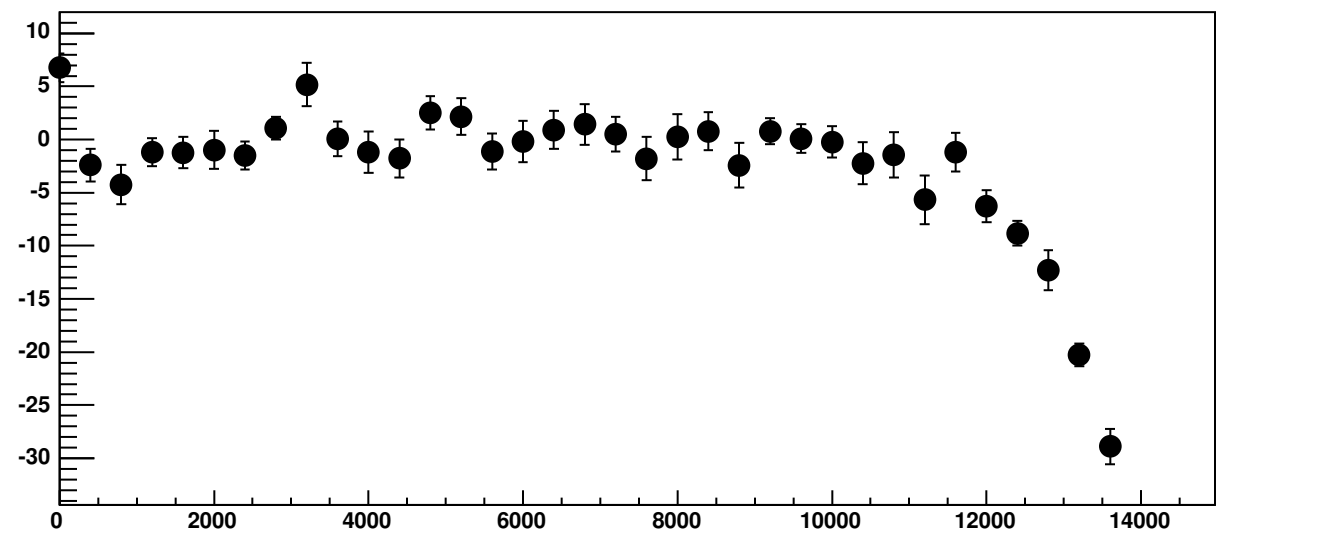
Chip 5, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC

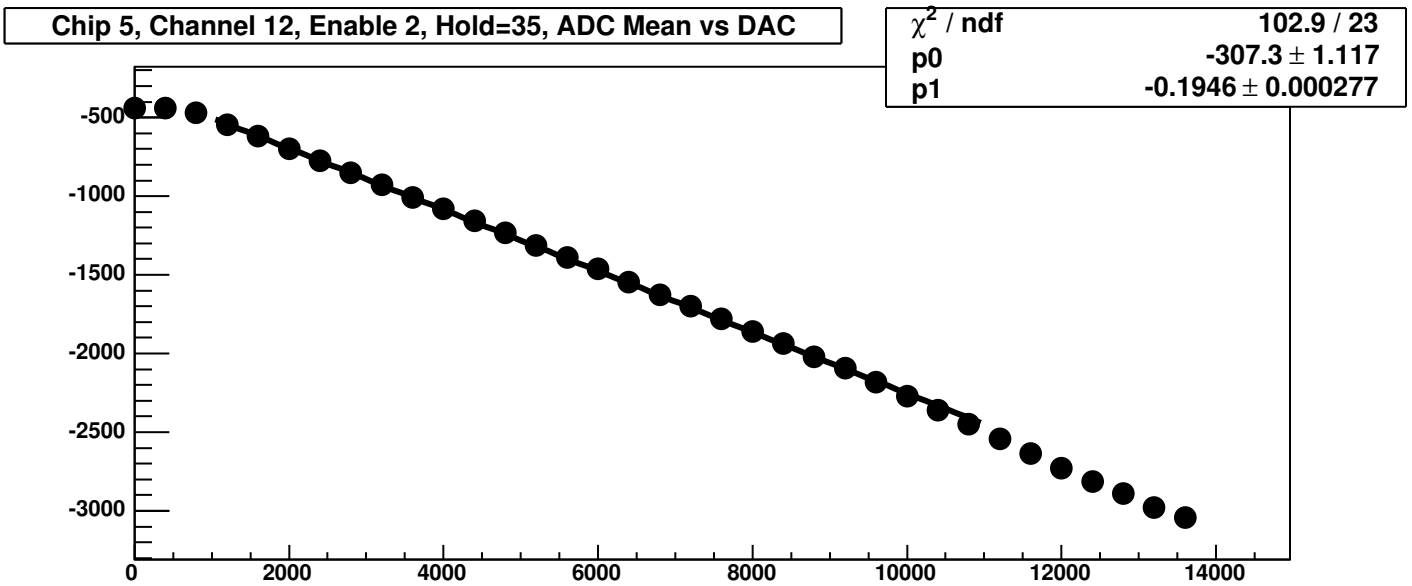


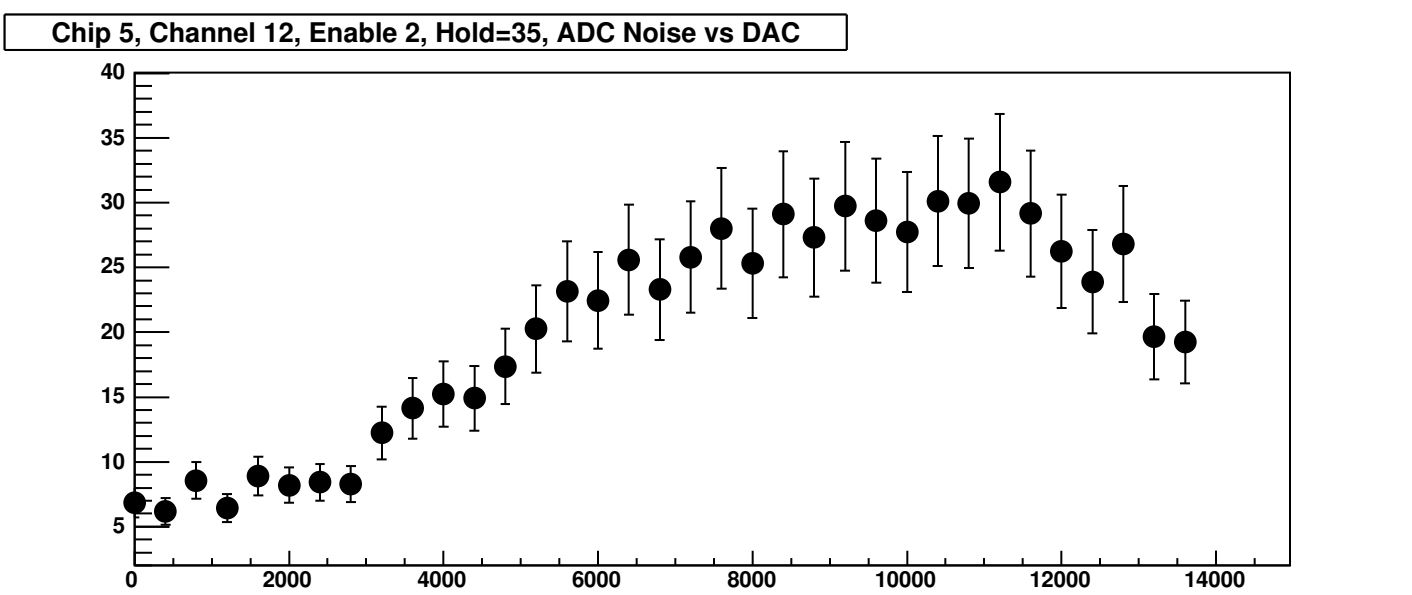
Chip 5, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC

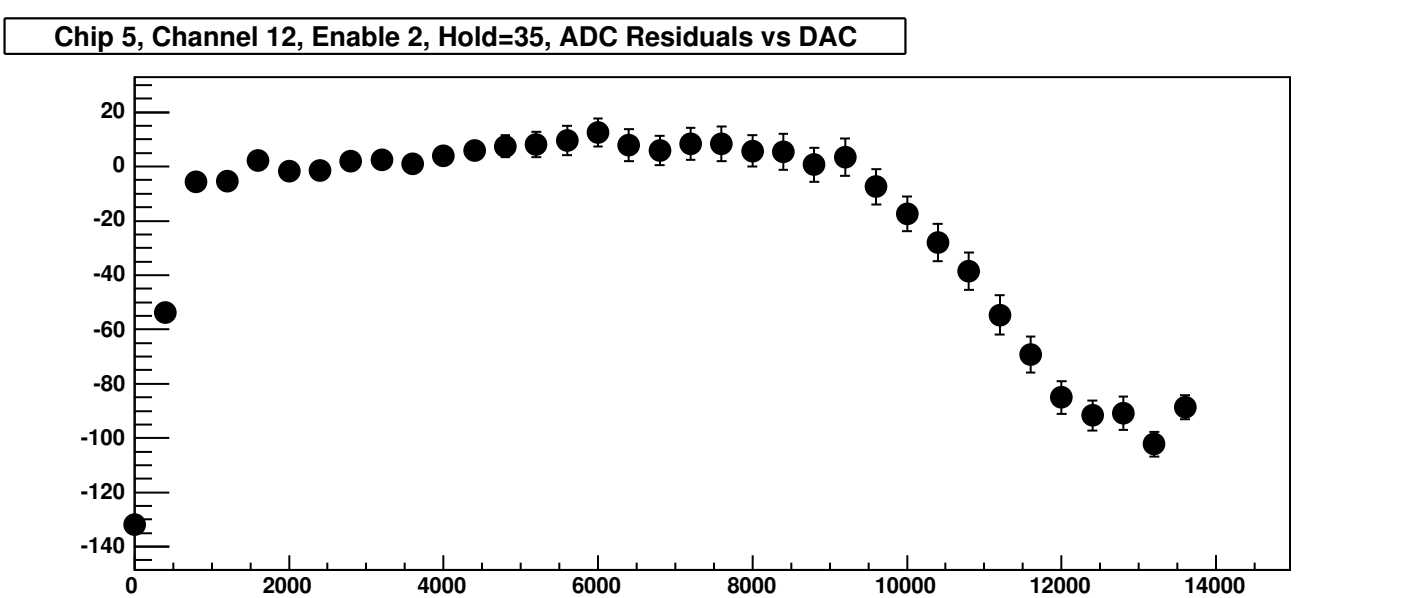


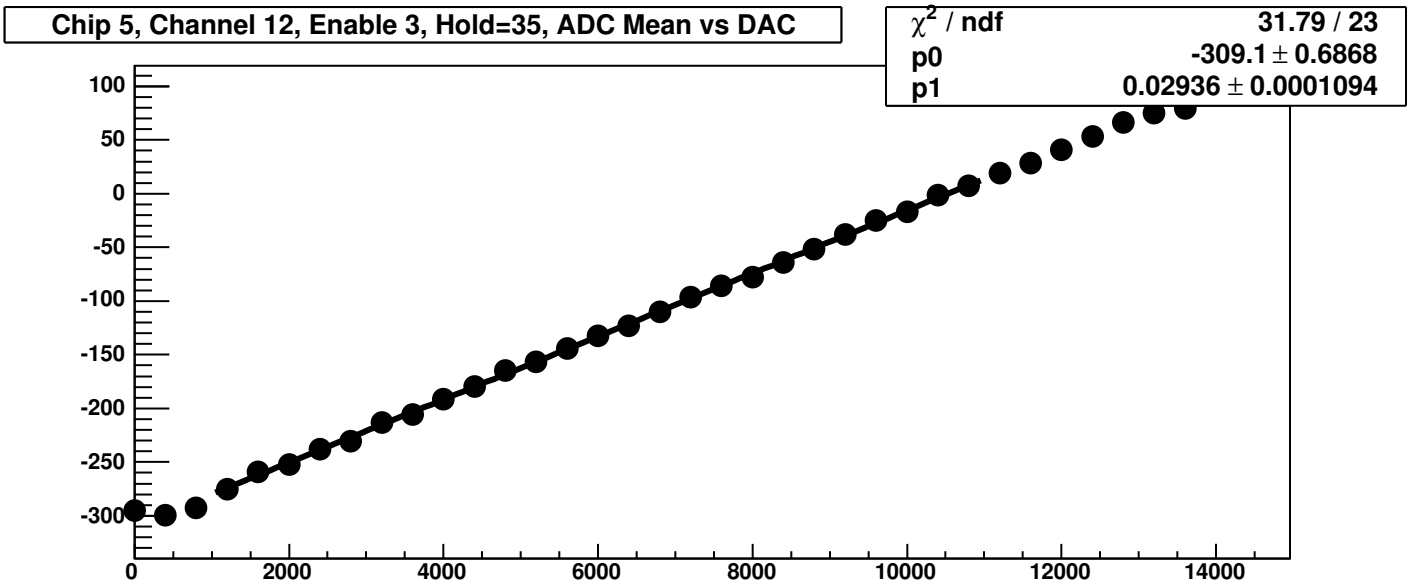
Chip 5, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC

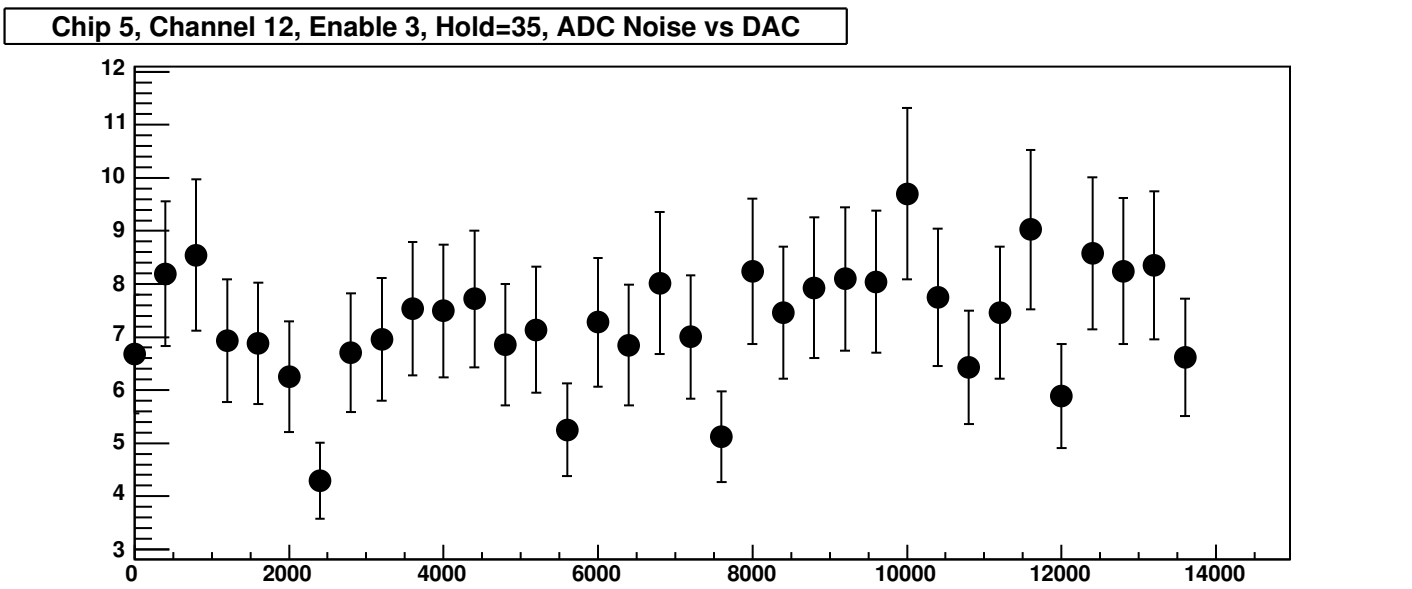


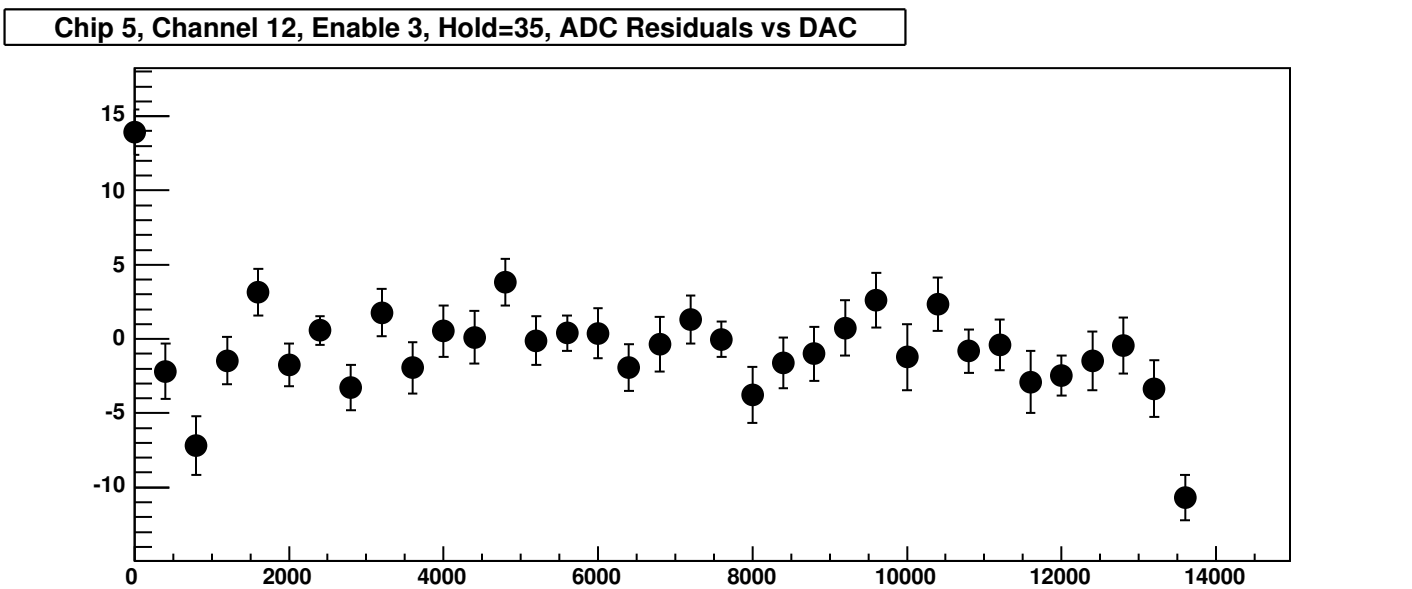

Chip 5, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 102.9 / 23
p0 -307.3 ± 1.117
p1 -0.1946 ± 0.000277
-500
-1000
-1500
-2000
-2500
-3000
0
2000
4000
6000
8000
10000
12000
14000

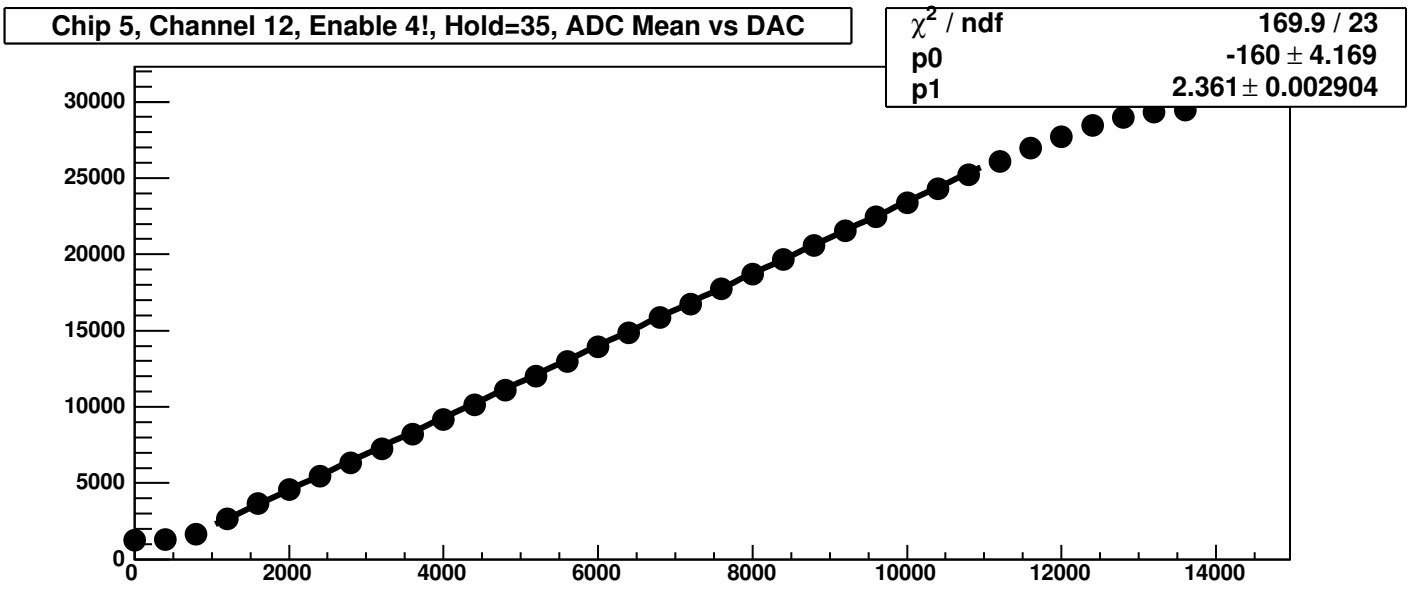

Chip 5, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

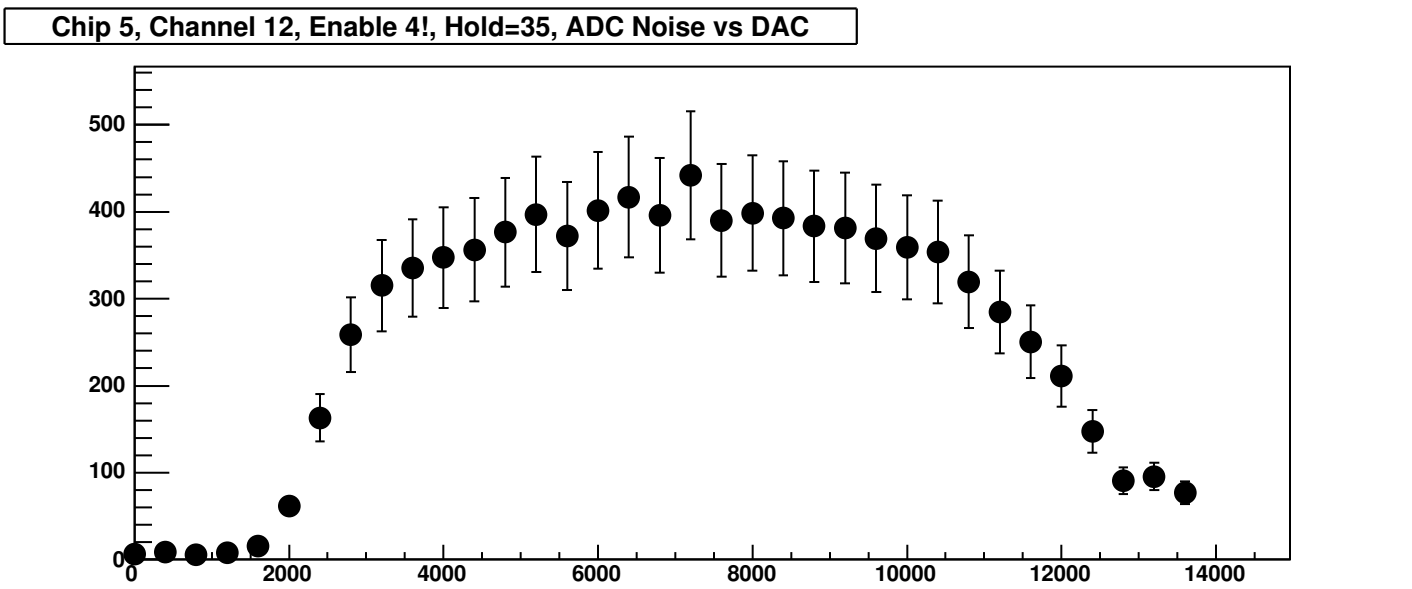

Chip 5, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
2000
4000
6000
8000
10000
12000
14000

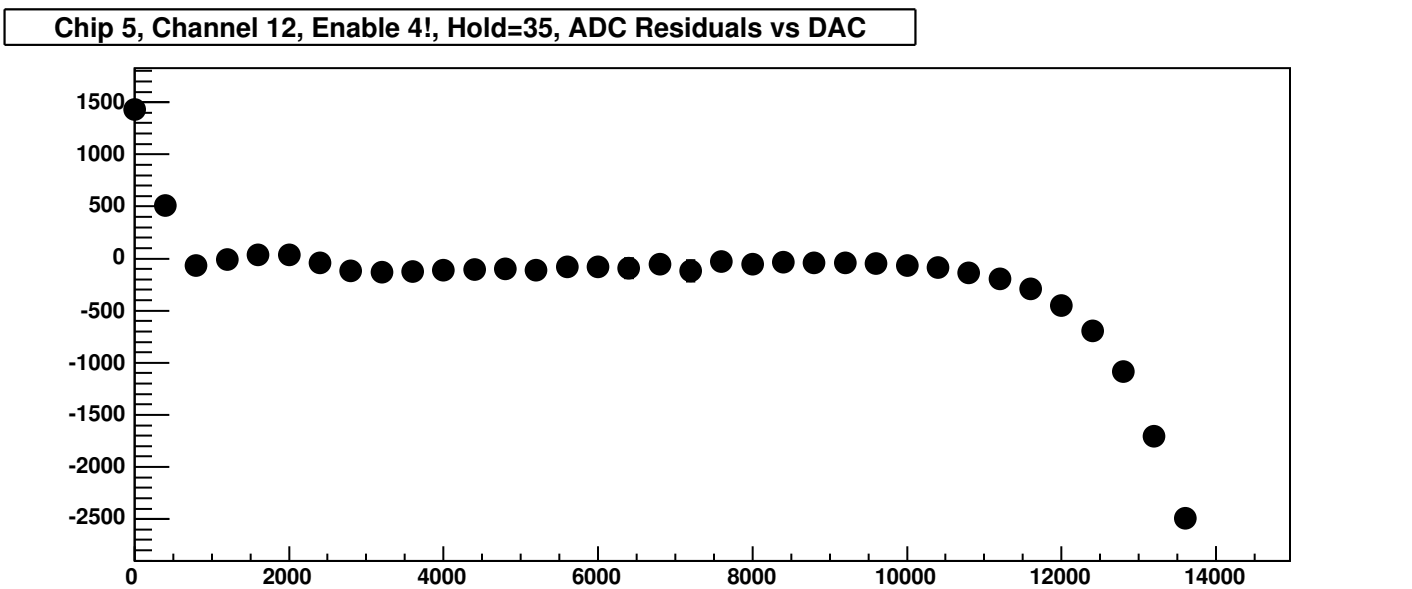

Chip 5, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.79 / 23
p0 -309.1 ± 0.6868
p1 0.02936 ± 0.0001094
100
50
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

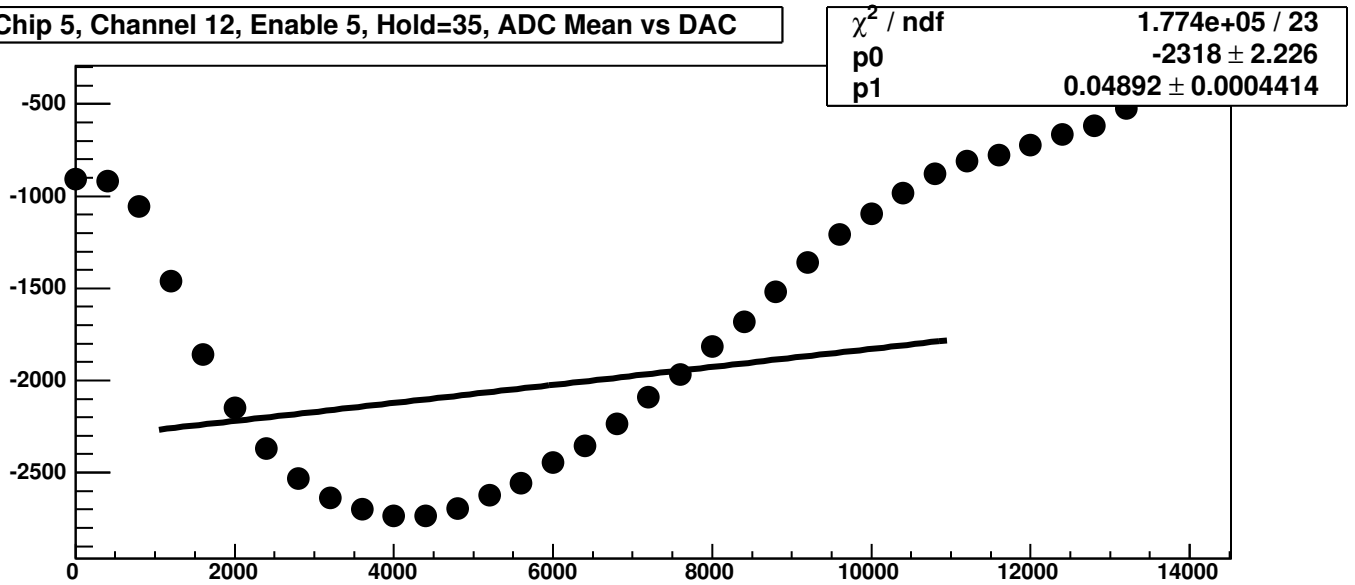

Chip 5, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 169.9 / 23
p0 -160 ± 4.169
p1 2.361 ± 0.002904
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

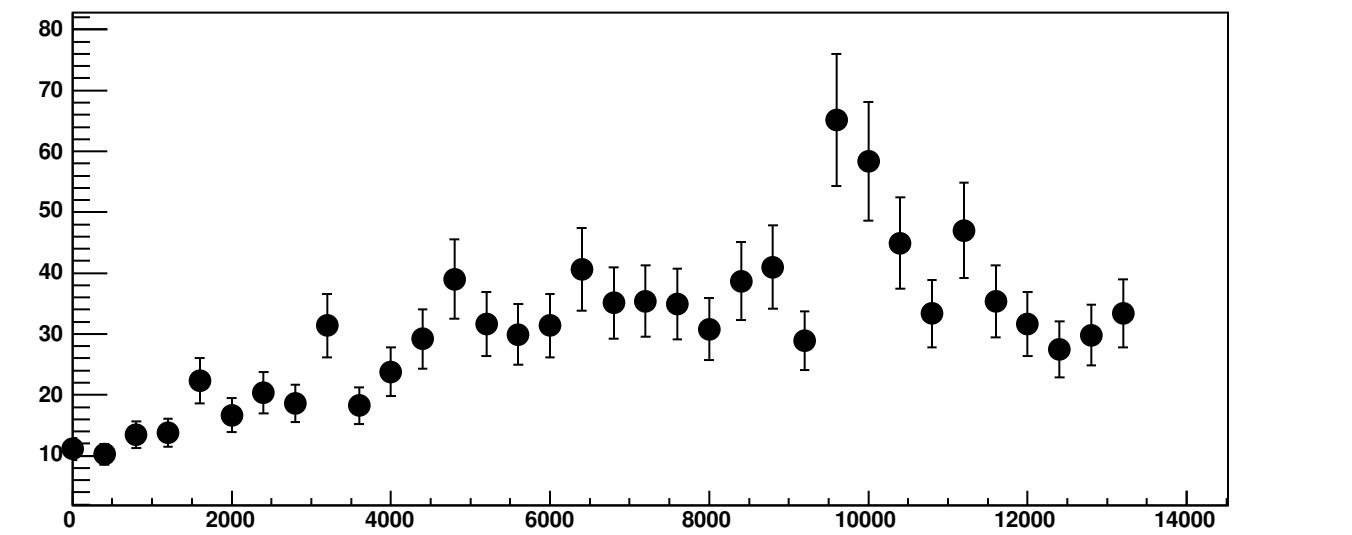

Chip 5, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

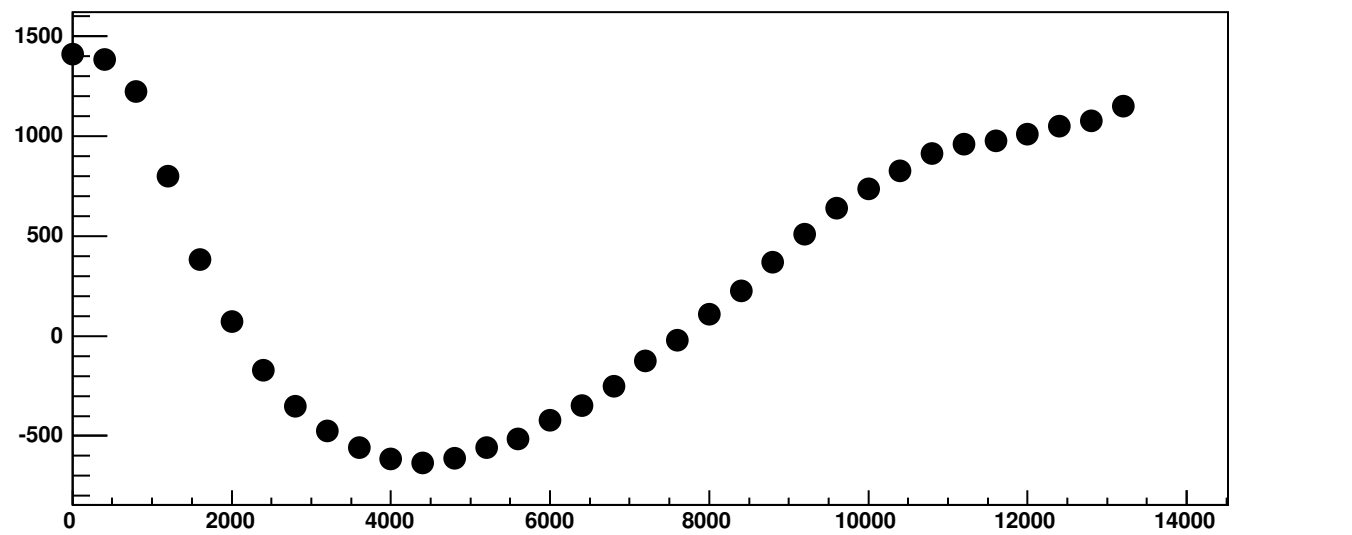
Chip 5, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

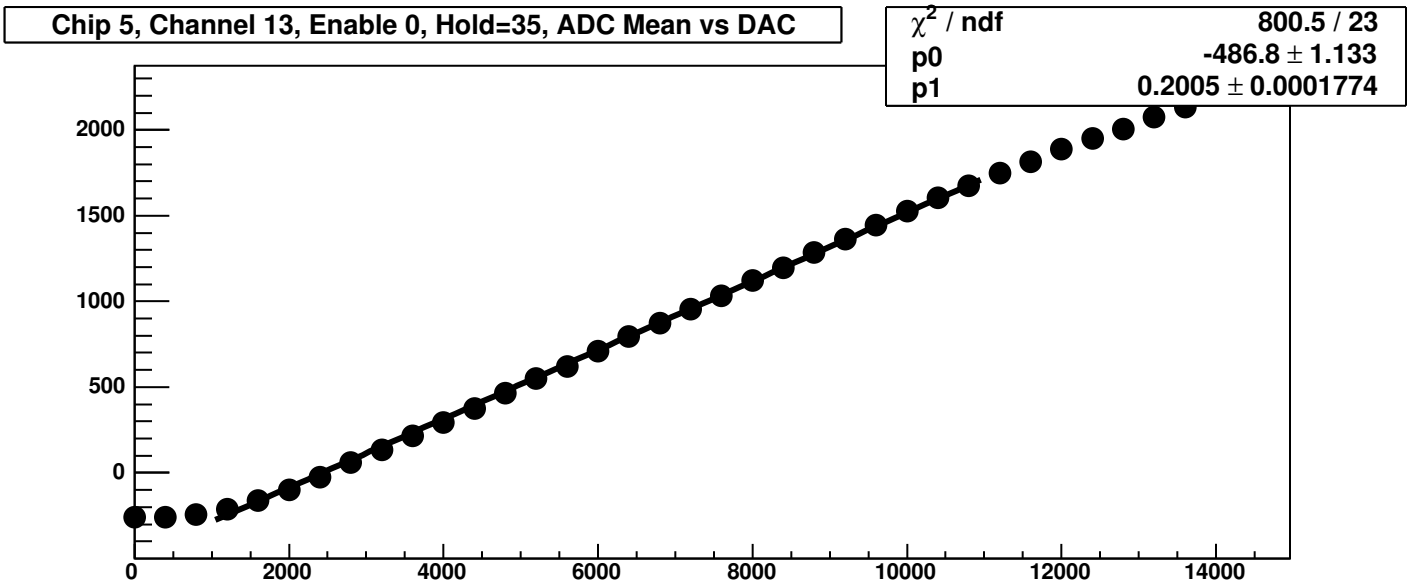


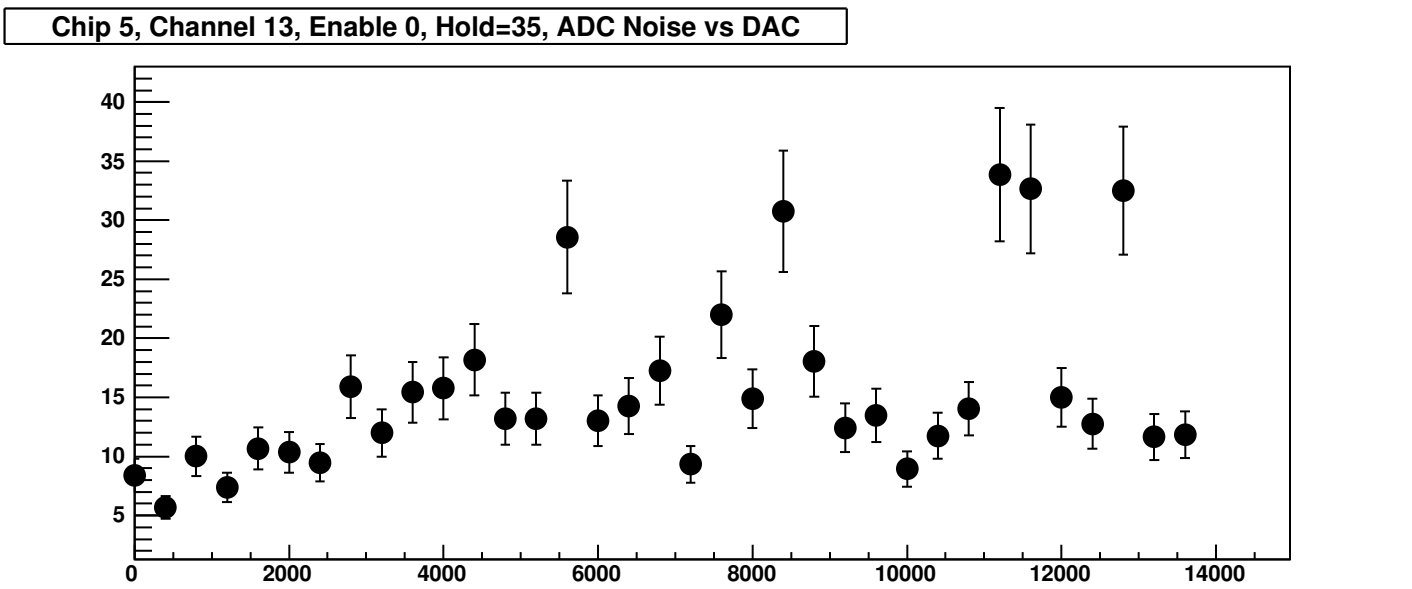
Chip 5, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

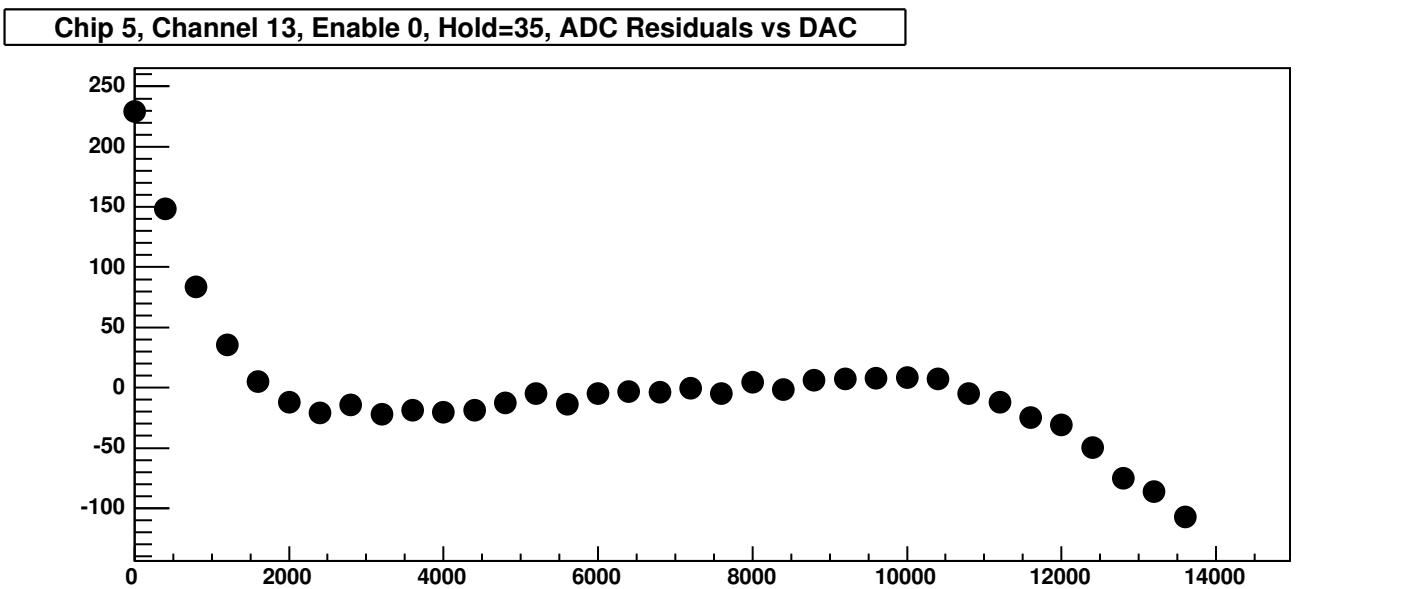


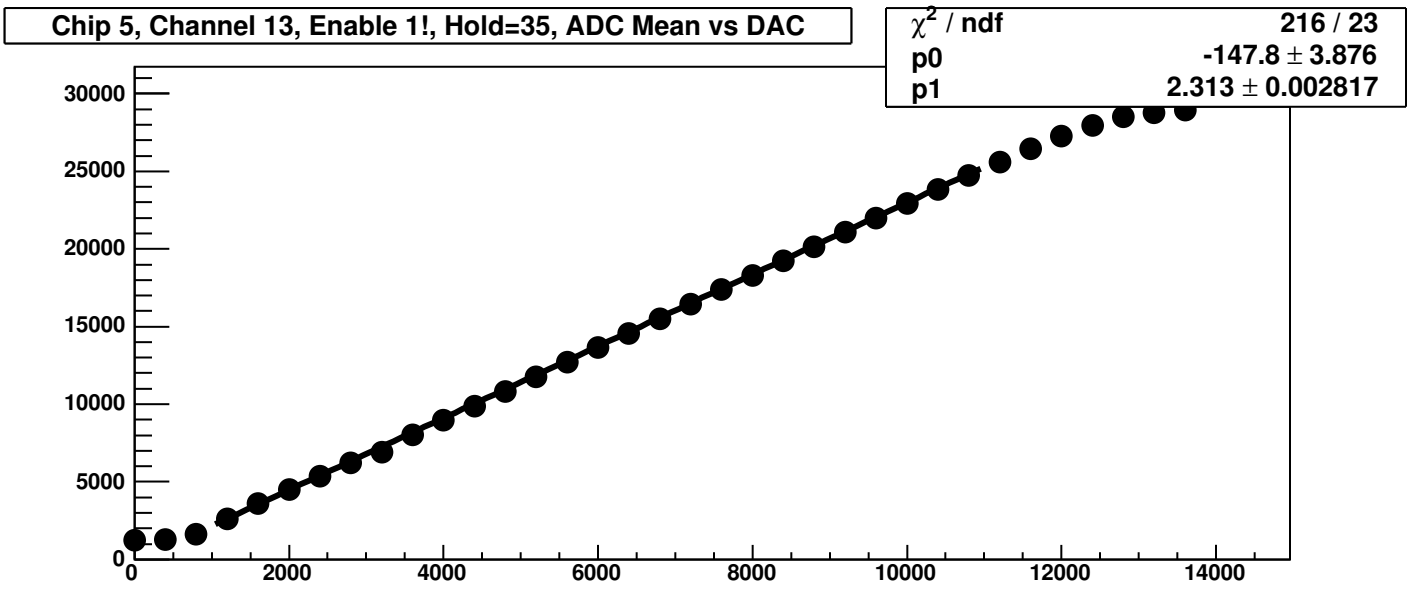
Chip 5, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

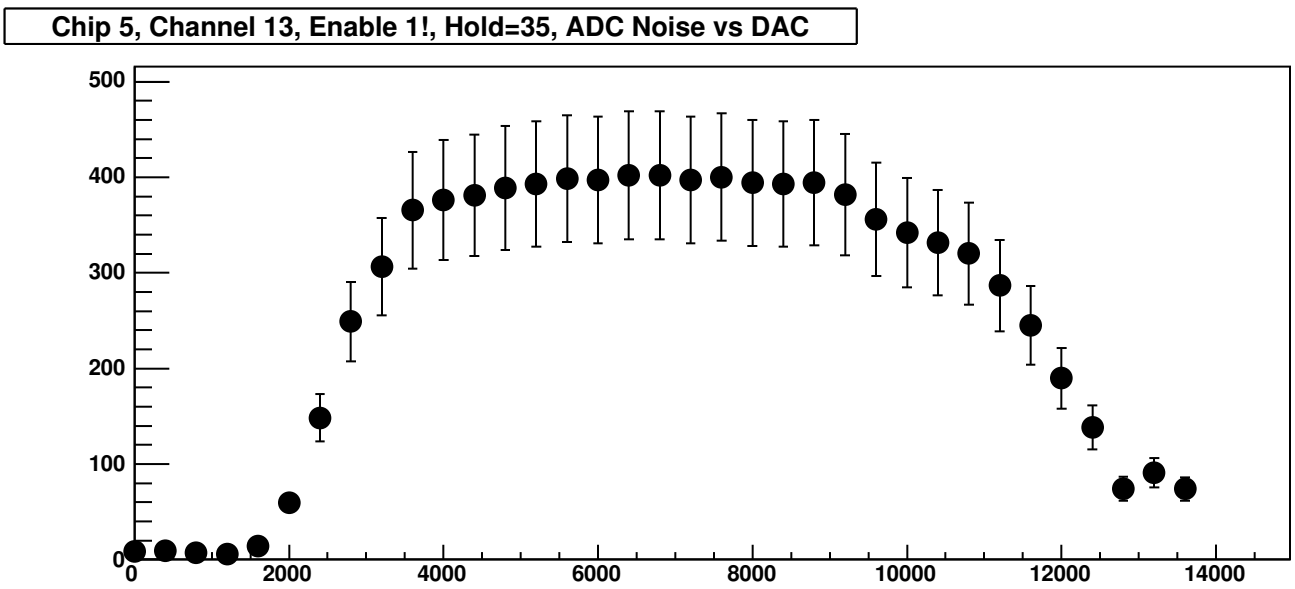


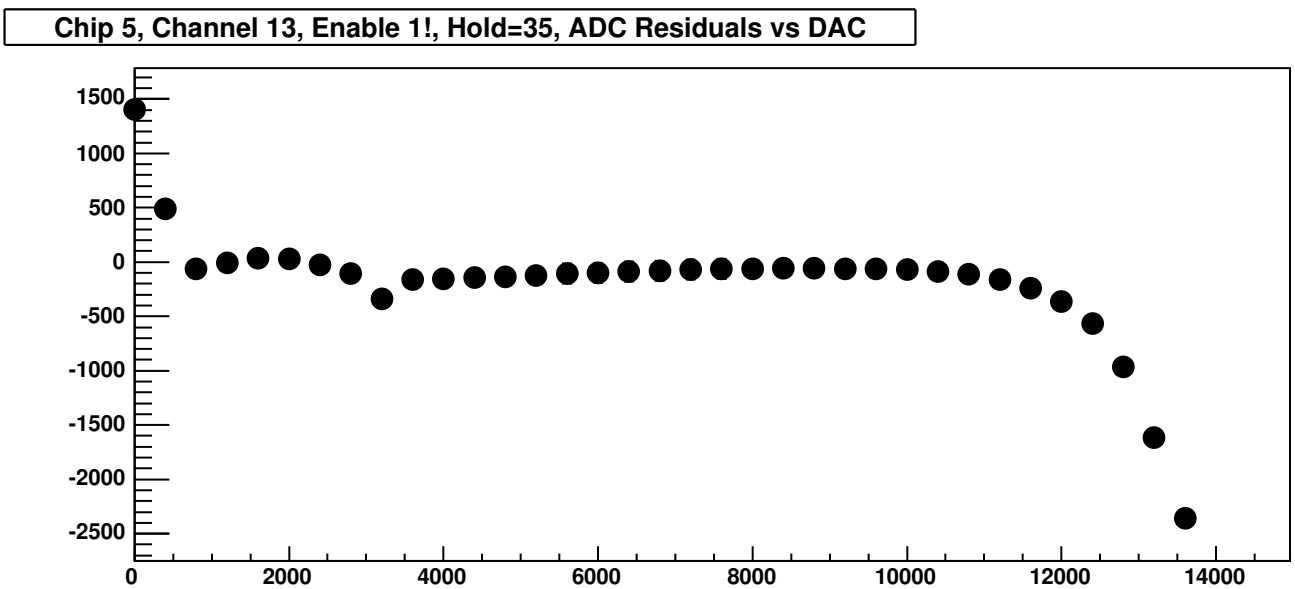

Chip 5, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 800.5 / 23
p0 -486.8 ± 1.133
p1 0.2005 ± 0.0001774
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

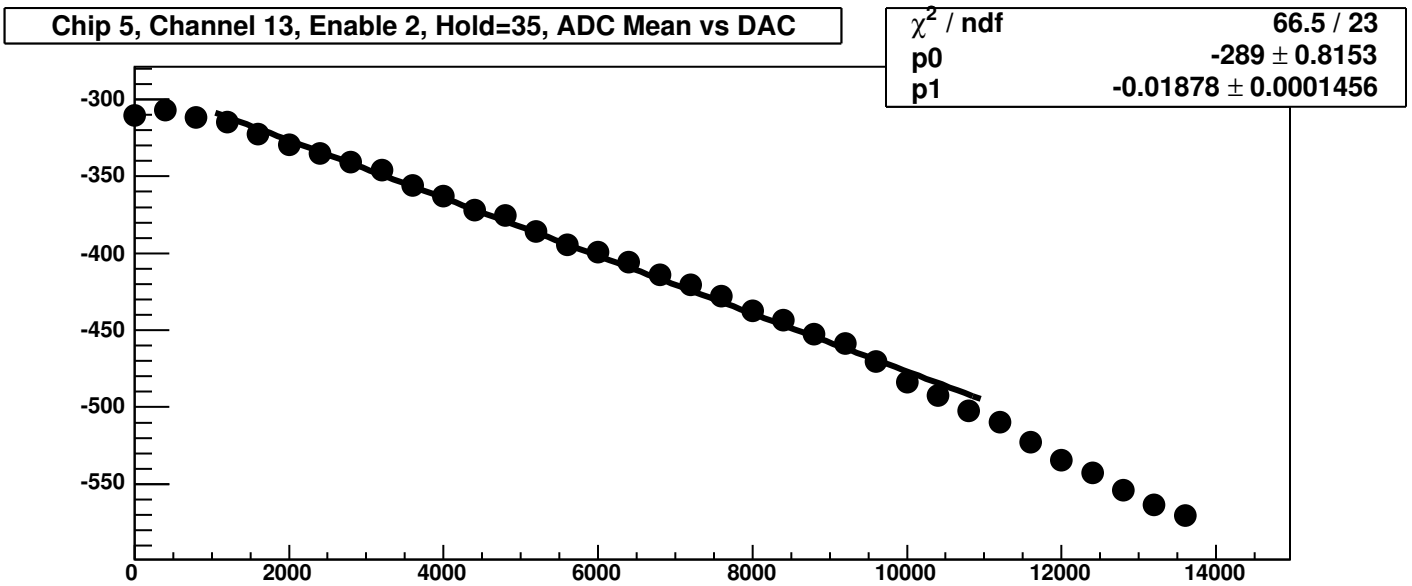

Chip 5, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

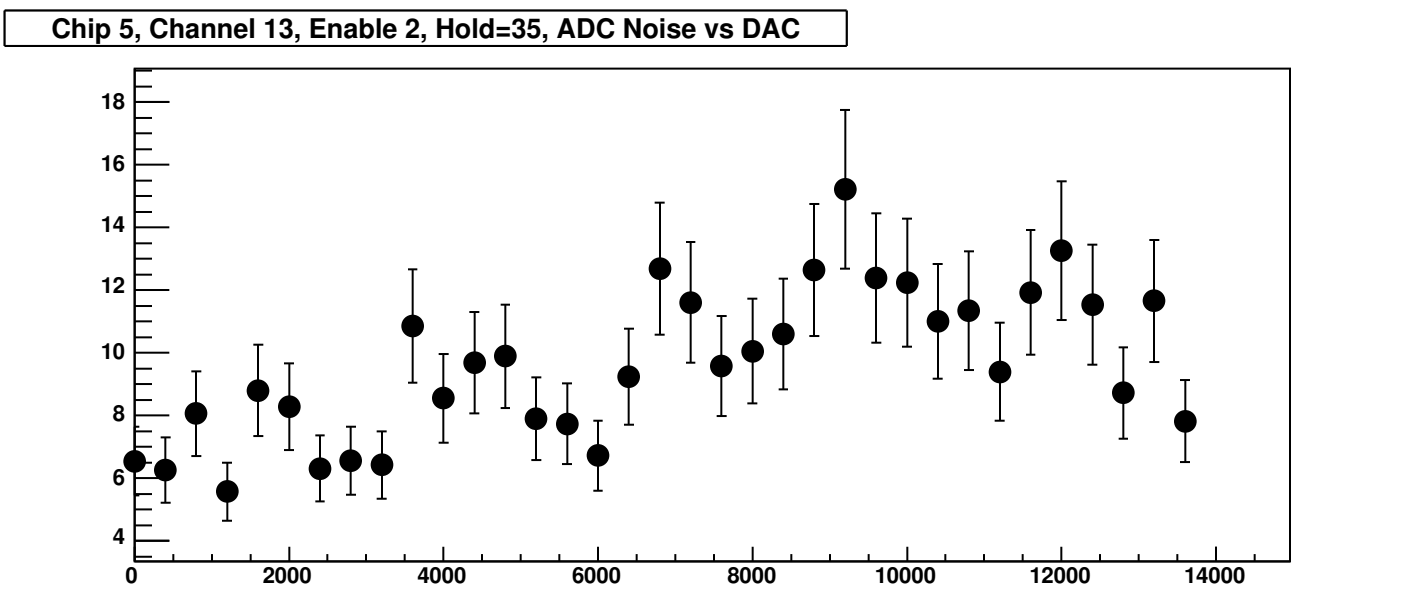

Chip 5, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

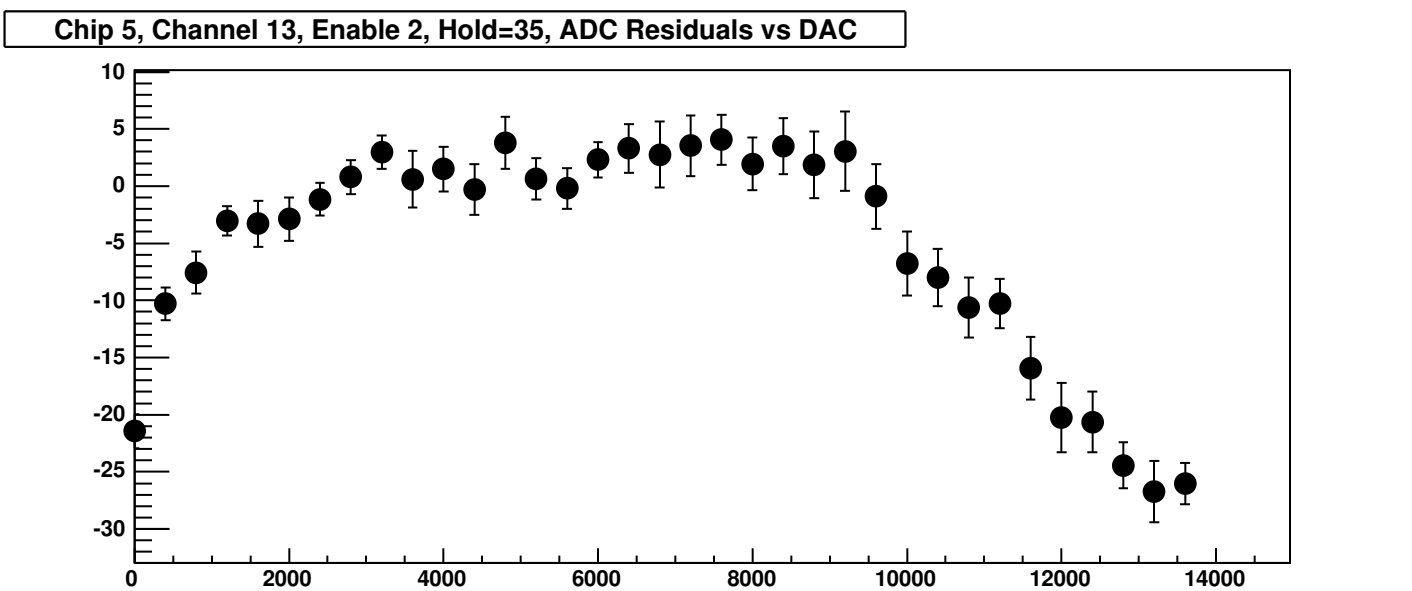

Chip 5, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 216 / 23
p0 -147.8 ± 3.876
p1 2.313 ± 0.002817
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

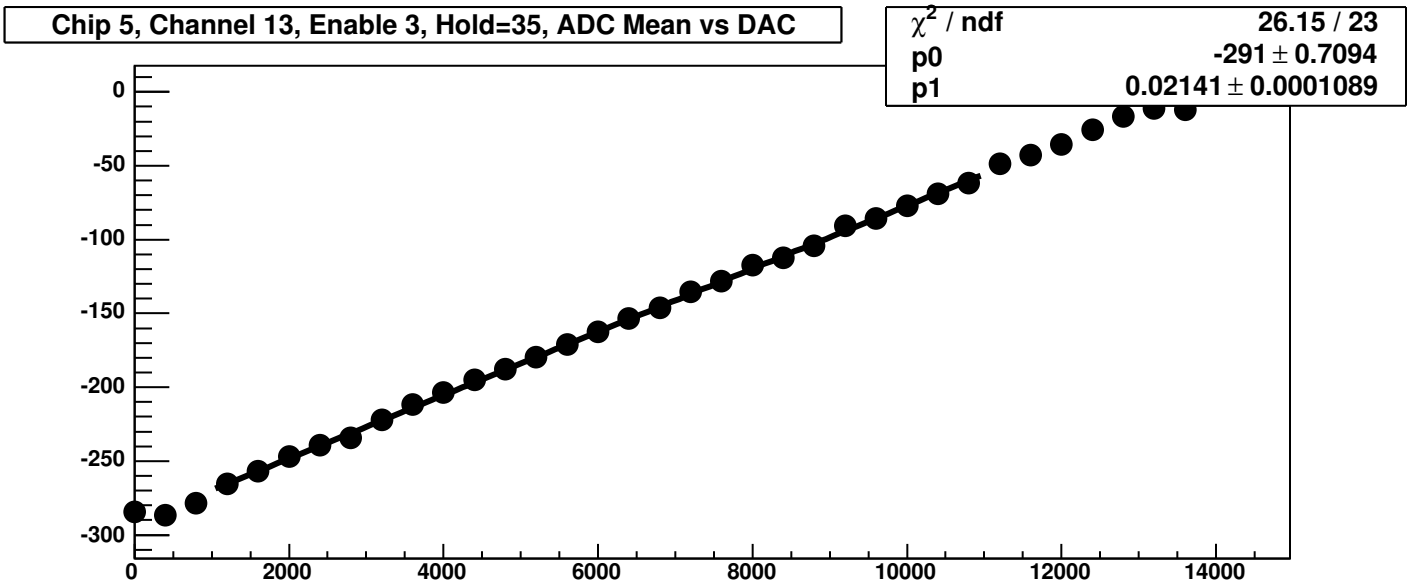

Chip 5, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

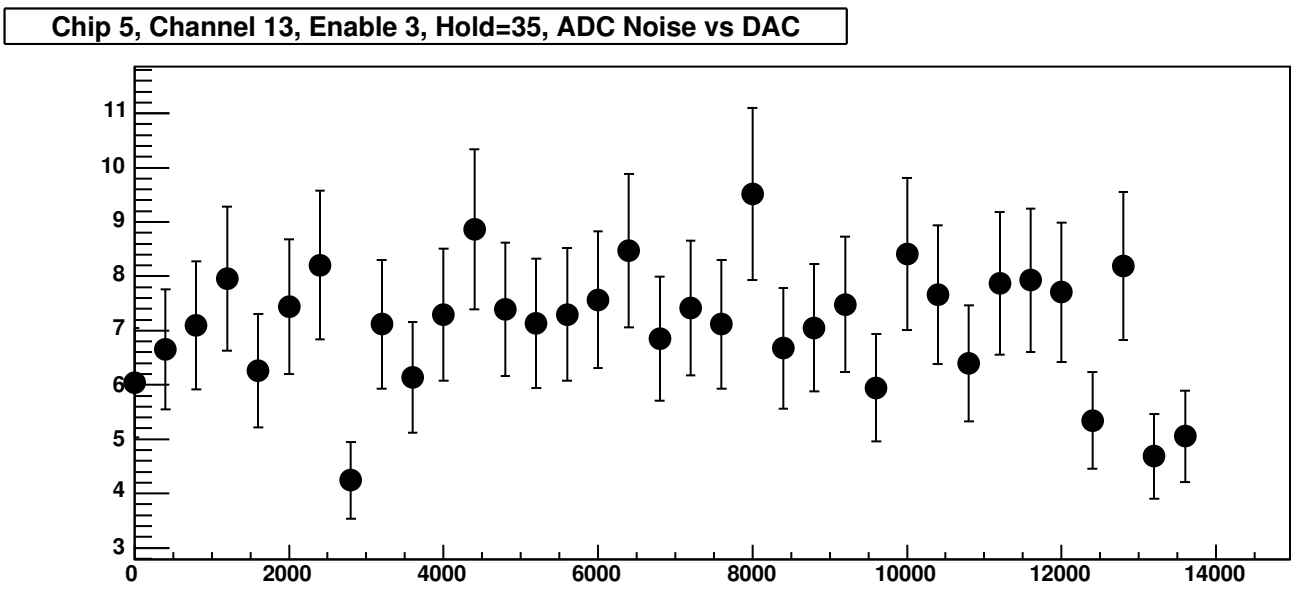

Chip 5, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

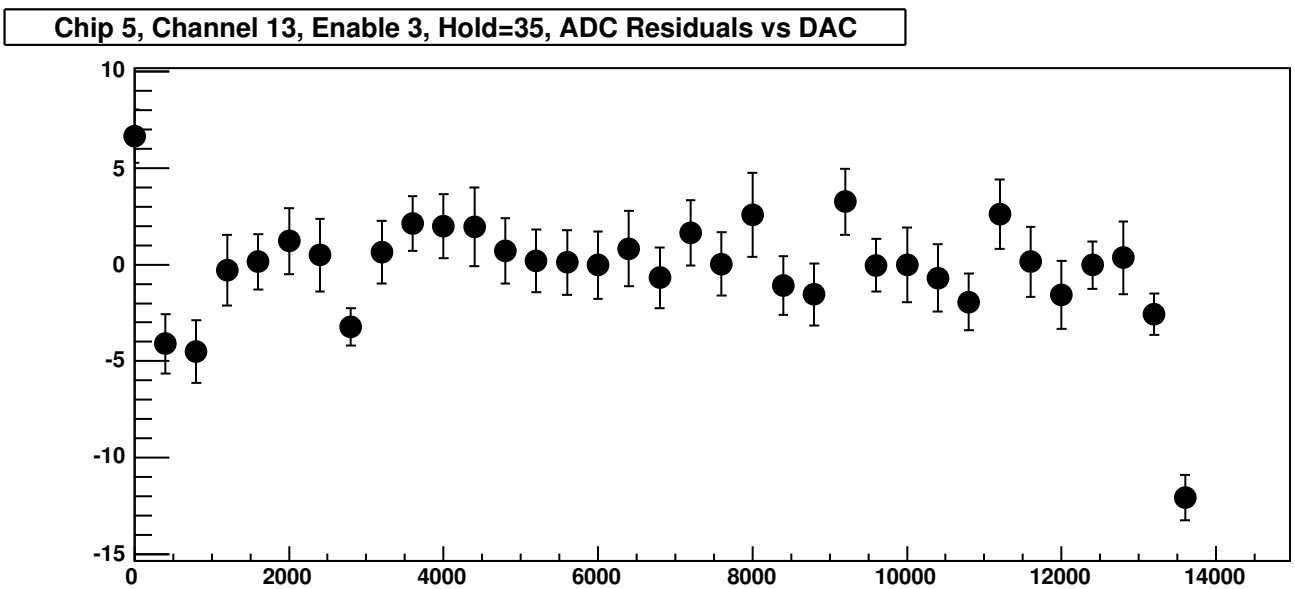

Chip 5, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 66.5 / 23
p0 -289 ± 0.8153
p1 -0.01878 ± 0.0001456
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

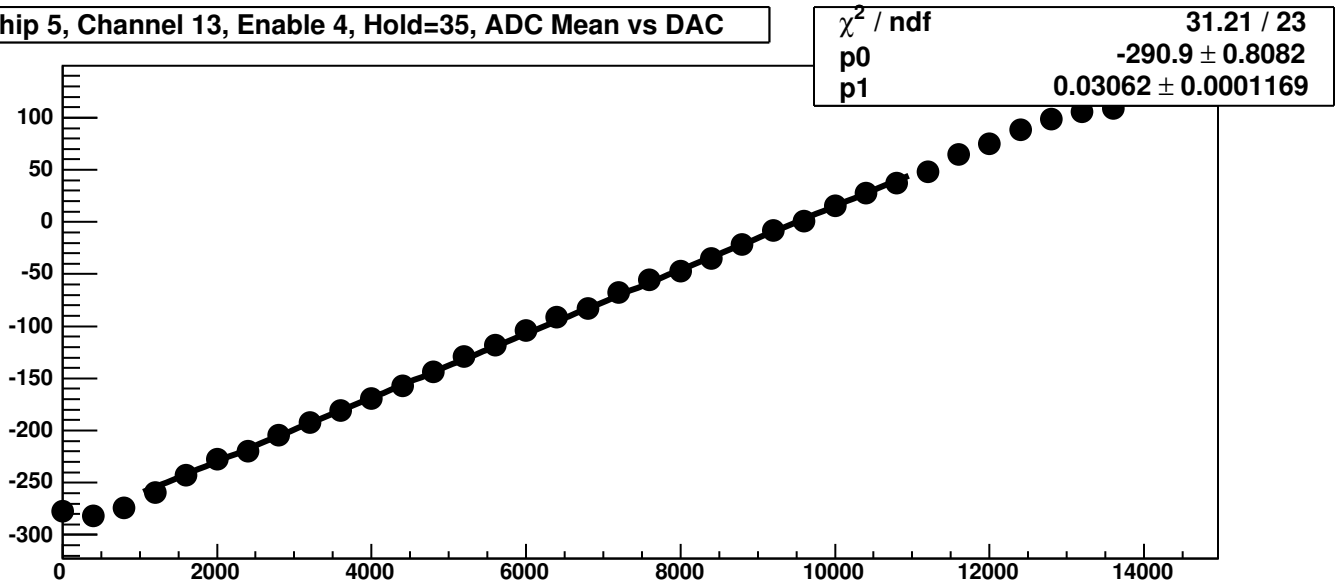

Chip 5, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.15 / 23
p0 -291 ± 0.7094
p1 0.02141 ± 0.0001089
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

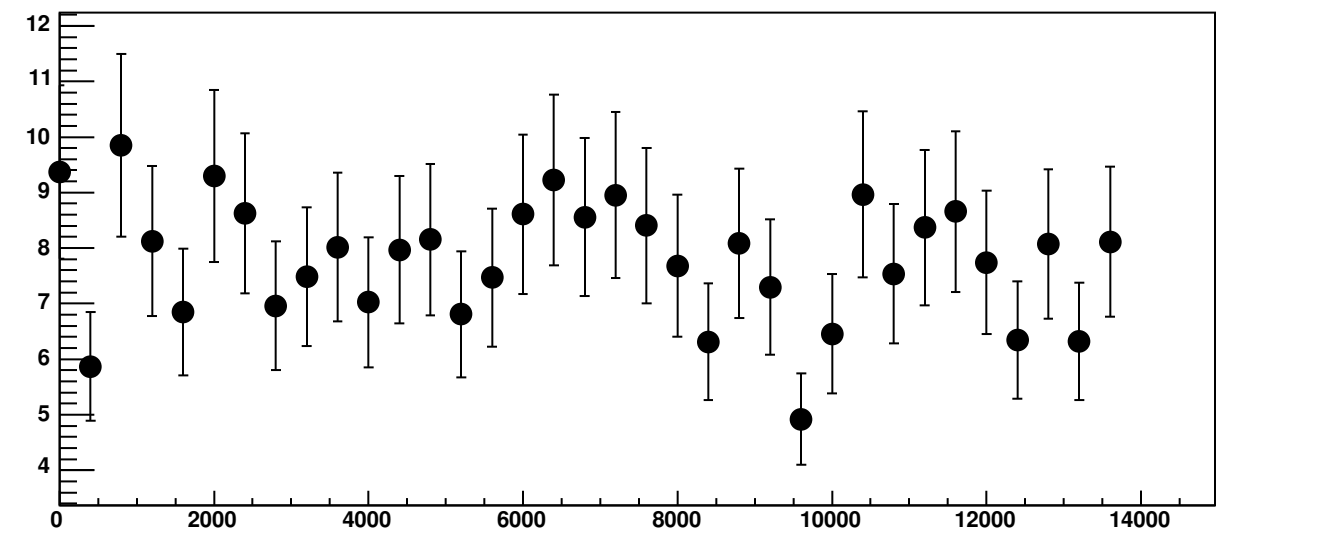

Chip 5, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

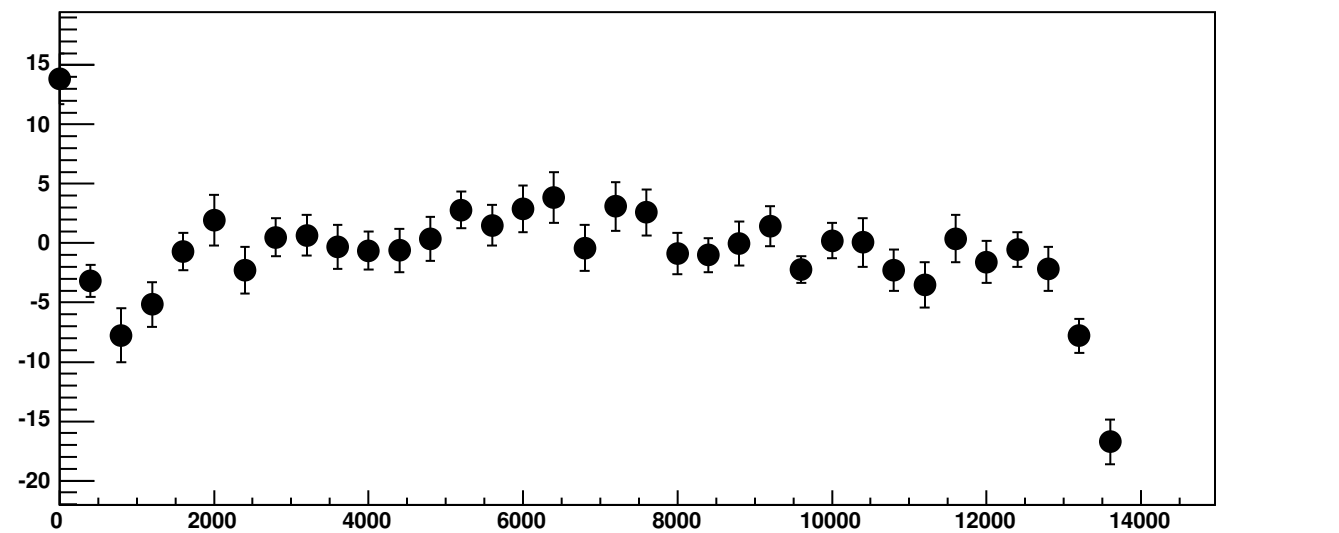
Chip 5, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC



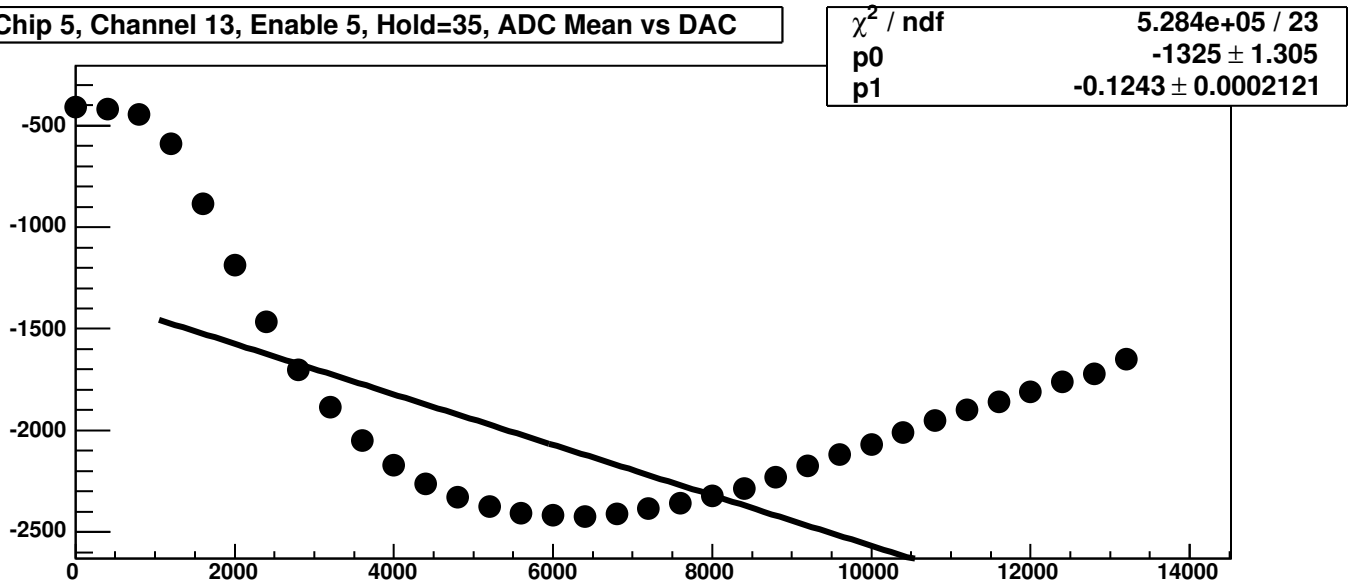
Chip 5, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC



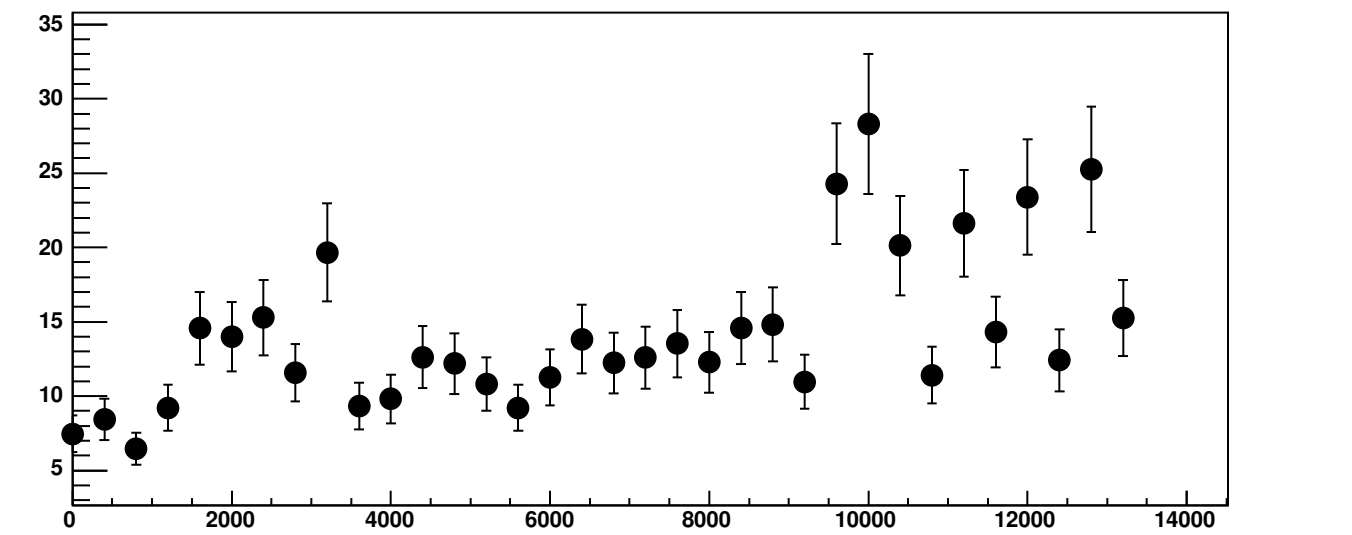
Chip 5, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC



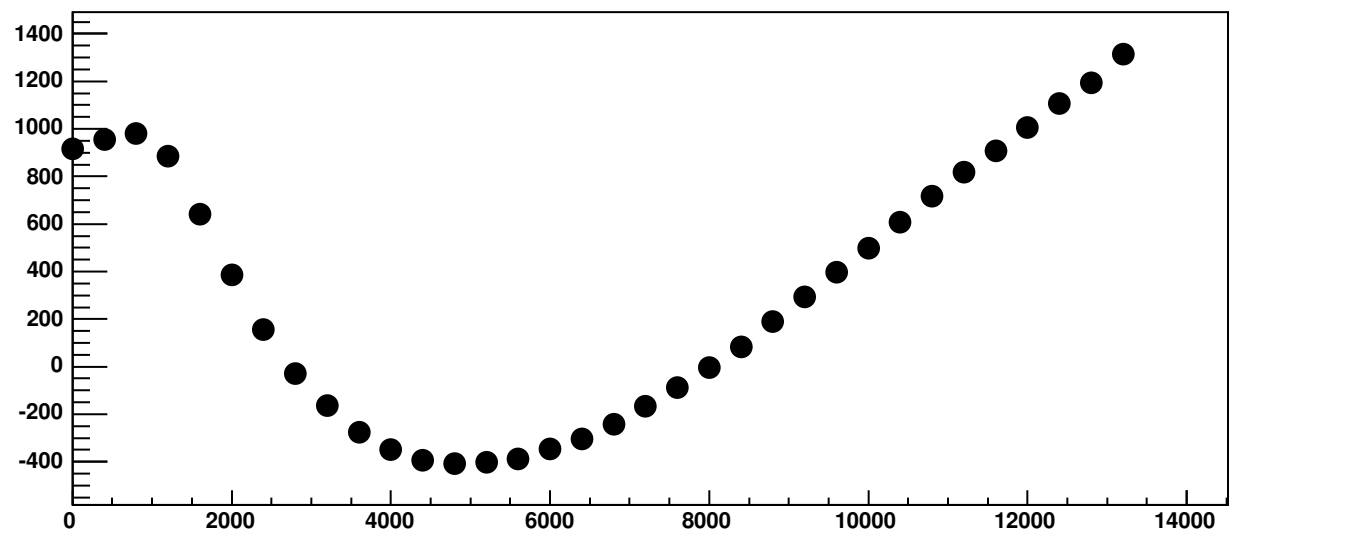
Chip 5, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

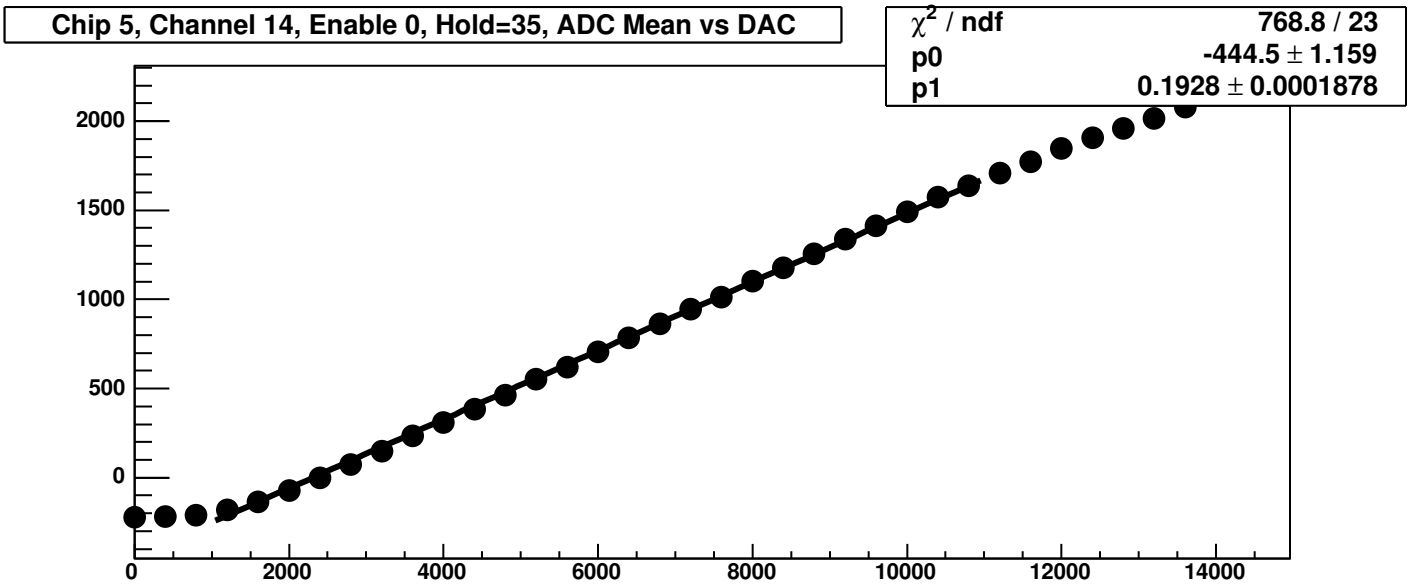


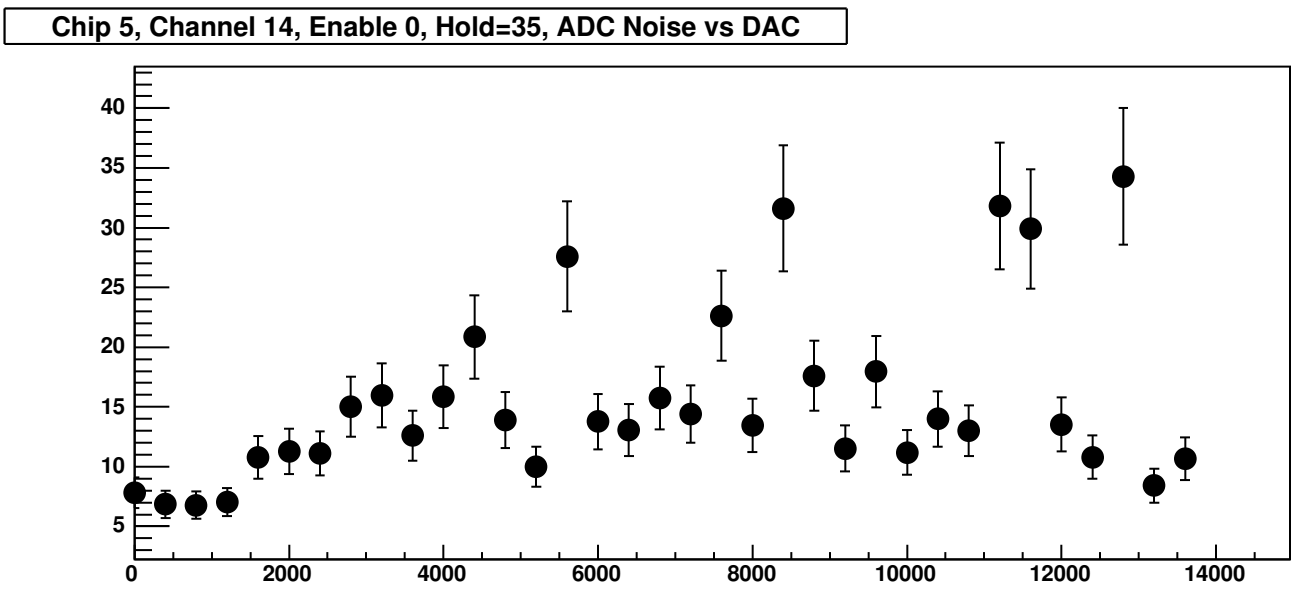
Chip 5, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

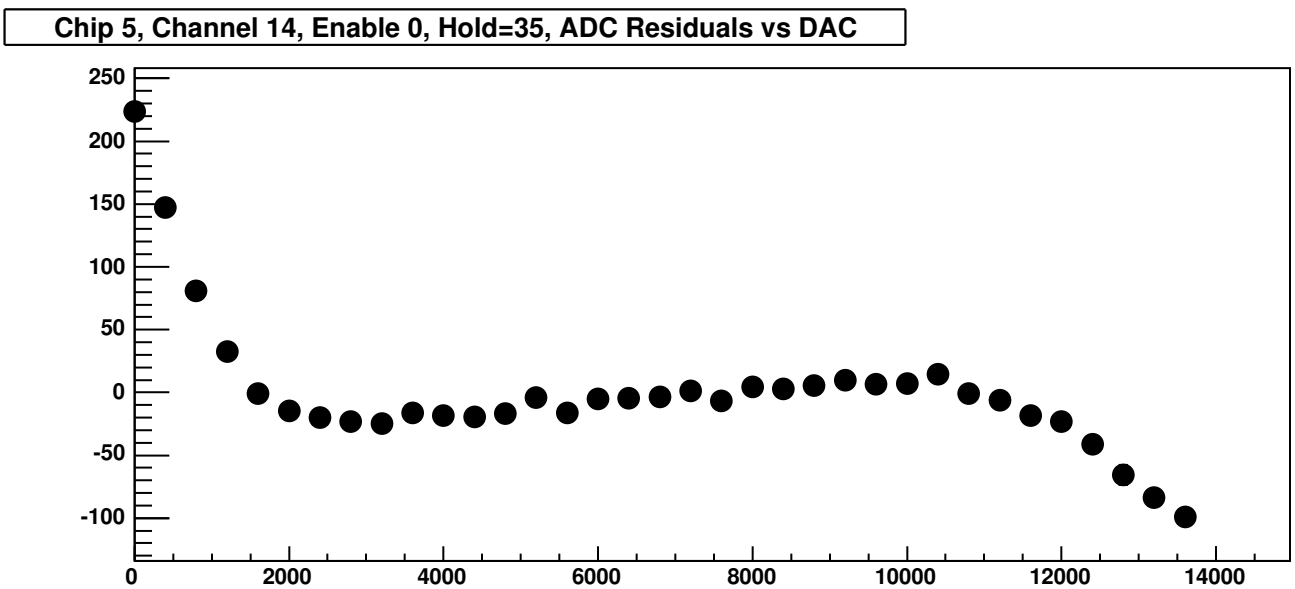


Chip 5, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

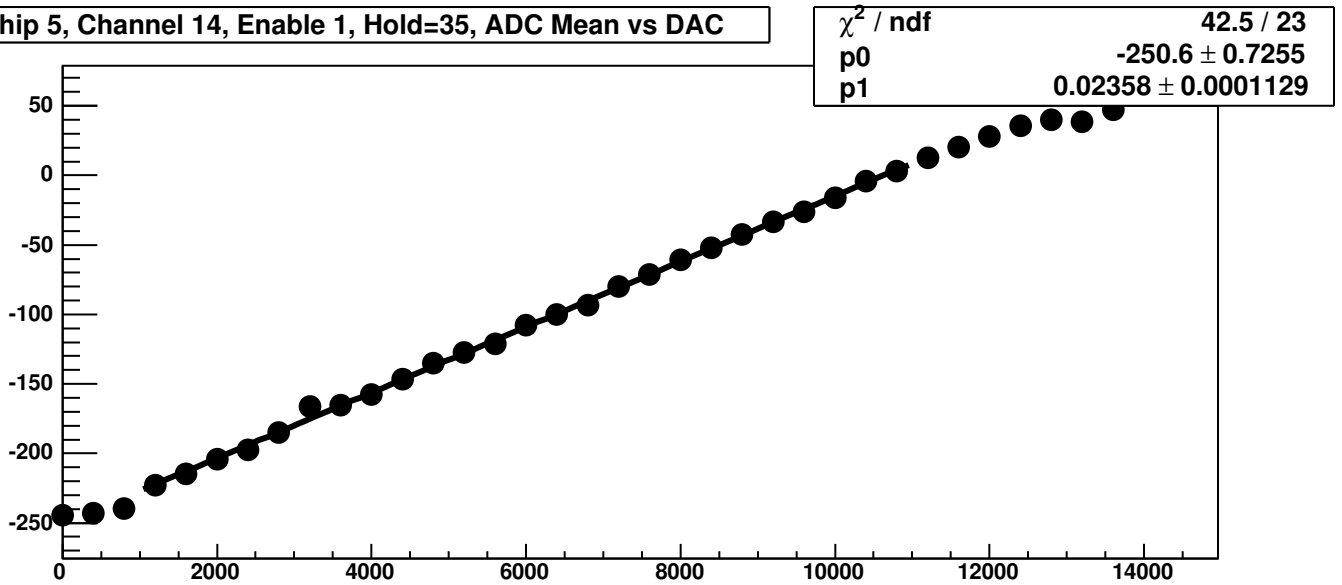



Chip 5, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 768.8 / 23
p0 -444.5 ± 1.159
p1 0.1928 ± 0.0001878
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

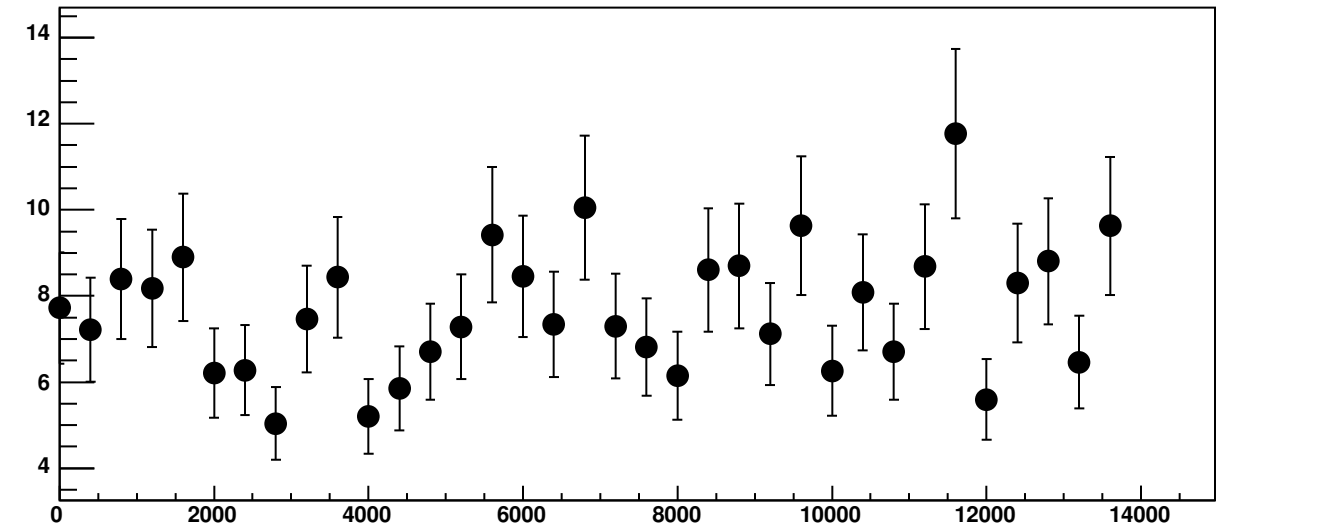

Chip 5, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

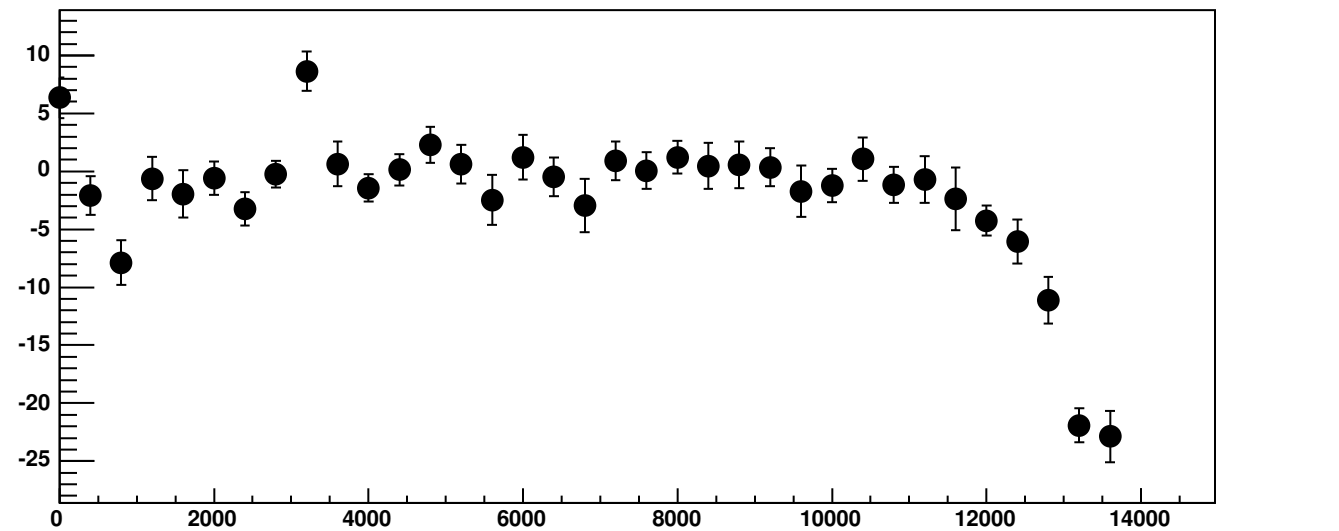
Chip 5, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC

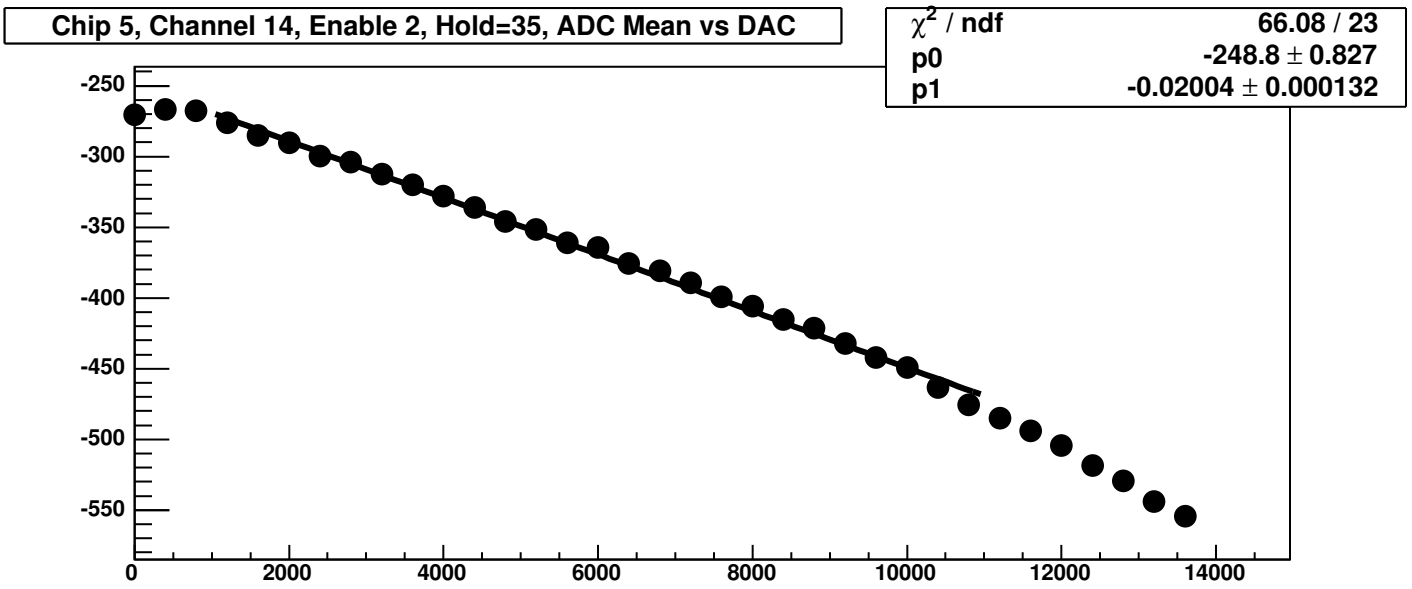


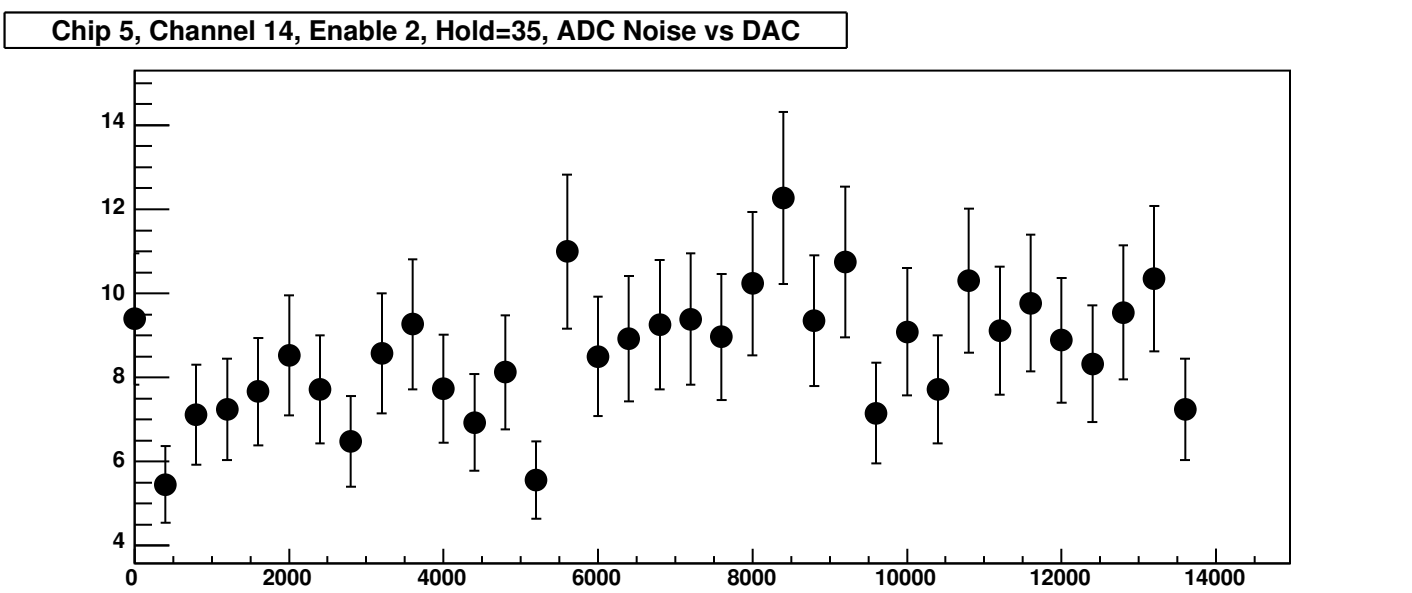
Chip 5, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC

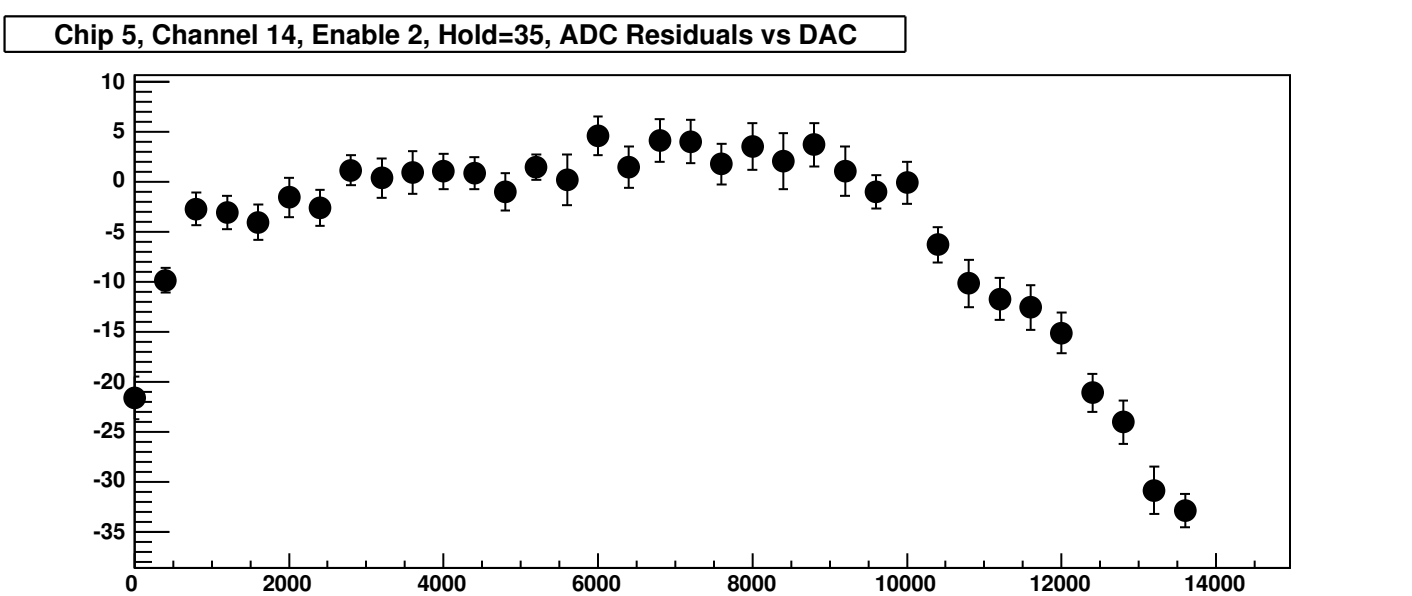


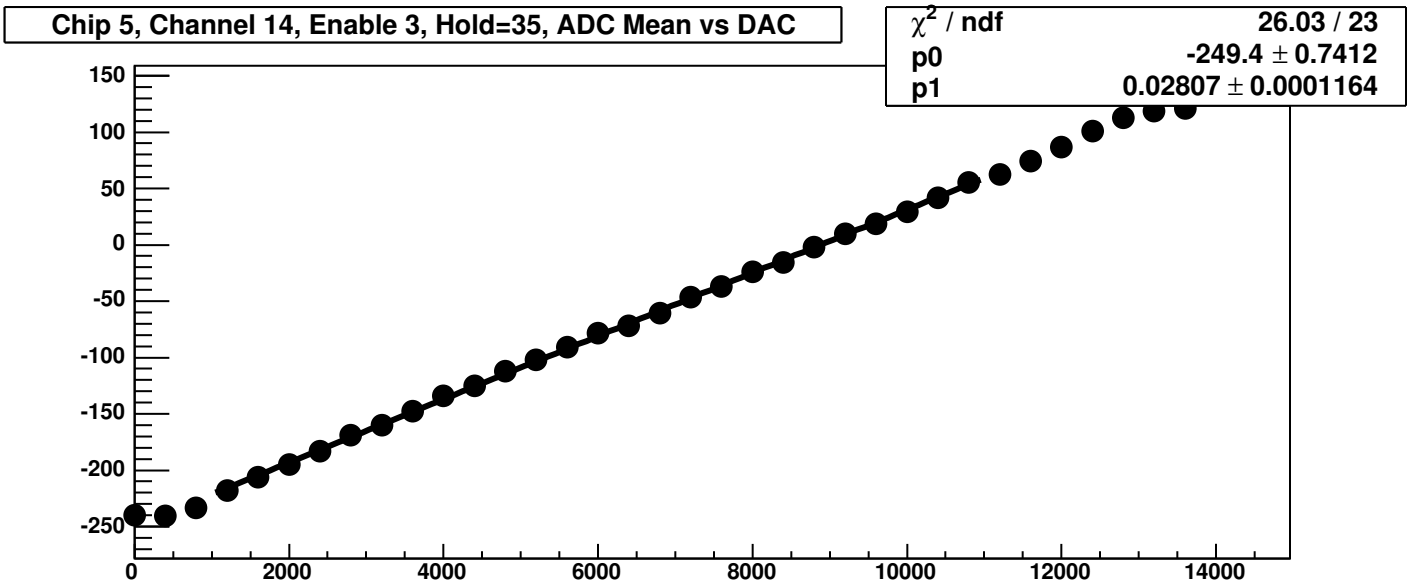
Chip 5, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC

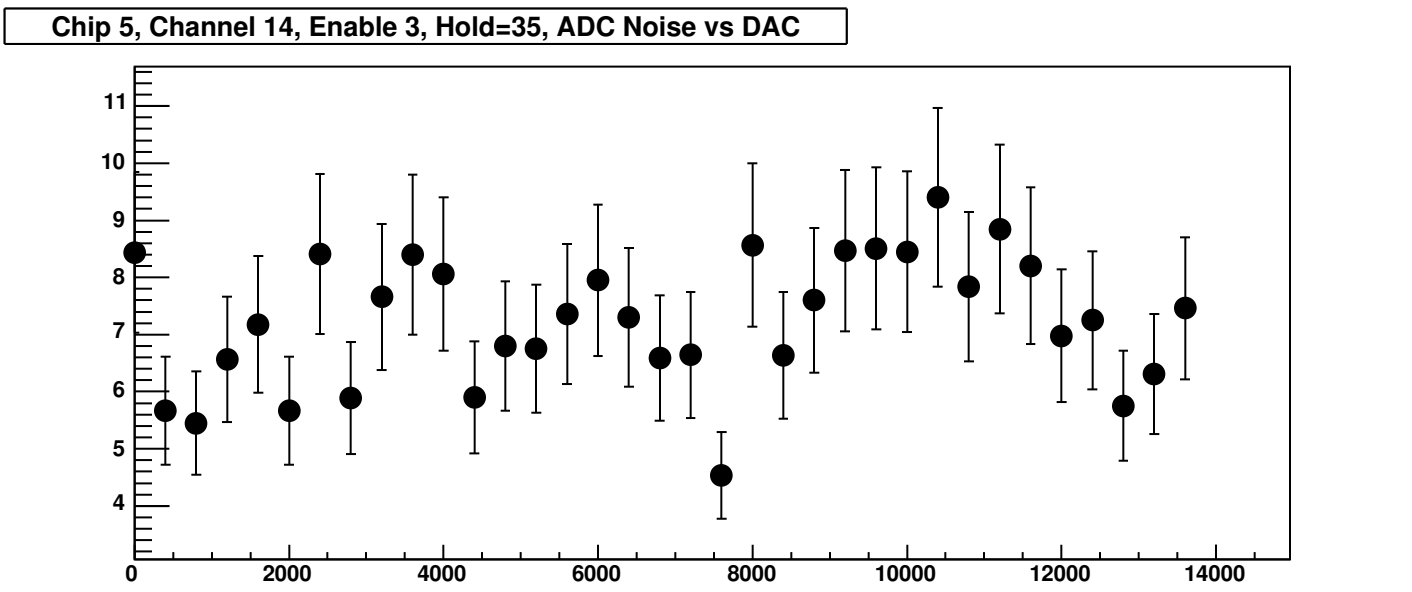


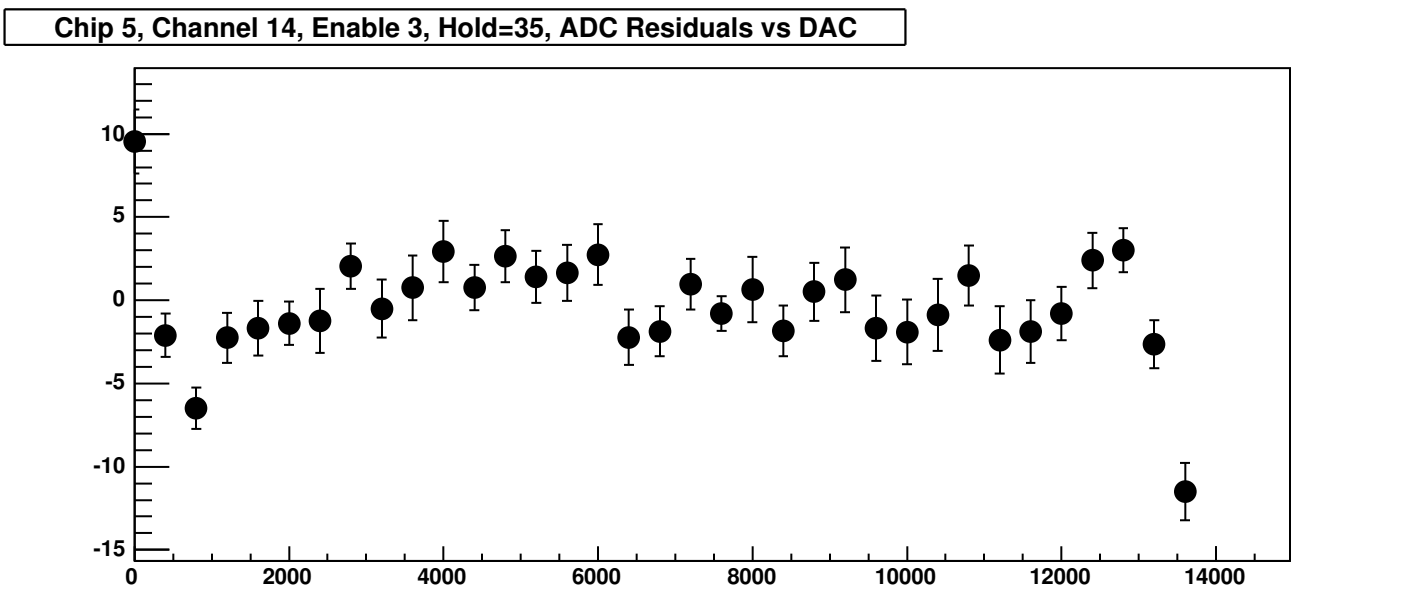

Chip 5, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 66.08 / 23
p0 -248.8 ± 0.827
p1 -0.02004 ± 0.000132
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

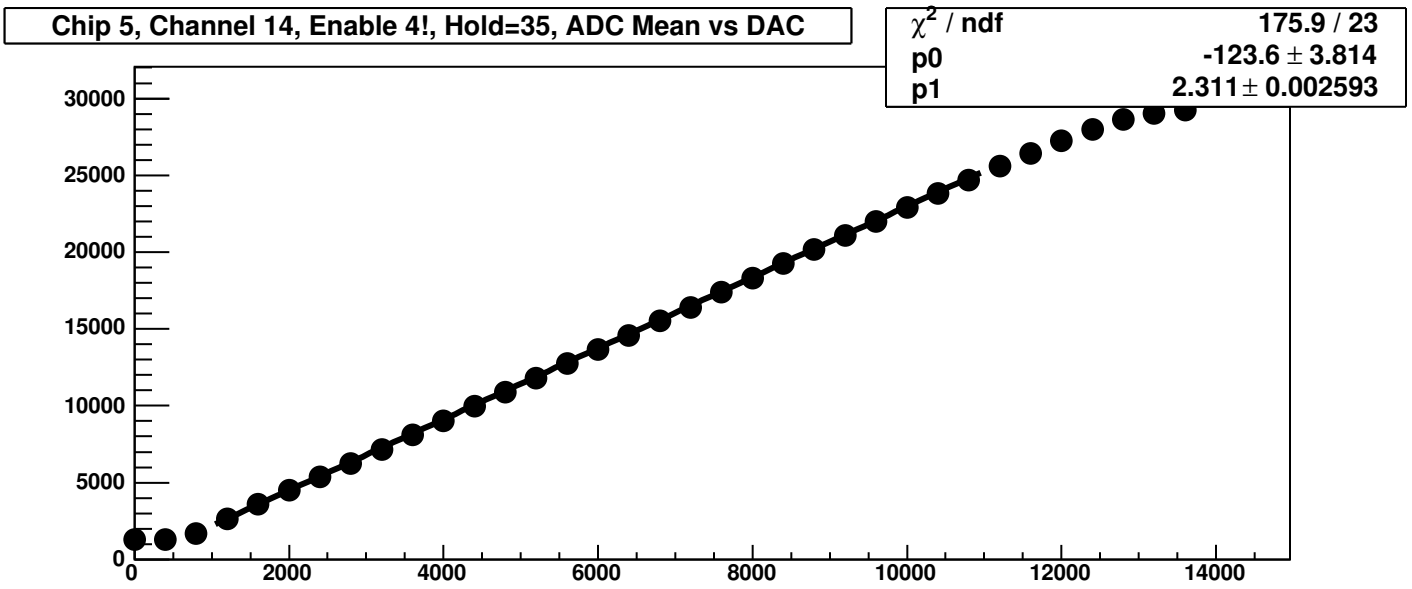

Chip 5, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

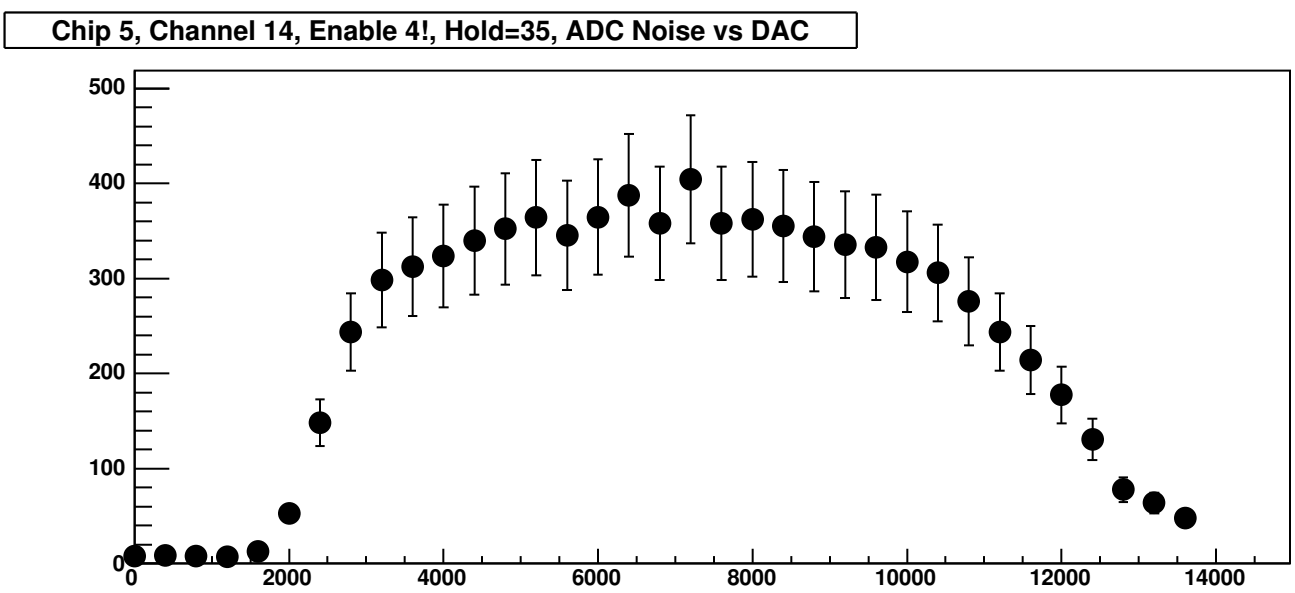

Chip 5, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
-35
0
2000
4000
6000
8000
10000
12000
14000

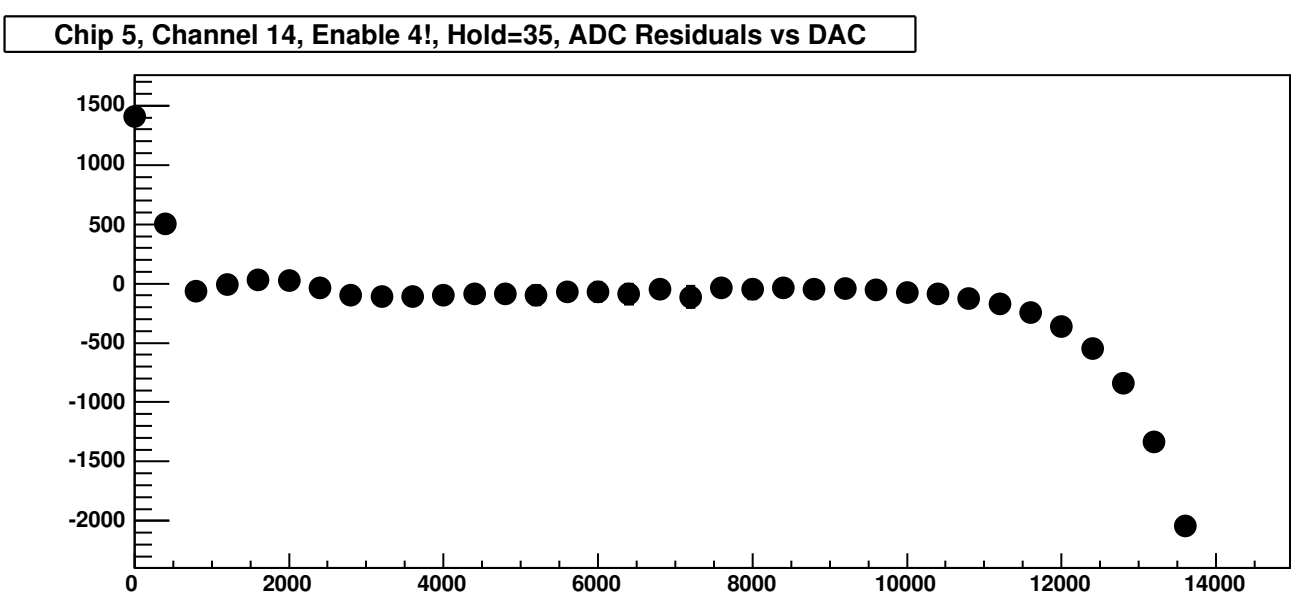

Chip 5, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.03 / 23
p0 -249.4 ± 0.7412
p1 0.02807 ± 0.0001164
150
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

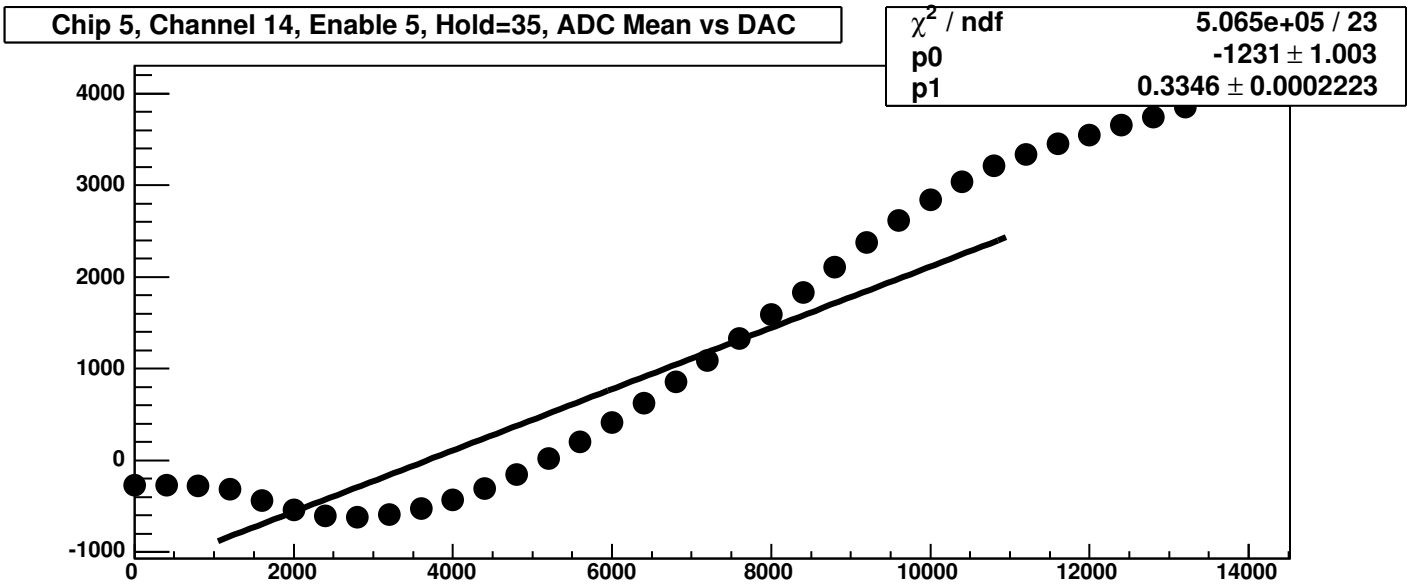

Chip 5, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

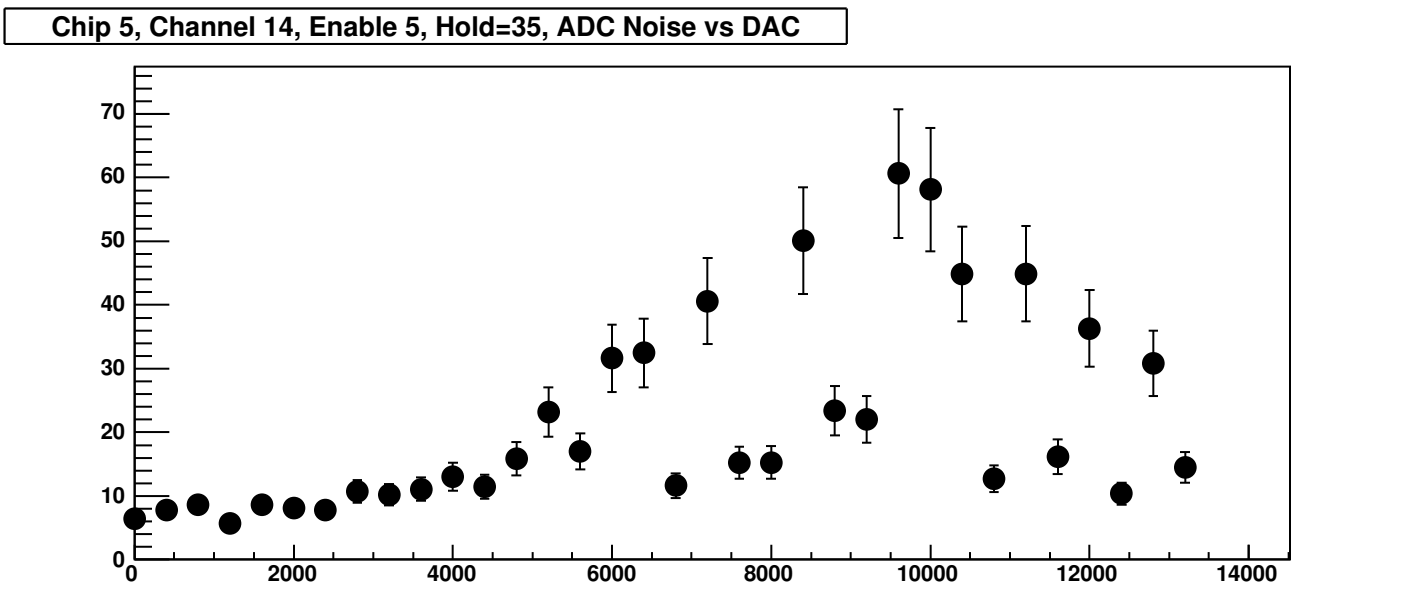

Chip 5, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

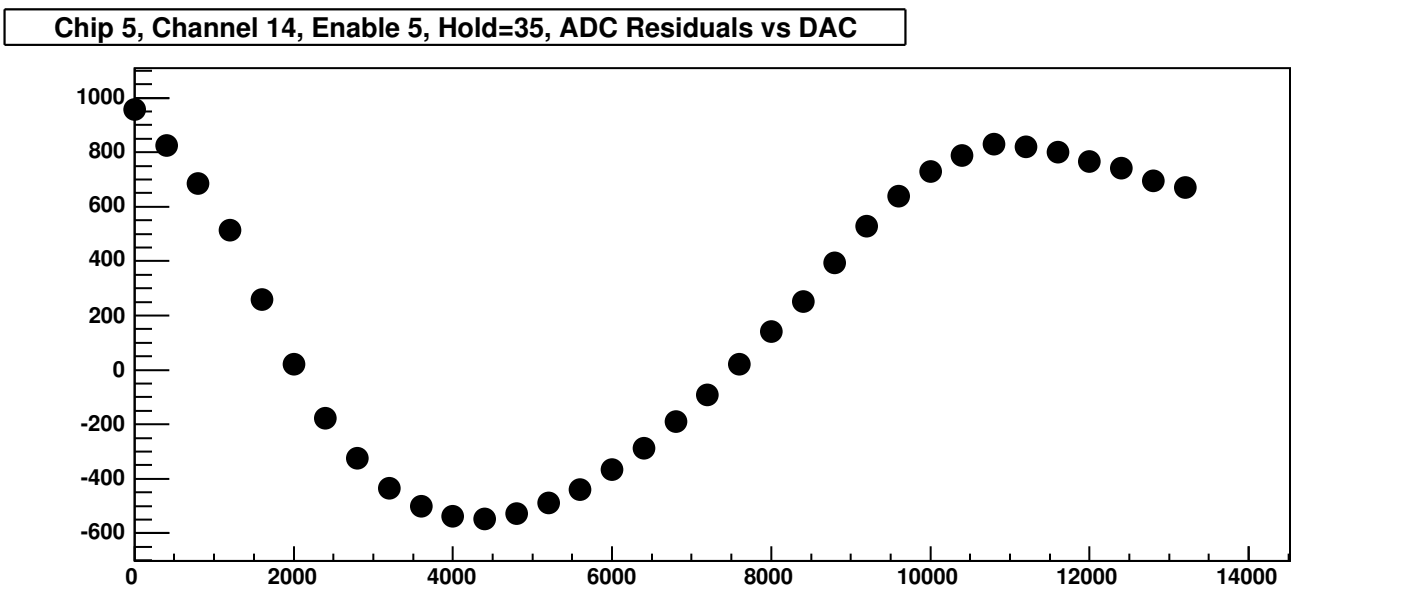

Chip 5, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 175.9 / 23
p0 -123.6 ± 3.814
p1 2.311 ± 0.002593
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

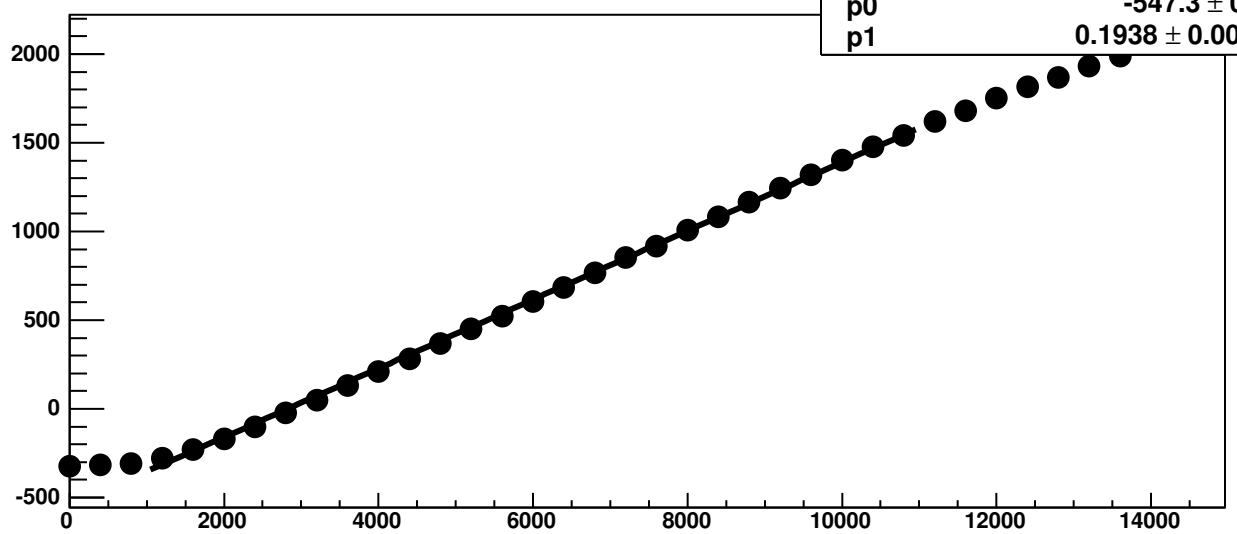

Chip 5, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5.065e+05 / 23
p0 -1231 ± 1.003
p1 0.3346 ± 0.0002223
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

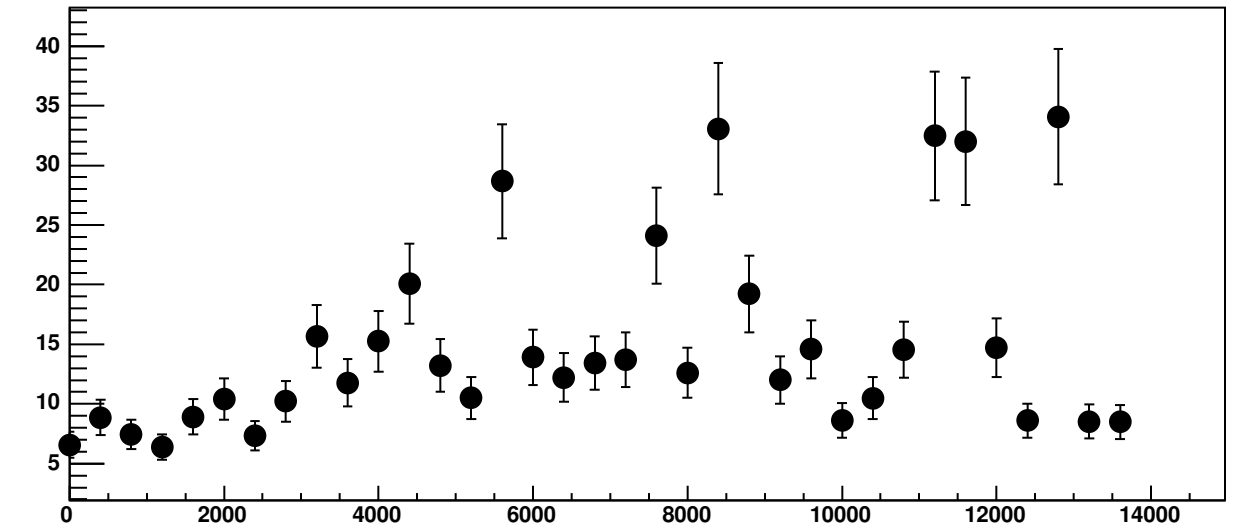

Chip 5, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

Chip 5, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

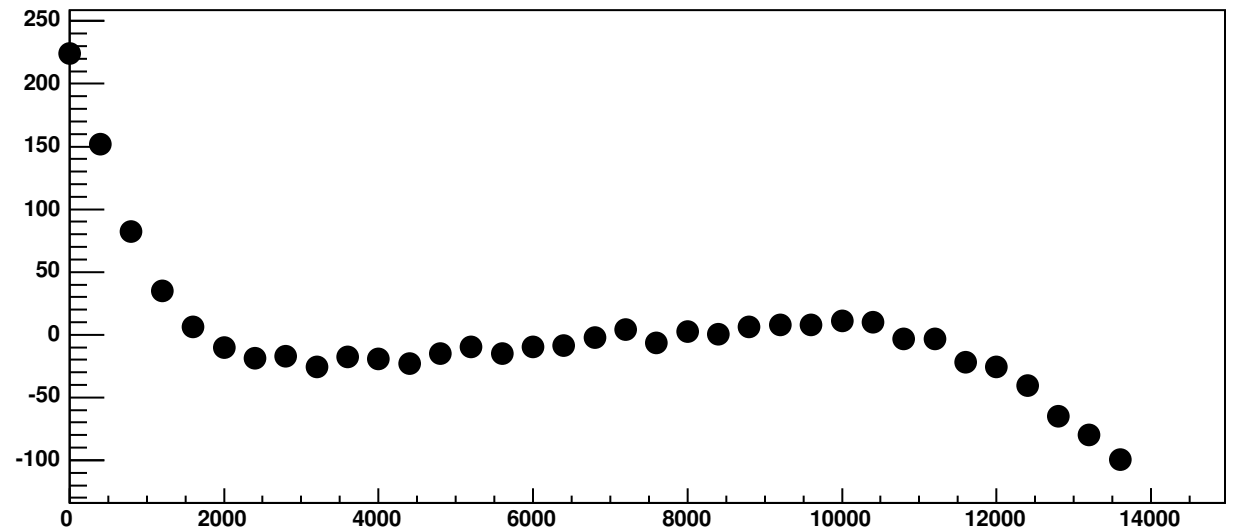
χ^2 / ndf	1054 / 23
p0	-547.3 ± 0.9919
p1	0.1938 ± 0.0001641

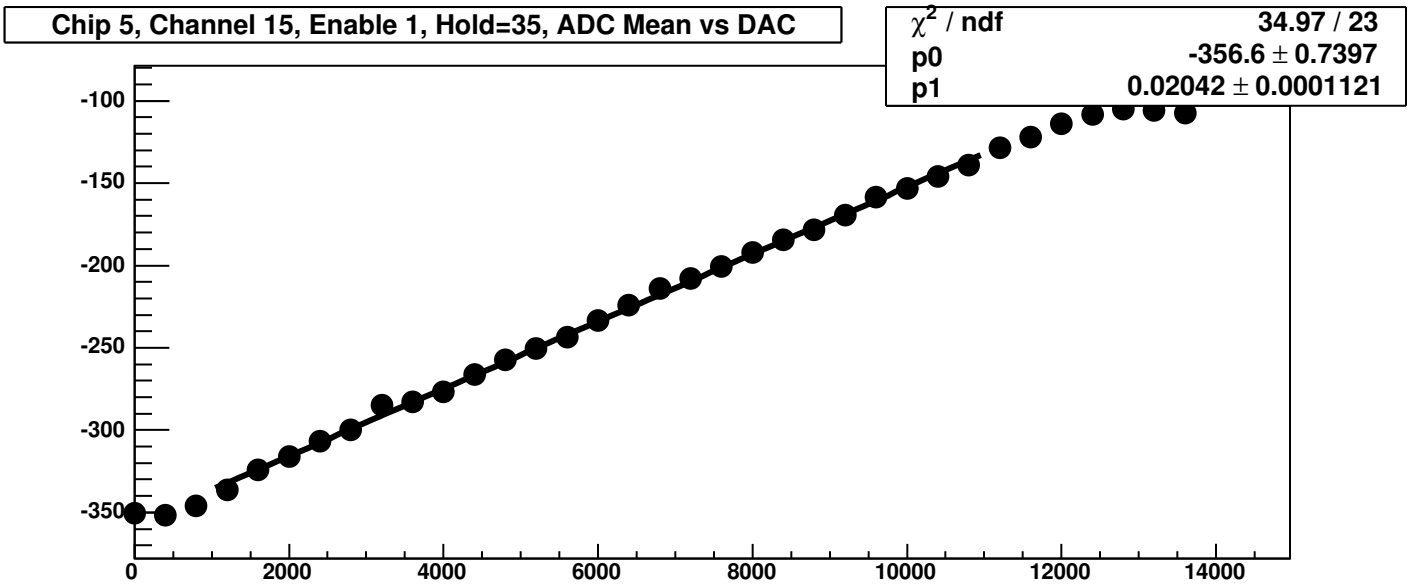


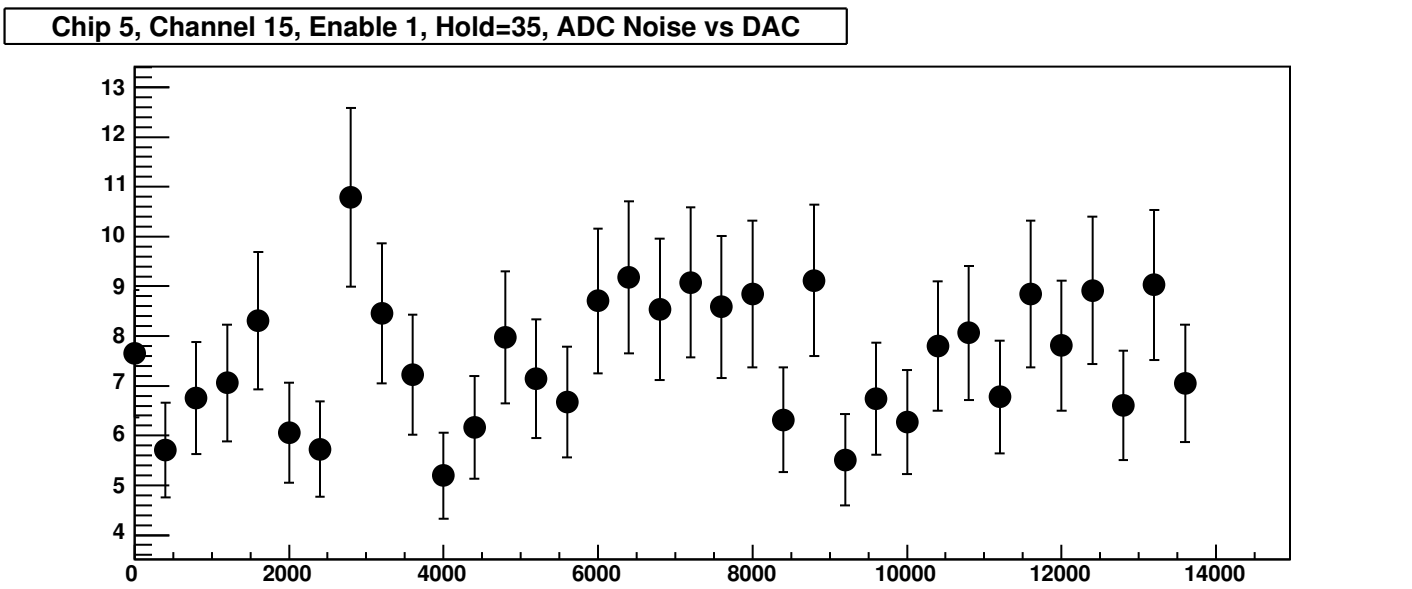
Chip 5, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC

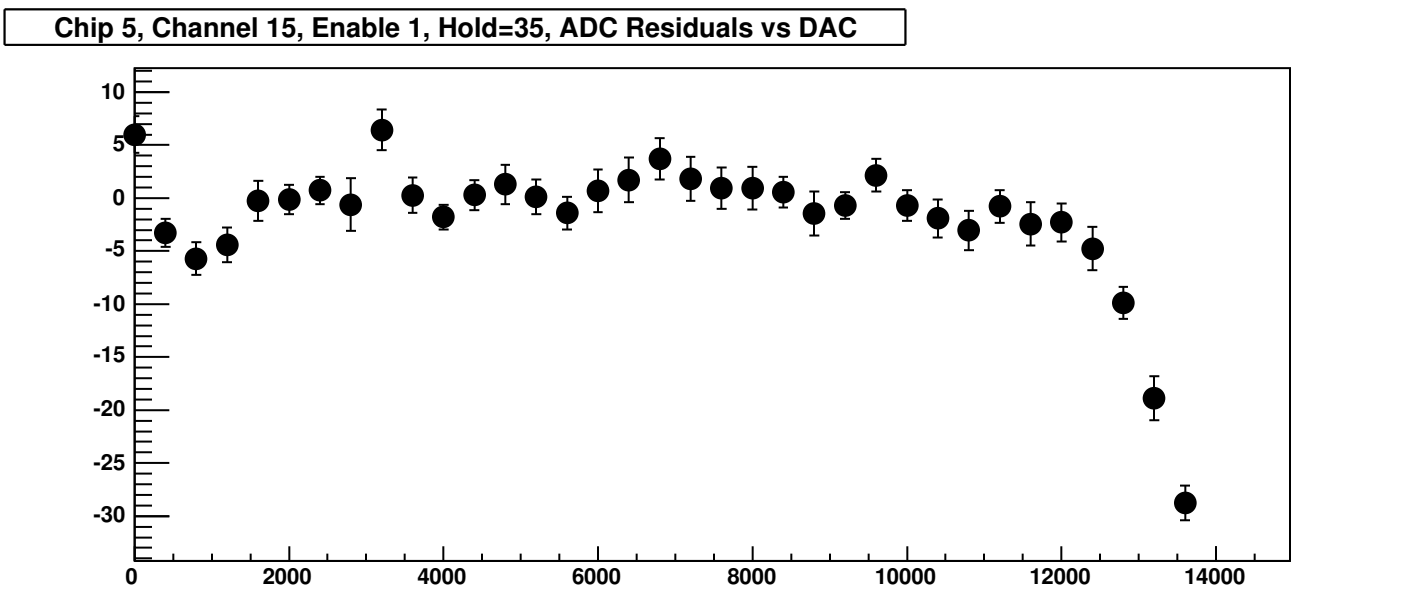


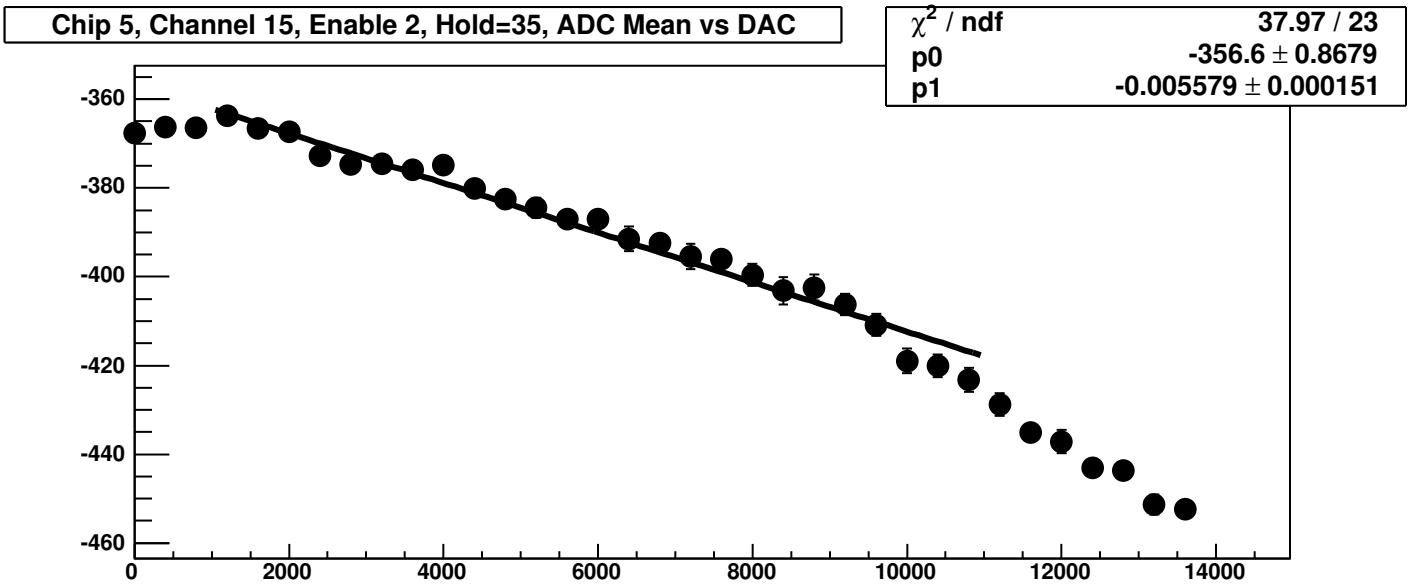
Chip 5, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

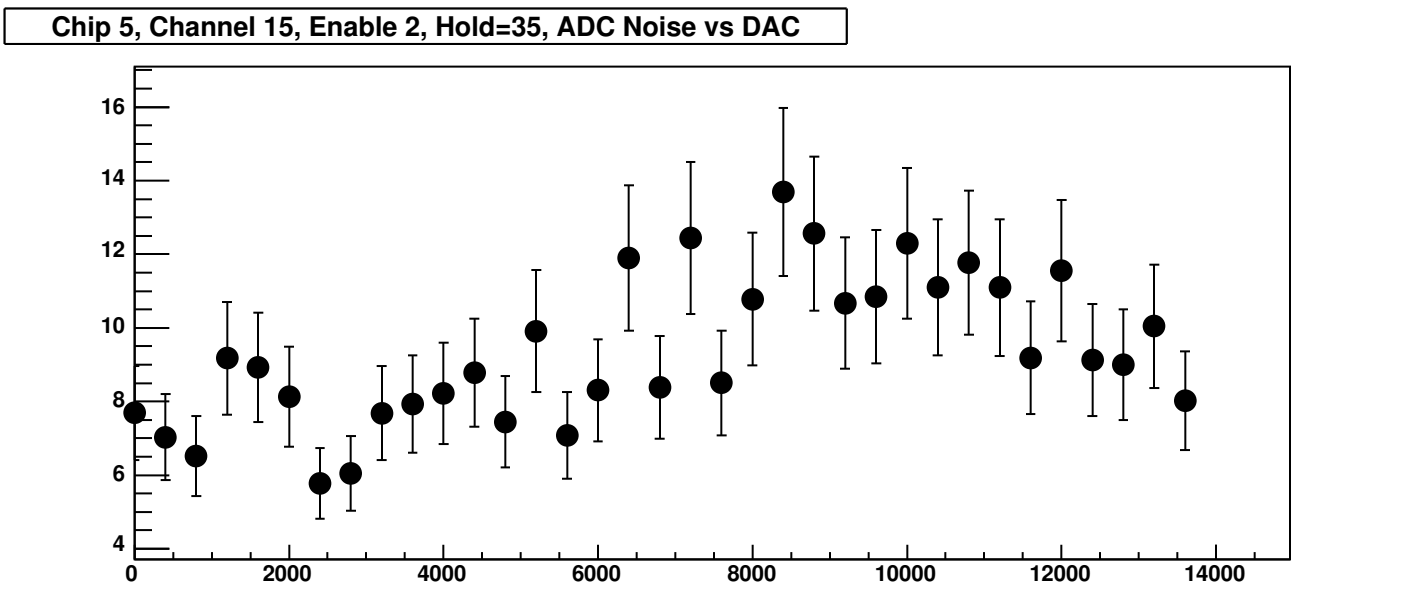


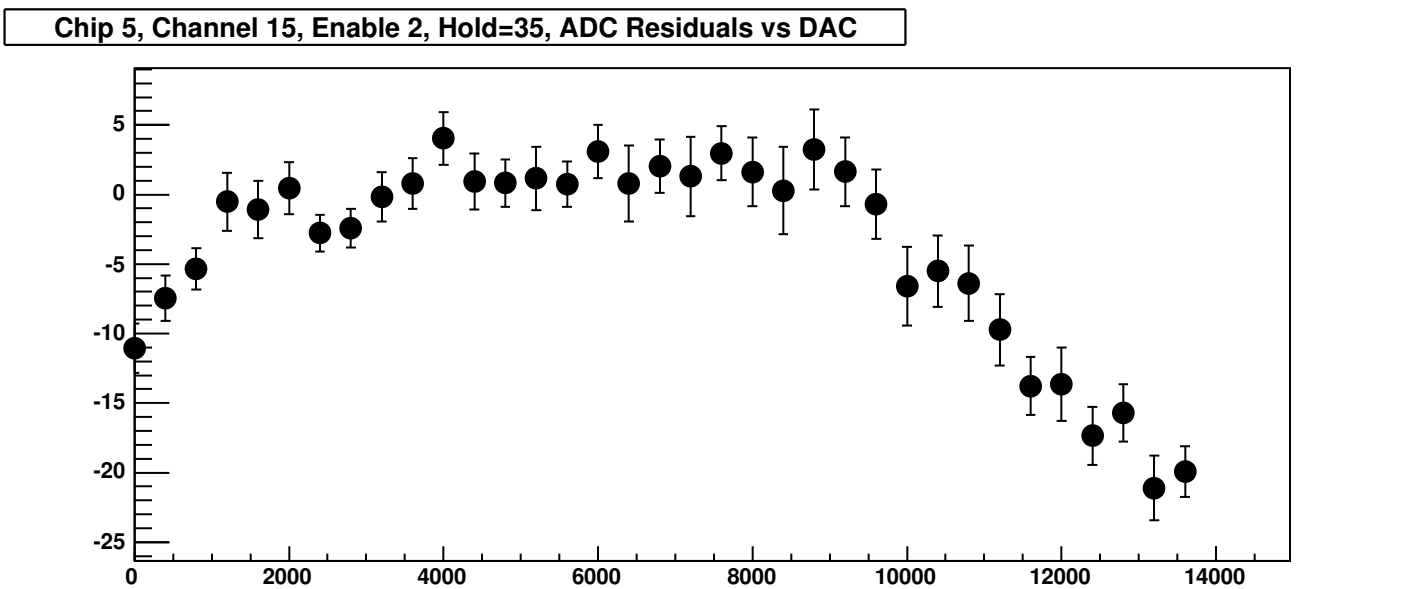

Chip 5, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 34.97 / 23
p0 -356.6 ± 0.7397
p1 0.02042 ± 0.0001121
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

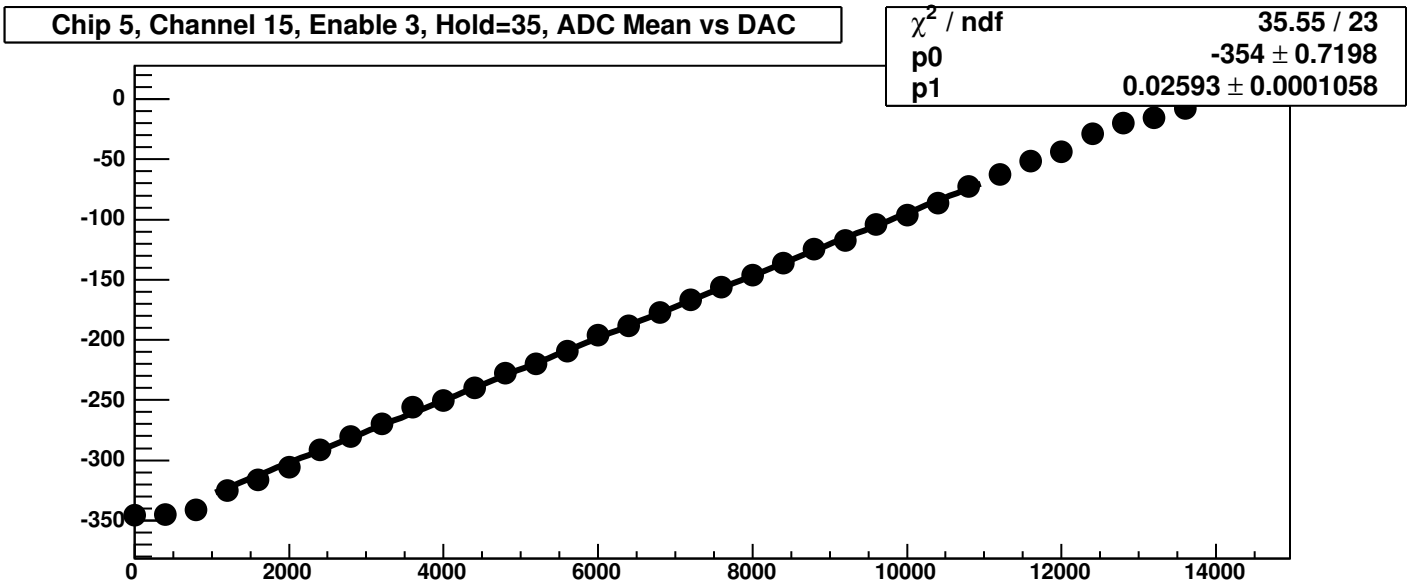

Chip 5, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

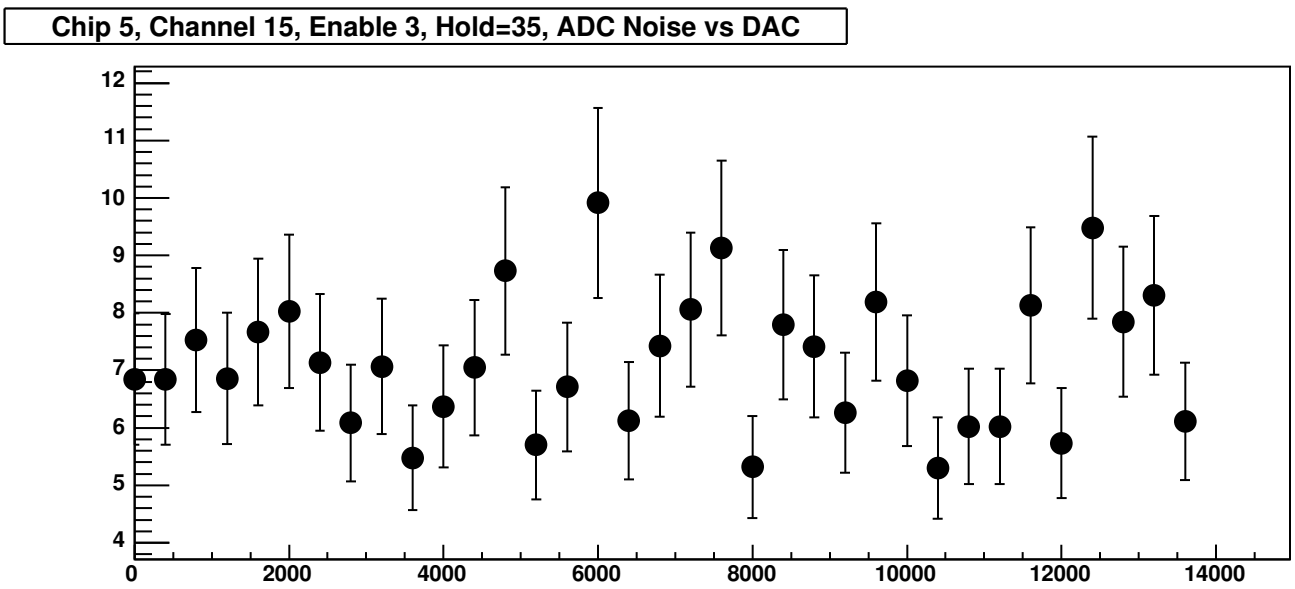

Chip 5, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
0
2000
4000
6000
8000
10000
12000
14000

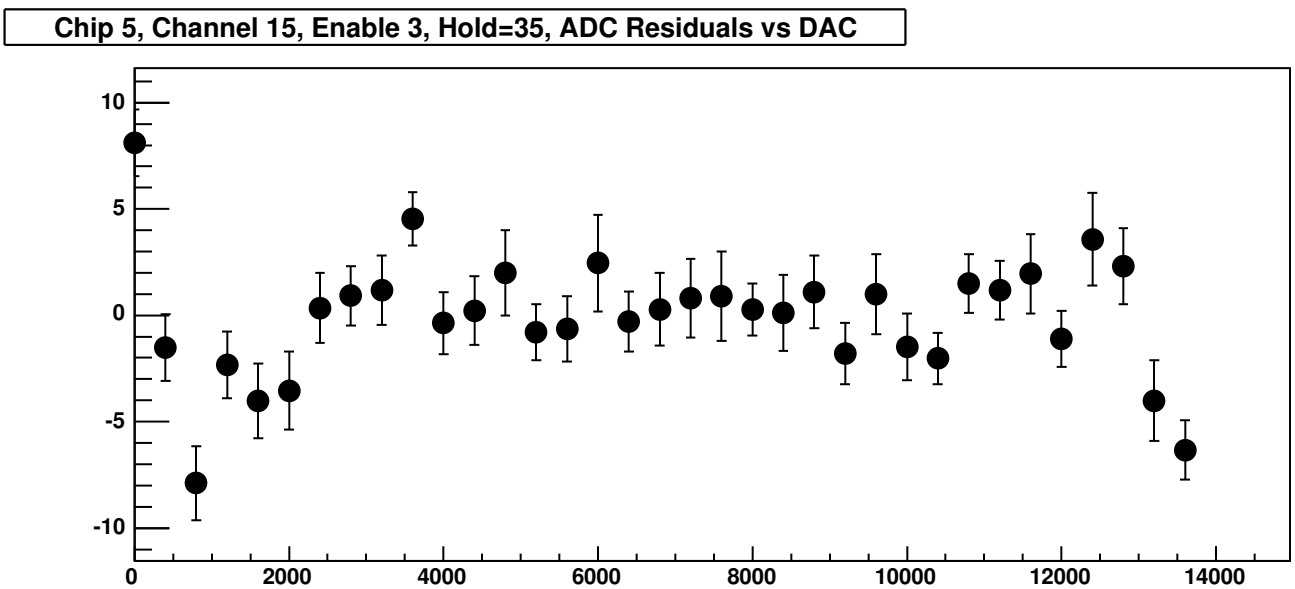

Chip 5, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 37.97 / 23
p0 -356.6 ± 0.8679
p1 -0.005579 ± 0.000151
-360
-380
-400
-420
-440
-460
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

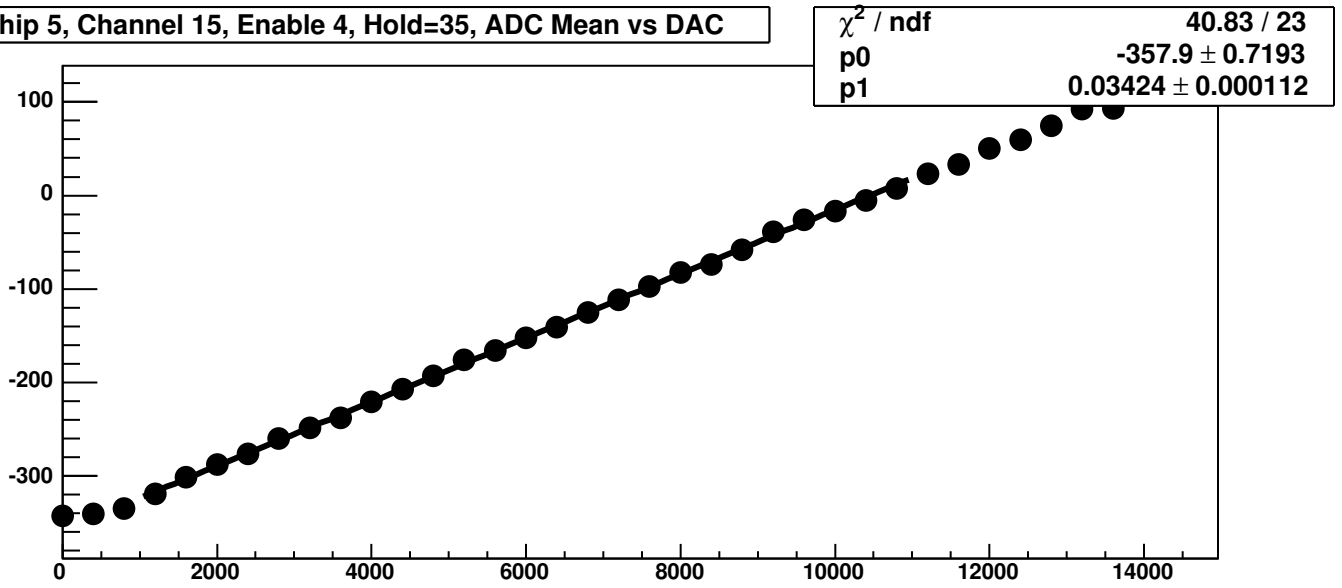

Chip 5, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 35.55 / 23
p0 -354 ± 0.7198
p1 0.02593 ± 0.0001058
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

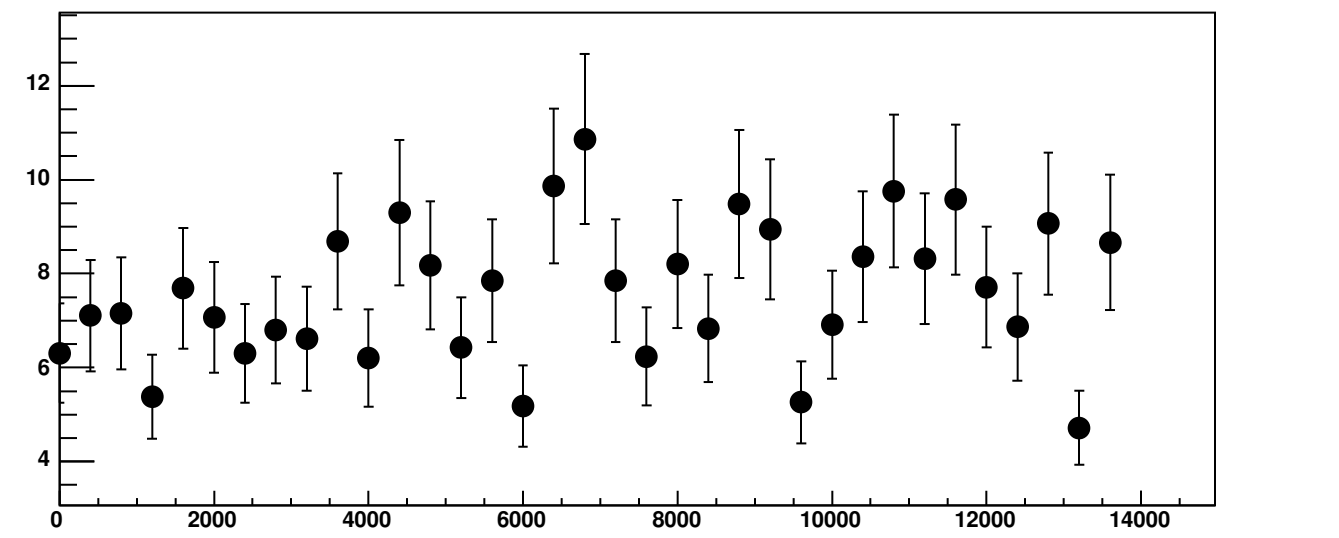

Chip 5, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

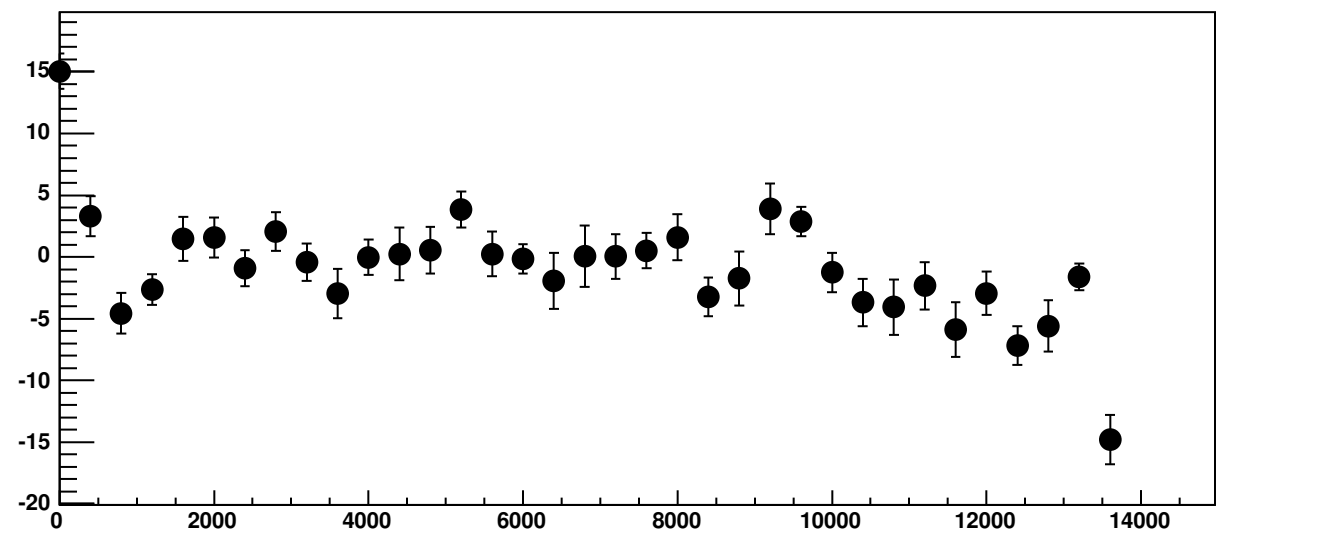
Chip 5, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

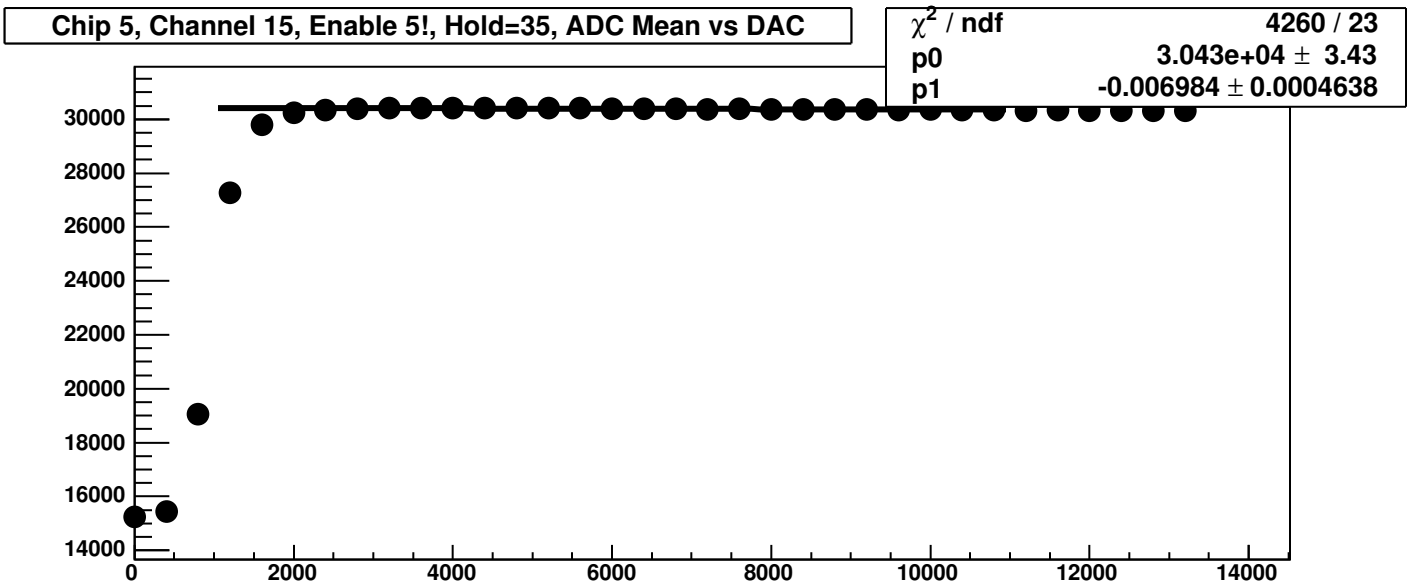


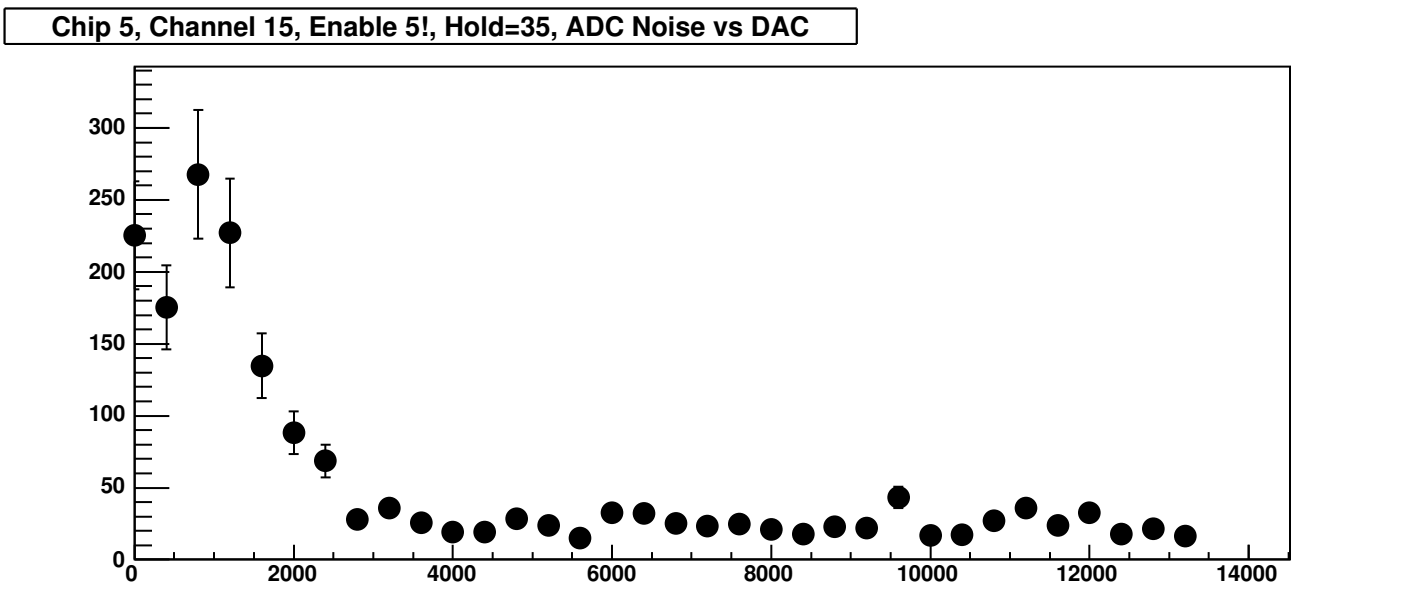
Chip 5, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC

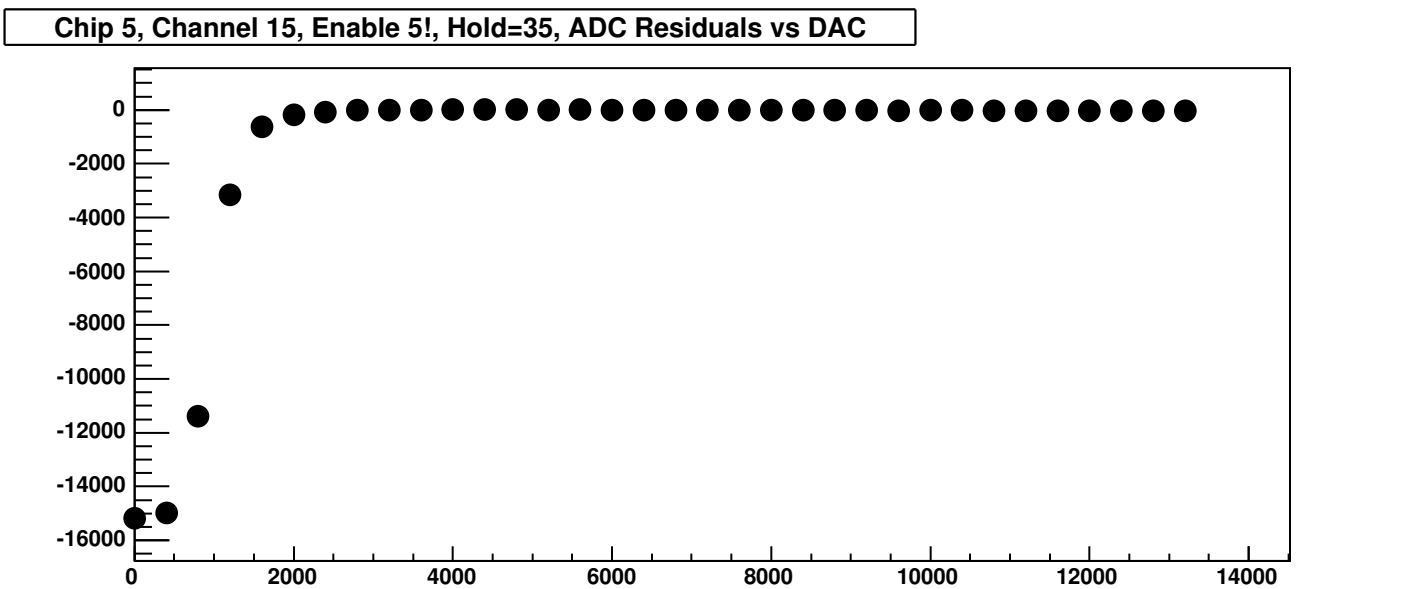


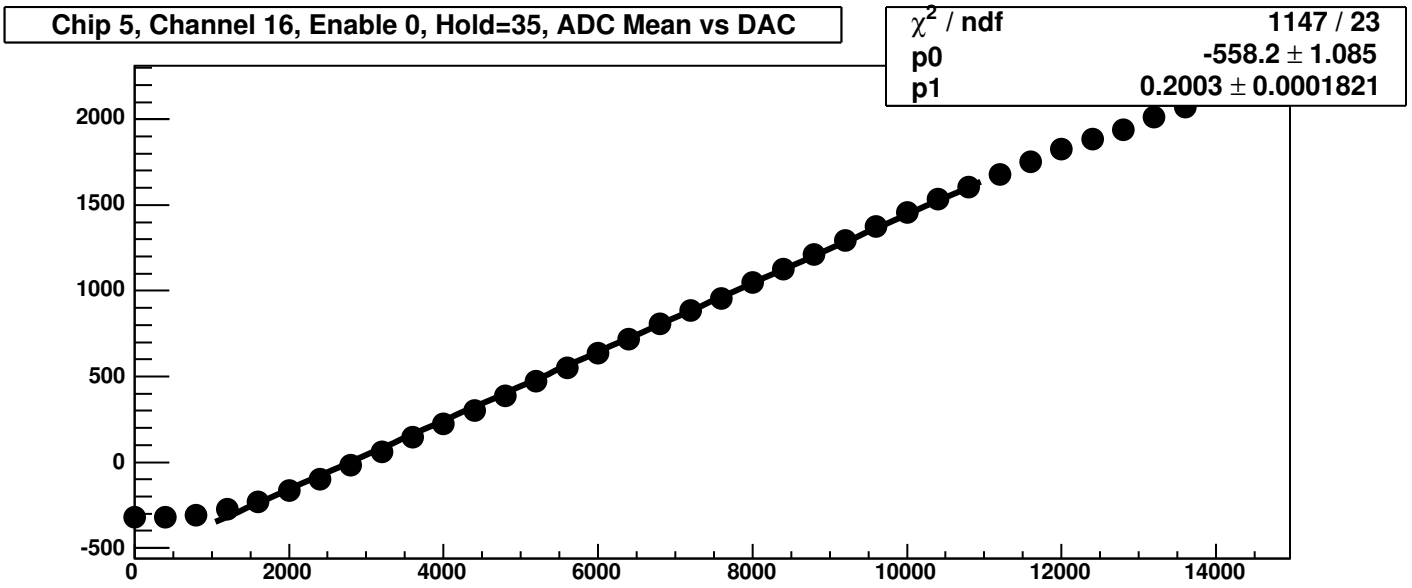
Chip 5, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

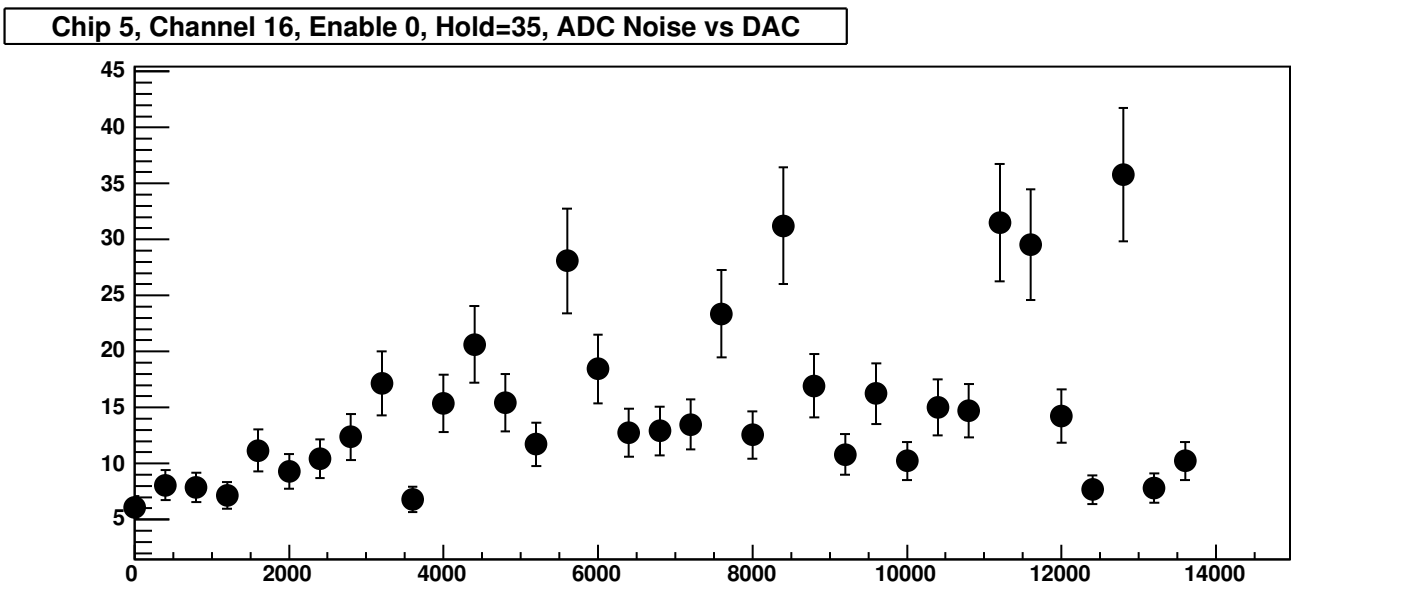


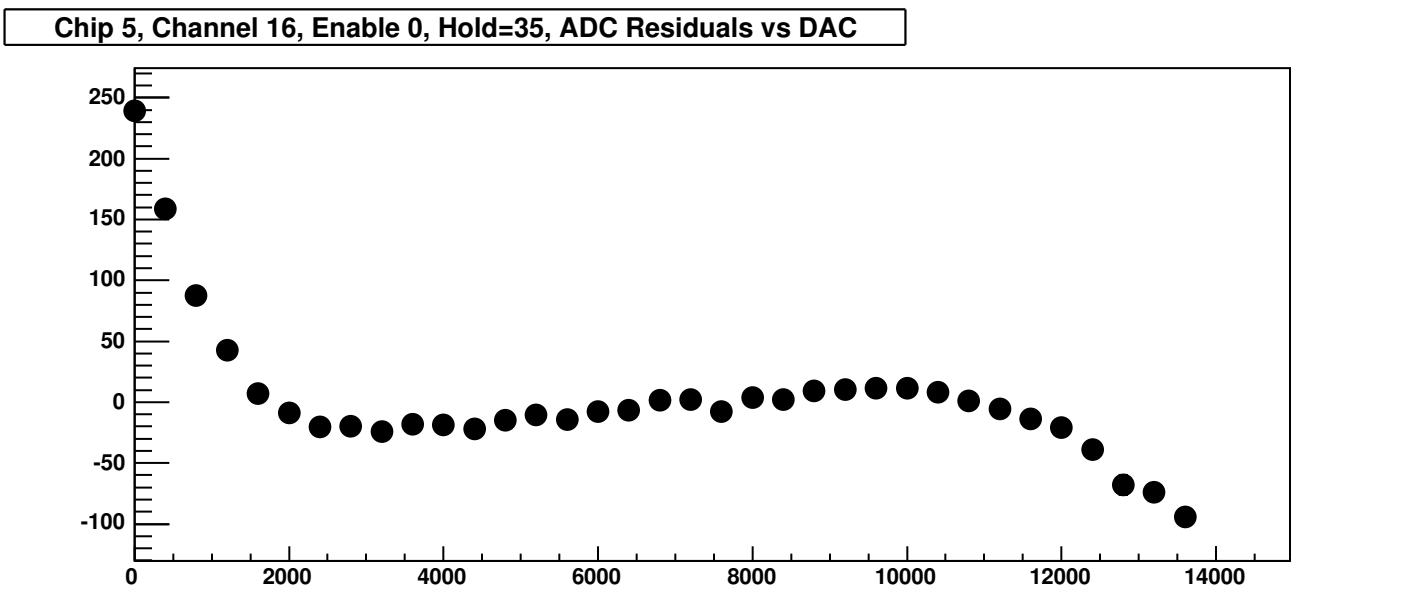

Chip 5, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4260 / 23
p0 3.043e+04 ± 3.43
p1 -0.006984 ± 0.0004638
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

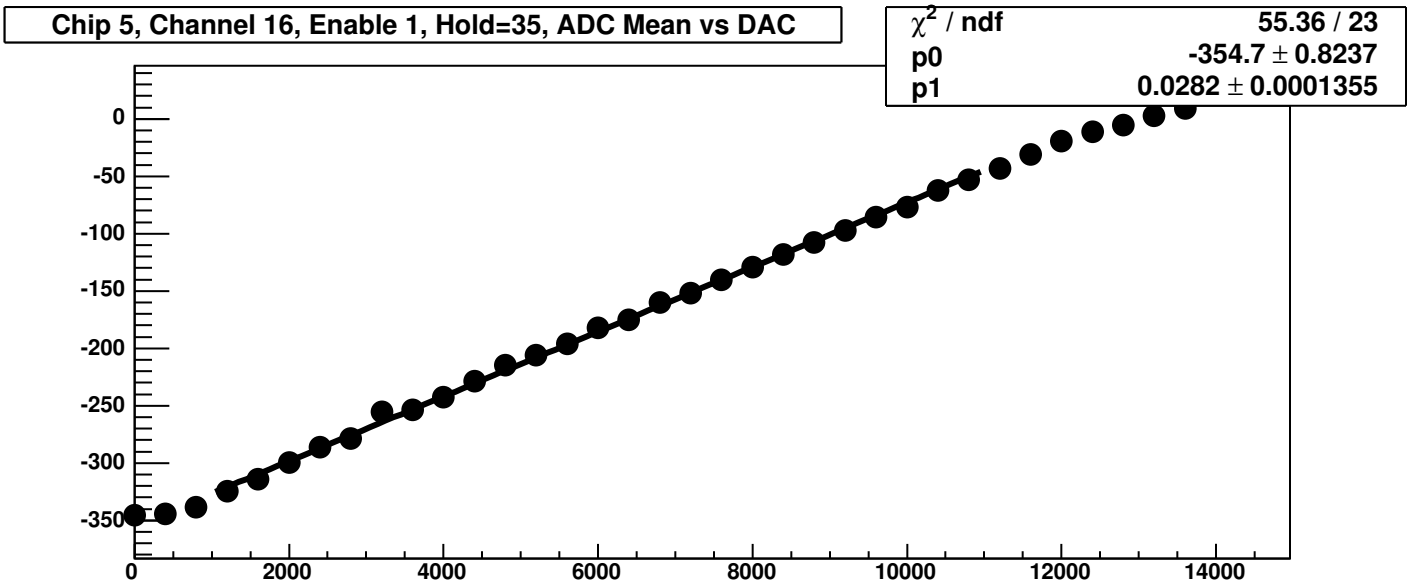

Chip 5, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

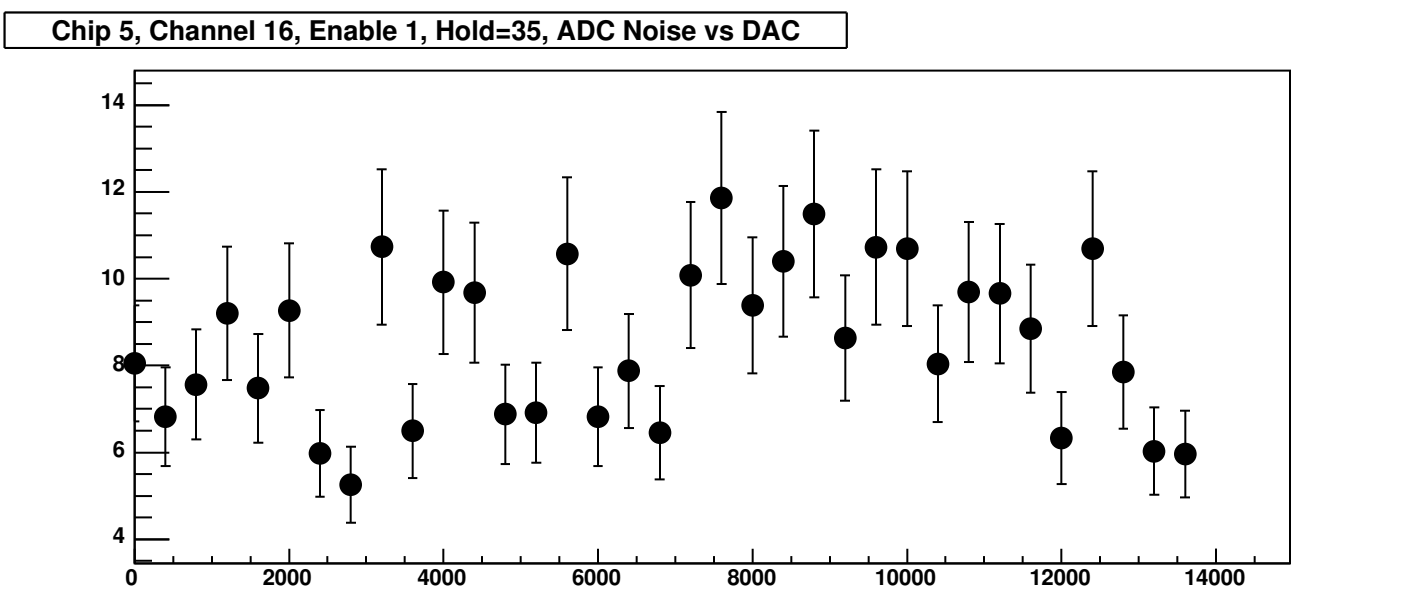

Chip 5, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

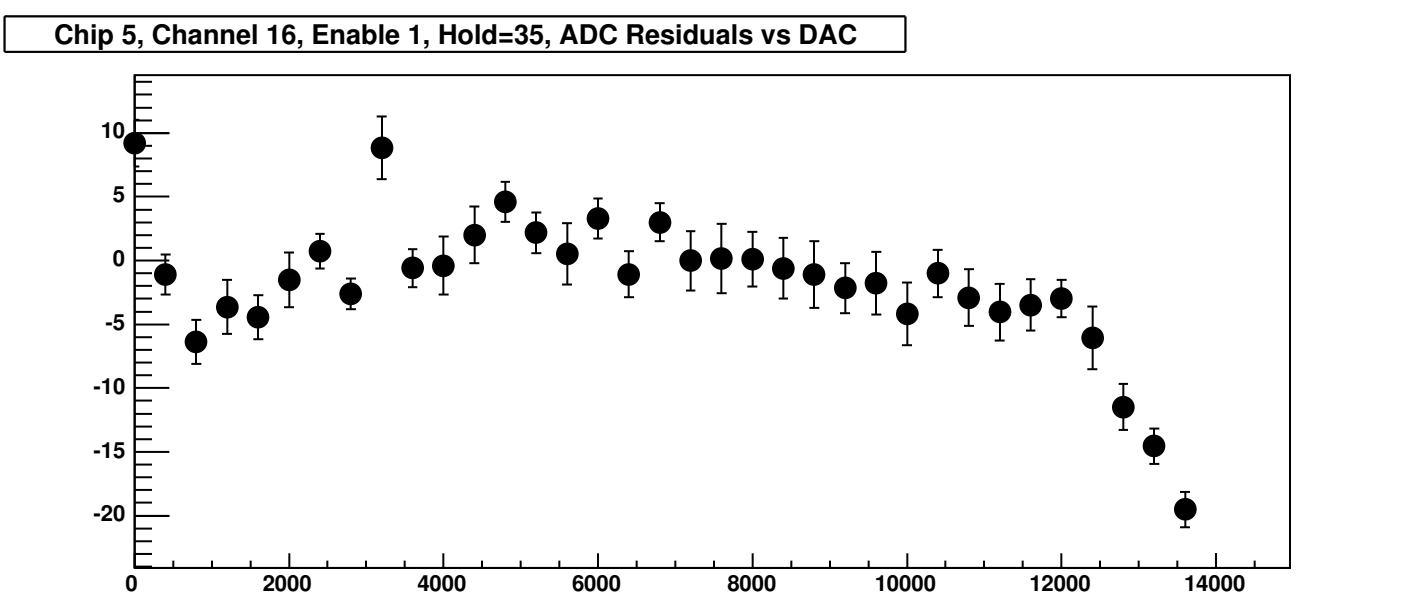

Chip 5, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1147 / 23
p0 -558.2 ± 1.085
p1 0.2003 ± 0.0001821
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

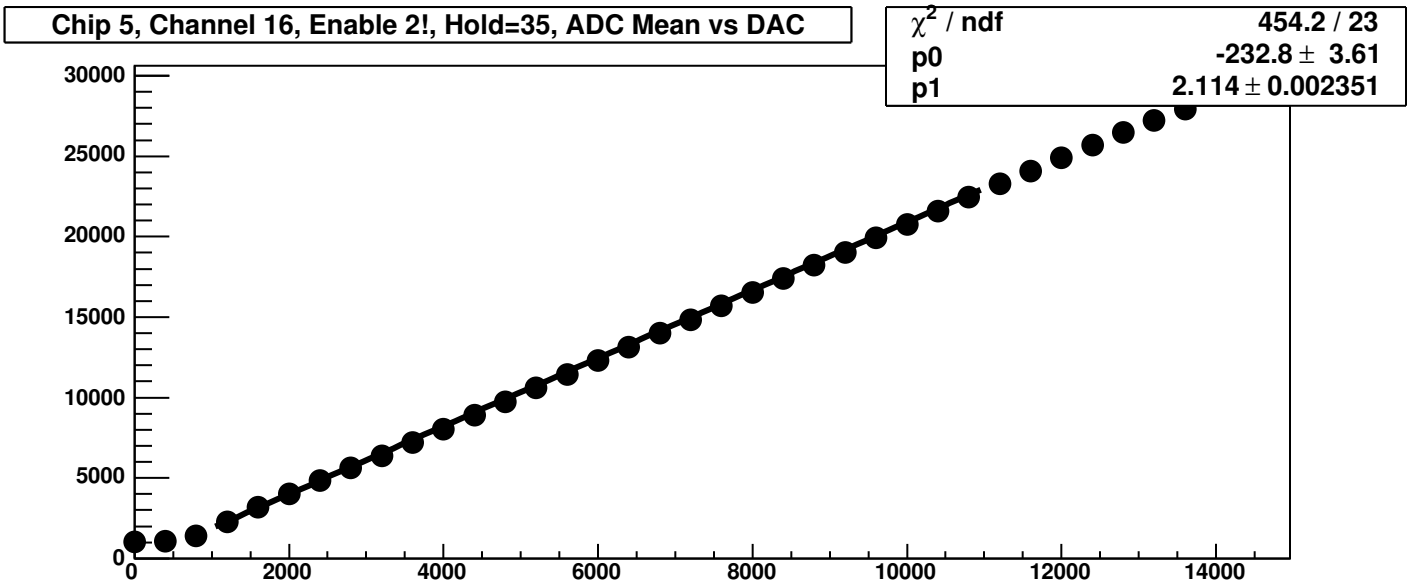

Chip 5, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

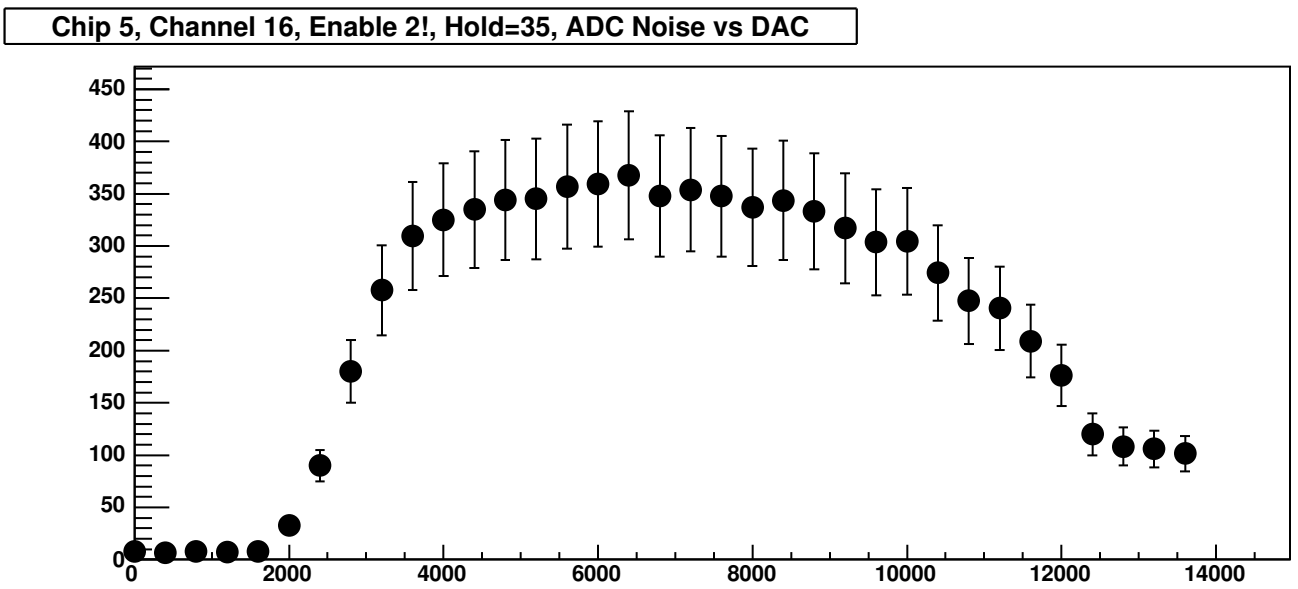

Chip 5, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

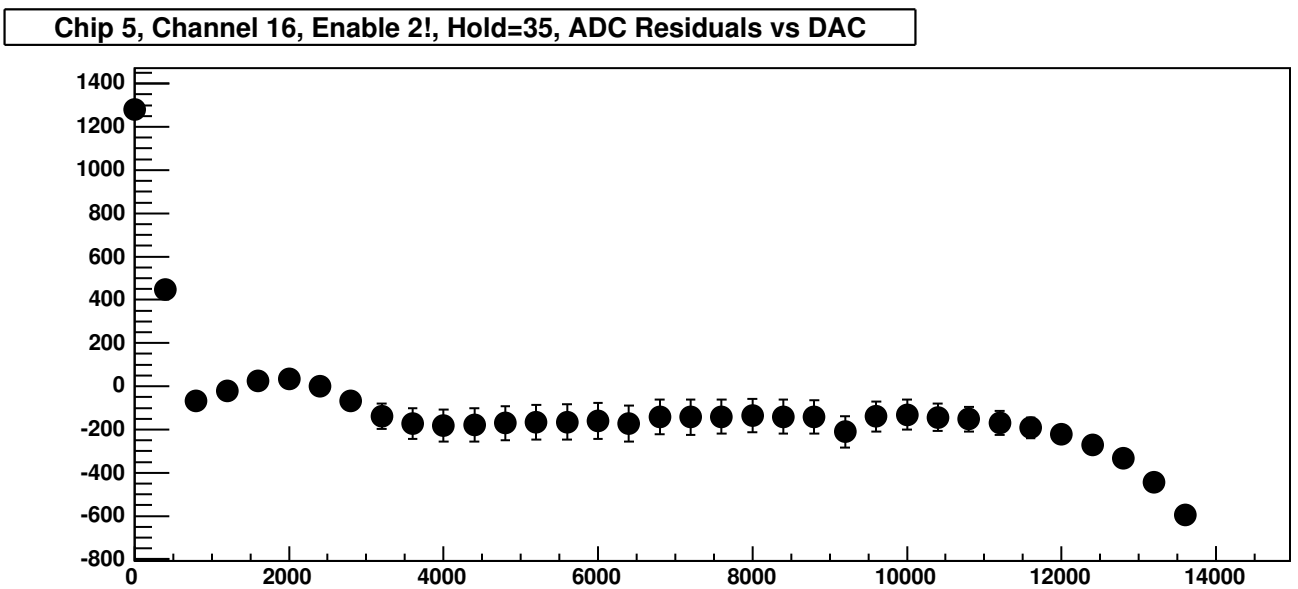

Chip 5, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 55.36 / 23
p0 -354.7 ± 0.8237
p1 0.0282 ± 0.0001355
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

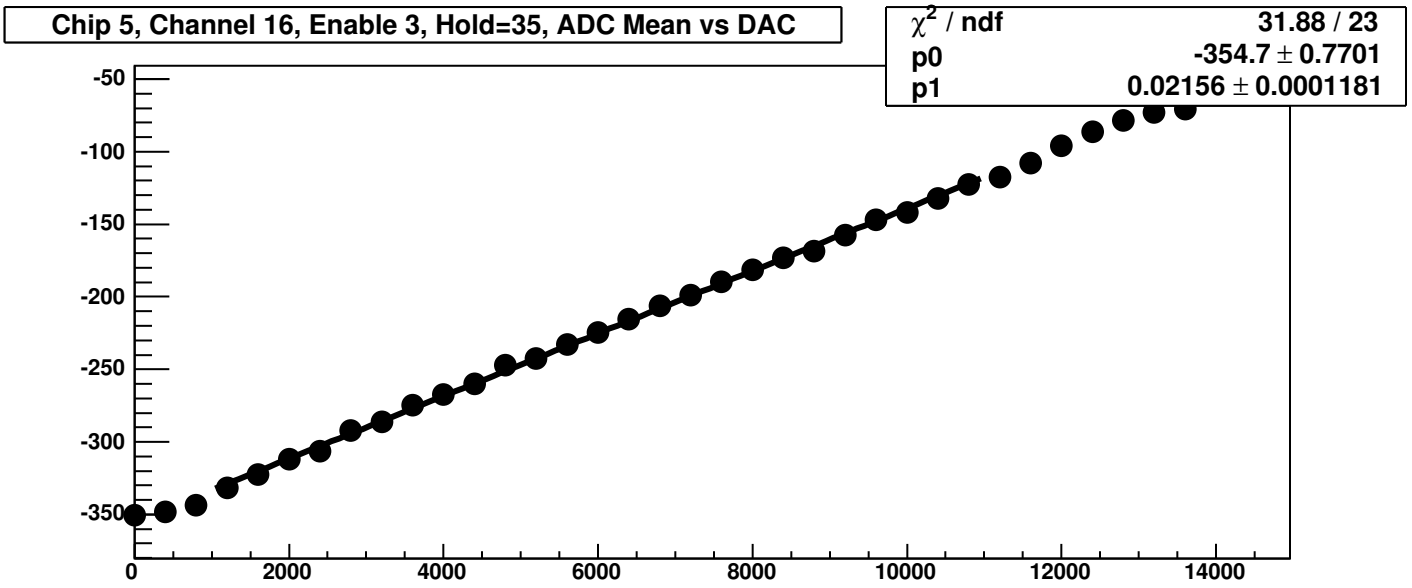

Chip 5, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

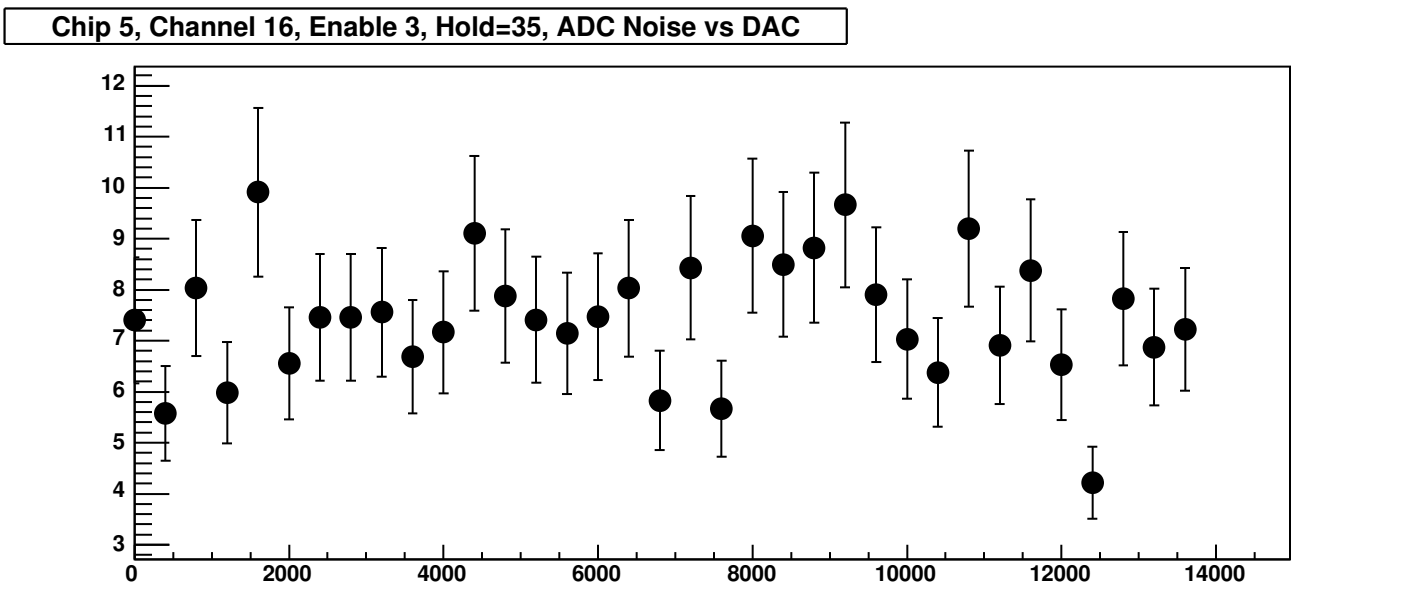

Chip 5, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

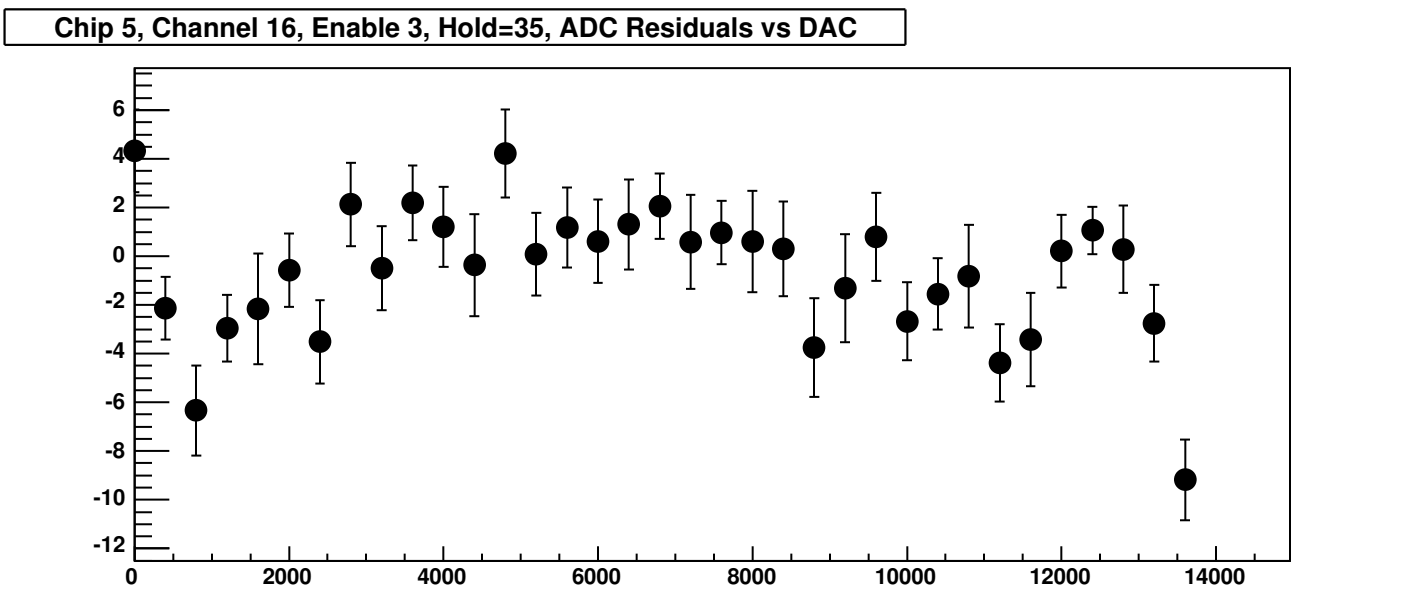

Chip 5, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 454.2 / 23
p0 -232.8 ± 3.61
p1 2.114 ± 0.002351
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

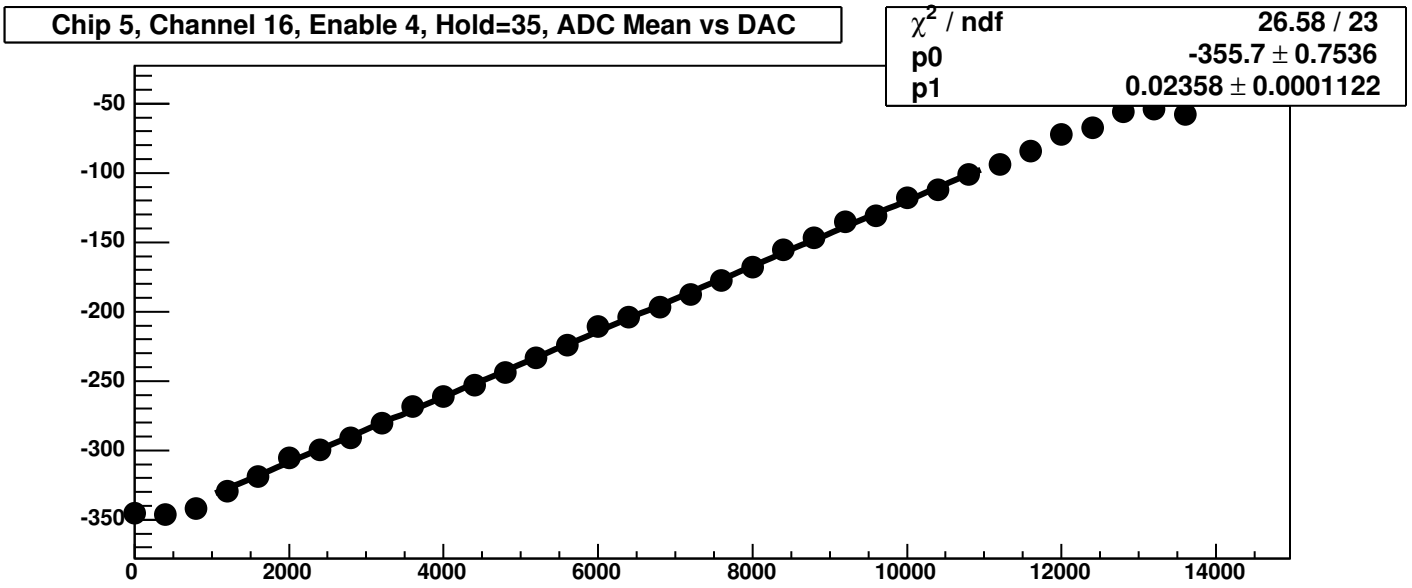

Chip 5, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

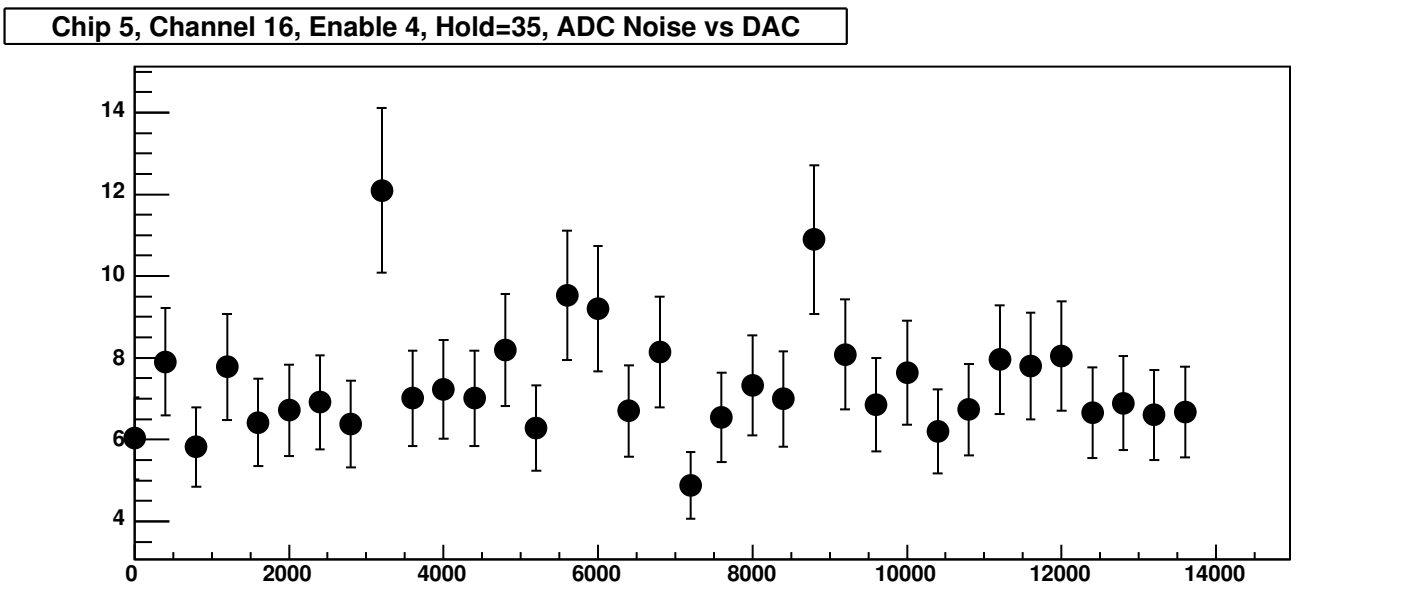

Chip 5, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

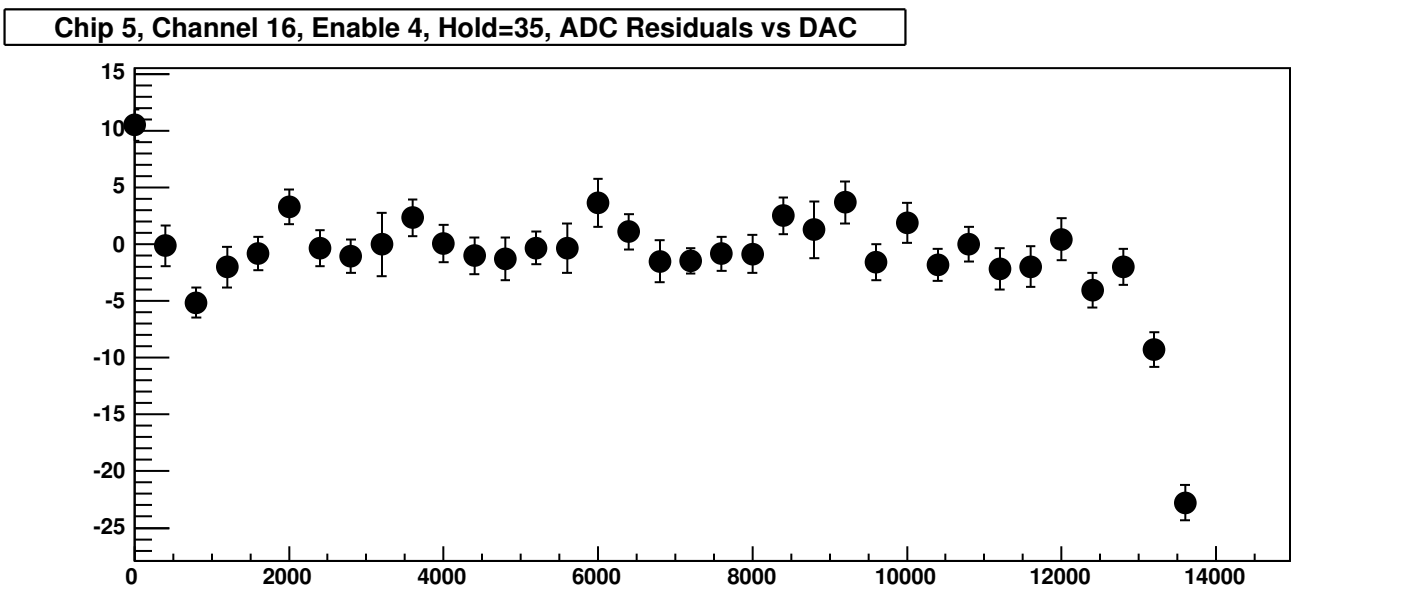

Chip 5, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.88 / 23
p0 -354.7 ± 0.7701
p1 0.02156 ± 0.0001181
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

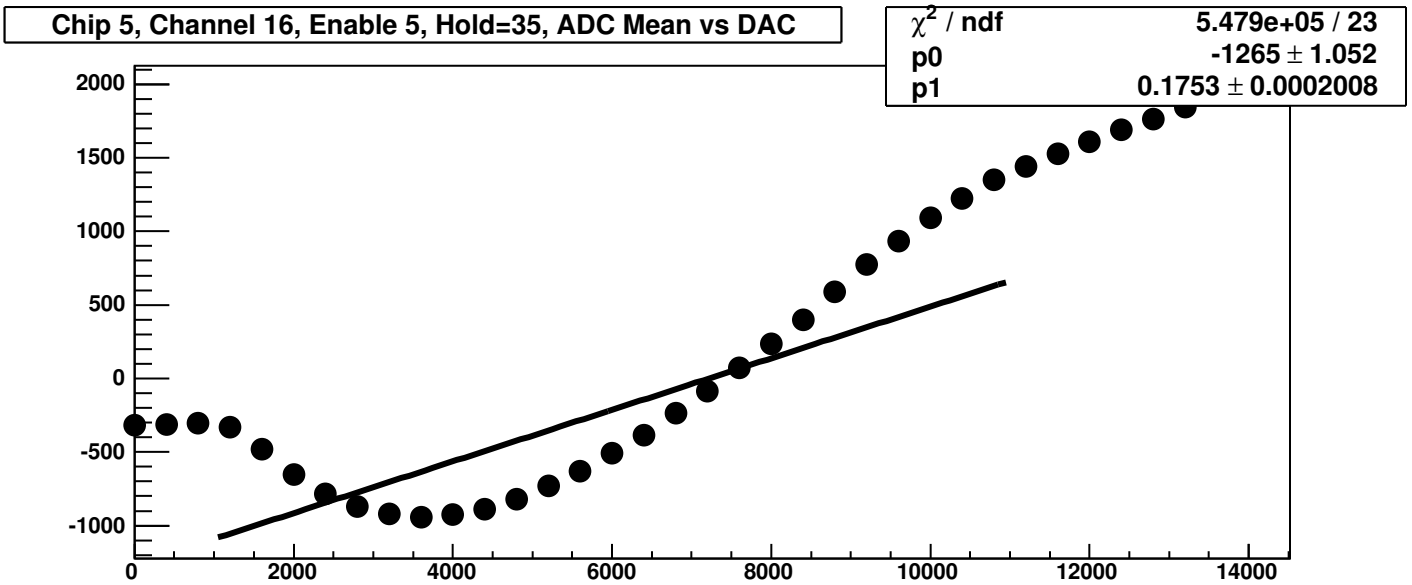

Chip 5, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

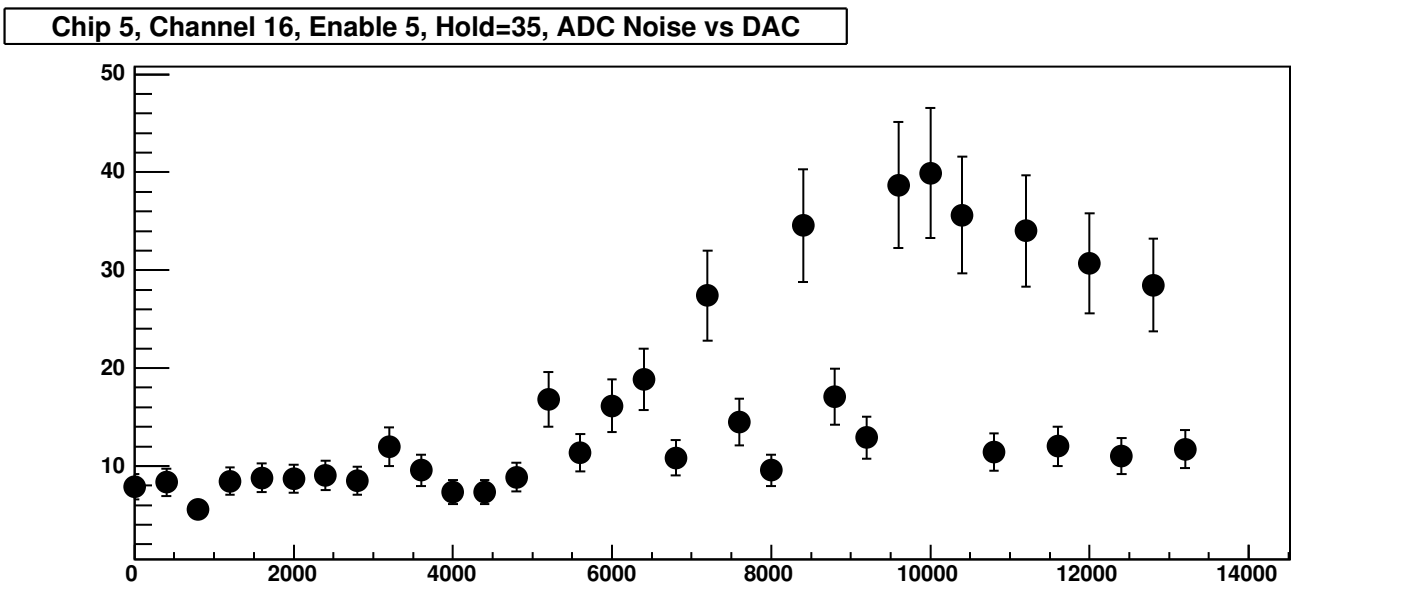

Chip 5, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
0
2000
4000
6000
8000
10000
12000
14000

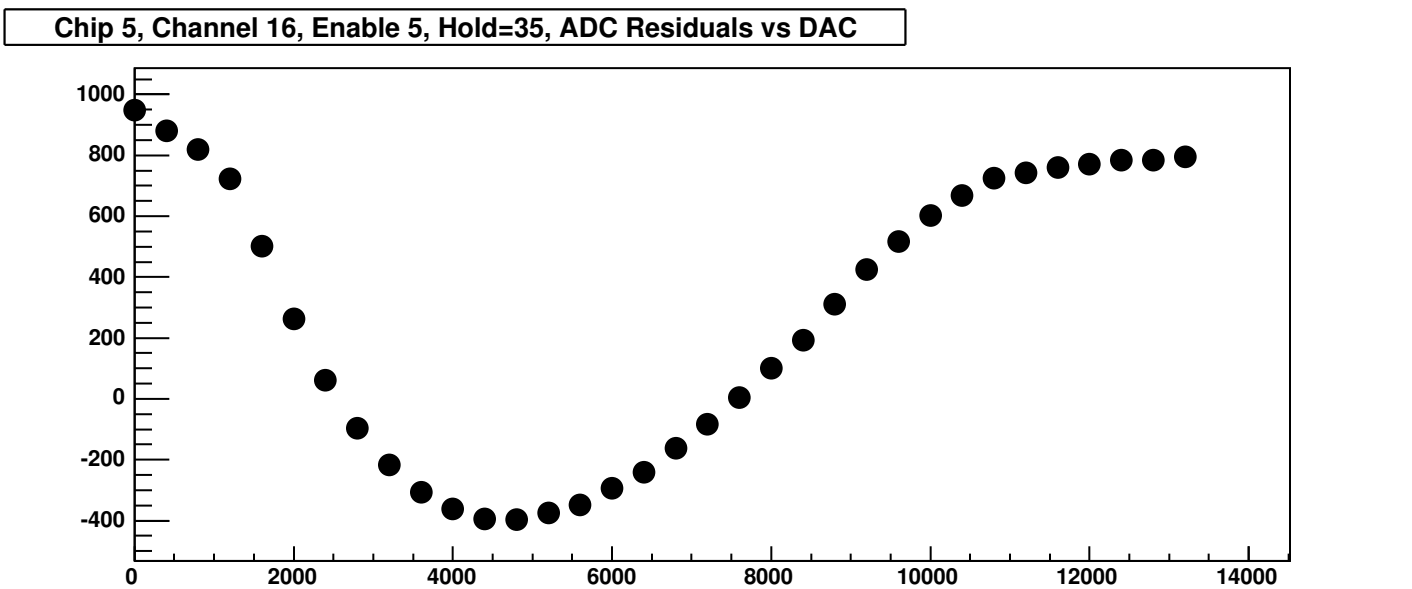

Chip 5, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.58 / 23
p0 -355.7 ± 0.7536
p1 0.02358 ± 0.0001122
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

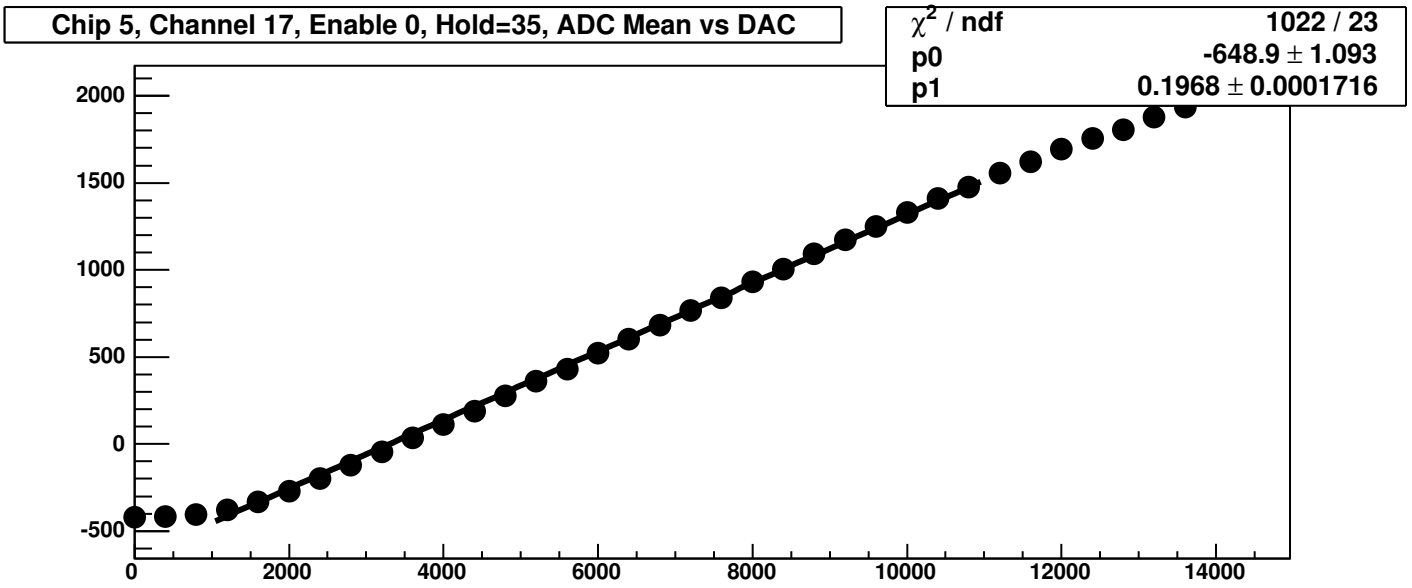

Chip 5, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

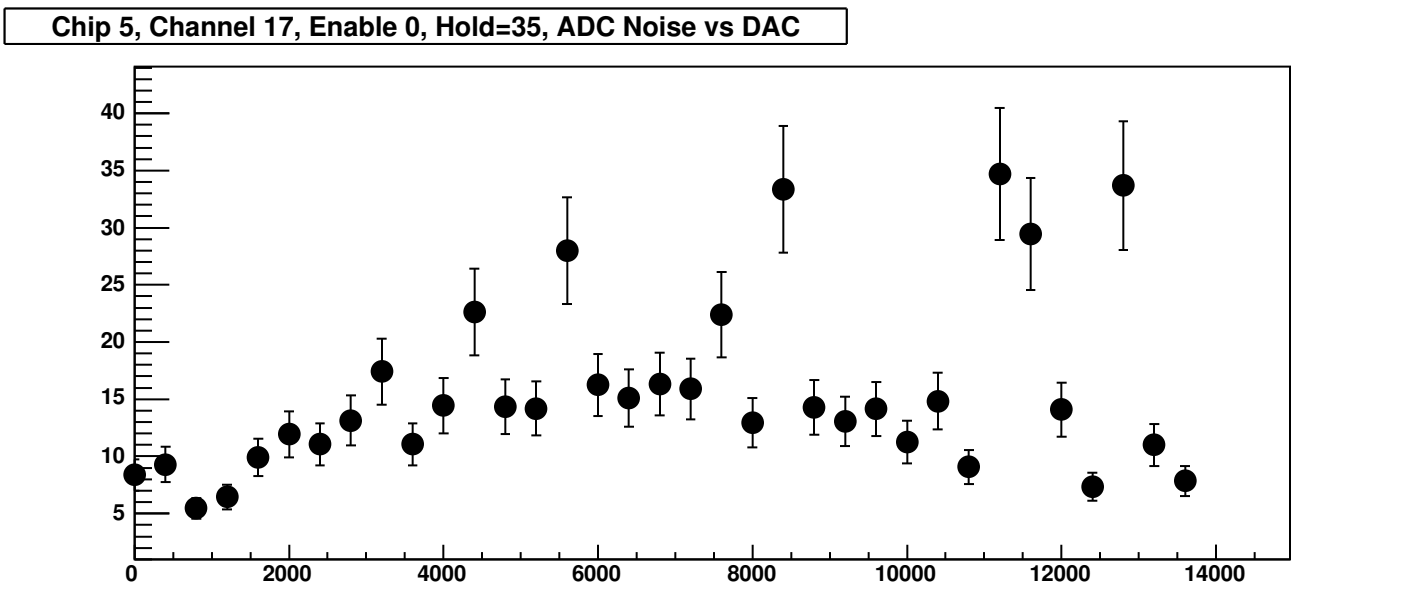

Chip 5, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

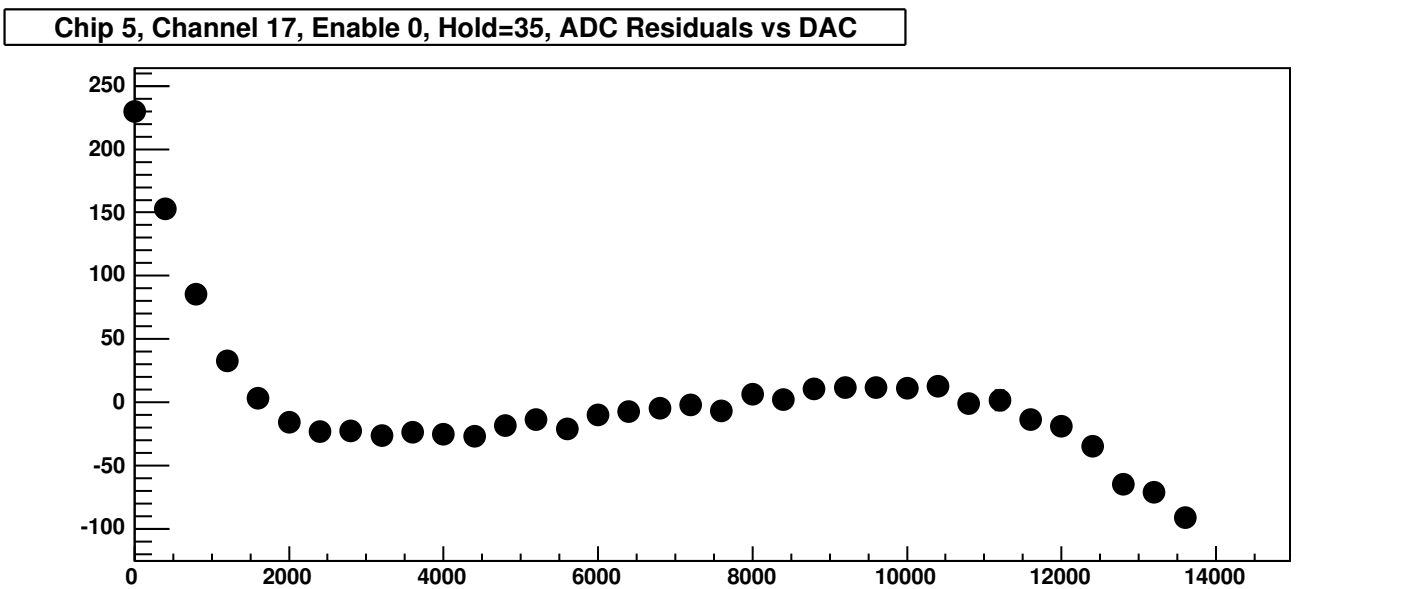

Chip 5, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5.479e+05 / 23
p0 -1265 ± 1.052
p1 0.1753 ± 0.0002008
2000
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

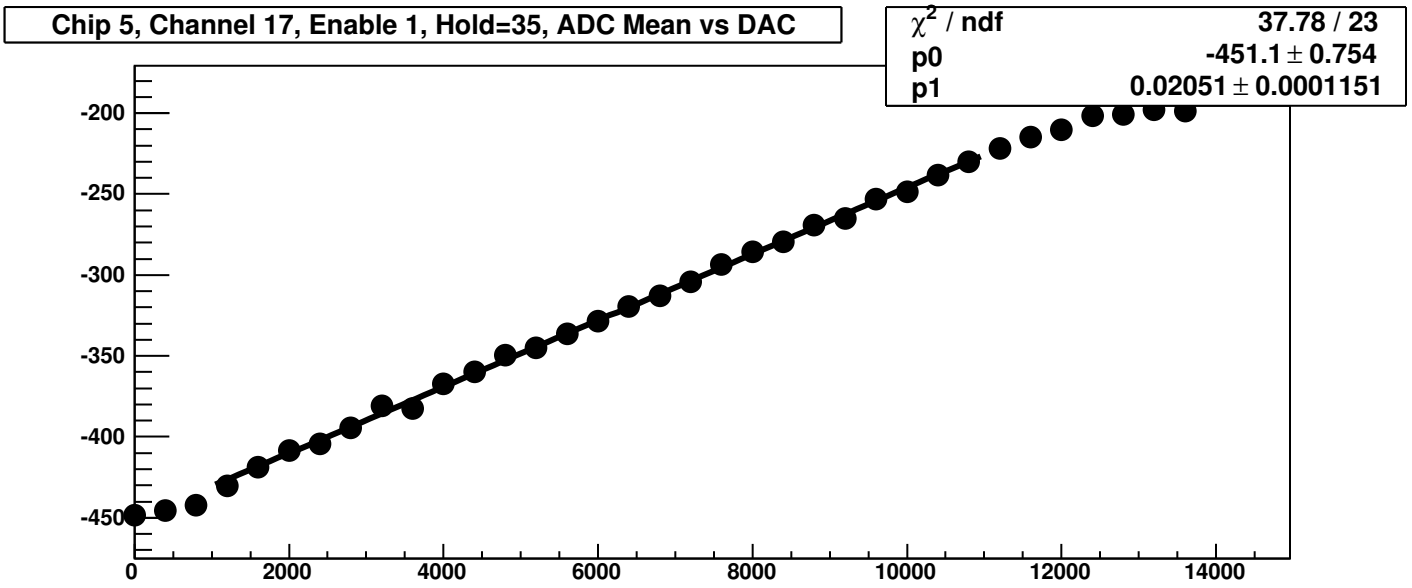

Chip 5, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

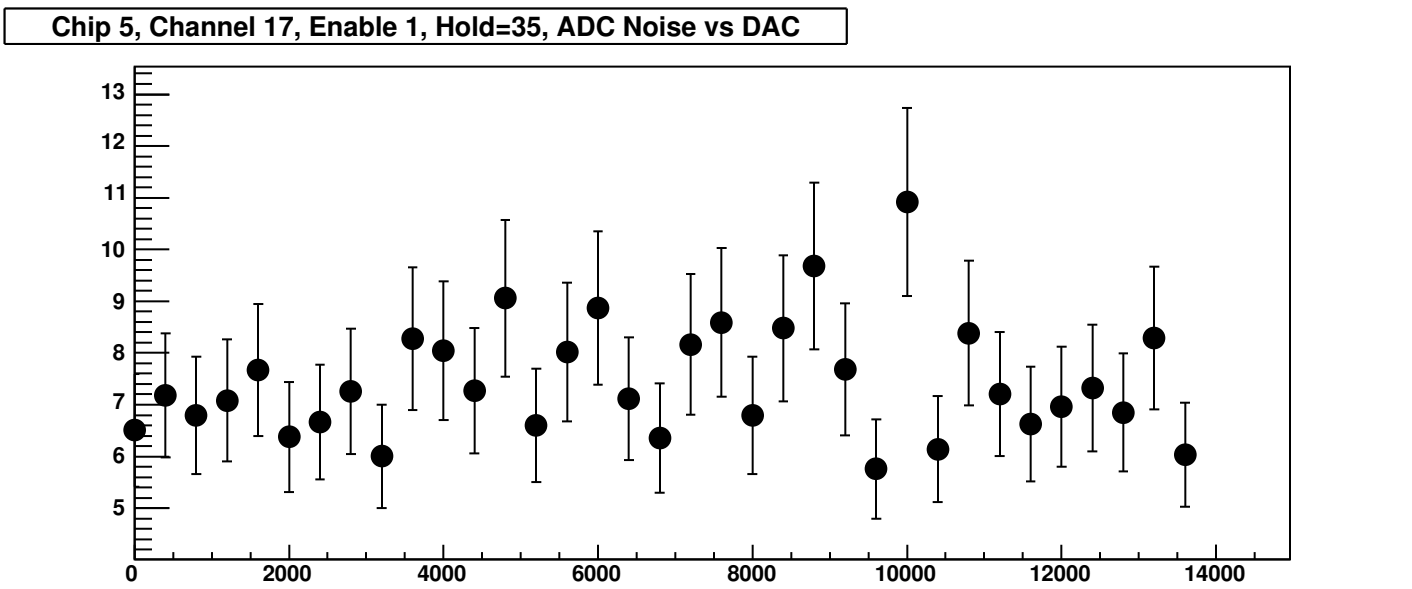

Chip 5, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

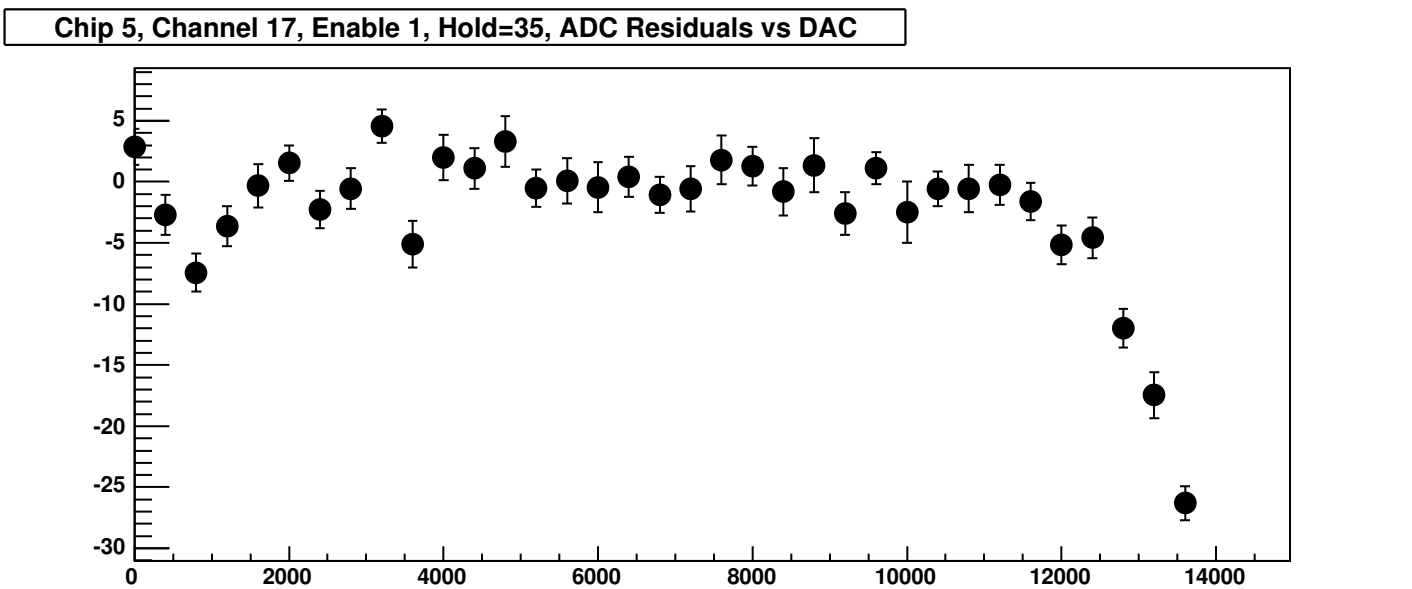

Chip 5, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1022 / 23
p0 -648.9 ± 1.093
p1 0.1968 ± 0.0001716
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

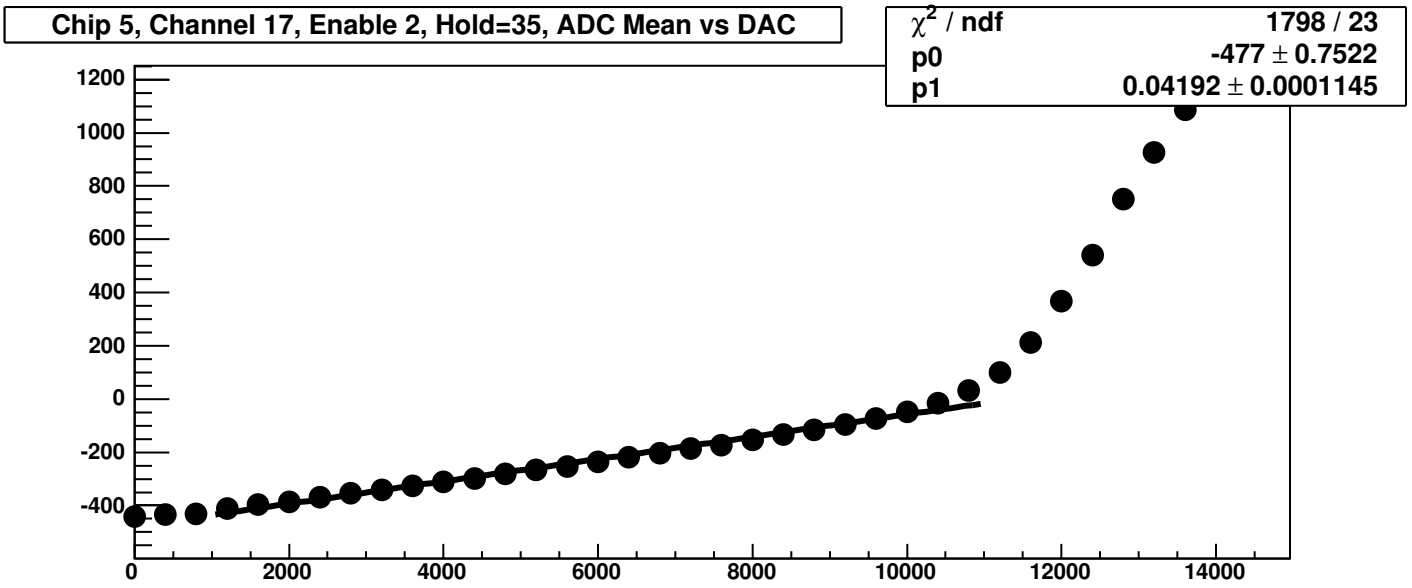

Chip 5, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

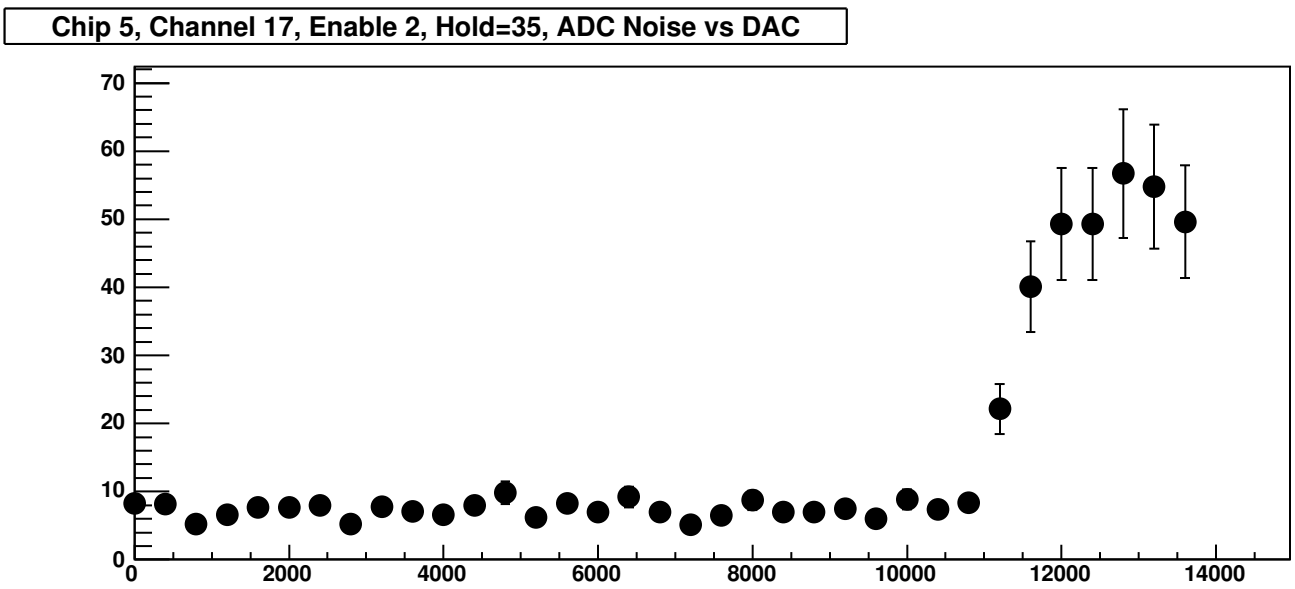

Chip 5, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

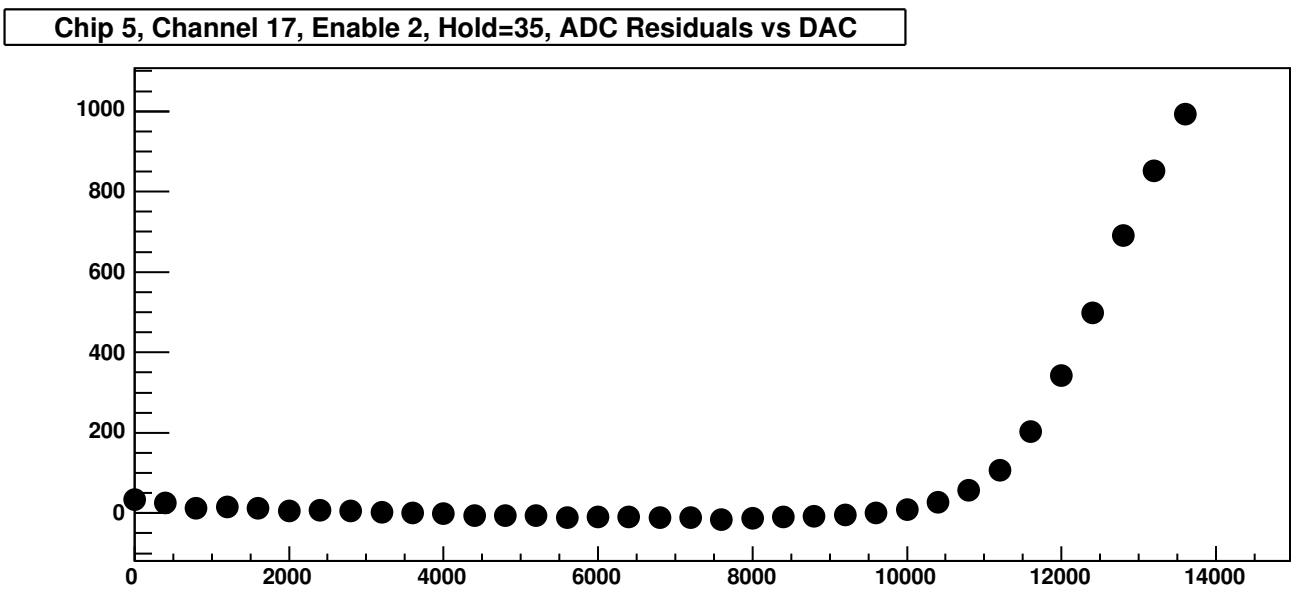

Chip 5, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 37.78 / 23
p0 -451.1 ± 0.754
p1 0.02051 ± 0.0001151
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

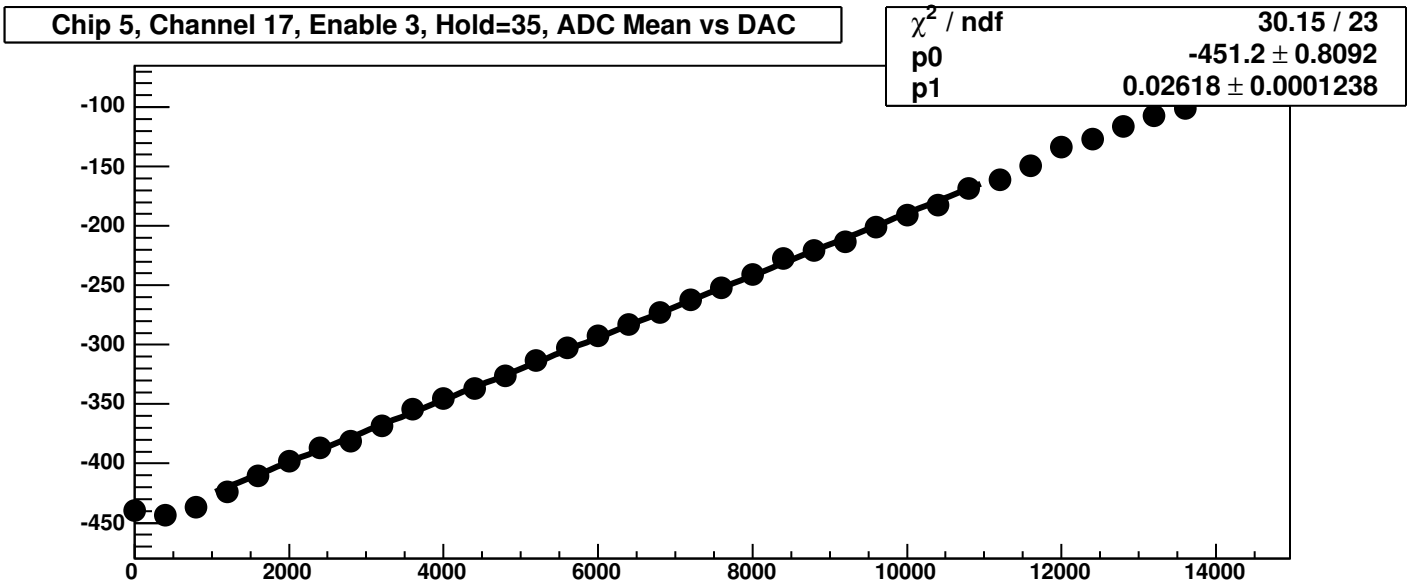

Chip 5, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

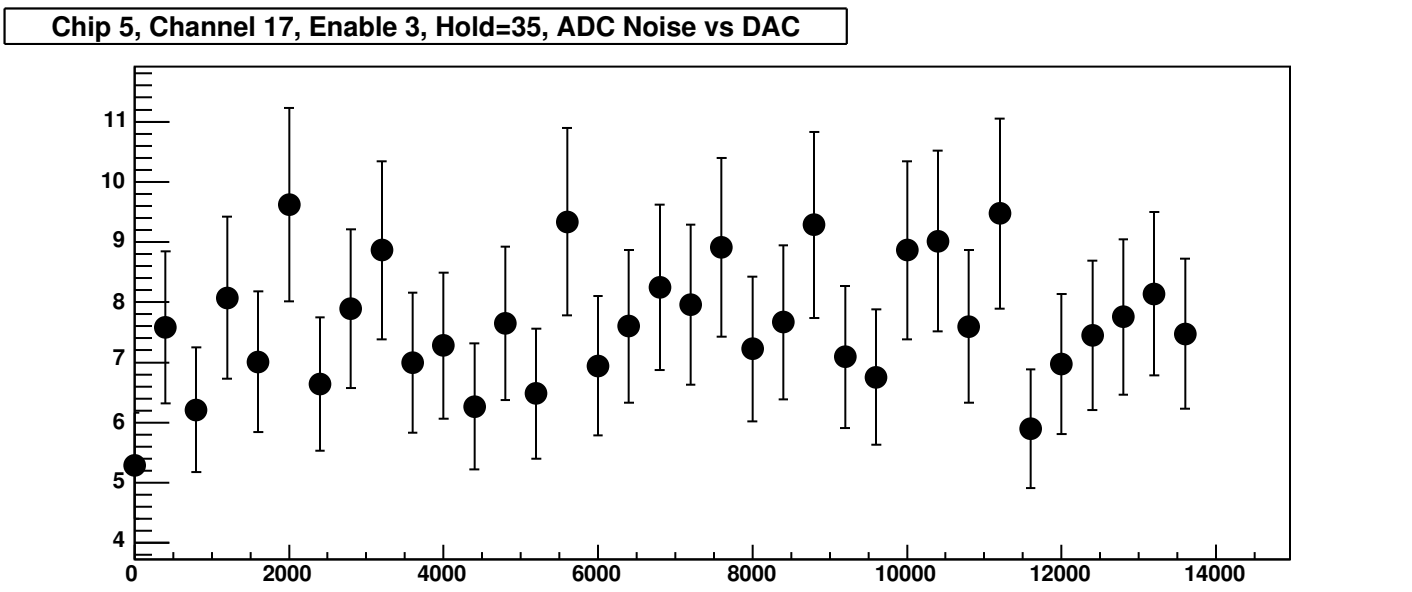

Chip 5, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
0
2000
4000
6000
8000
10000
12000
14000

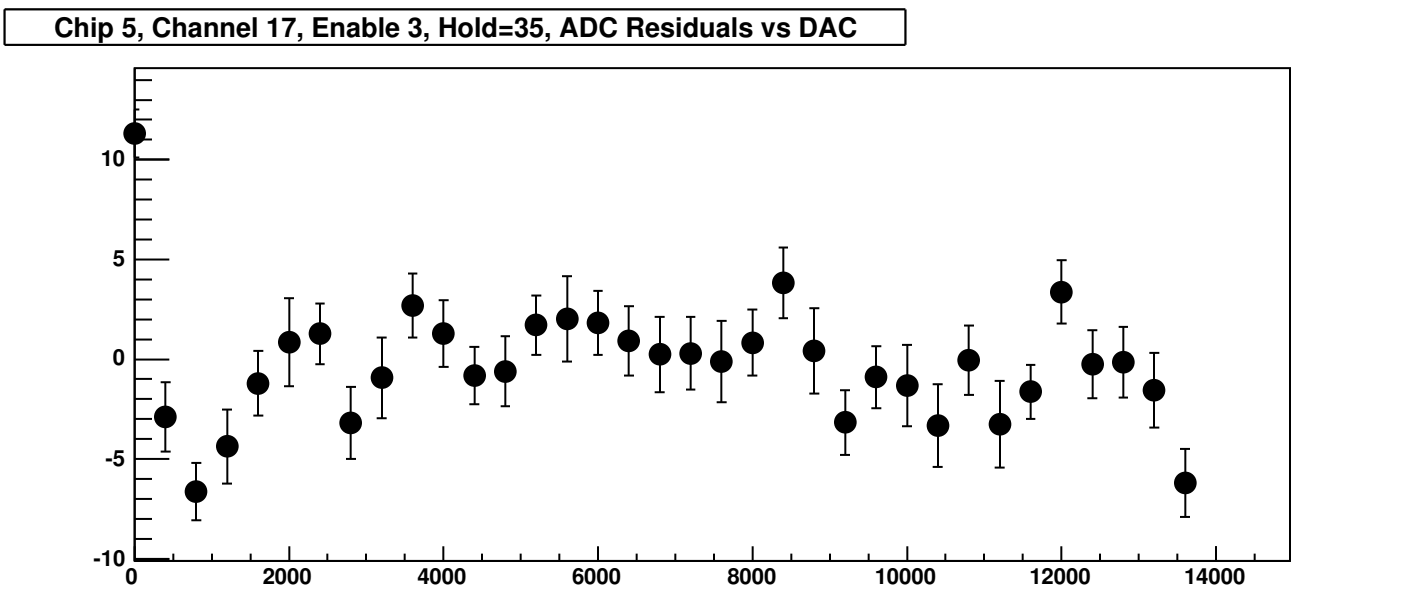

Chip 5, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1798 / 23
p0 -477 ± 0.7522
p1 0.04192 ± 0.0001145
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

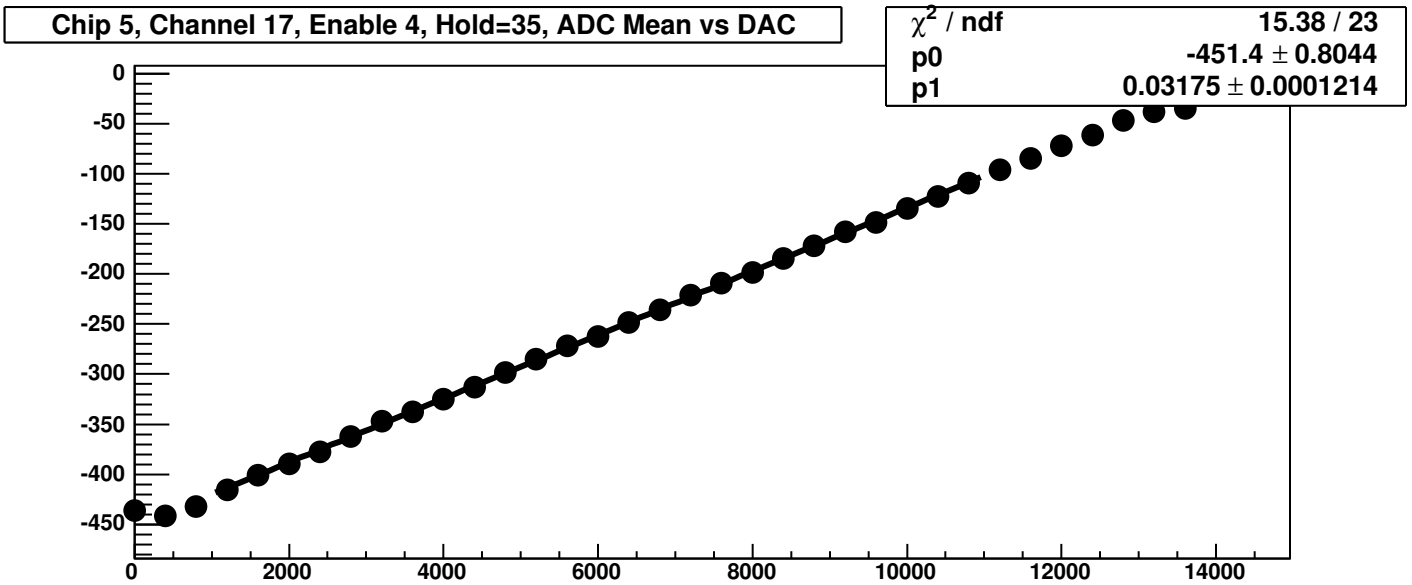

Chip 5, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

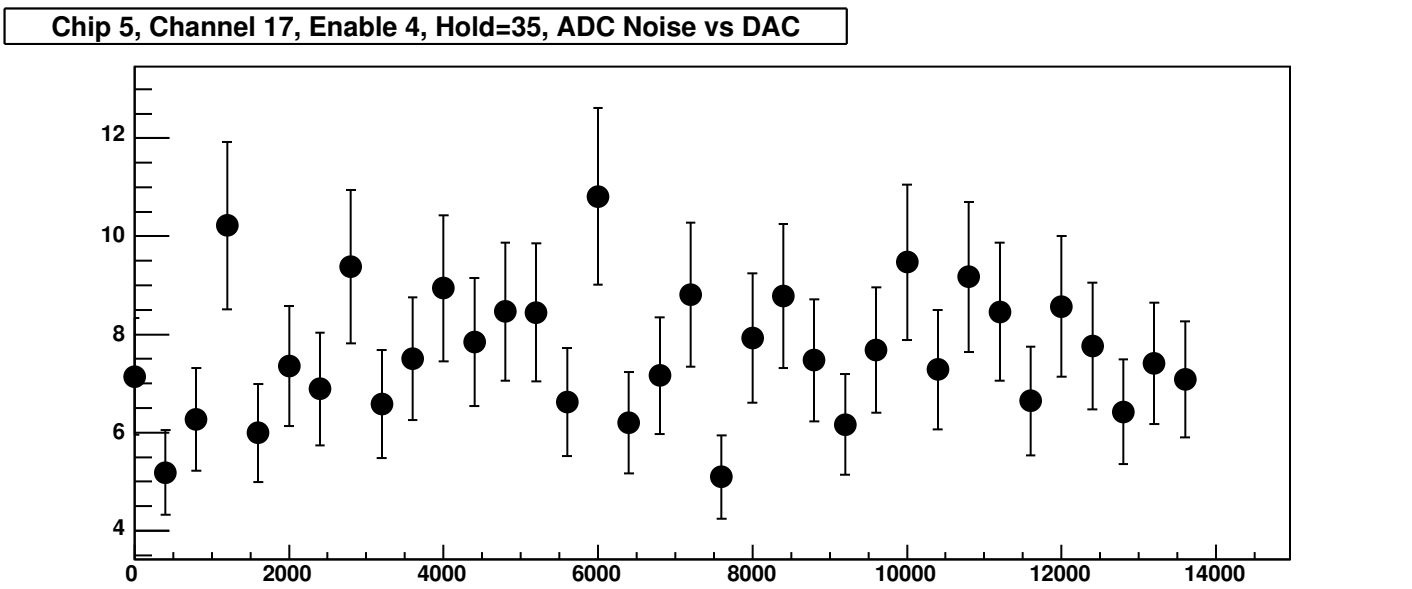

Chip 5, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

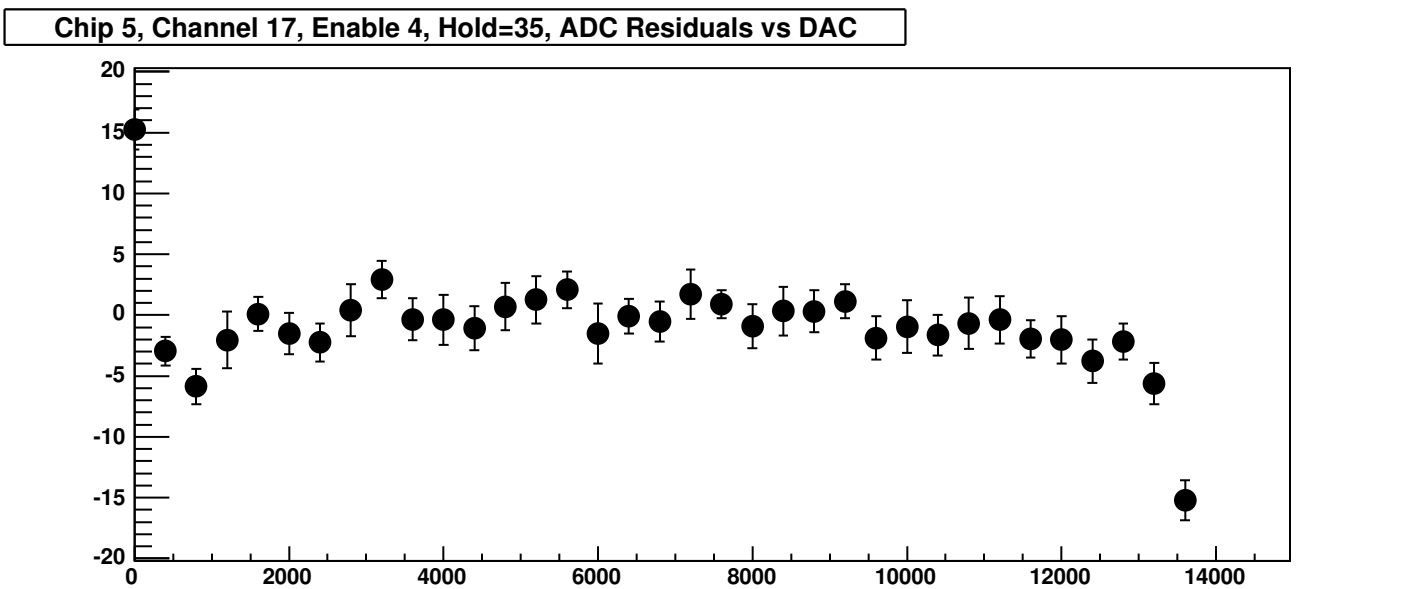

Chip 5, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 30.15 / 23
p0 -451.2 ± 0.8092
p1 0.02618 ± 0.0001238
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

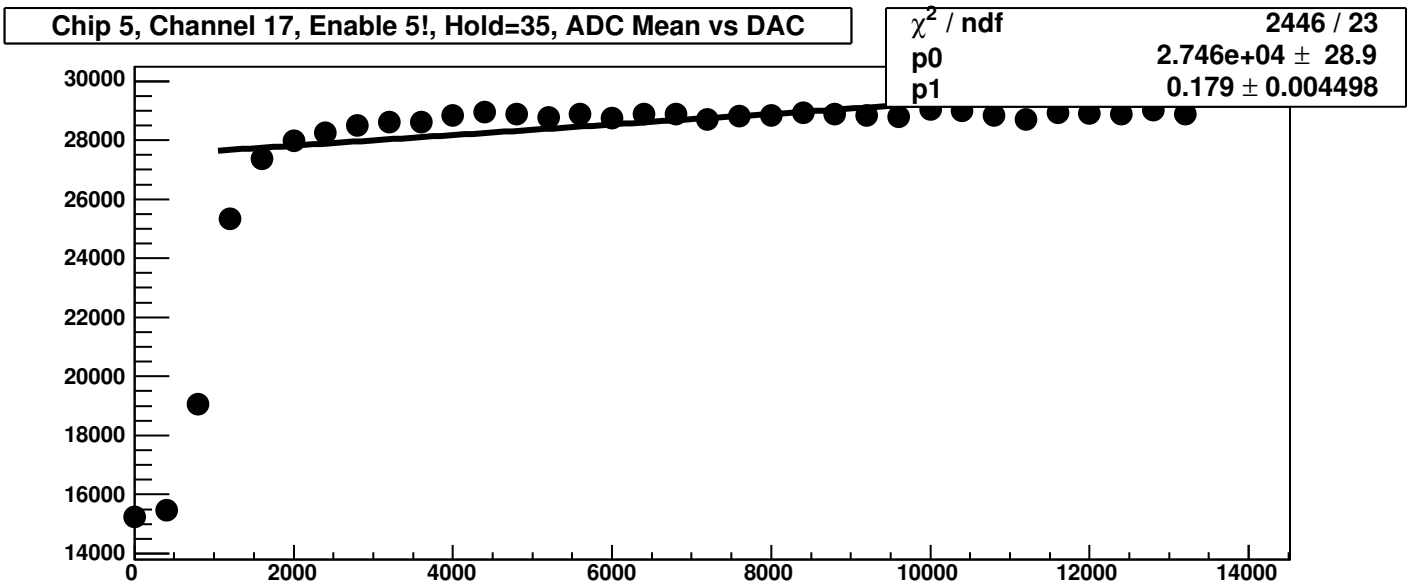

Chip 5, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

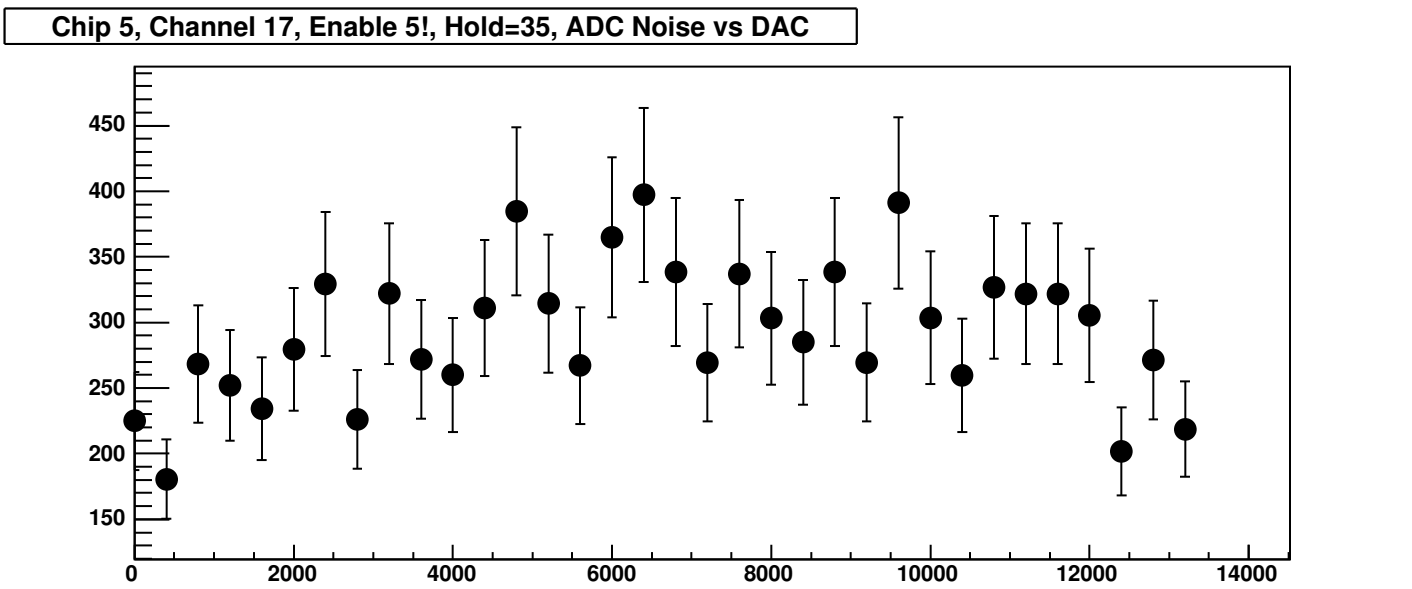

Chip 5, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

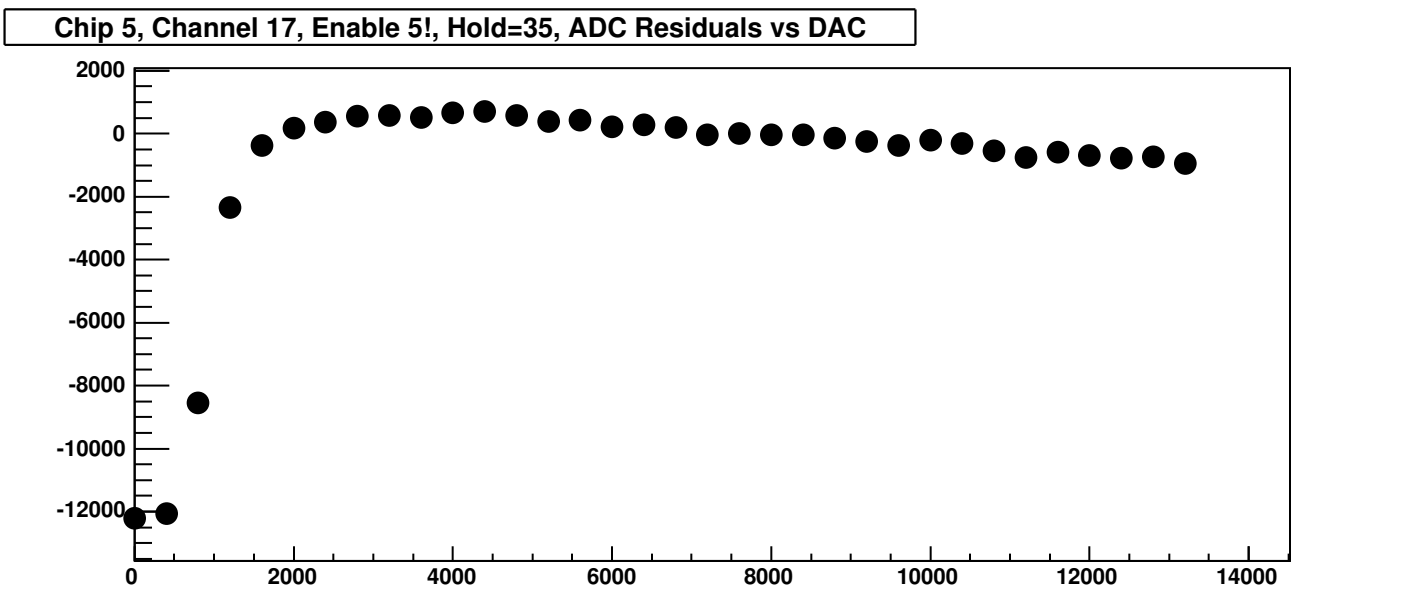

Chip 5, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 15.38 / 23
p0 -451.4 ± 0.8044
p1 0.03175 ± 0.0001214
0
-50
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

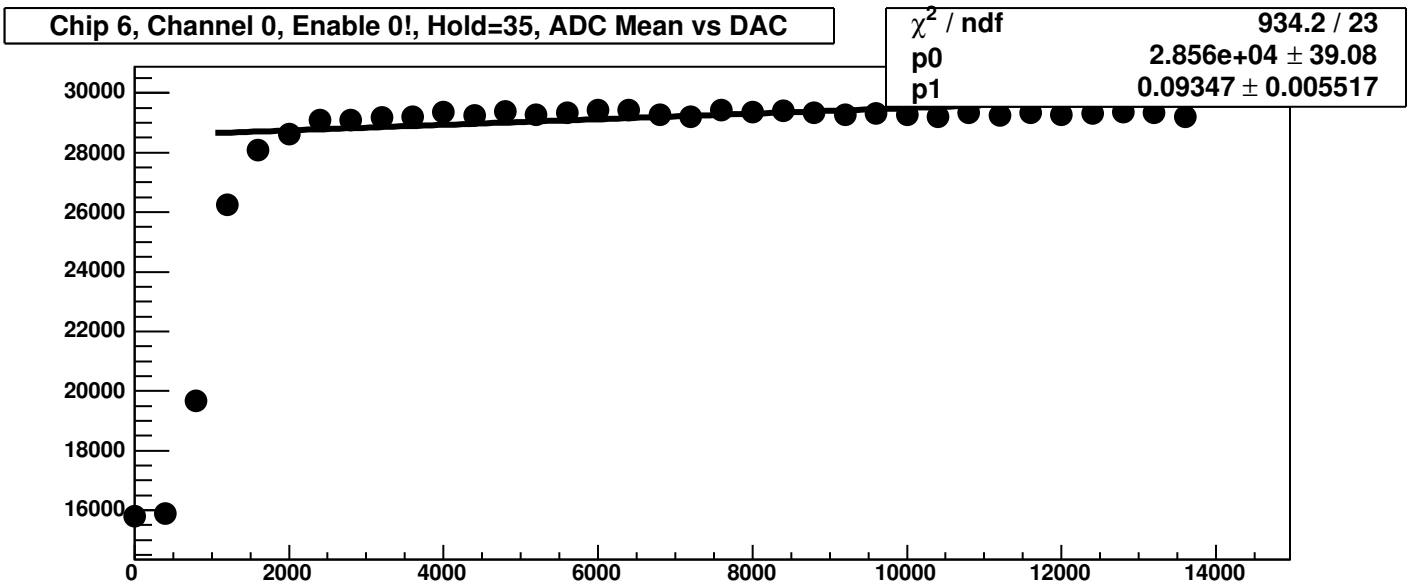

Chip 5, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

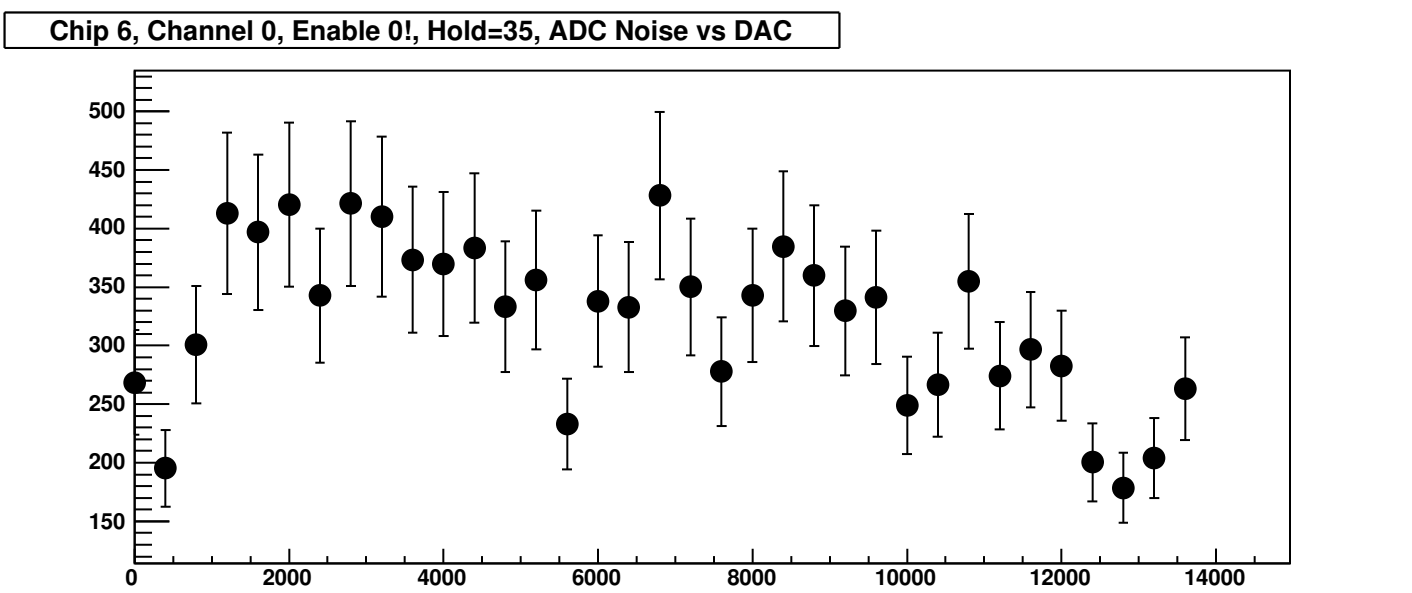

Chip 5, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

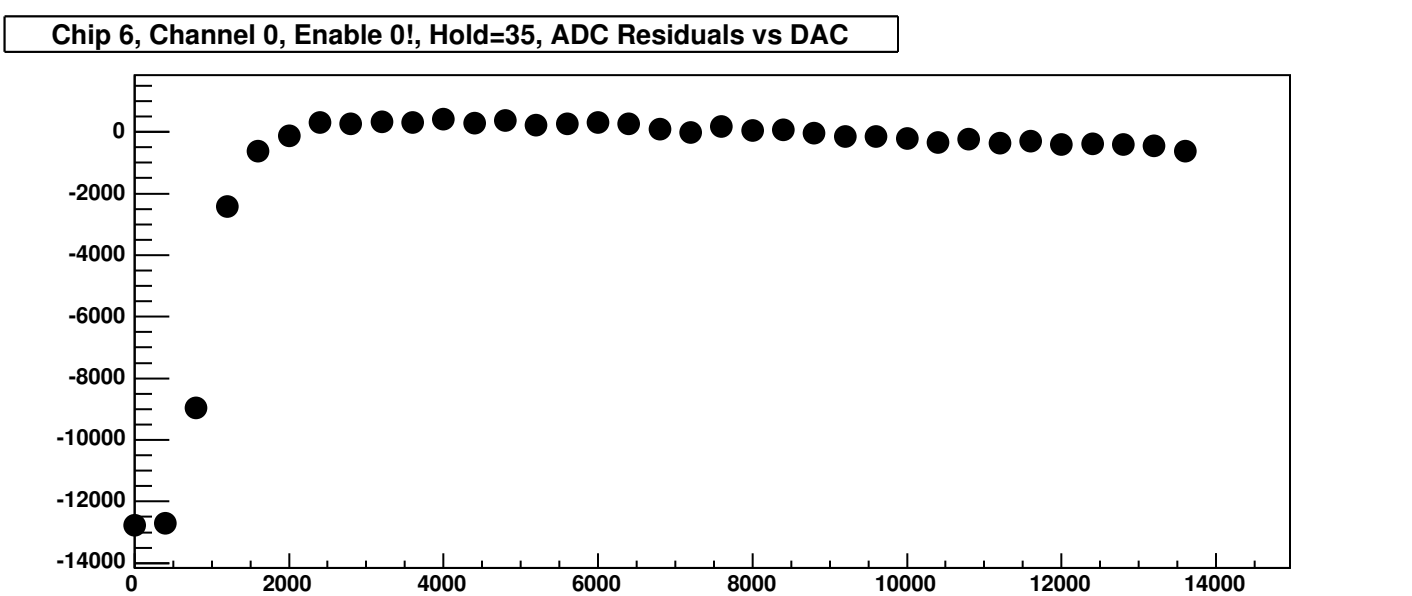

Chip 5, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2446 / 23
p0 2.746e+04 ± 28.9
p1 0.179 ± 0.004498
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

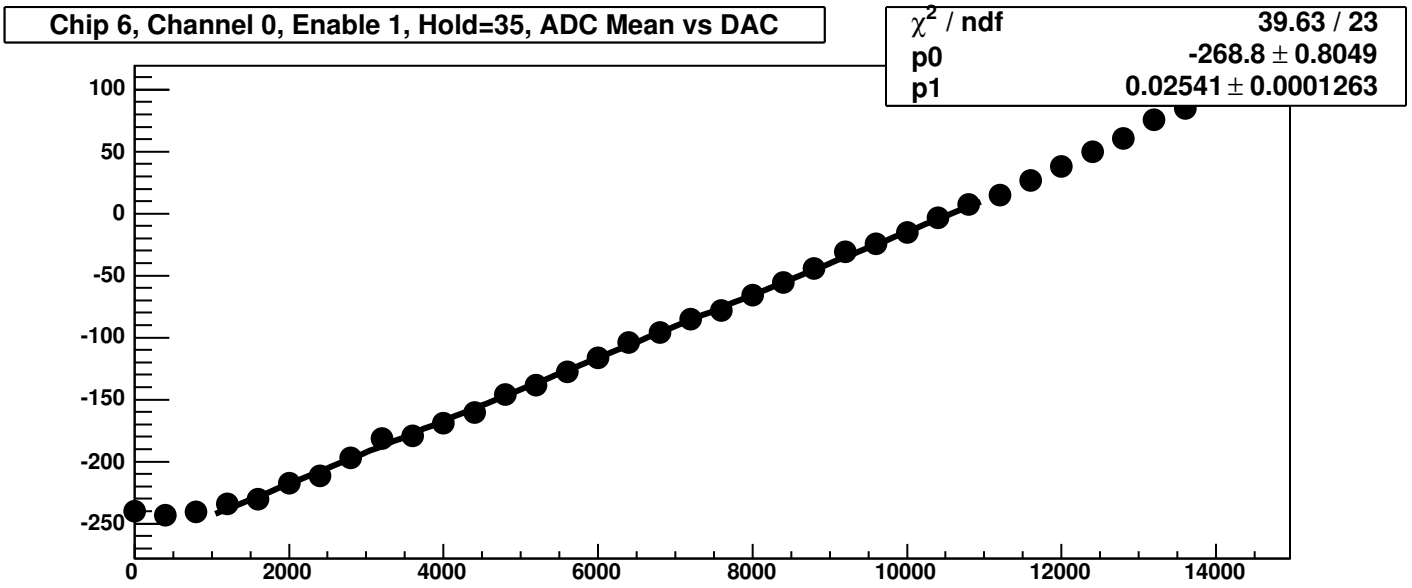

Chip 5, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

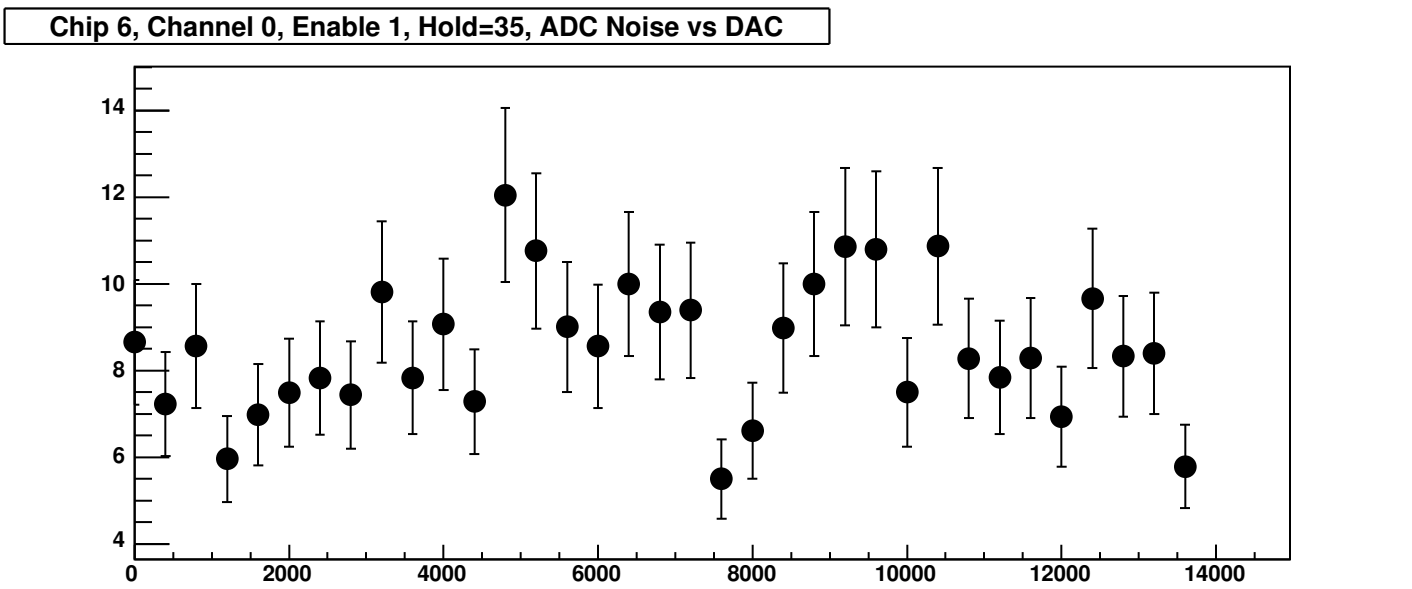

Chip 5, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
0
2000
4000
6000
8000
10000
12000
14000

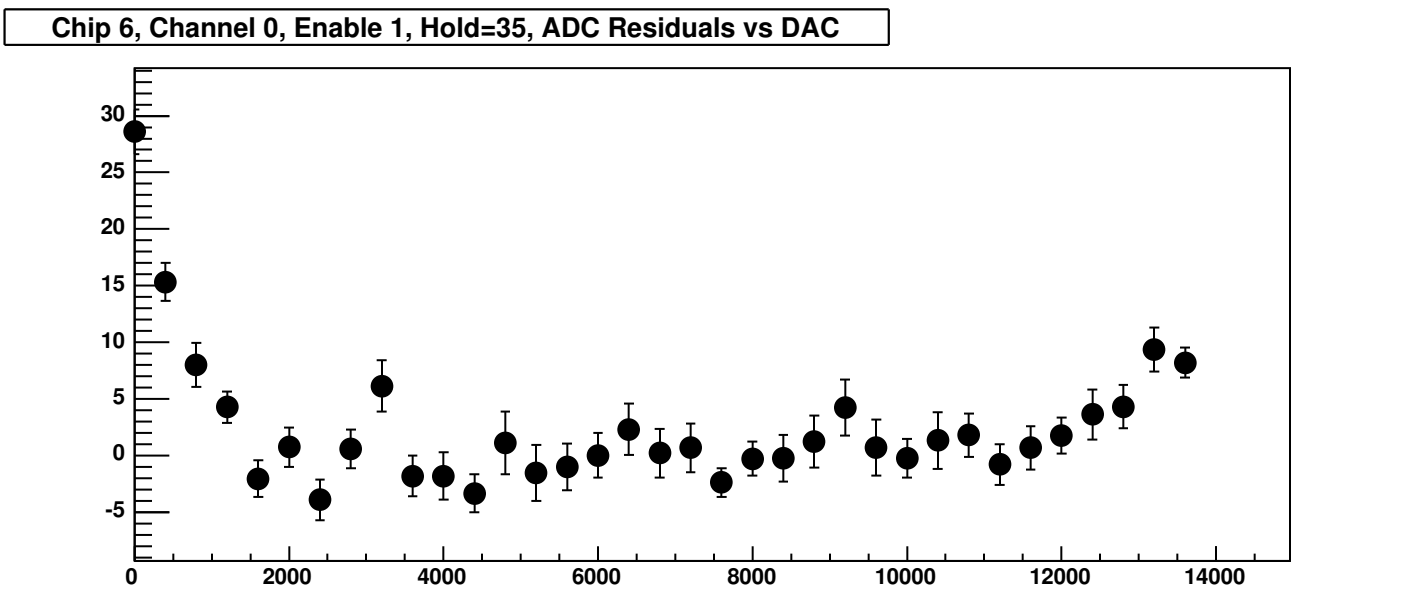

Chip 6, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 934.2 / 23
p0 2.856e+04 ± 39.08
p1 0.09347 ± 0.005517
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

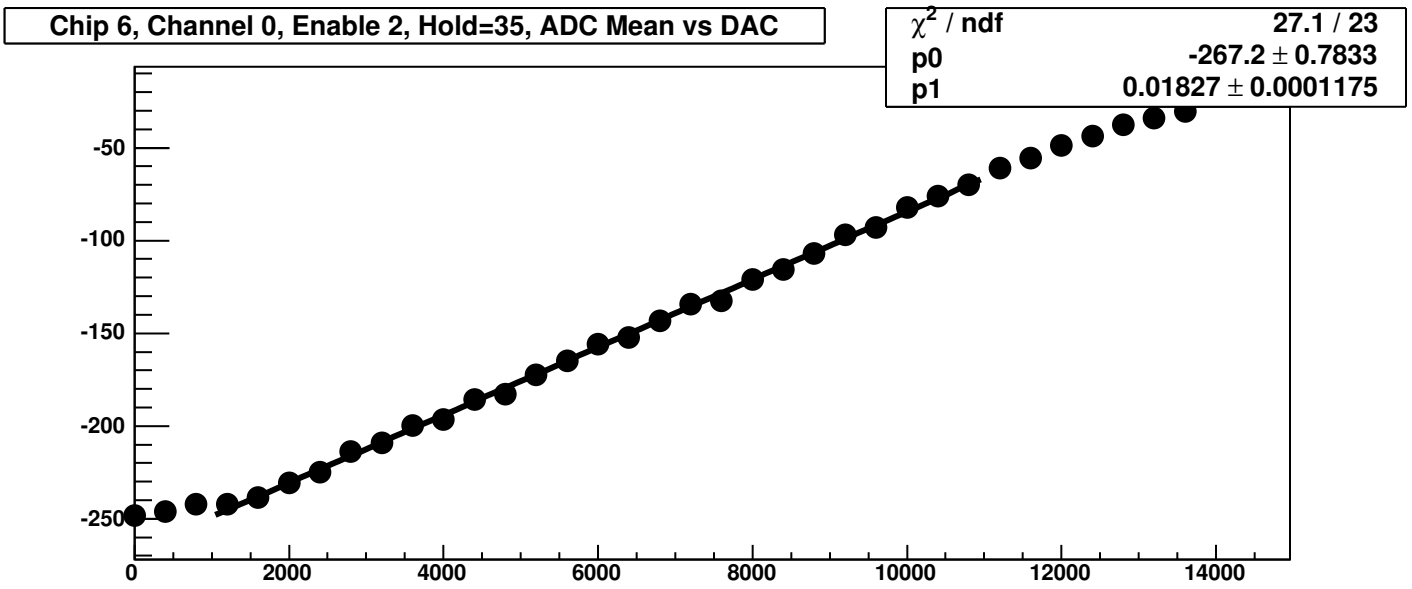

Chip 6, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
500
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

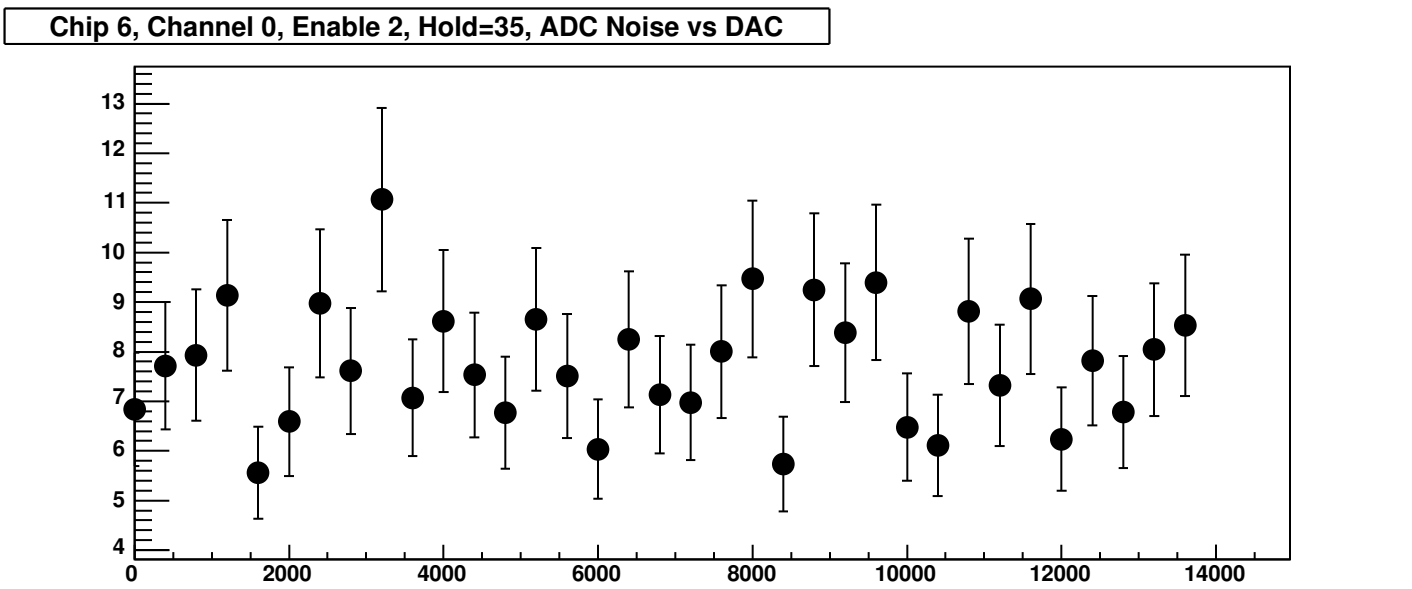

Chip 6, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

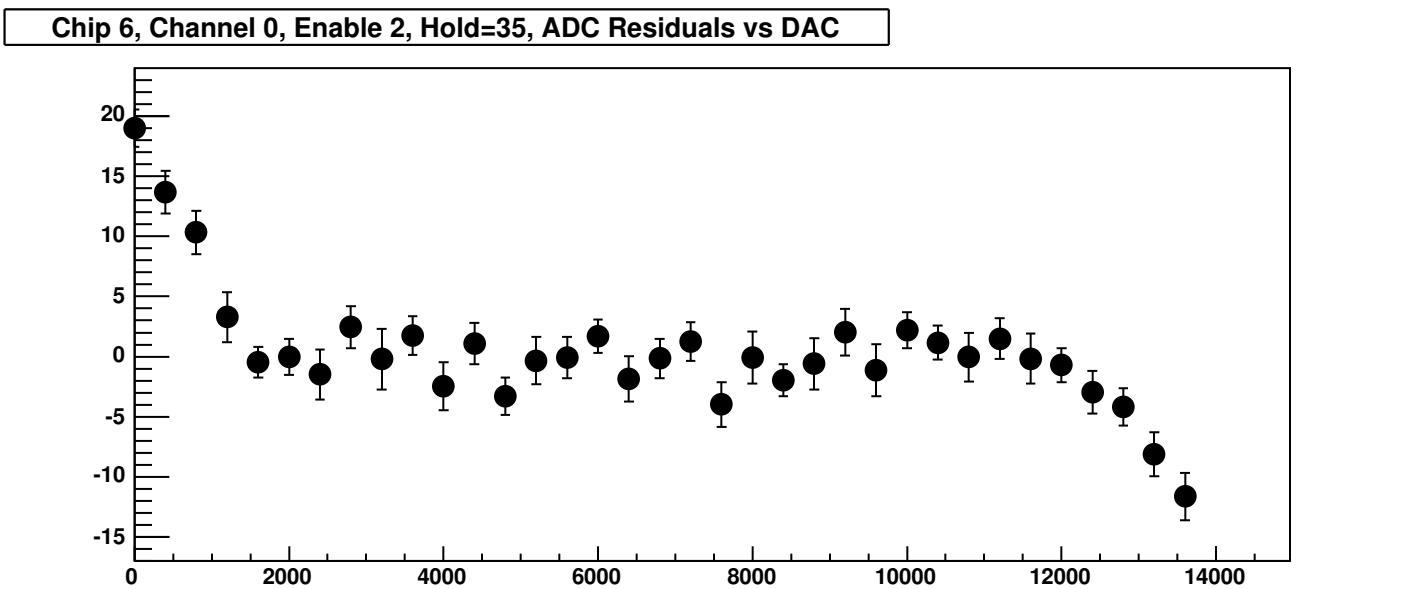

Chip 6, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 39.63 / 23
p0 -268.8 ± 0.8049
p1 0.02541 ± 0.0001263
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

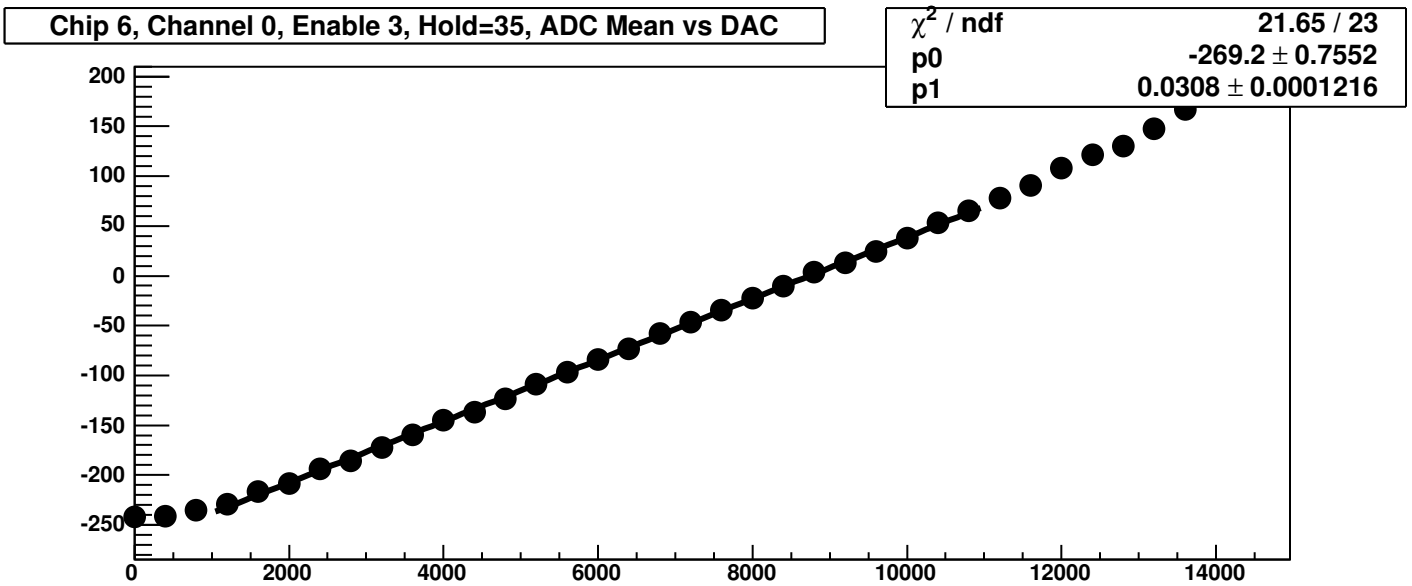

Chip 6, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

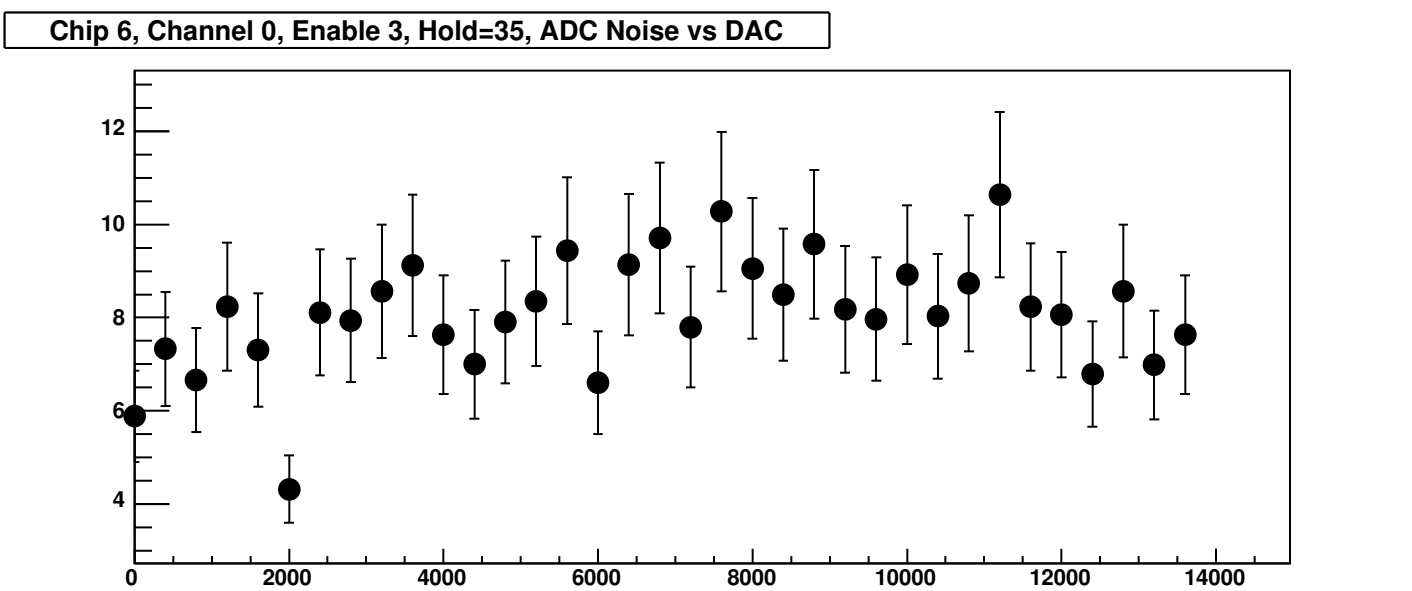

Chip 6, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

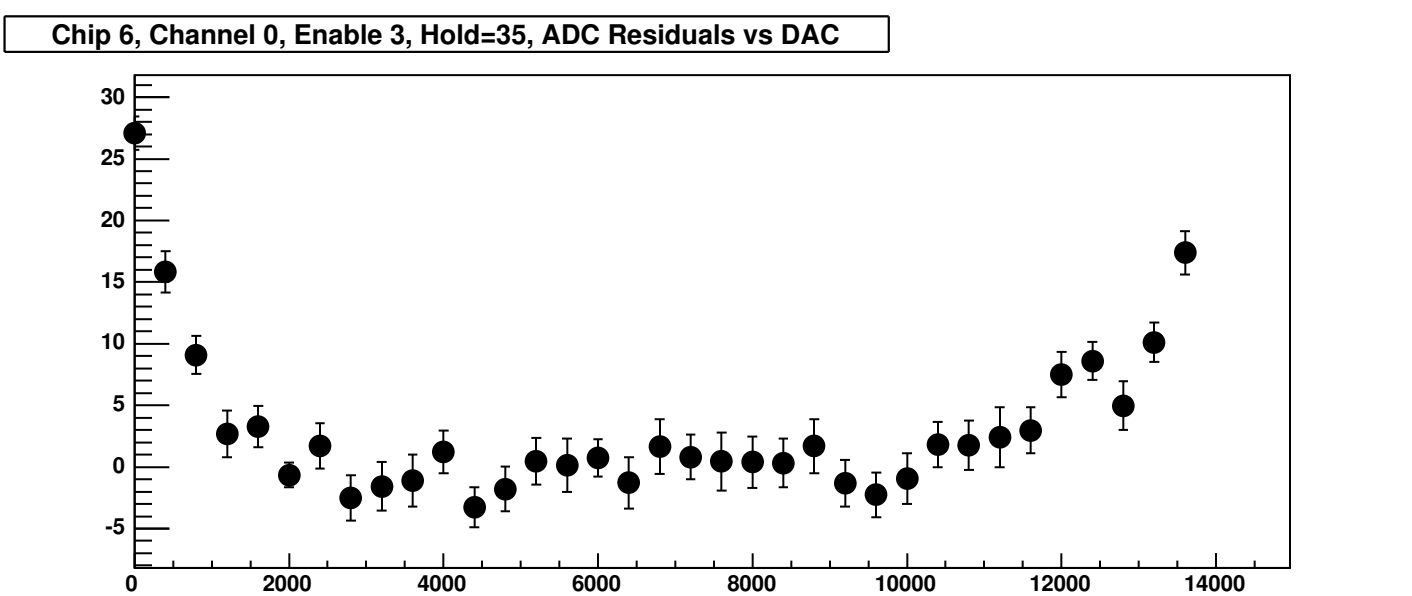

Chip 6, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 27.1 / 23
p0 -267.2 ± 0.7833
p1 0.01827 ± 0.0001175
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

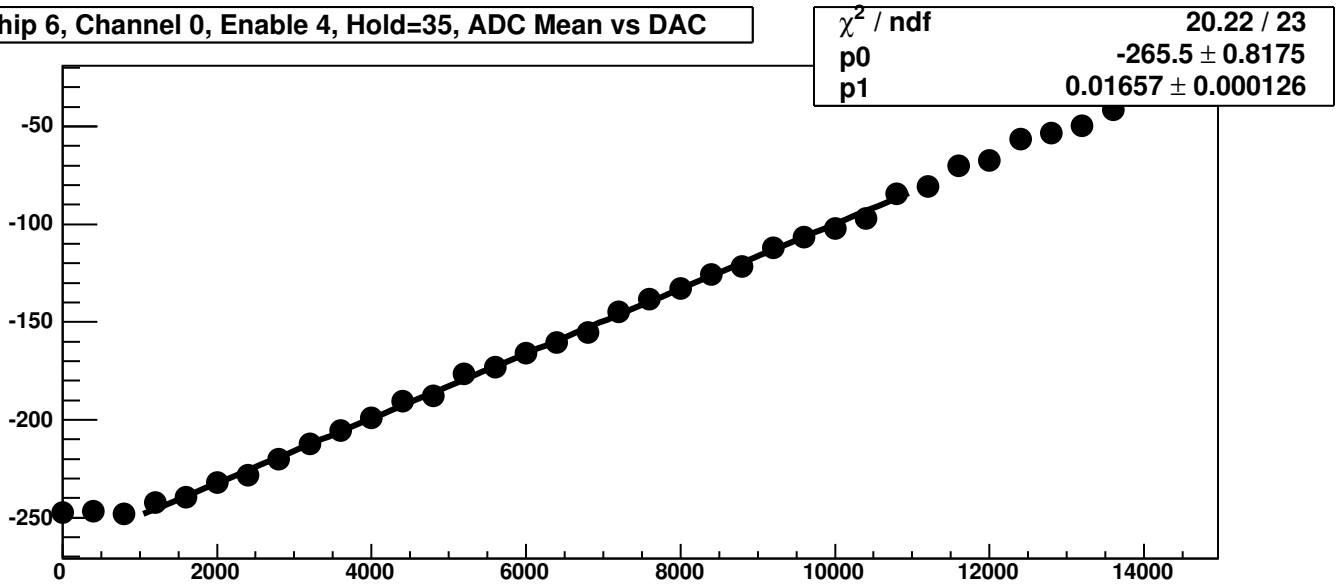

Chip 6, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.65 / 23
p0 -269.2 ± 0.7552
p1 0.0308 ± 0.0001216
200
150
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

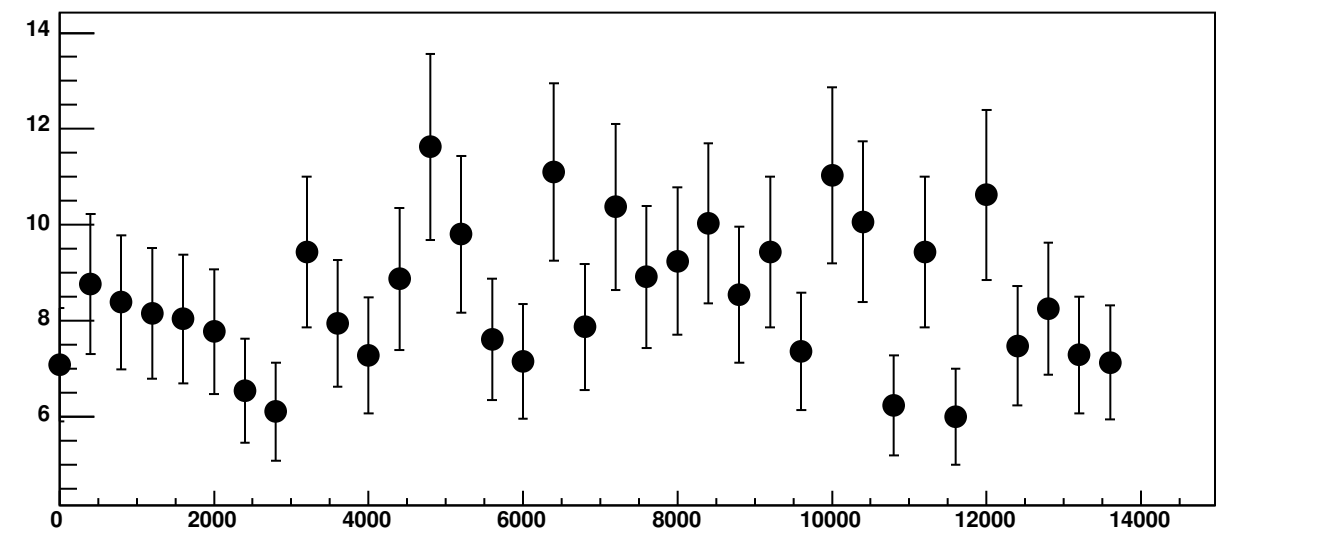

Chip 6, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

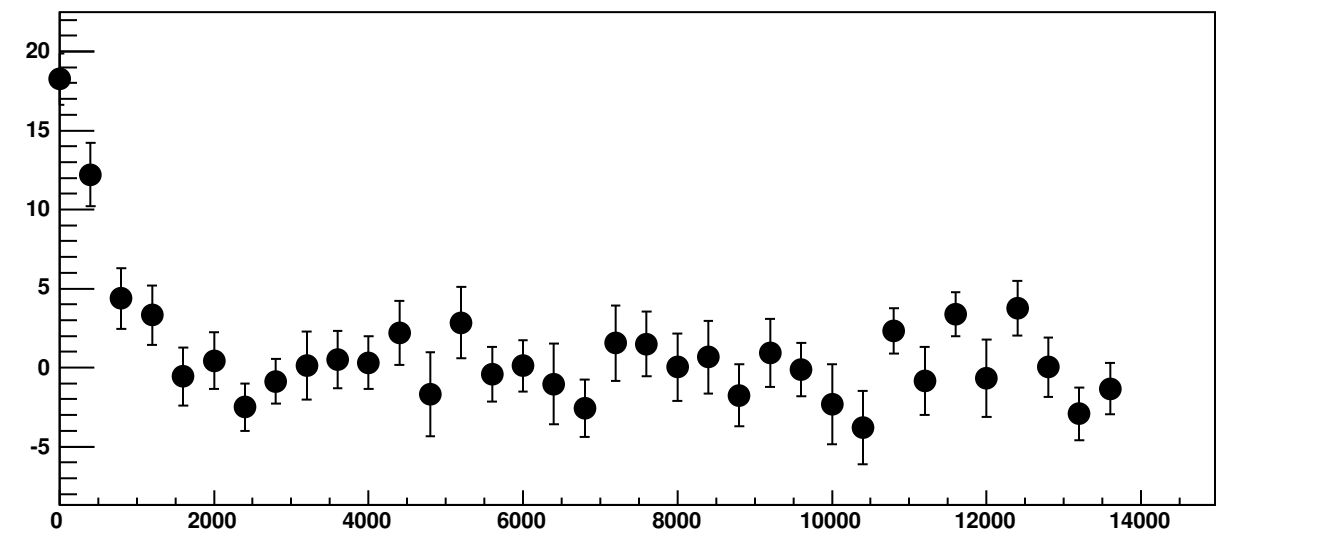
Chip 6, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

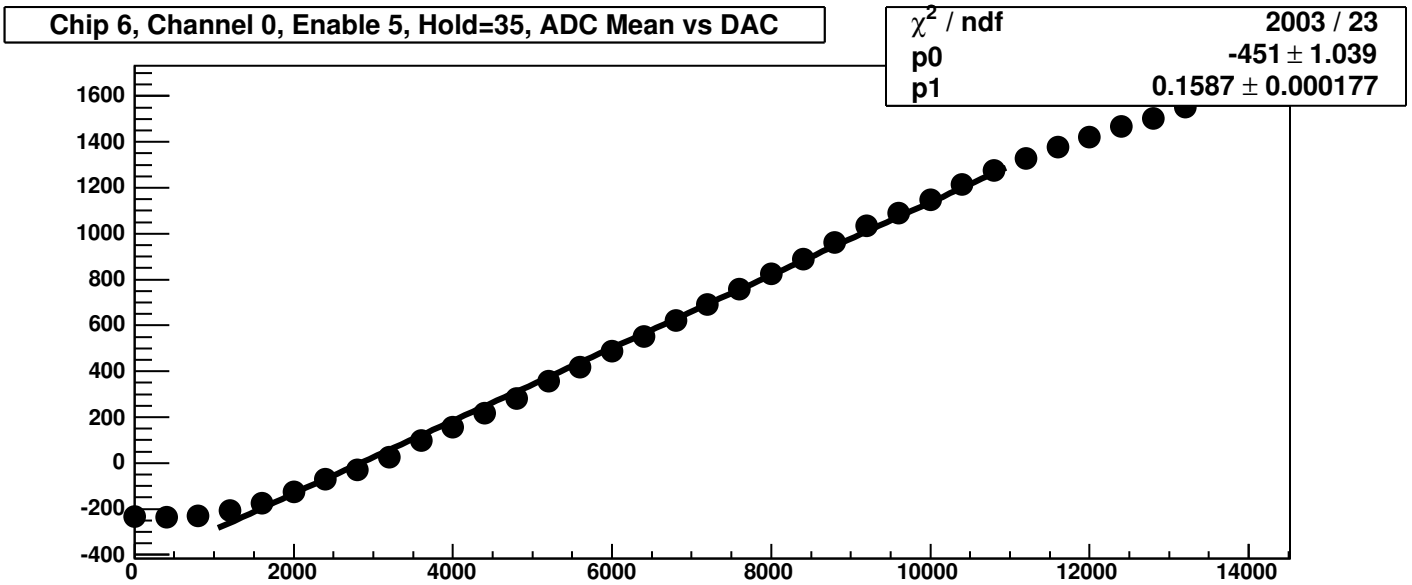


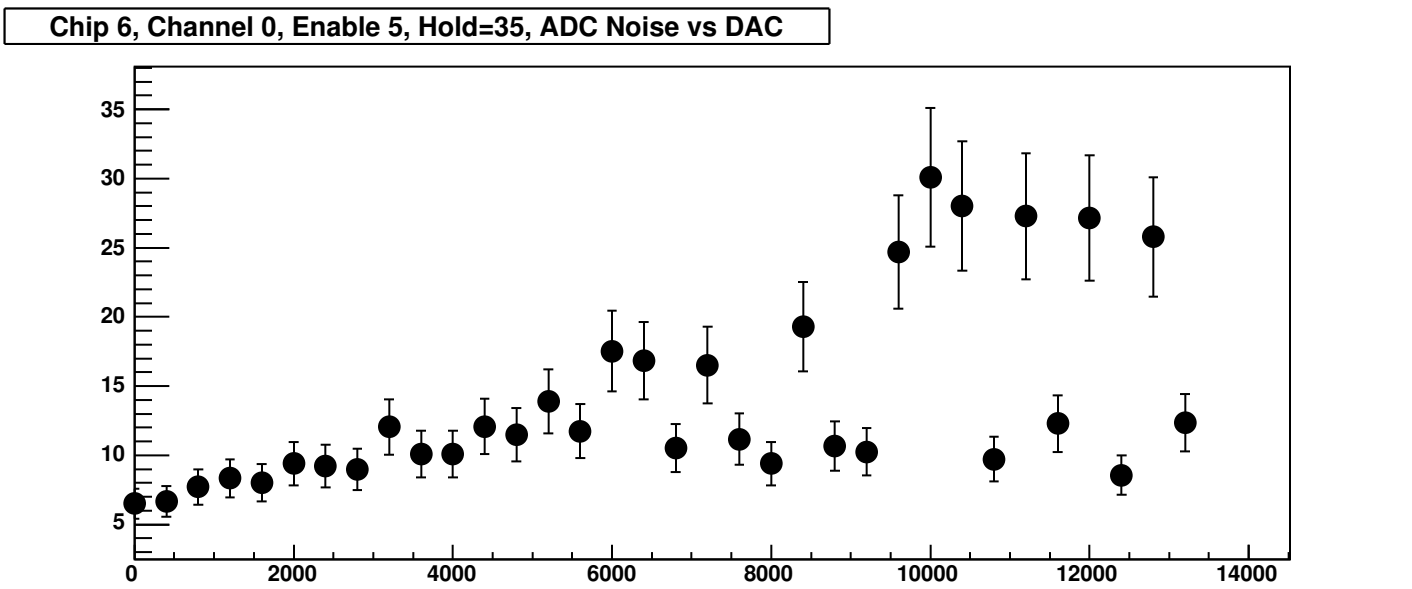
Chip 6, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

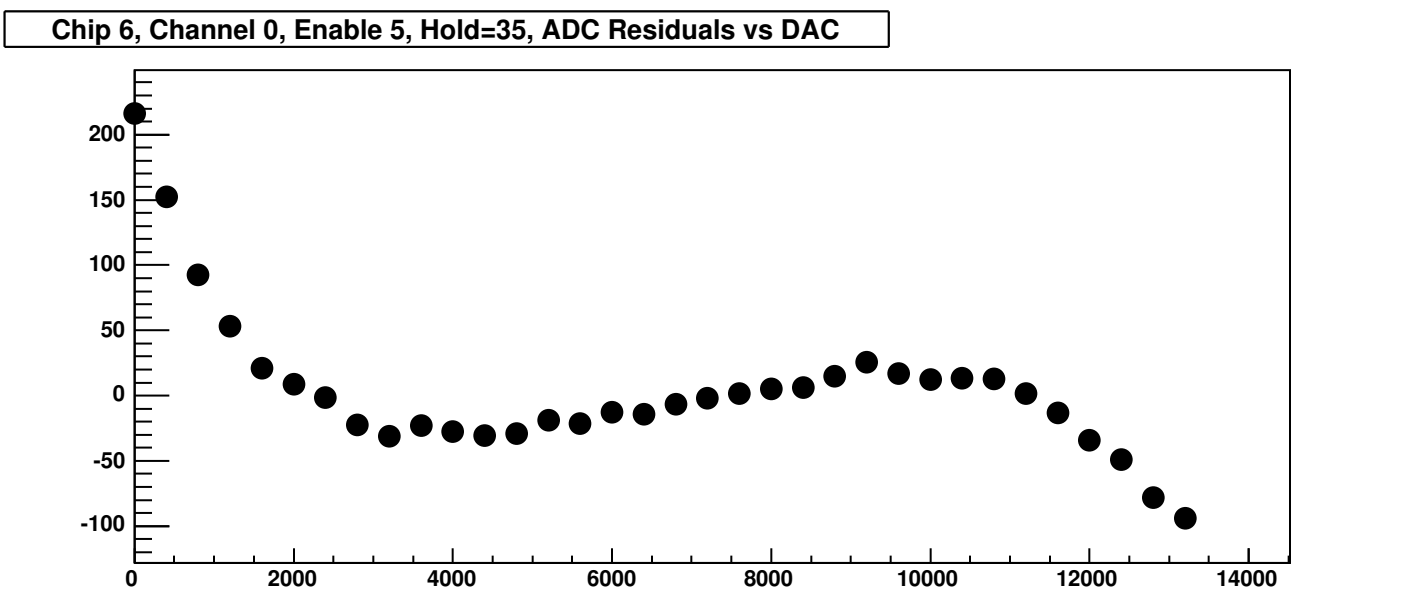


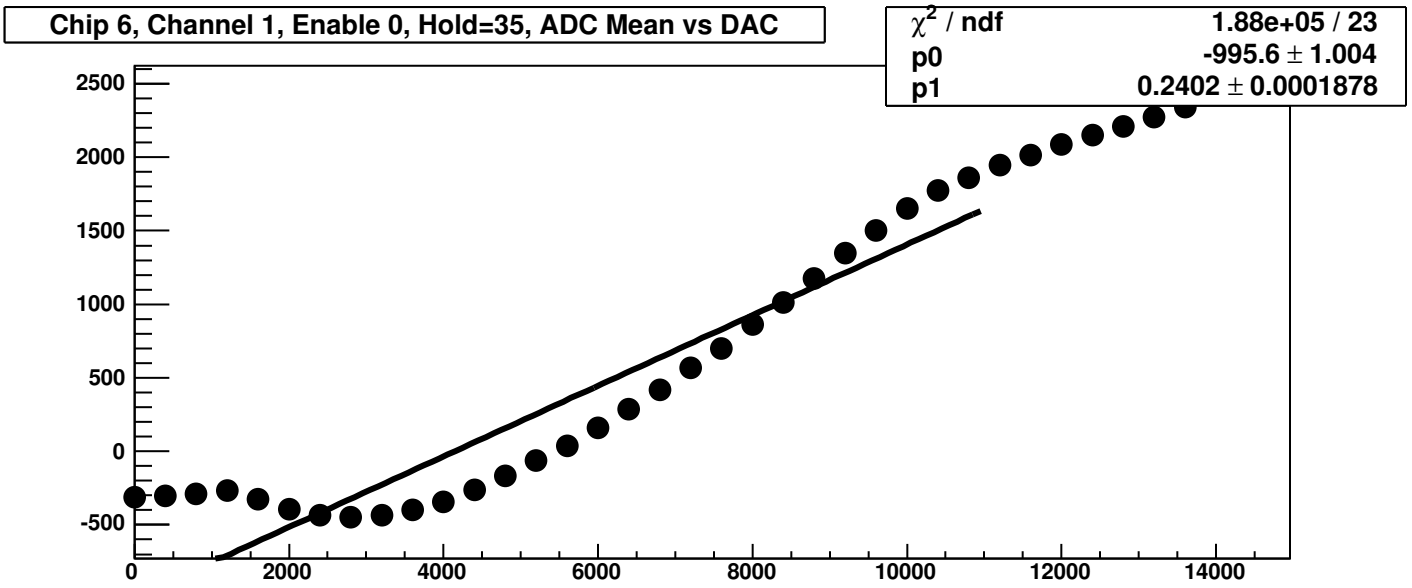
Chip 6, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

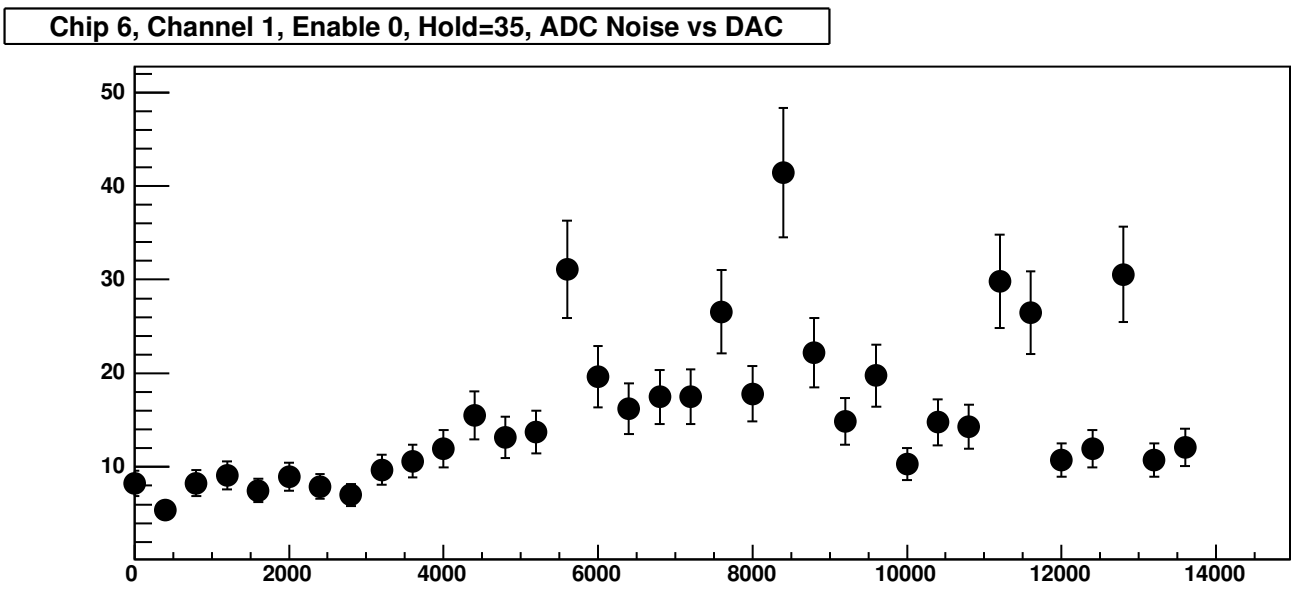


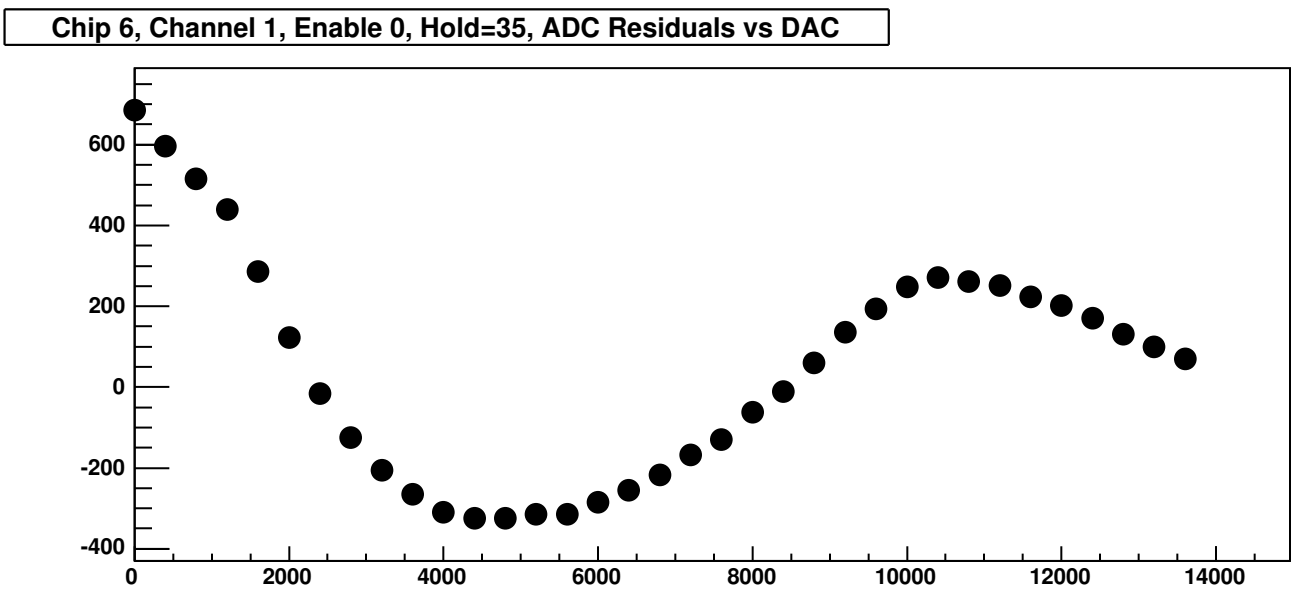

Chip 6, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2003 / 23
p0 -451 ± 1.039
p1 0.1587 ± 0.000177
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

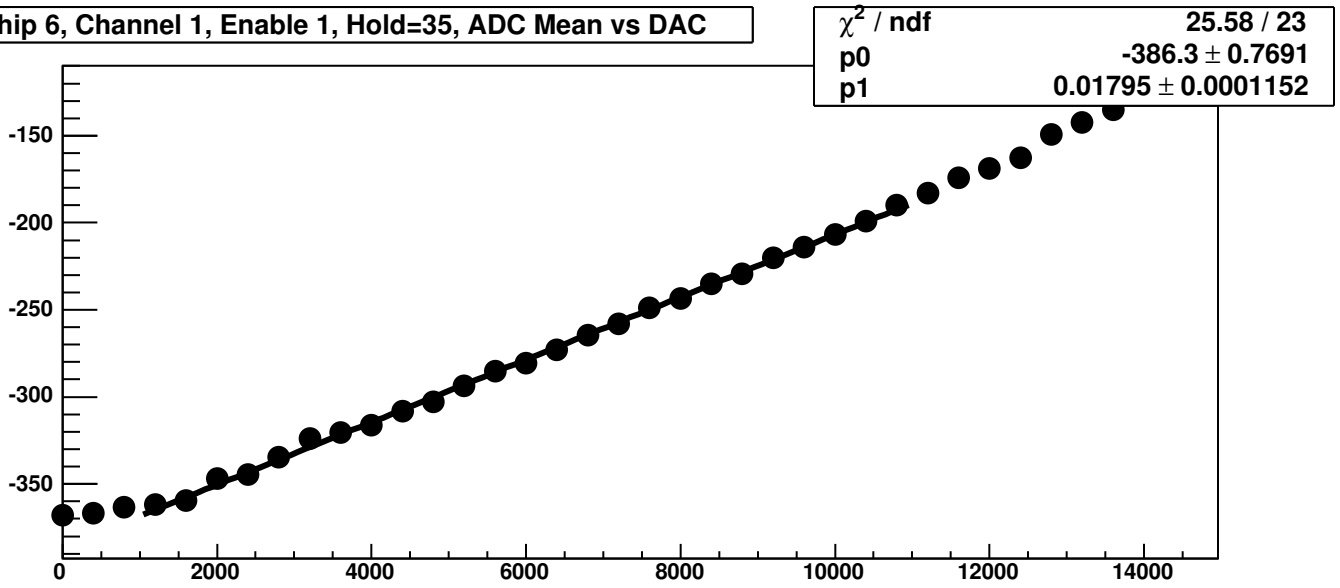

Chip 6, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.88e+05 / 23
p0 -995.6 ± 1.004
p1 0.2402 ± 0.0001878
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

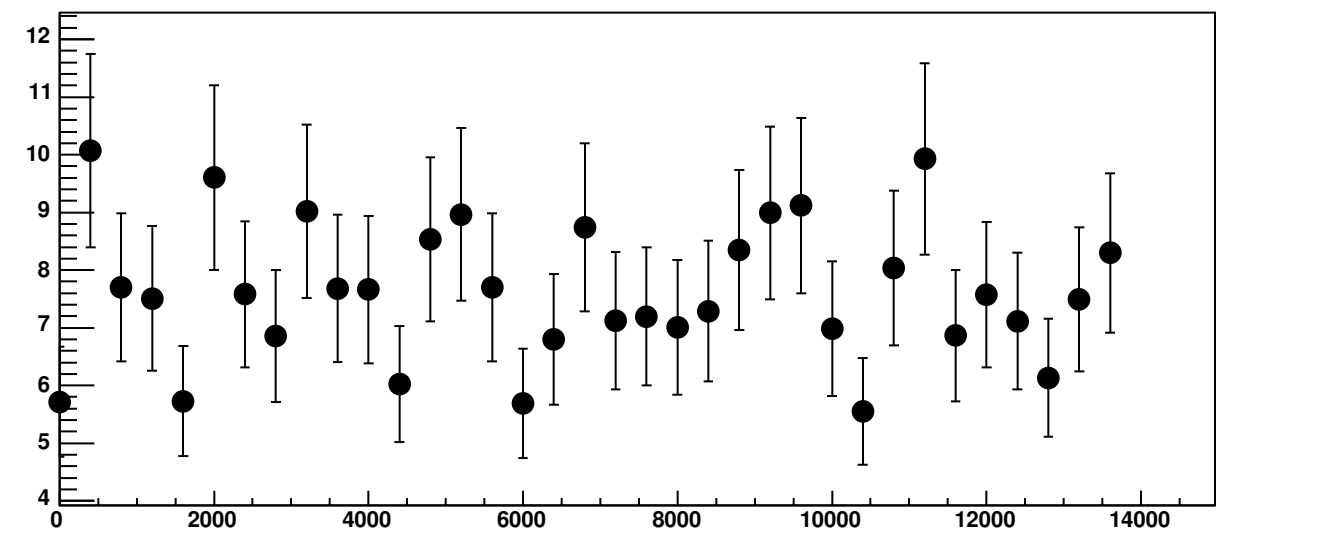

Chip 6, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

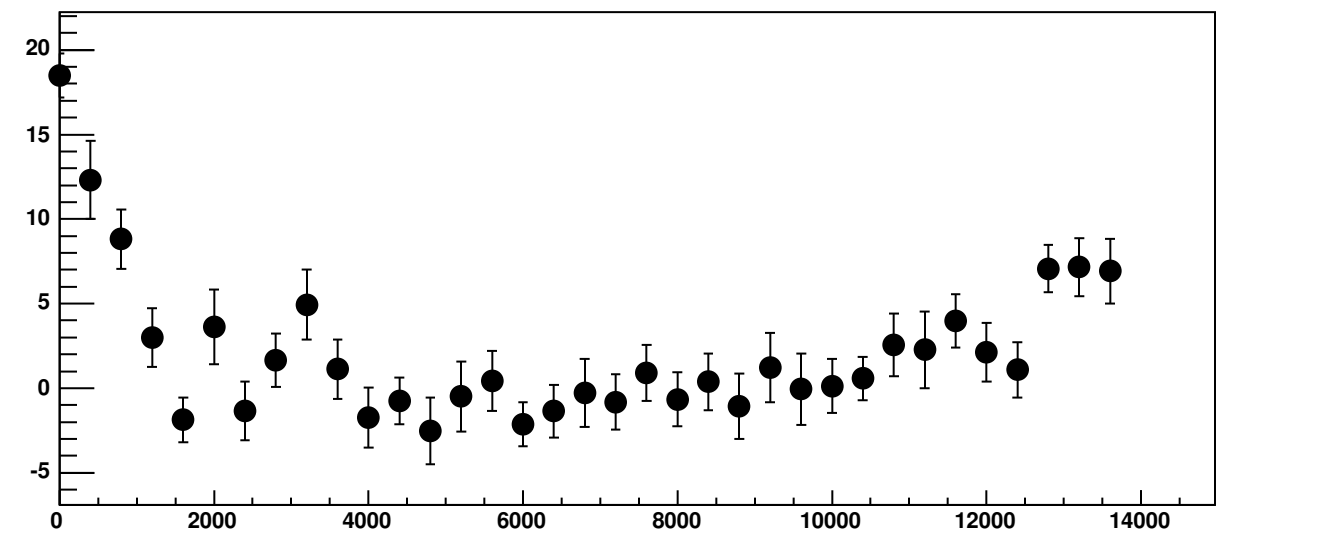
Chip 6, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

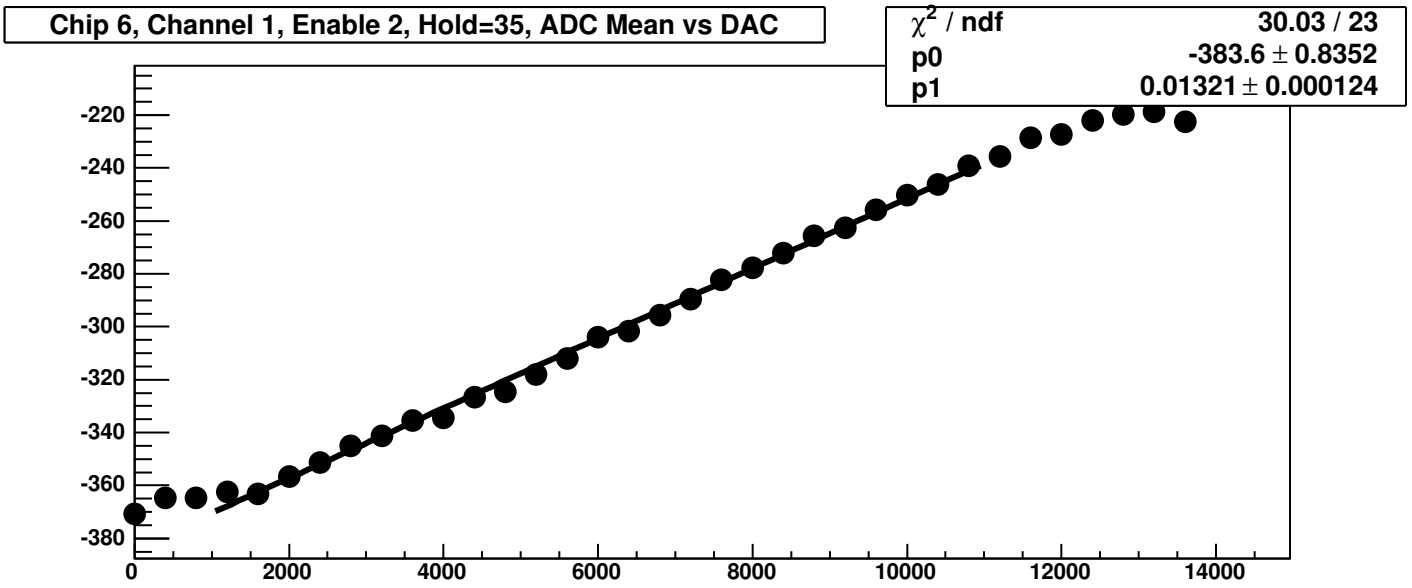


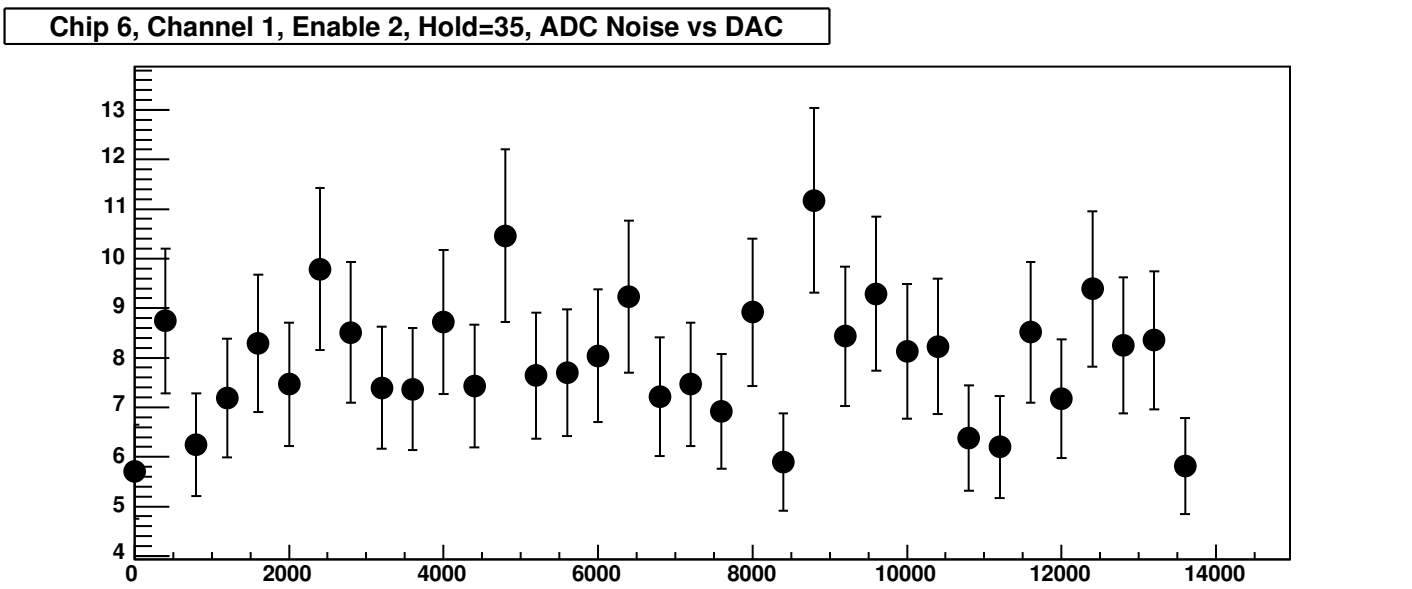
Chip 6, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC

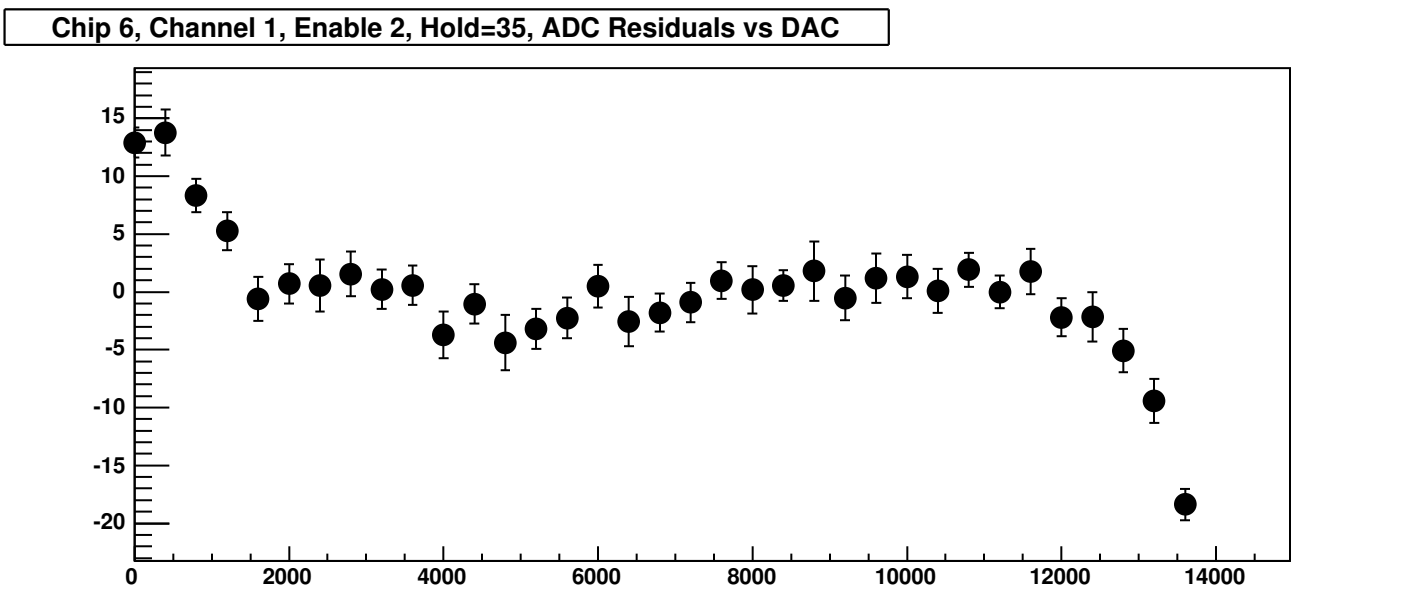


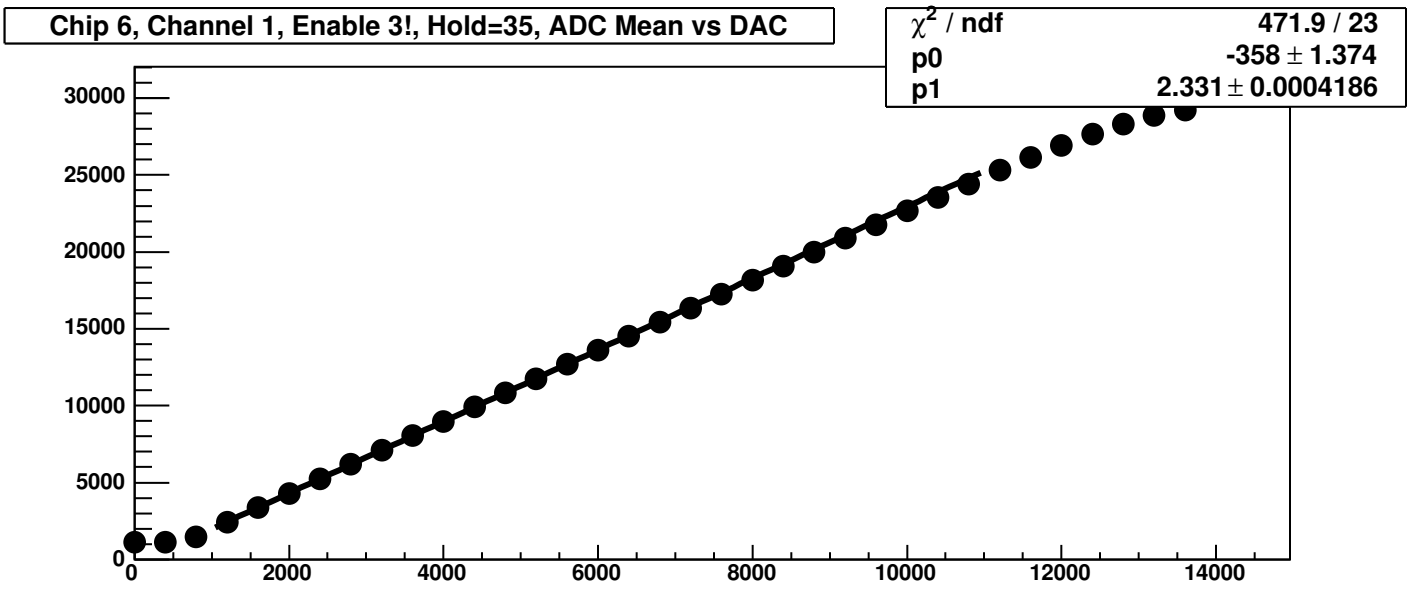
Chip 6, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

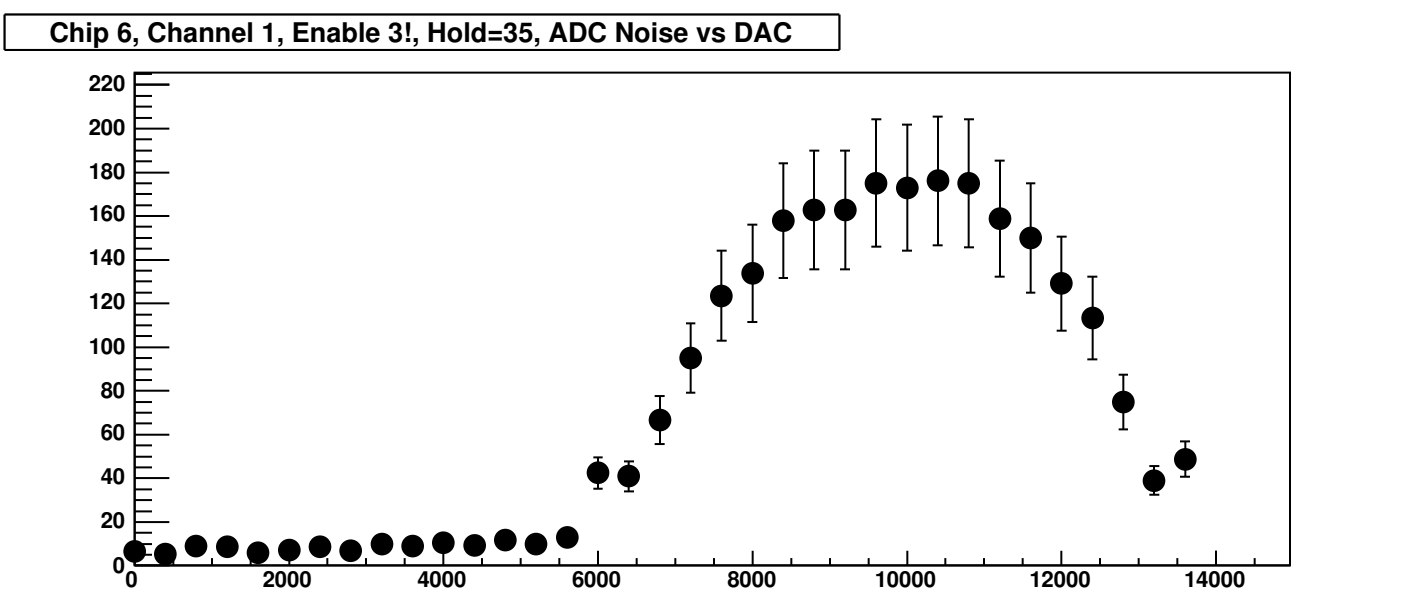


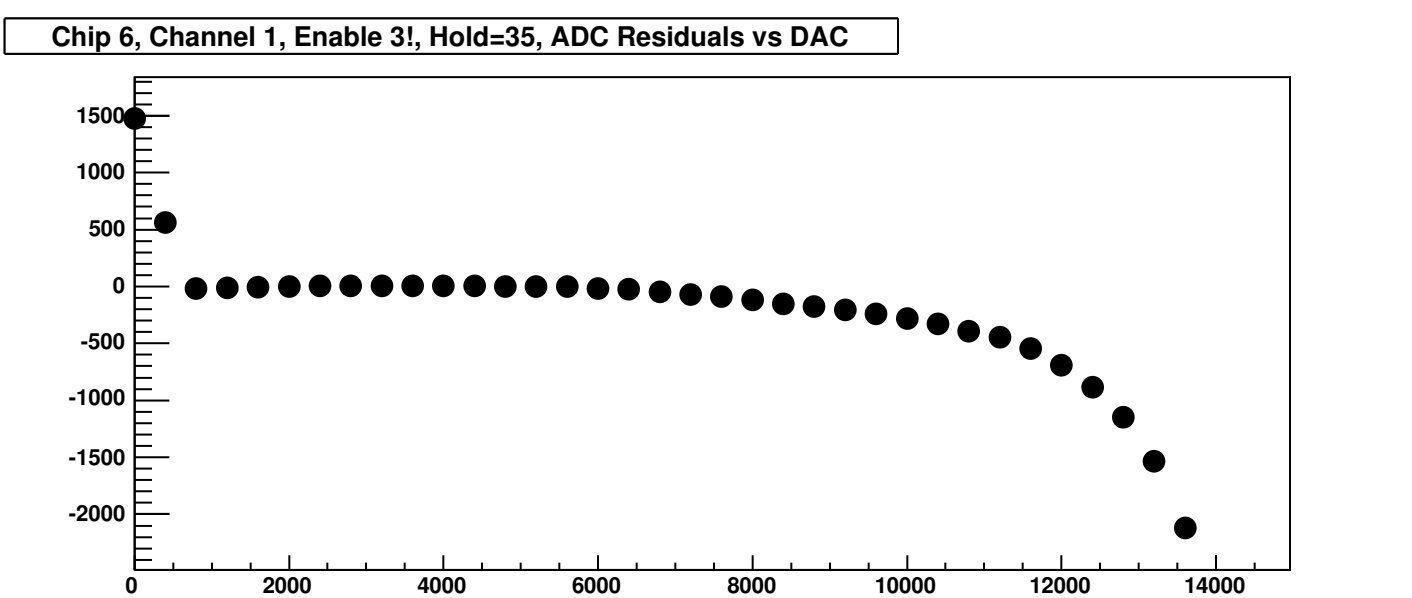

Chip 6, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 30.03 / 23
p0 -383.6 ± 0.8352
p1 0.01321 ± 0.000124
-220
-240
-260
-280
-300
-320
-340
-360
-380
0
2000
4000
6000
8000
10000
12000
14000

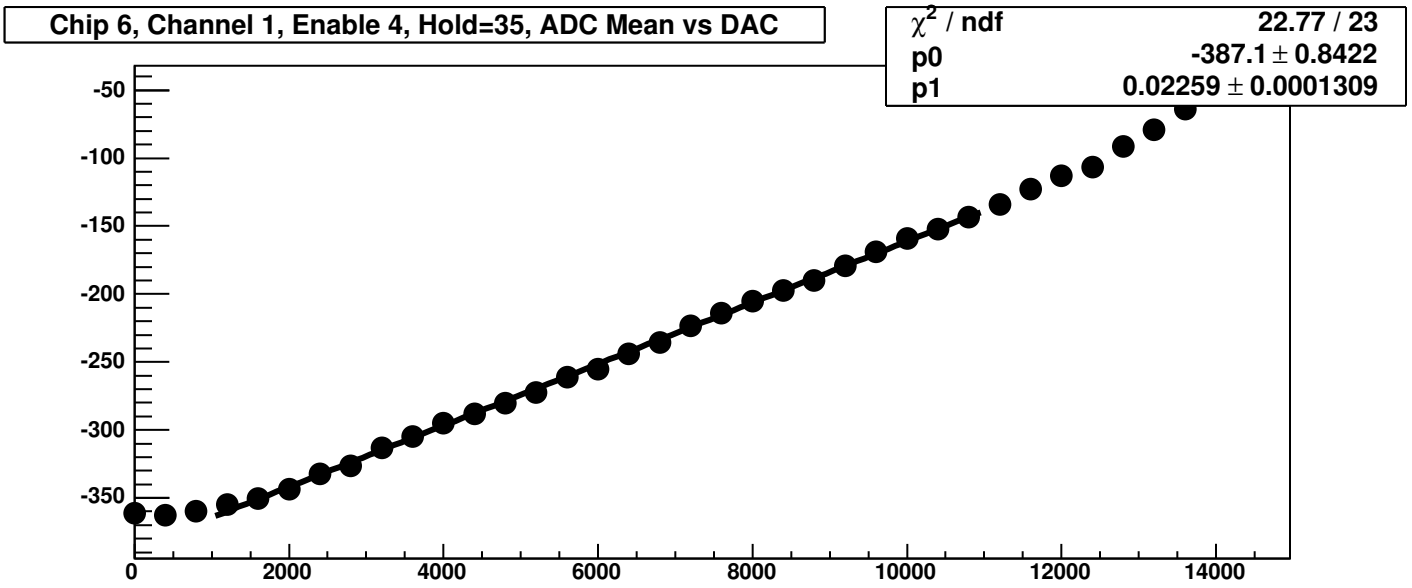

Chip 6, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

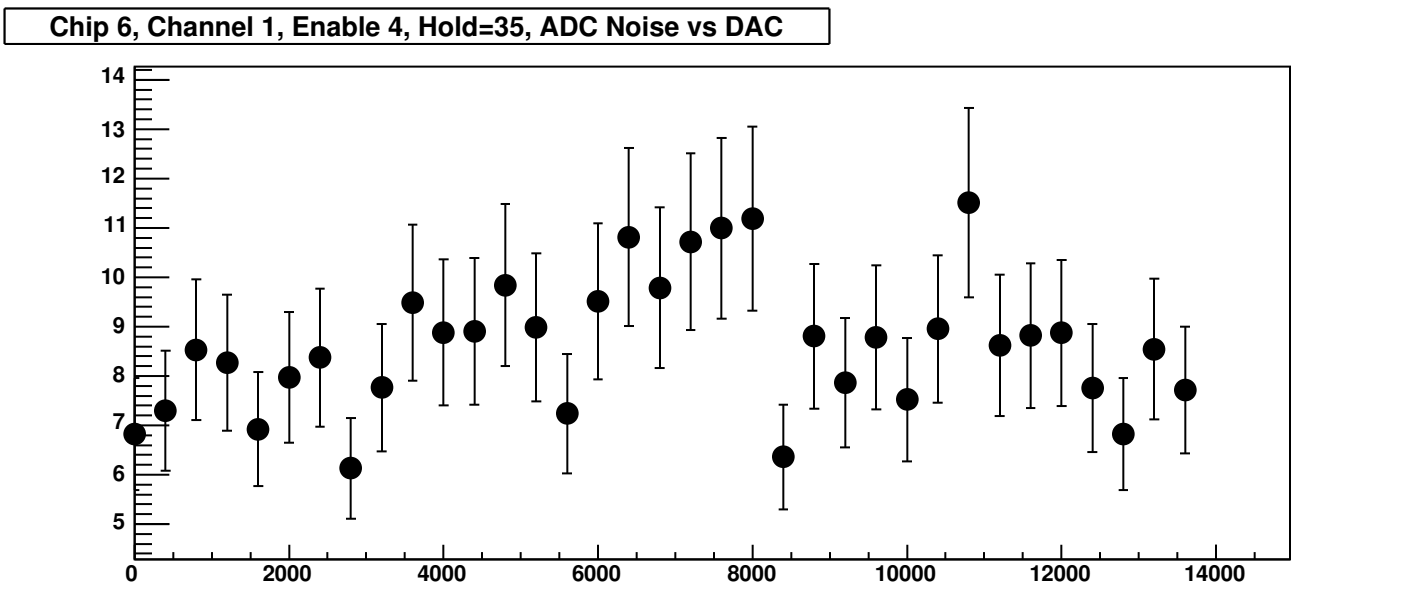

Chip 6, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

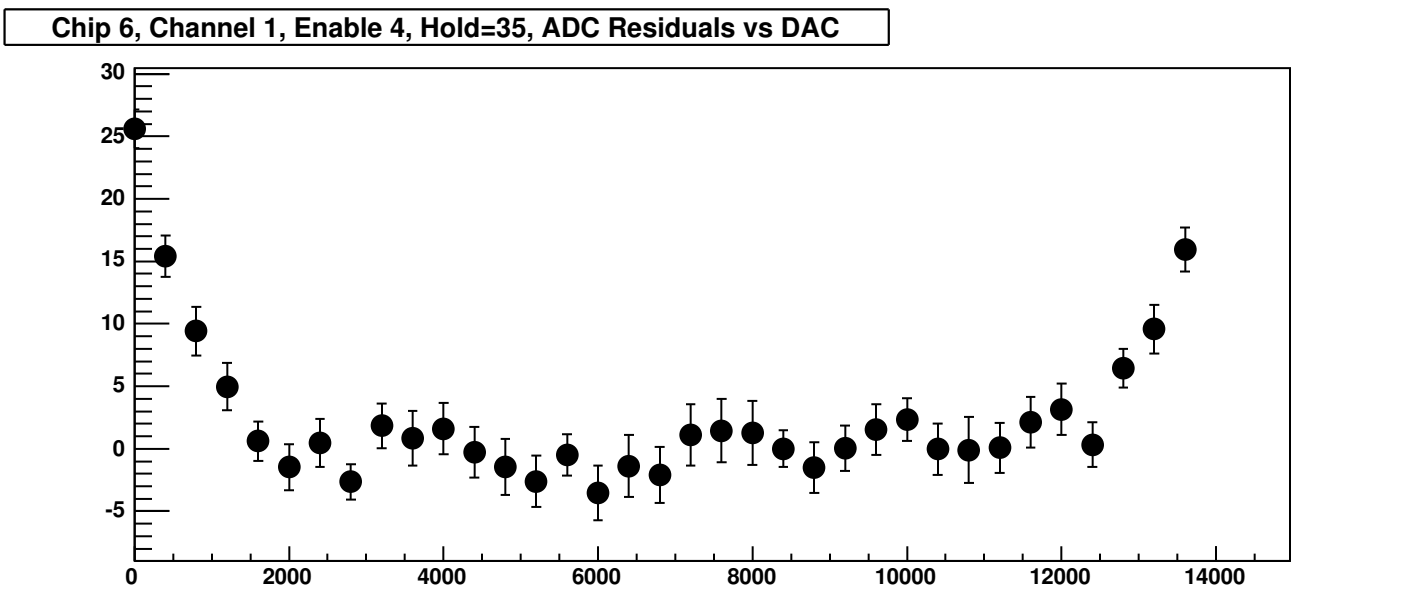

Chip 6, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 471.9 / 23
p0 -358 ± 1.374
p1 2.331 ± 0.0004186
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

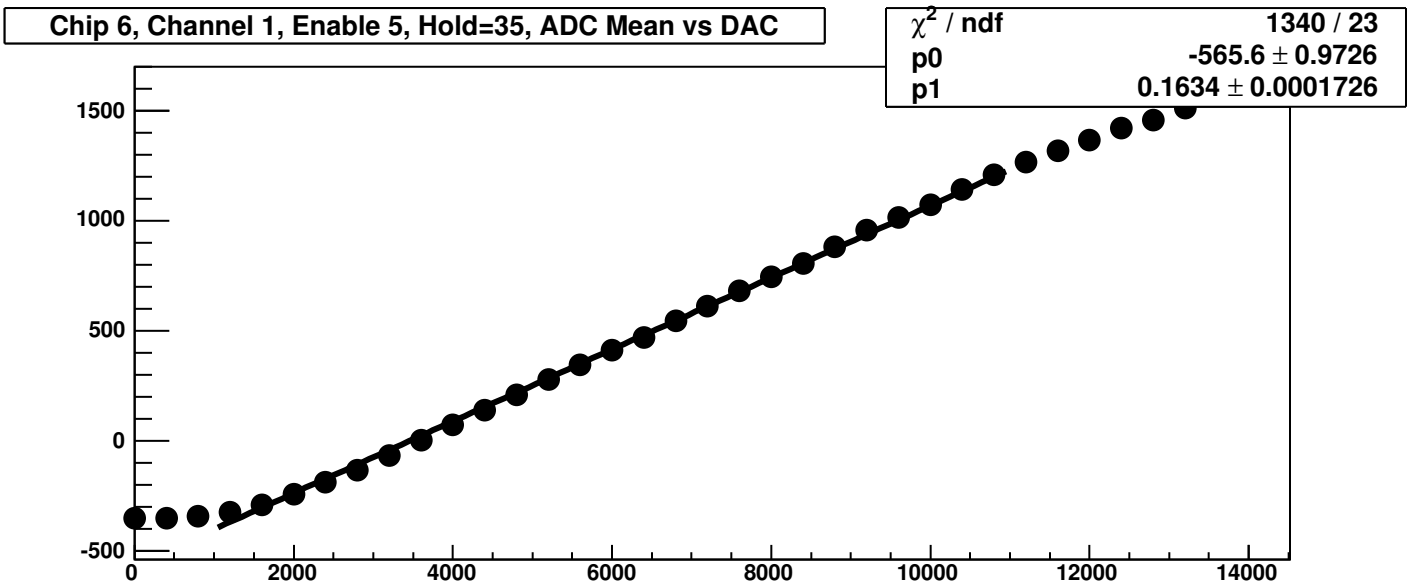

Chip 6, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

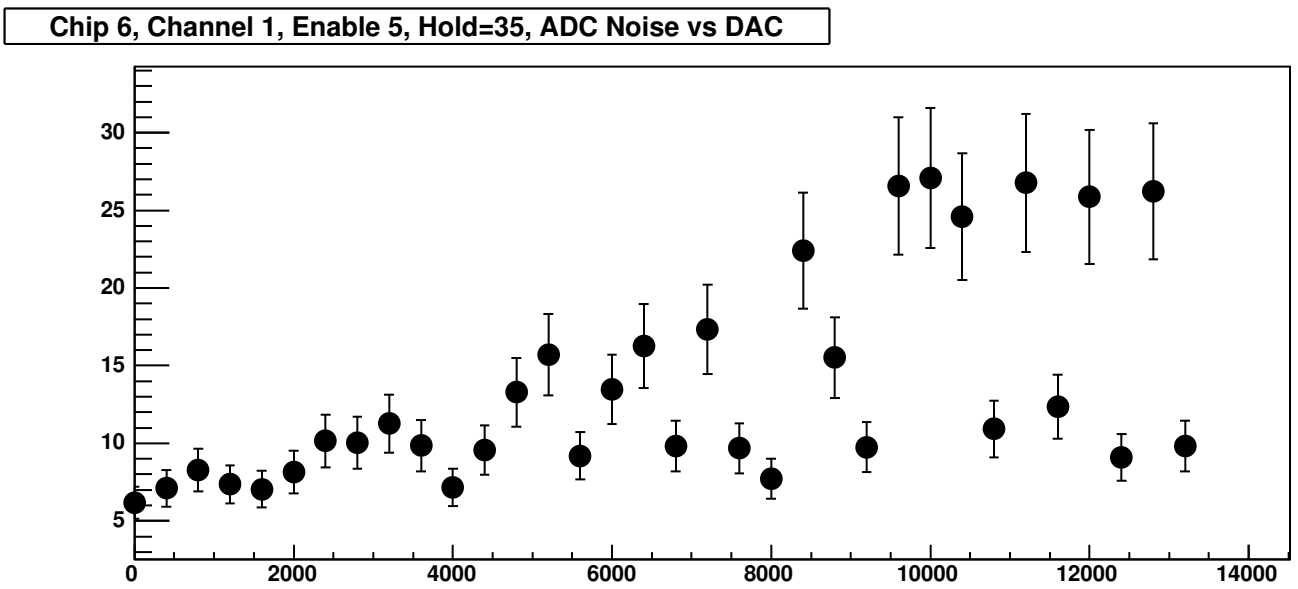

Chip 6, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

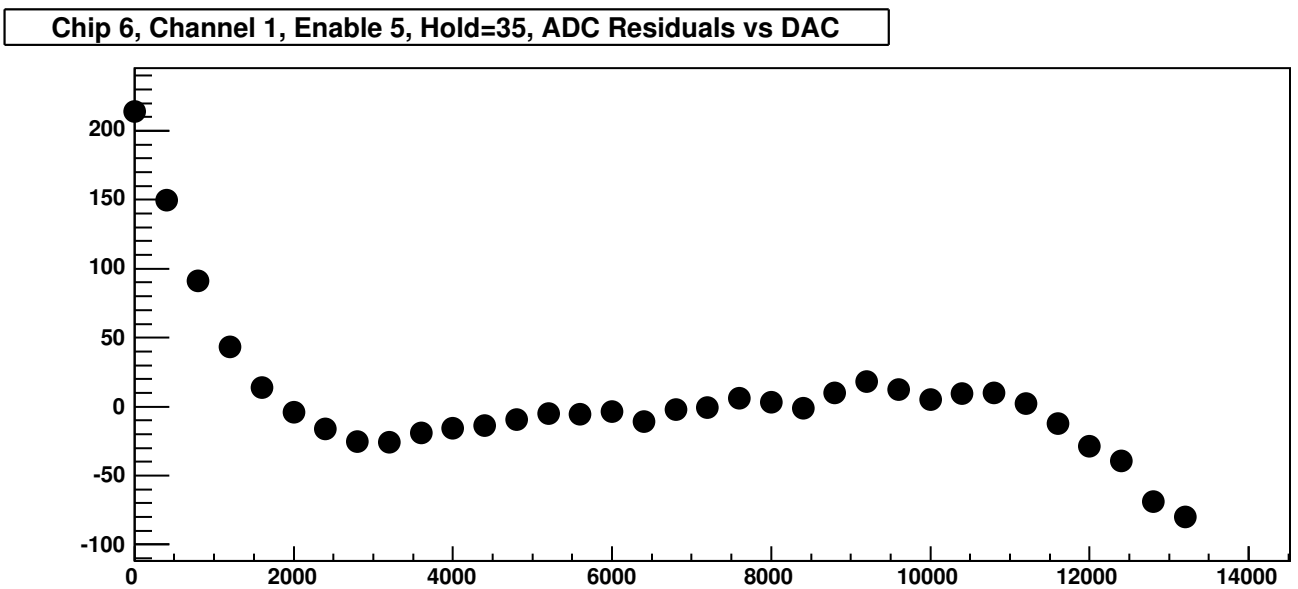

Chip 6, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 22.77 / 23
p0 -387.1 ± 0.8422
p1 0.02259 ± 0.0001309
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

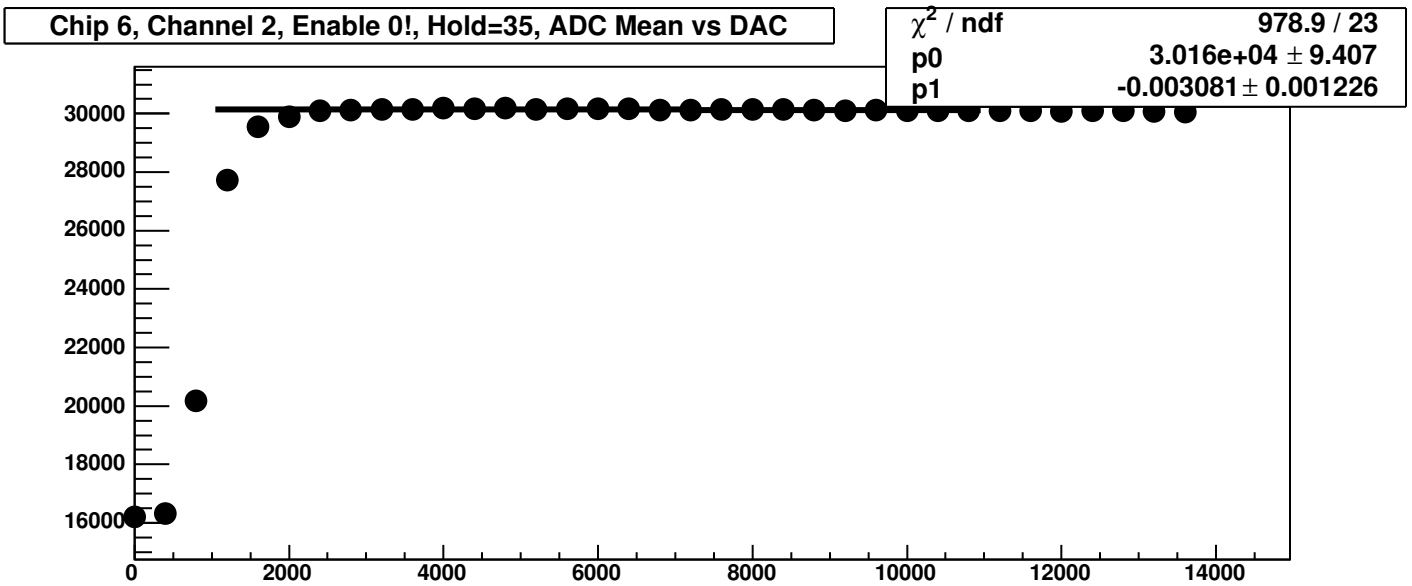

Chip 6, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
14
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

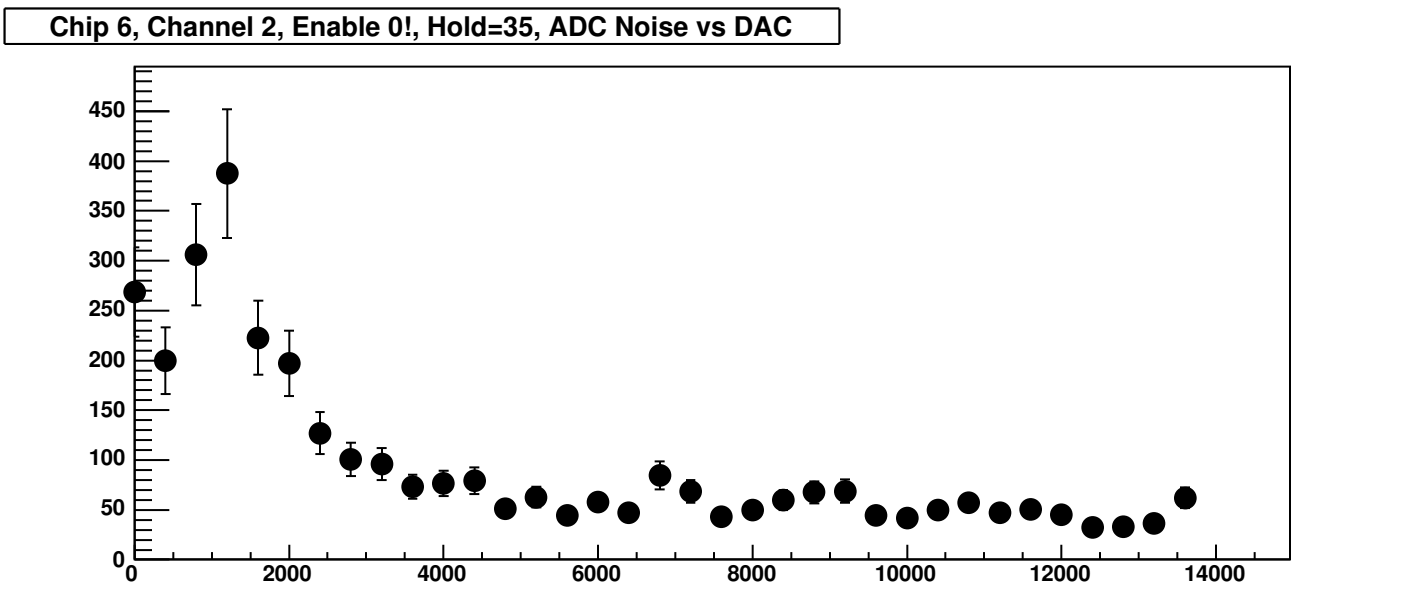

Chip 6, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

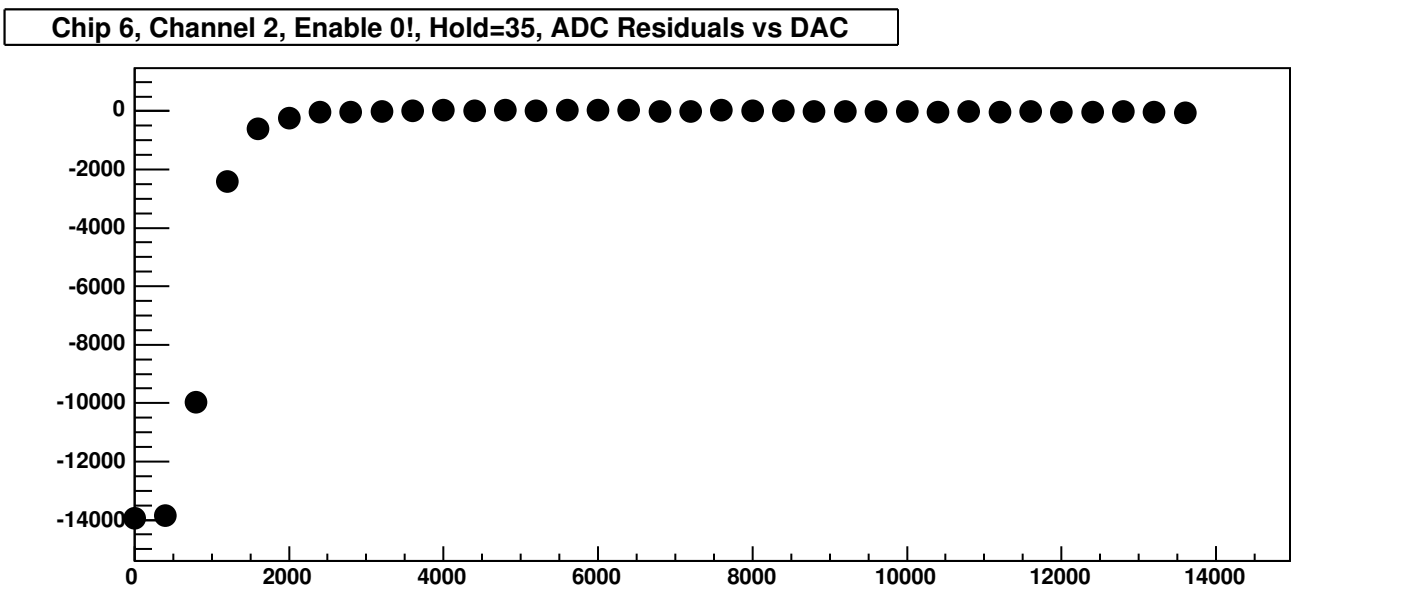

Chip 6, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1340 / 23
p0 -565.6 ± 0.9726
p1 0.1634 ± 0.0001726
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

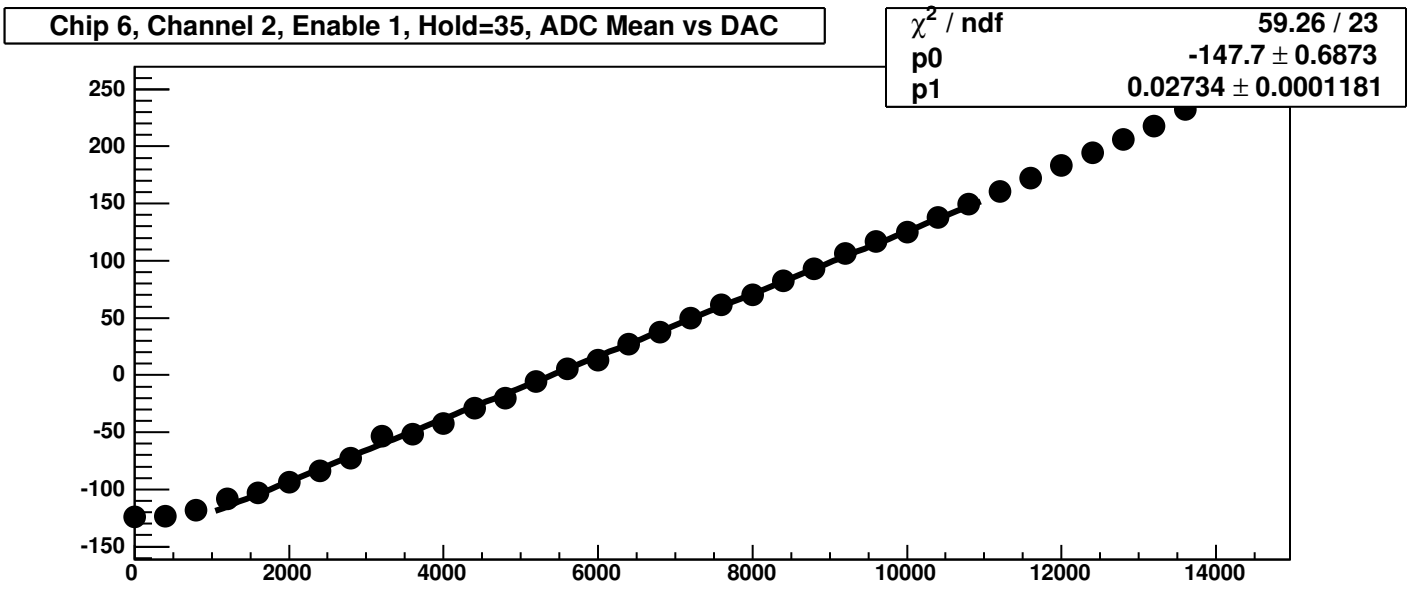

Chip 6, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

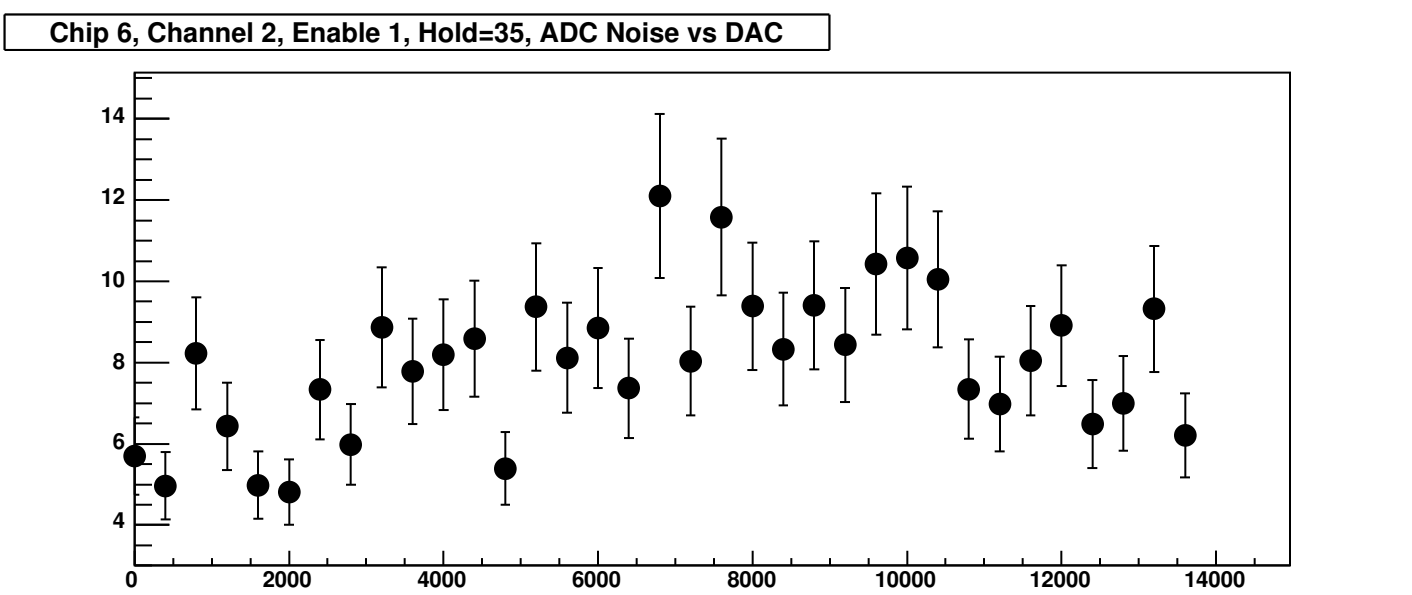

Chip 6, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

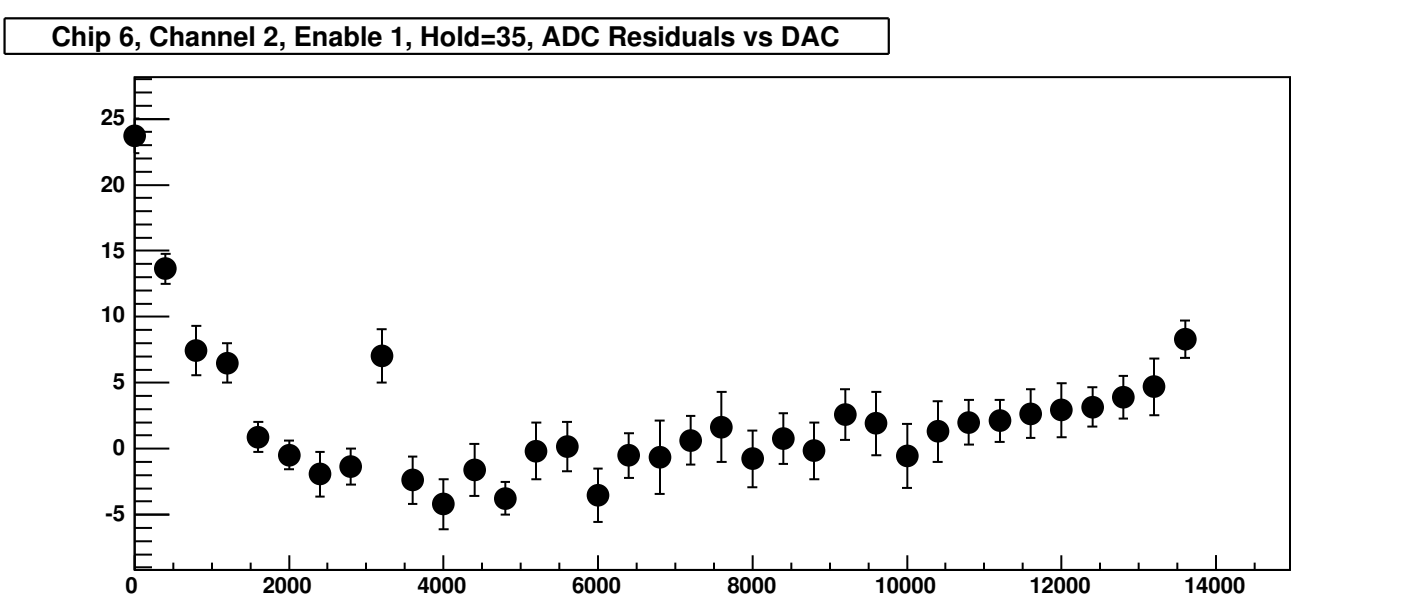

Chip 6, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 978.9 / 23
p0 3.016e+04 ± 9.407
p1 -0.003081 ± 0.001226
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

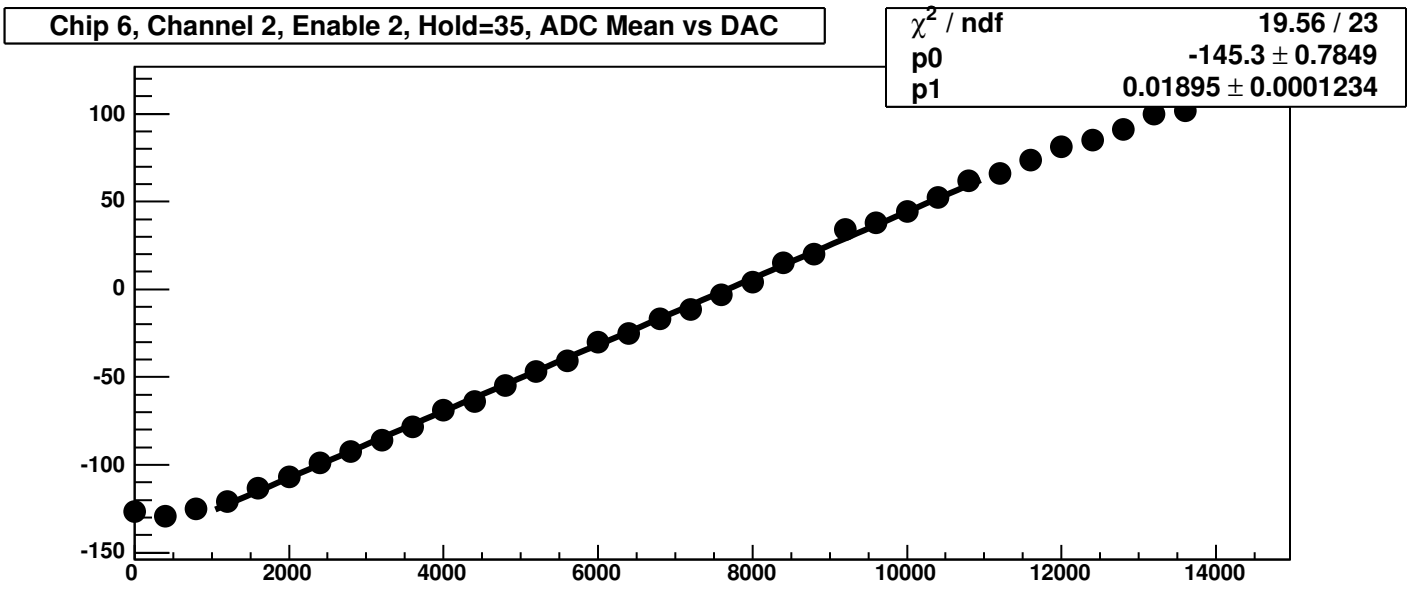

Chip 6, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

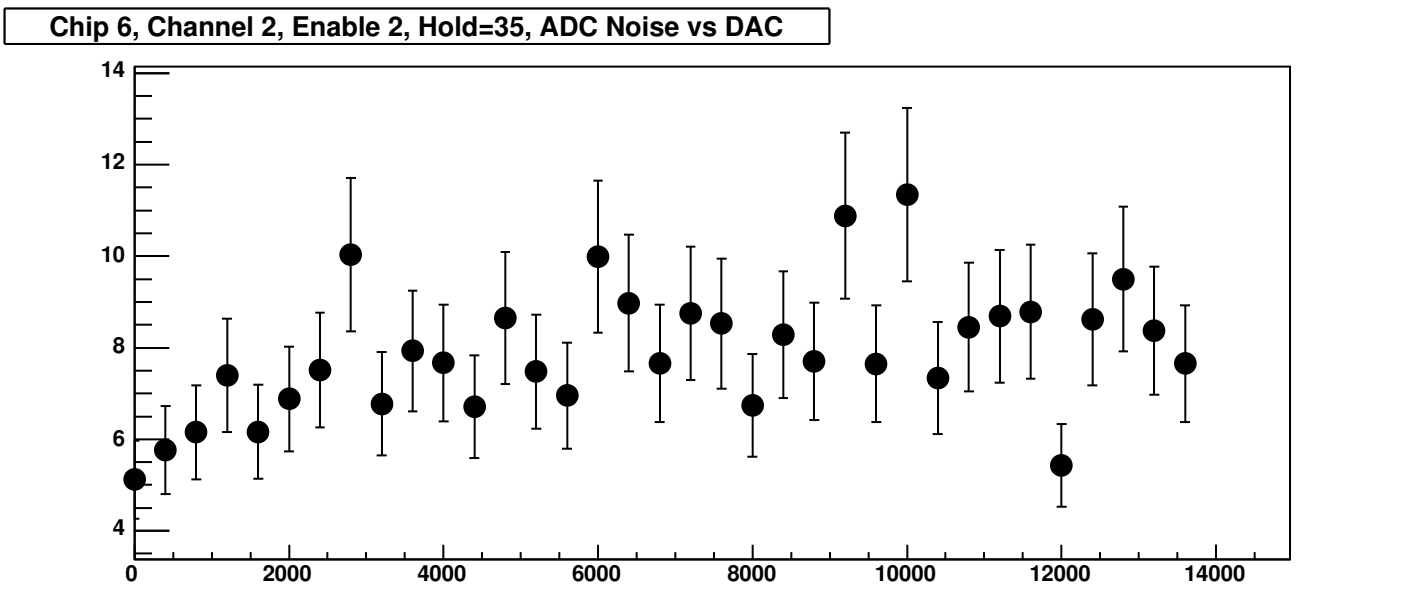

Chip 6, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

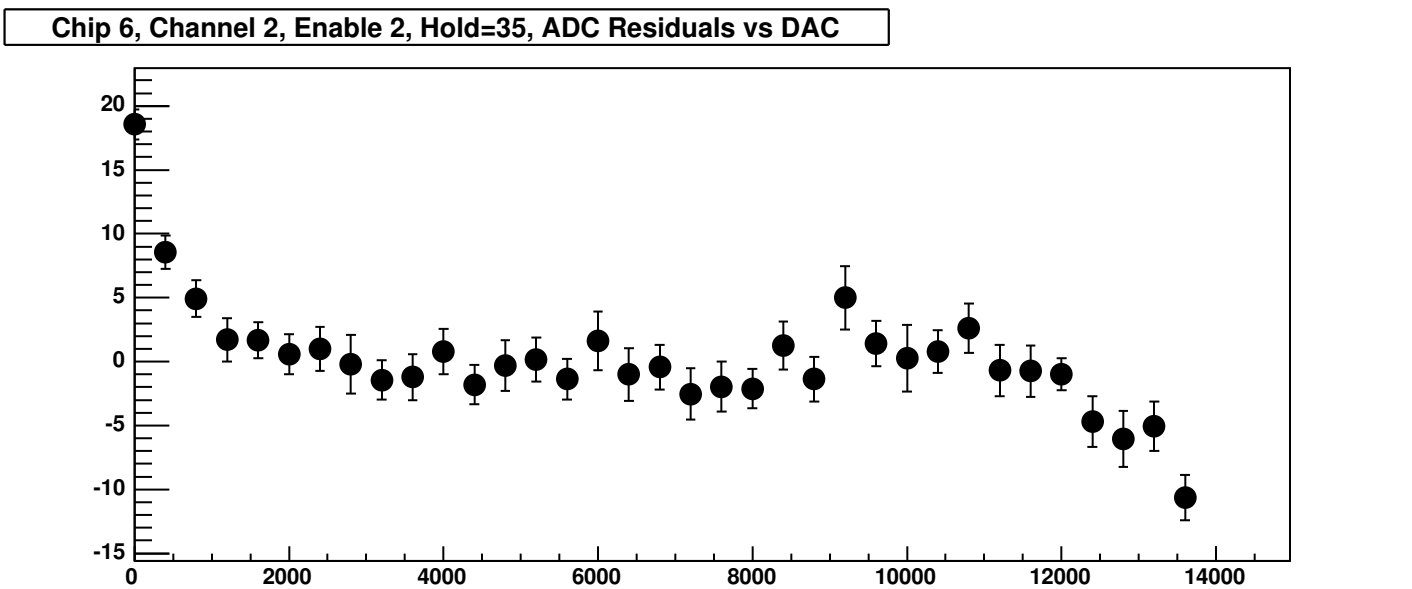

Chip 6, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 59.26 / 23
p0 -147.7 ± 0.6873
p1 0.02734 ± 0.0001181
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

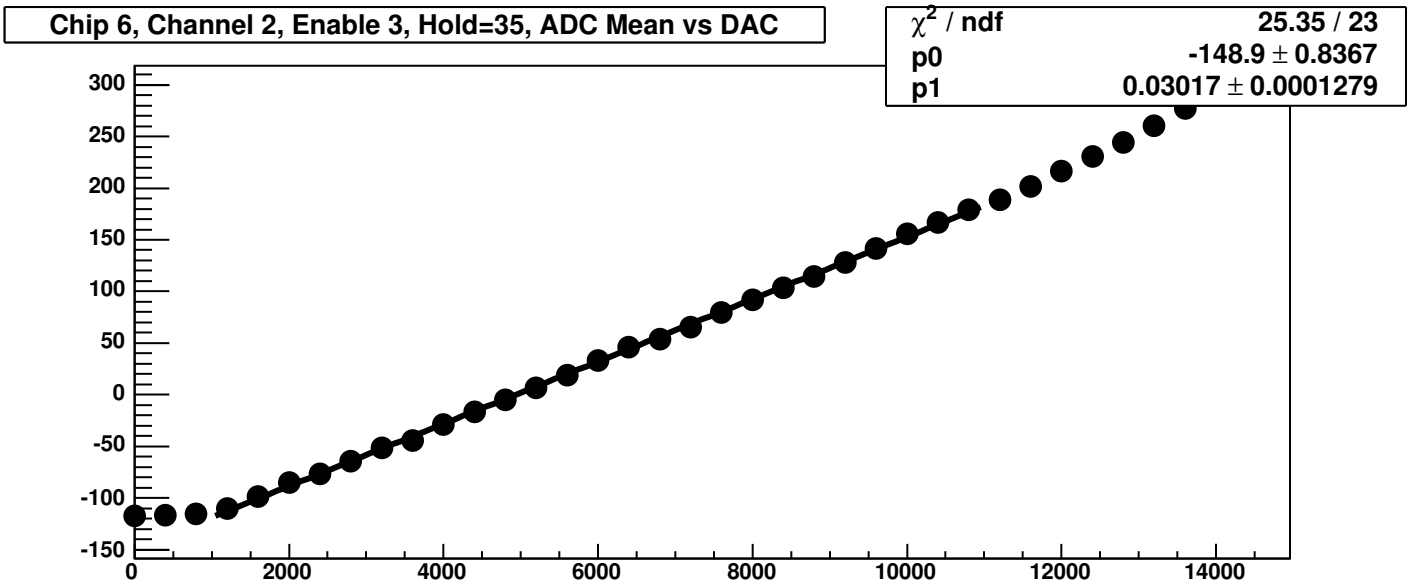

Chip 6, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

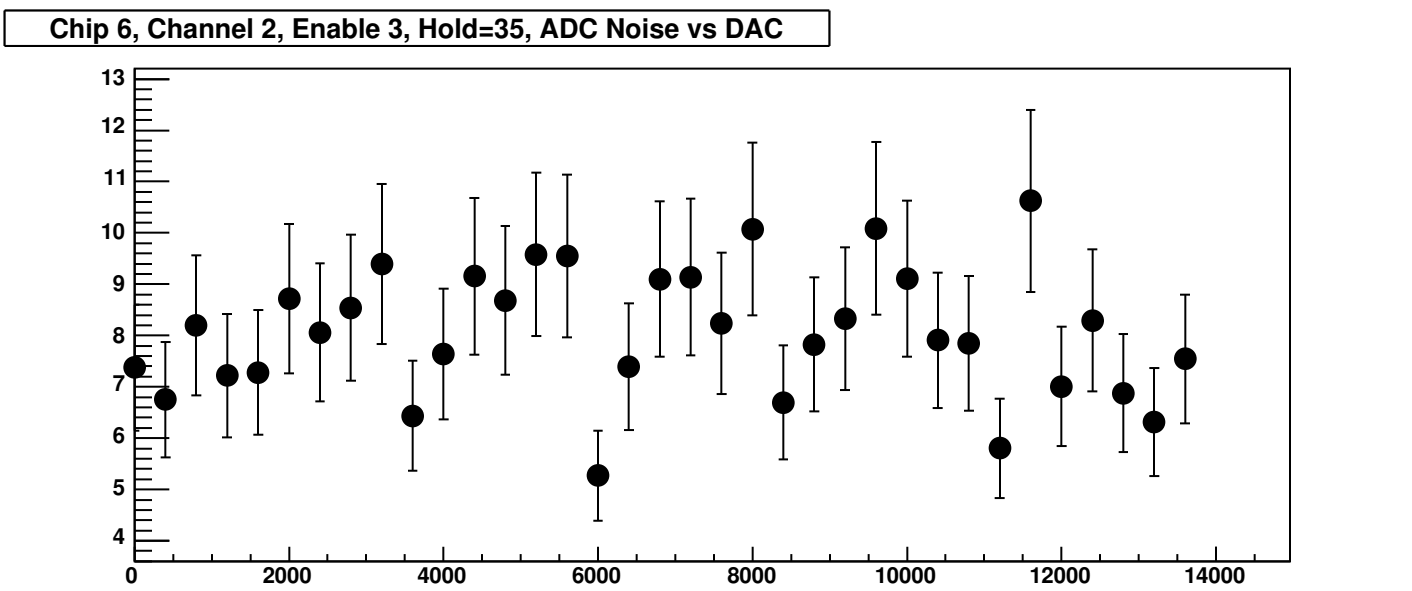

Chip 6, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

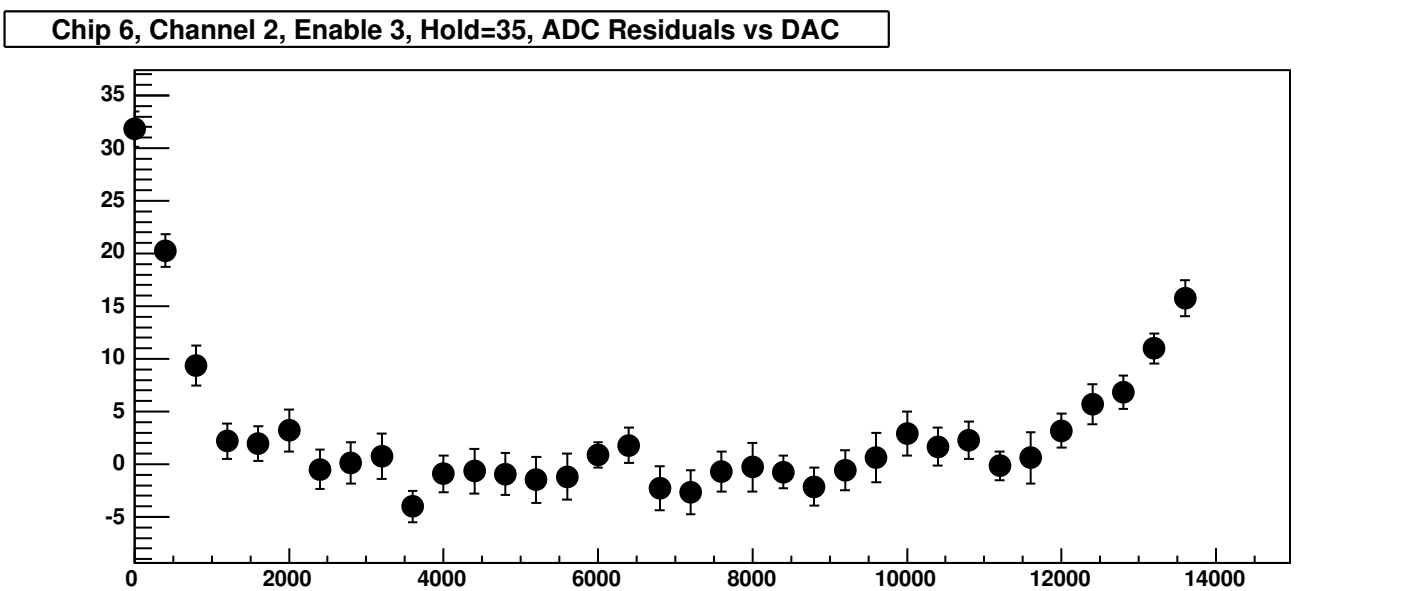

Chip 6, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 19.56 / 23
p0 -145.3 ± 0.7849
p1 0.01895 ± 0.0001234
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

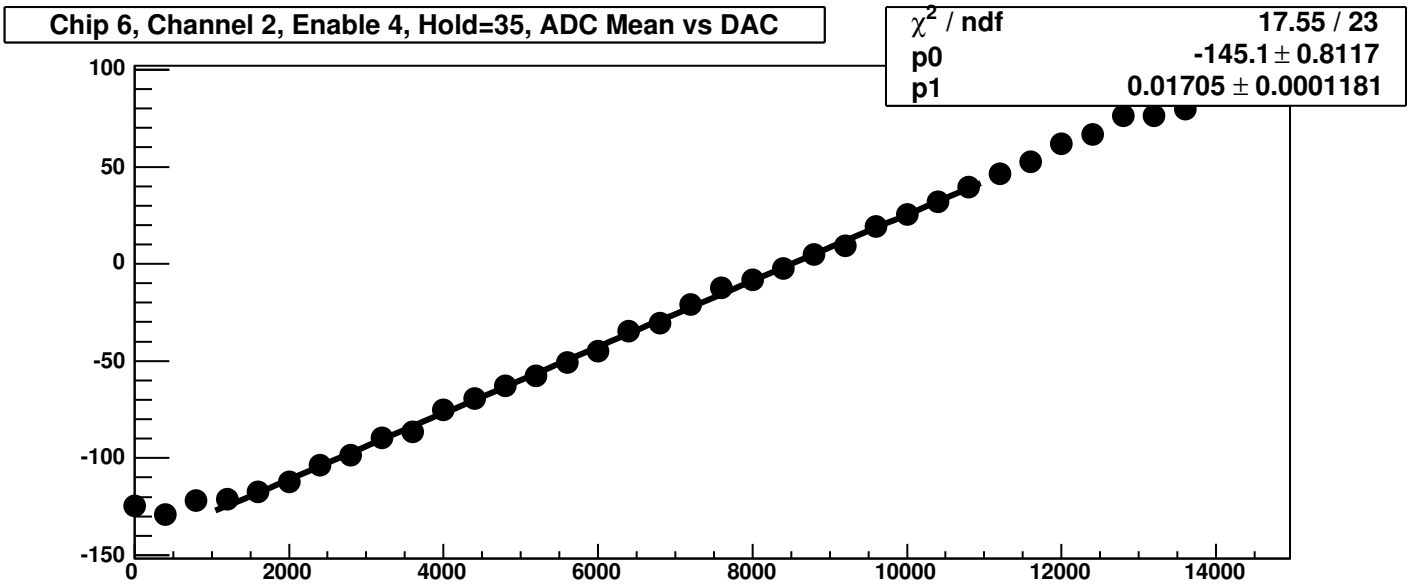

Chip 6, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

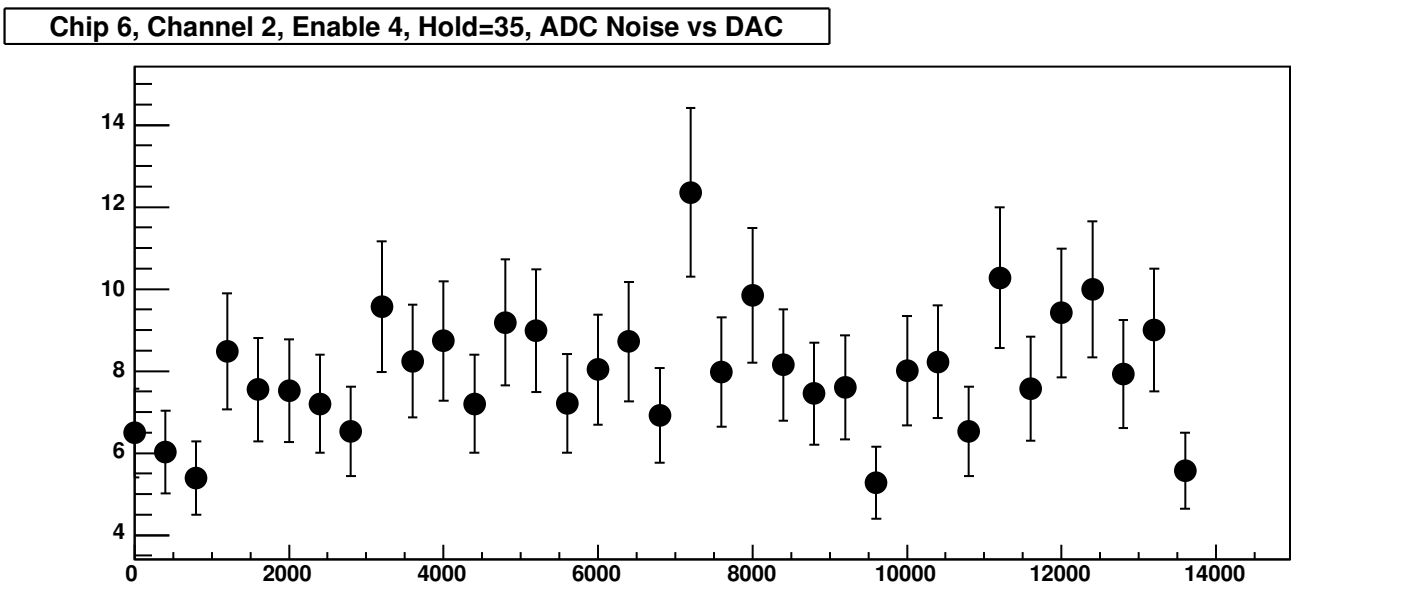

Chip 6, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

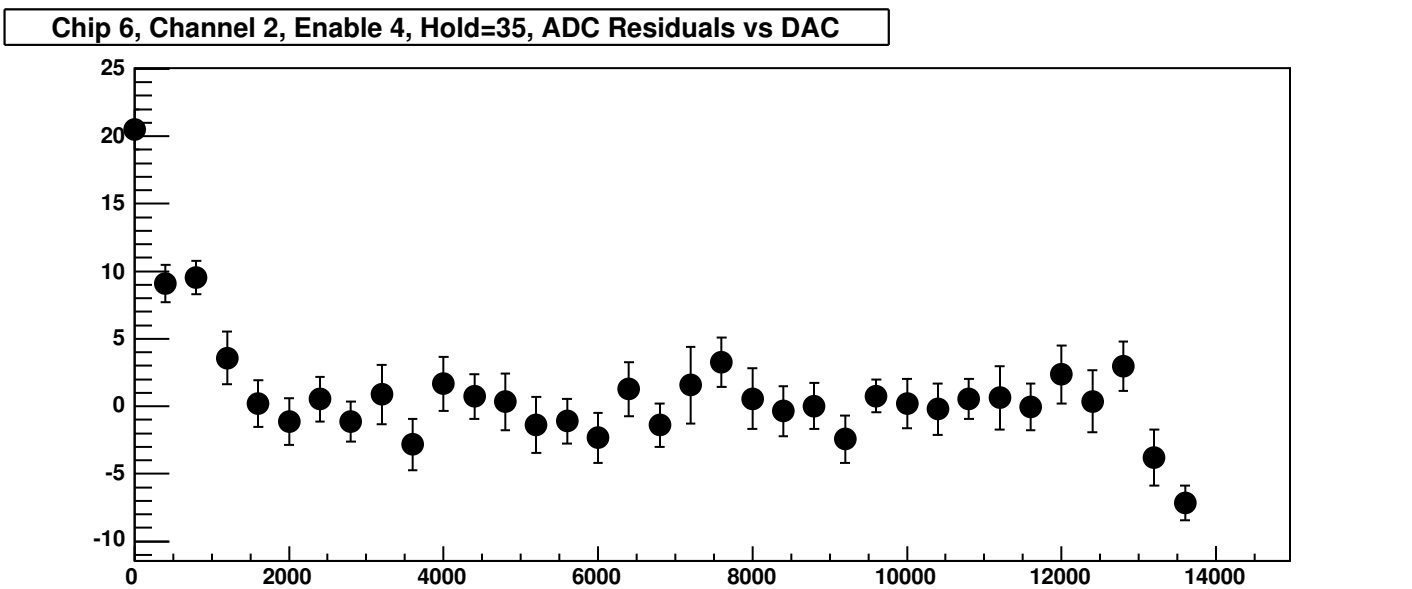

Chip 6, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.35 / 23
p0 -148.9 ± 0.8367
p1 0.03017 ± 0.0001279
300
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

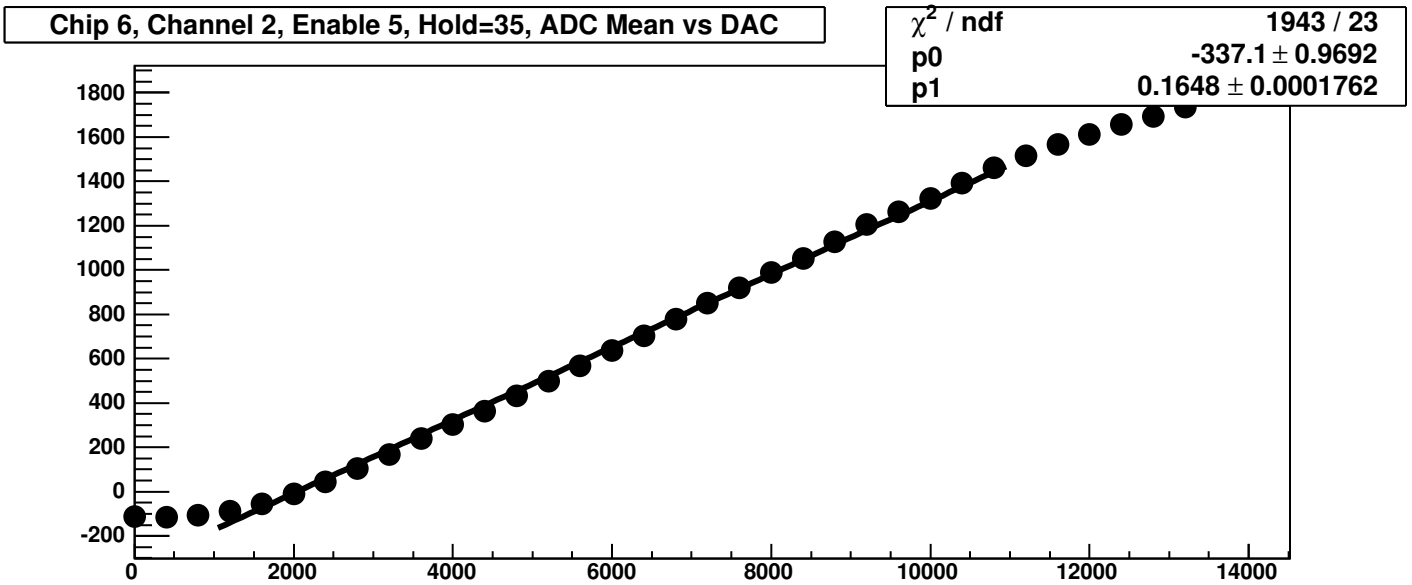

Chip 6, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

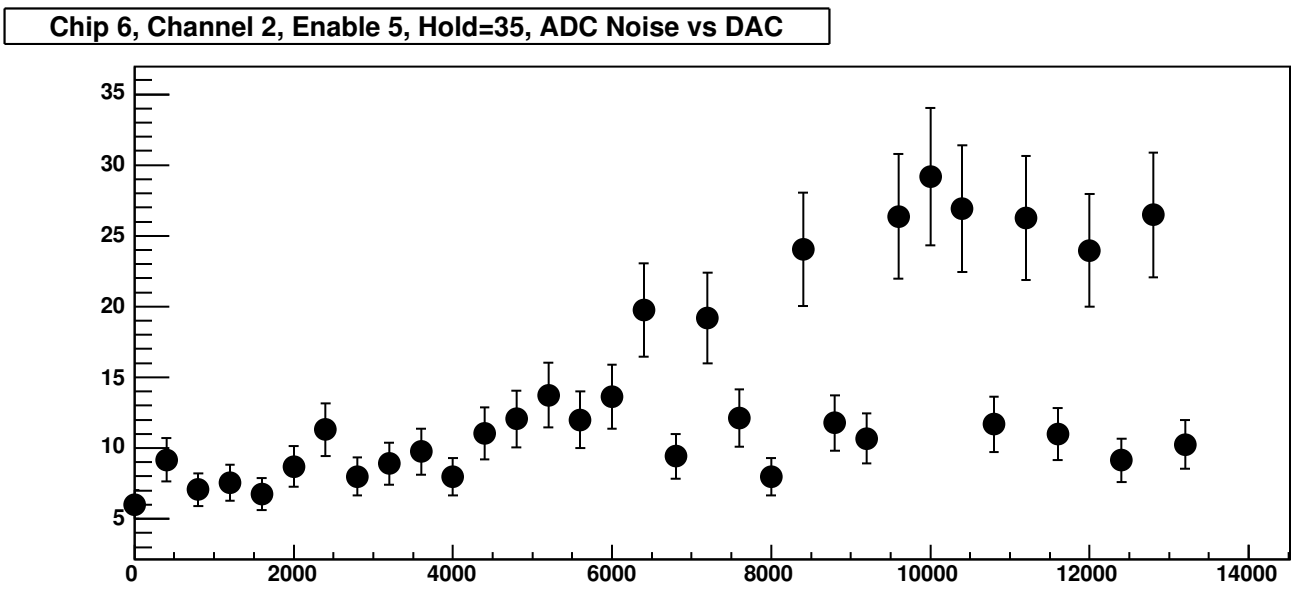

Chip 6, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

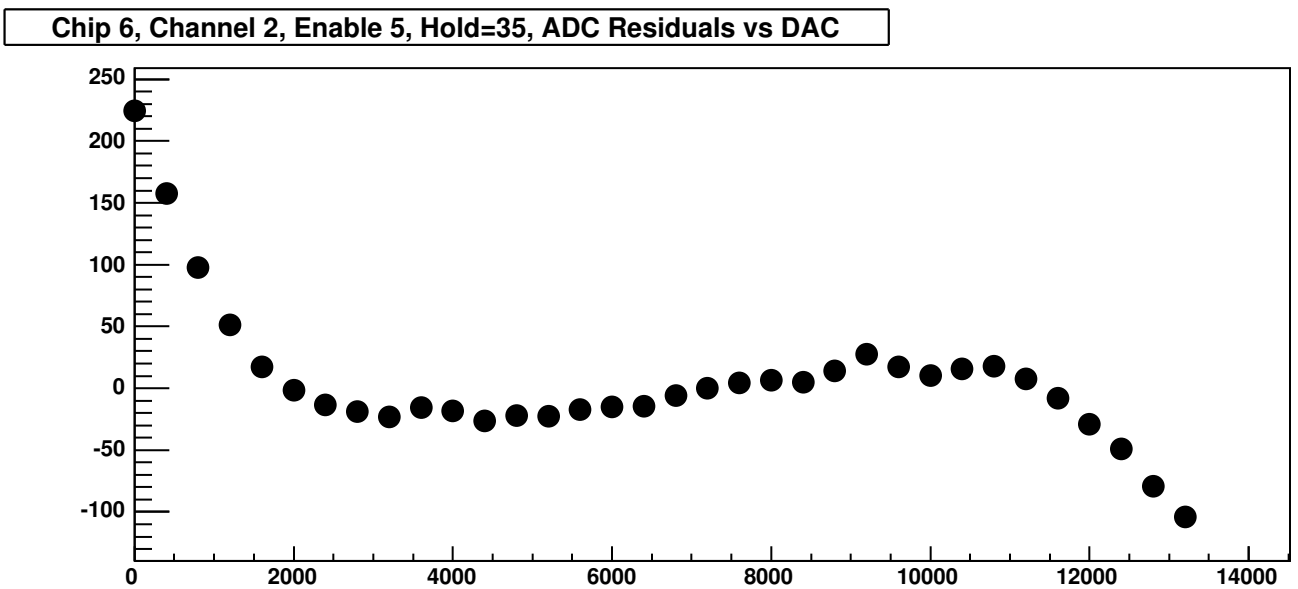

Chip 6, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 17.55 / 23
p0 -145.1 ± 0.8117
p1 0.01705 ± 0.0001181
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

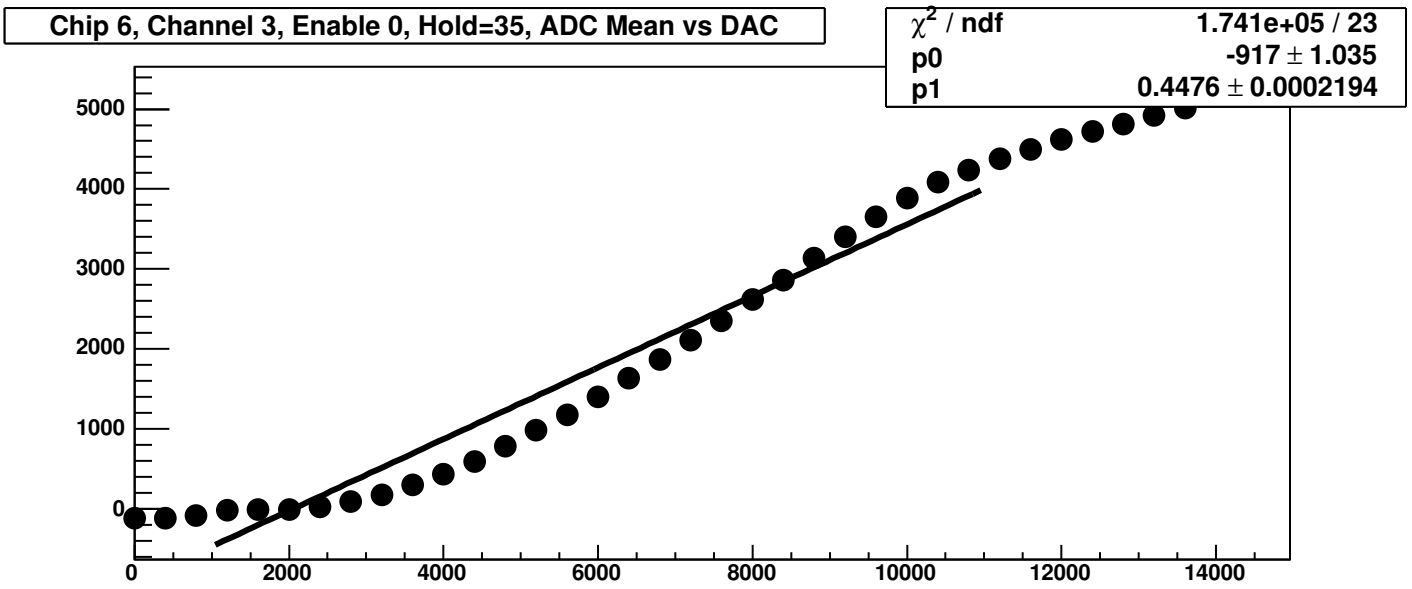

Chip 6, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

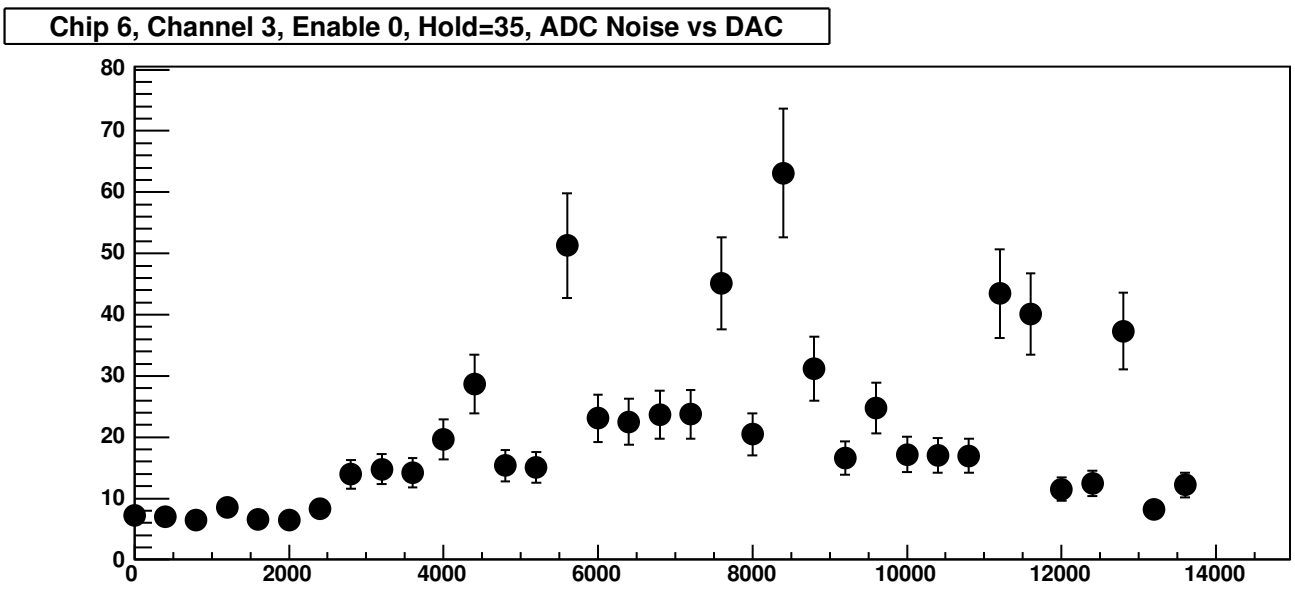

Chip 6, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

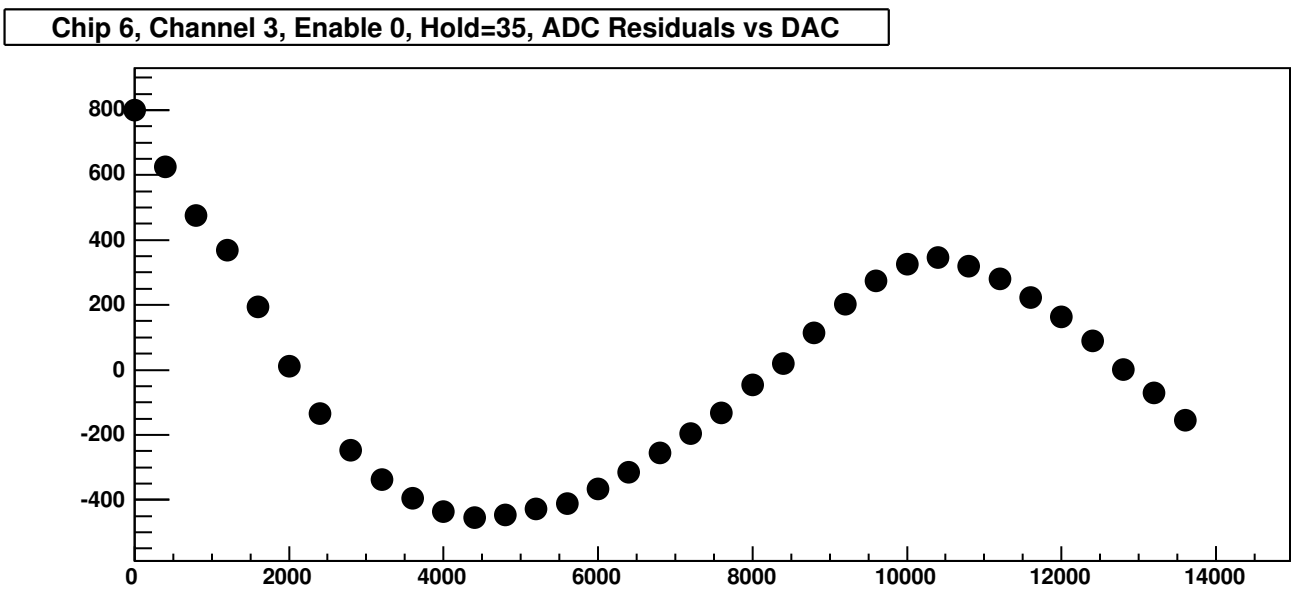

Chip 6, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1943 / 23
p0 -337.1 ± 0.9692
p1 0.1648 ± 0.0001762
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

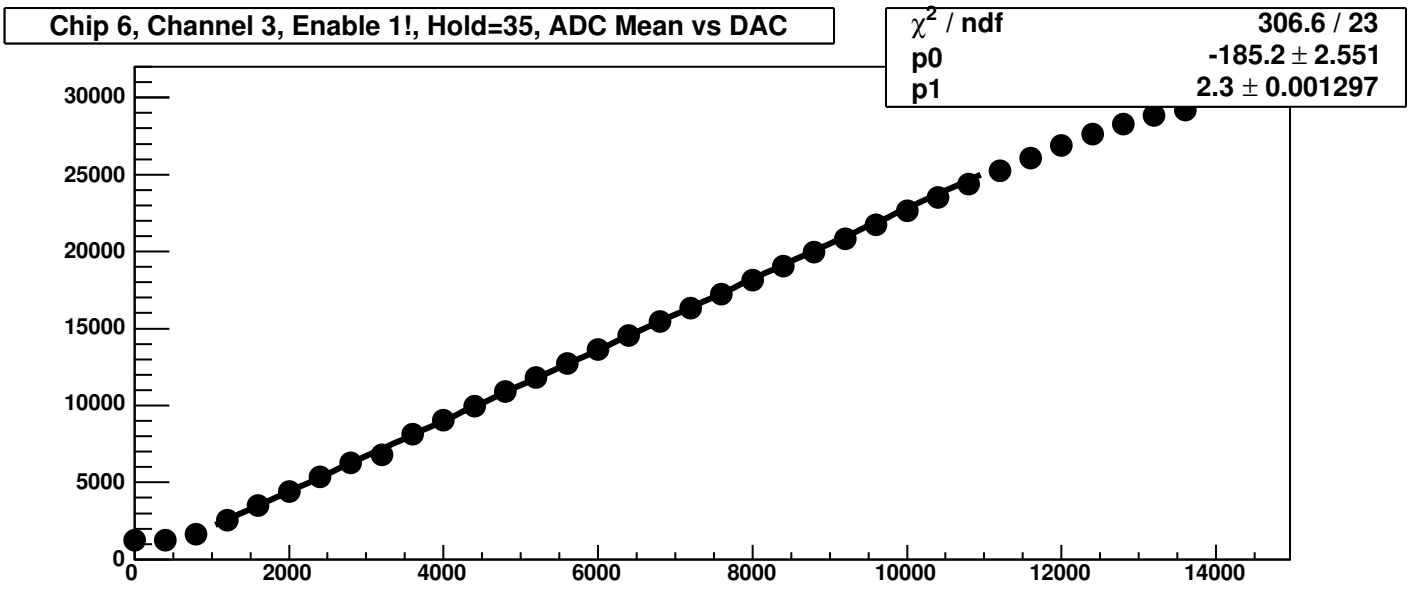

Chip 6, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

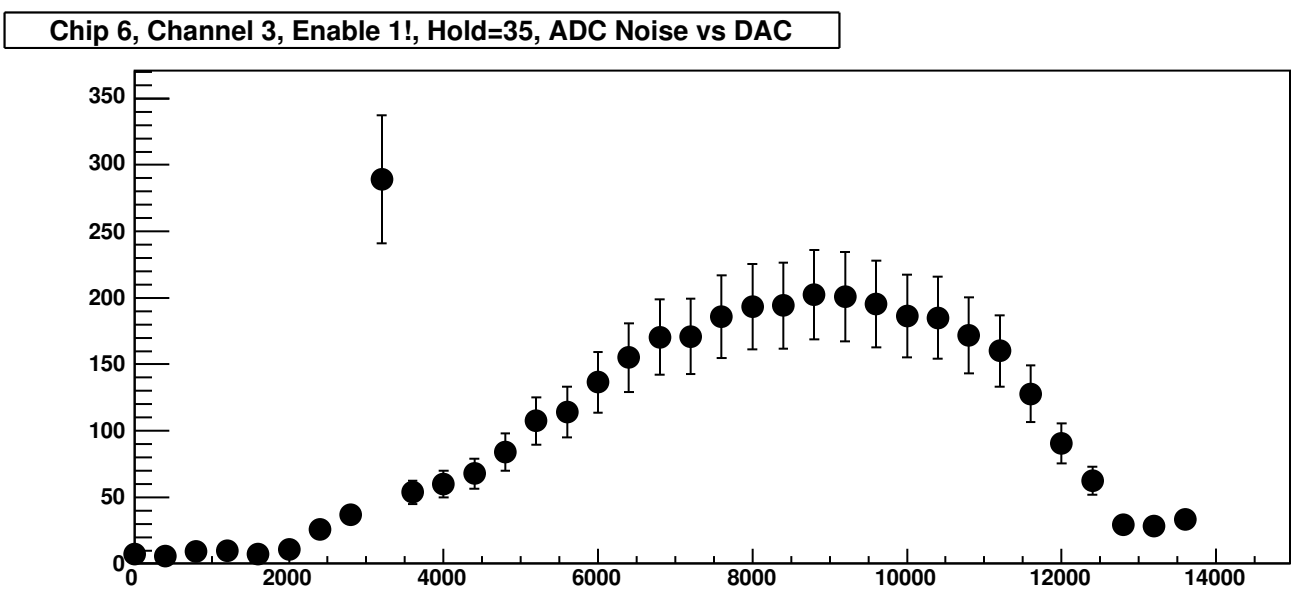

Chip 6, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

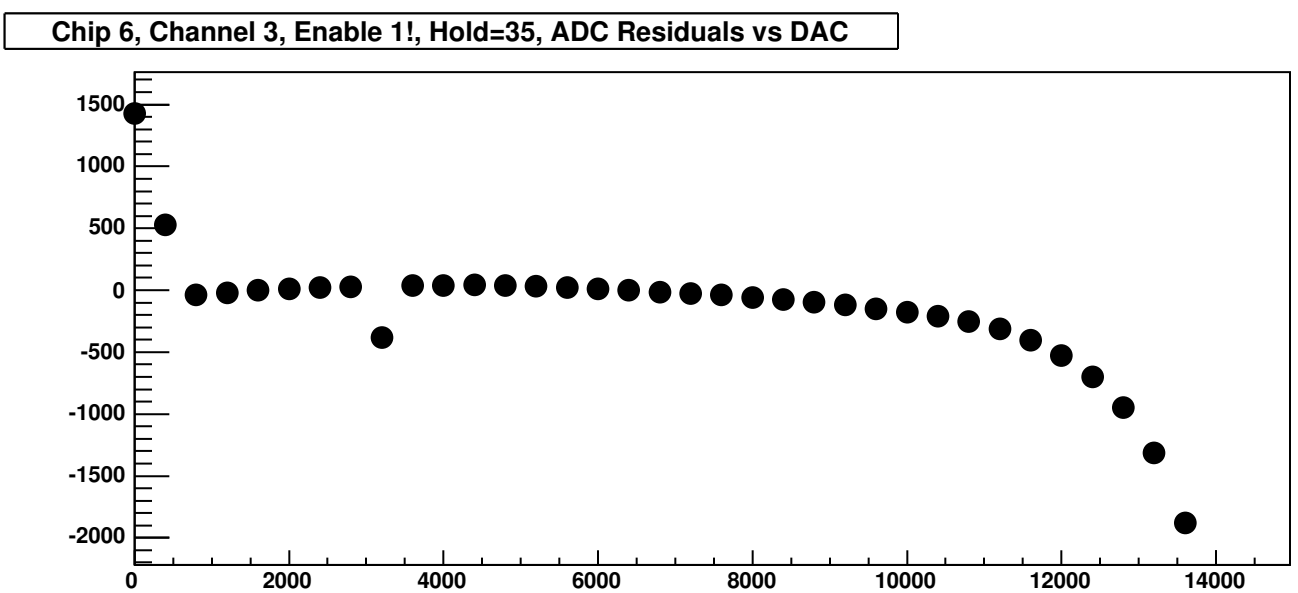

Chip 6, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.741e+05 / 23
p0 -917 ± 1.035
p1 0.4476 ± 0.0002194
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

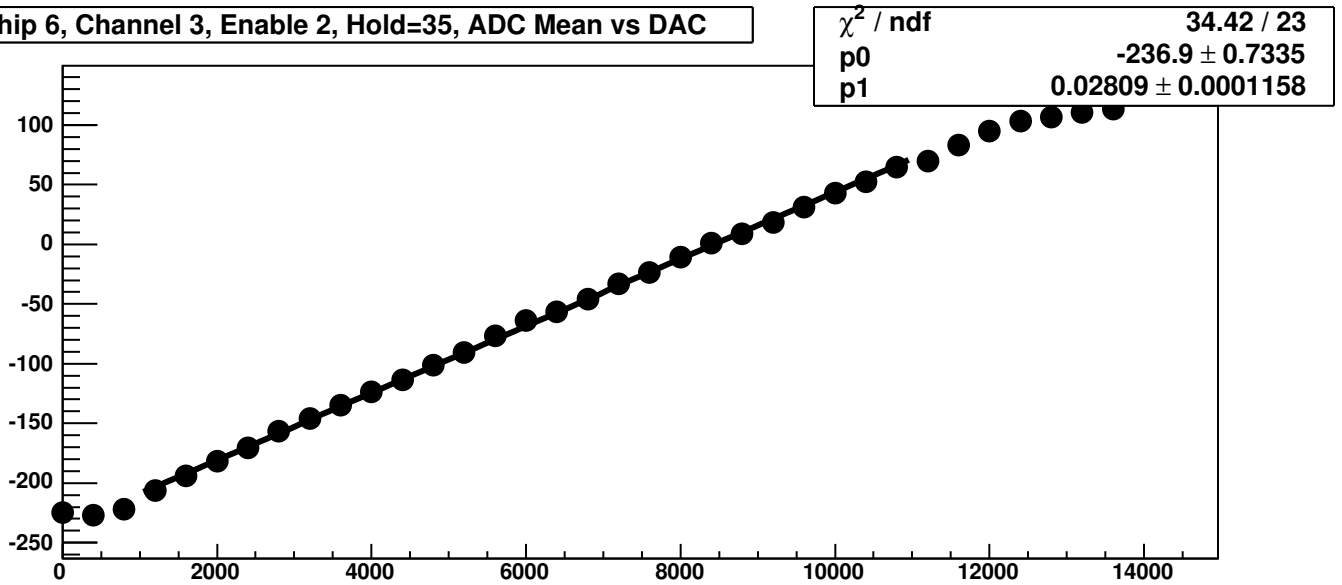

Chip 6, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 306.6 / 23
p0 -185.2 ± 2.551
p1 2.3 ± 0.001297
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

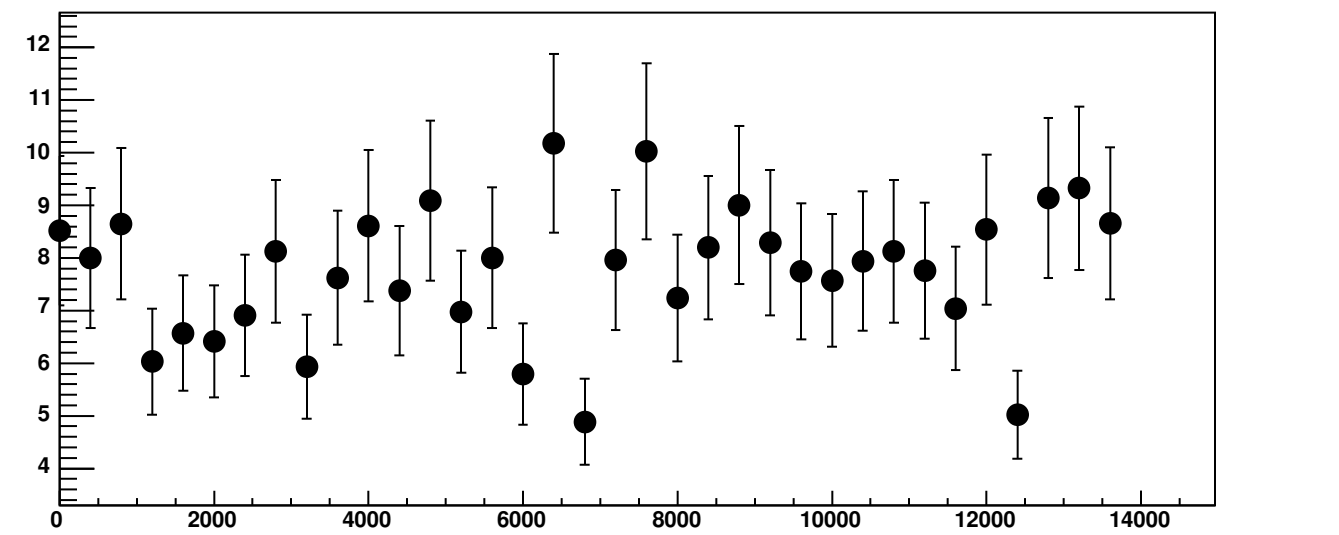

Chip 6, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

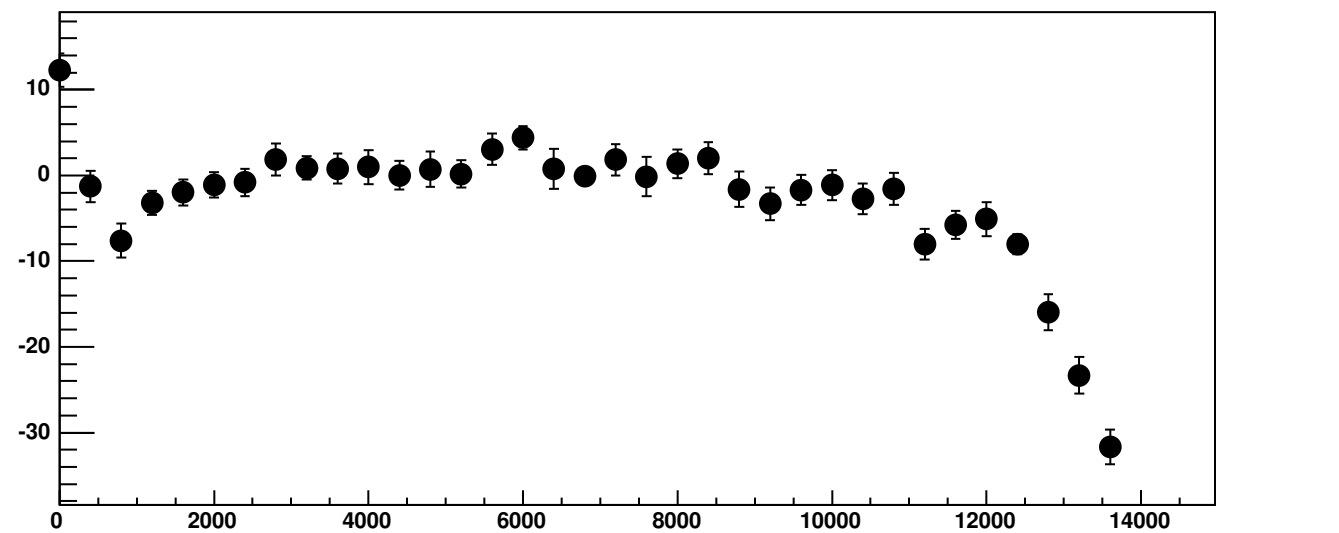
Chip 6, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

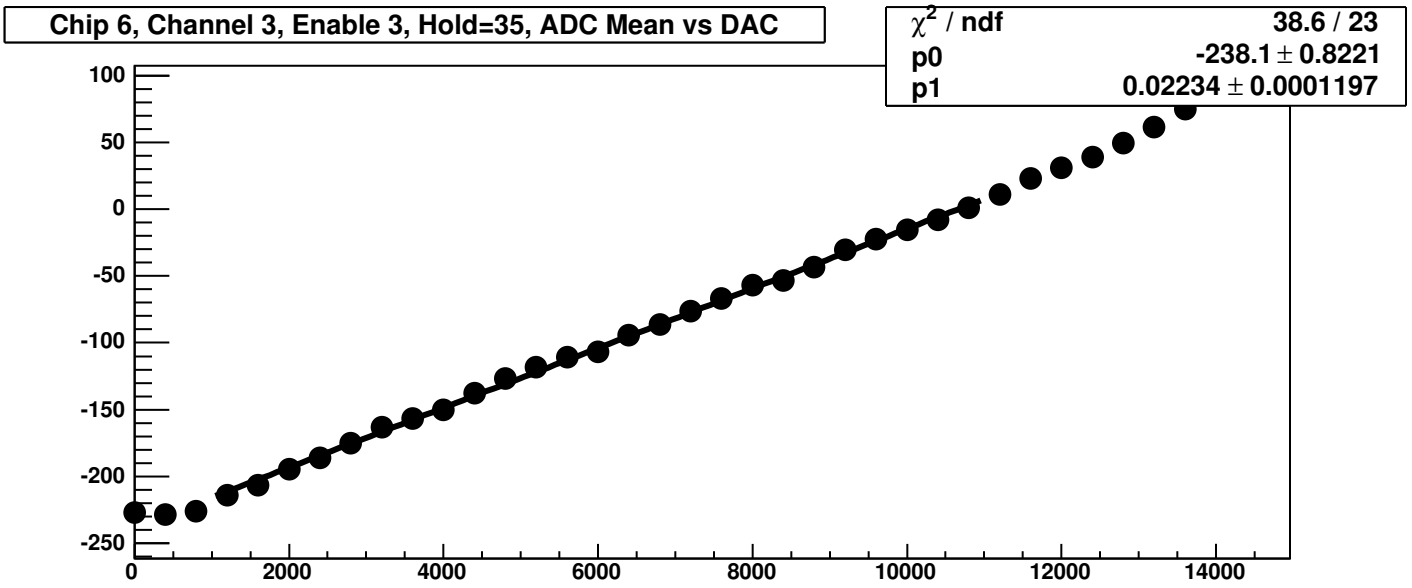


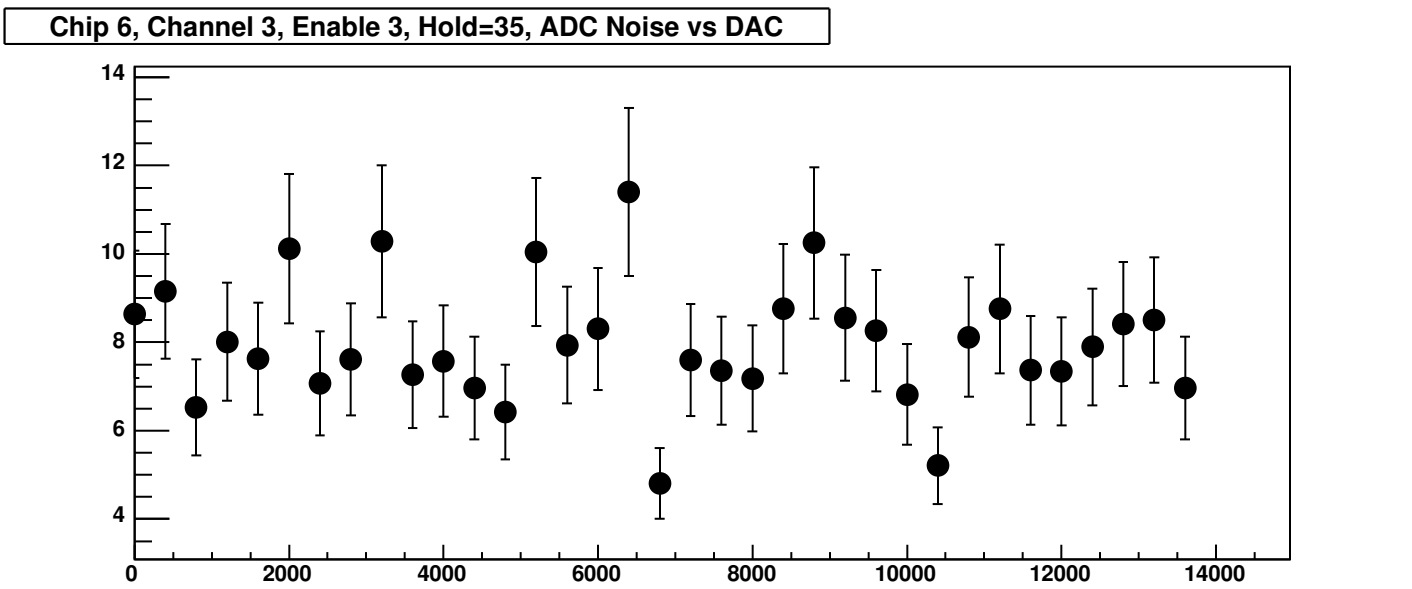
Chip 6, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC

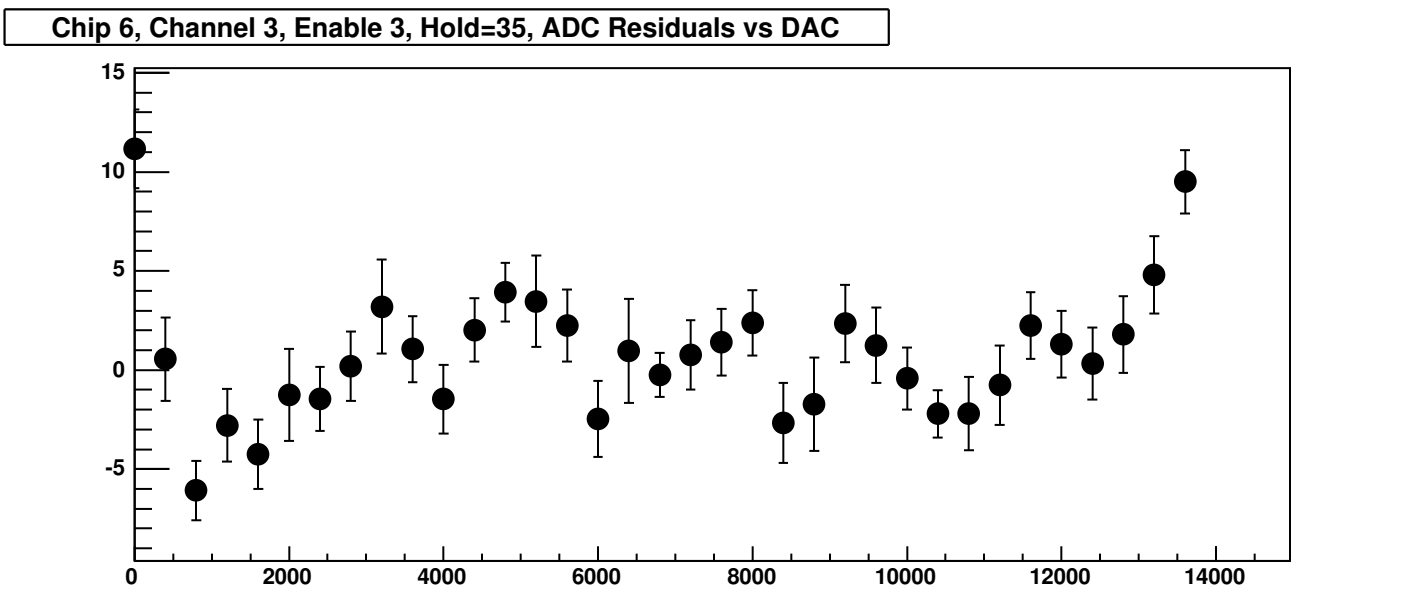


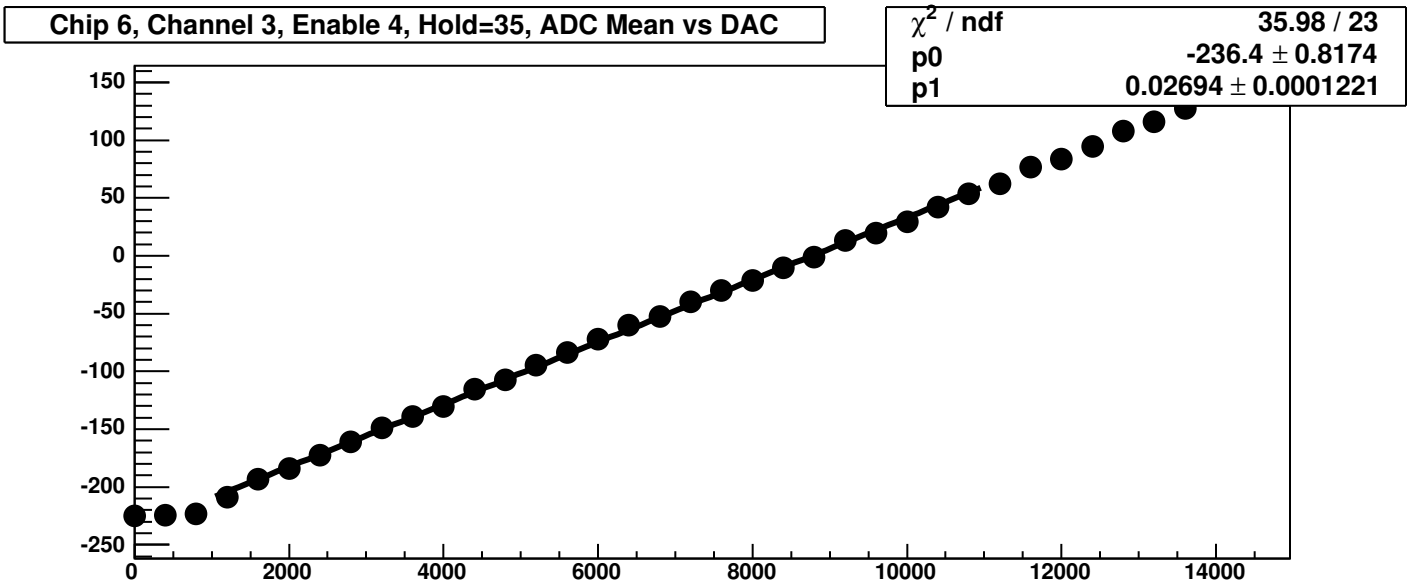
Chip 6, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

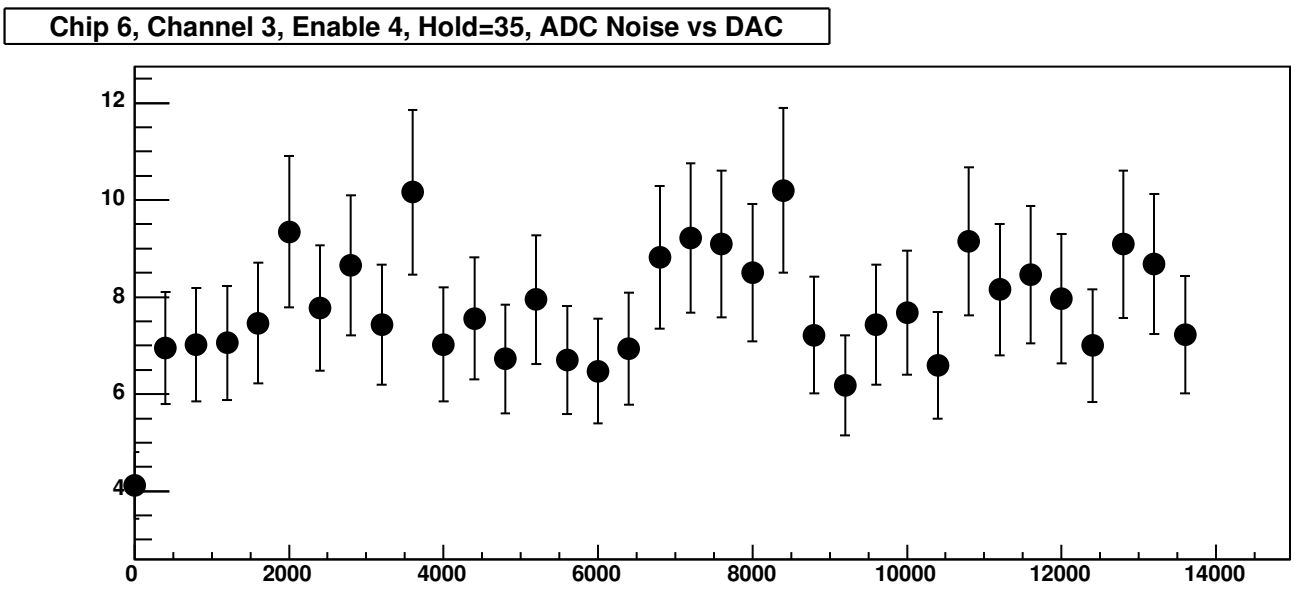


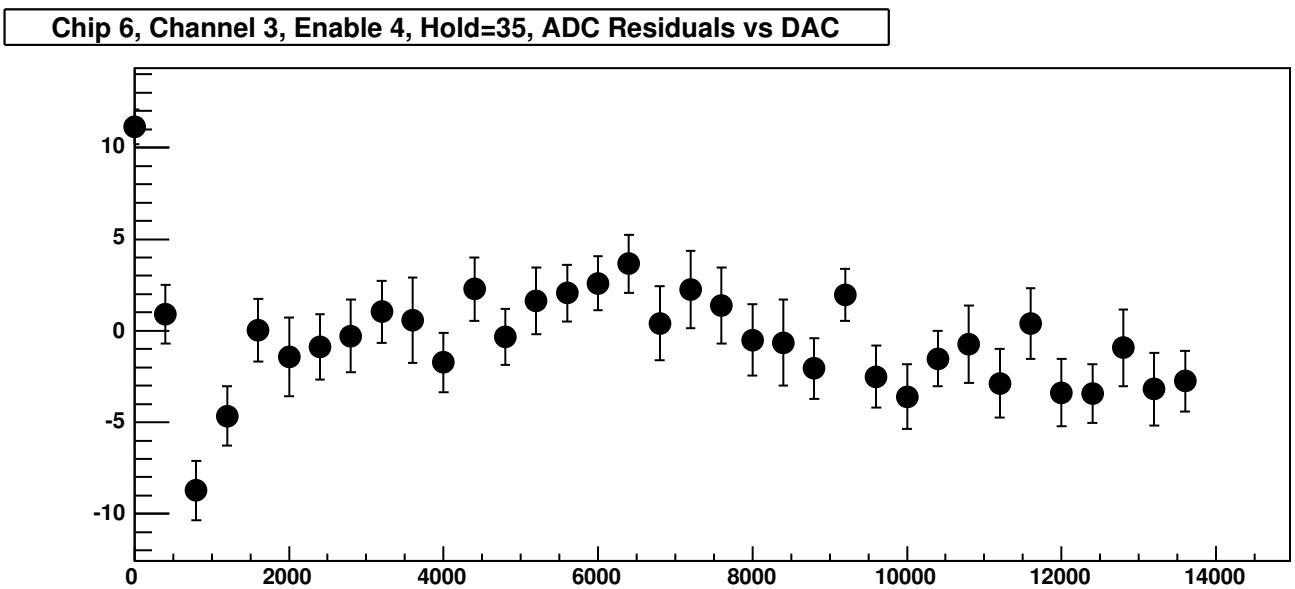

Chip 6, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.6 / 23
p0 -238.1 ± 0.8221
p1 0.02234 ± 0.0001197
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

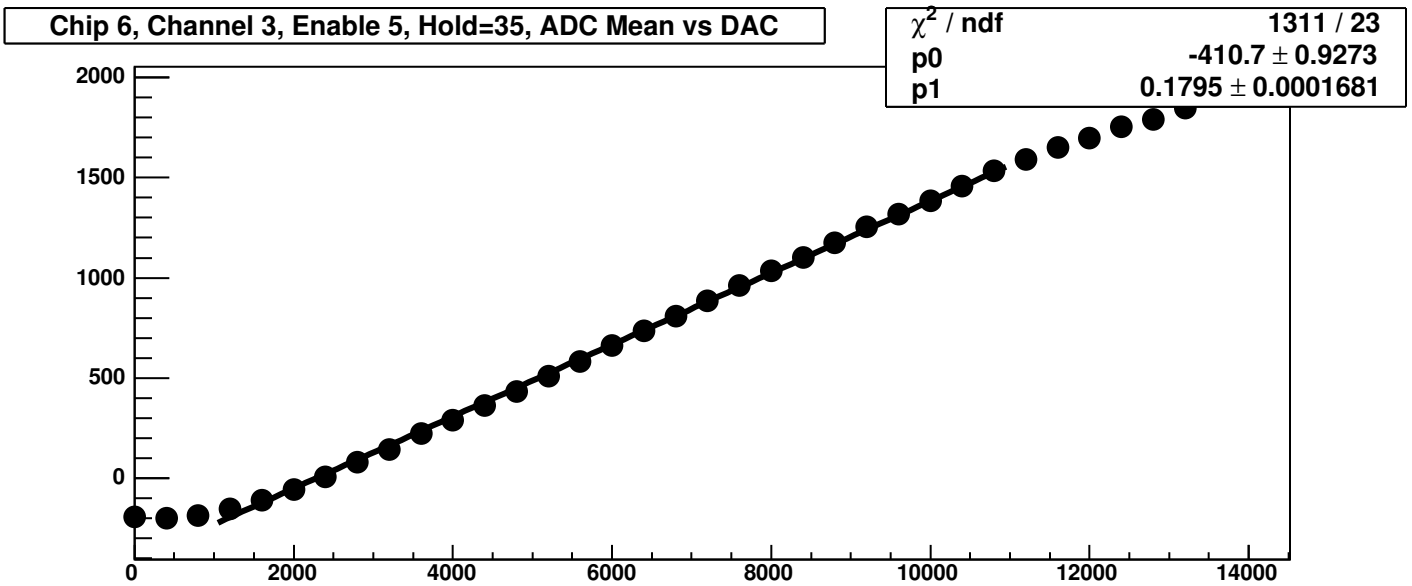

Chip 6, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

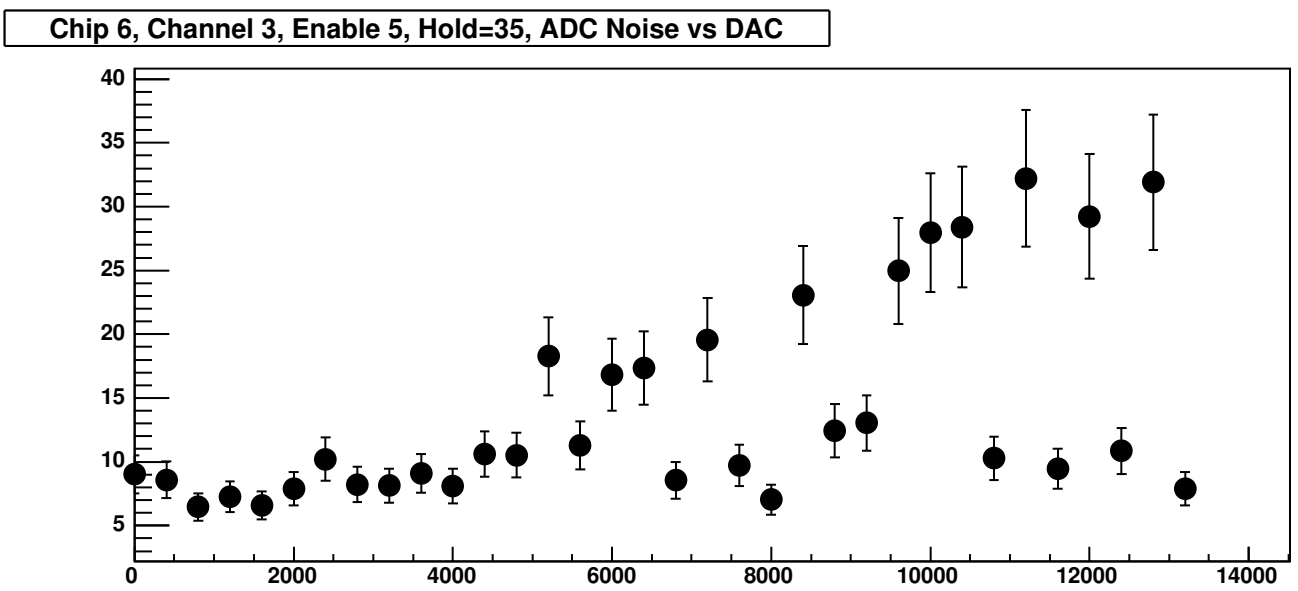

Chip 6, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

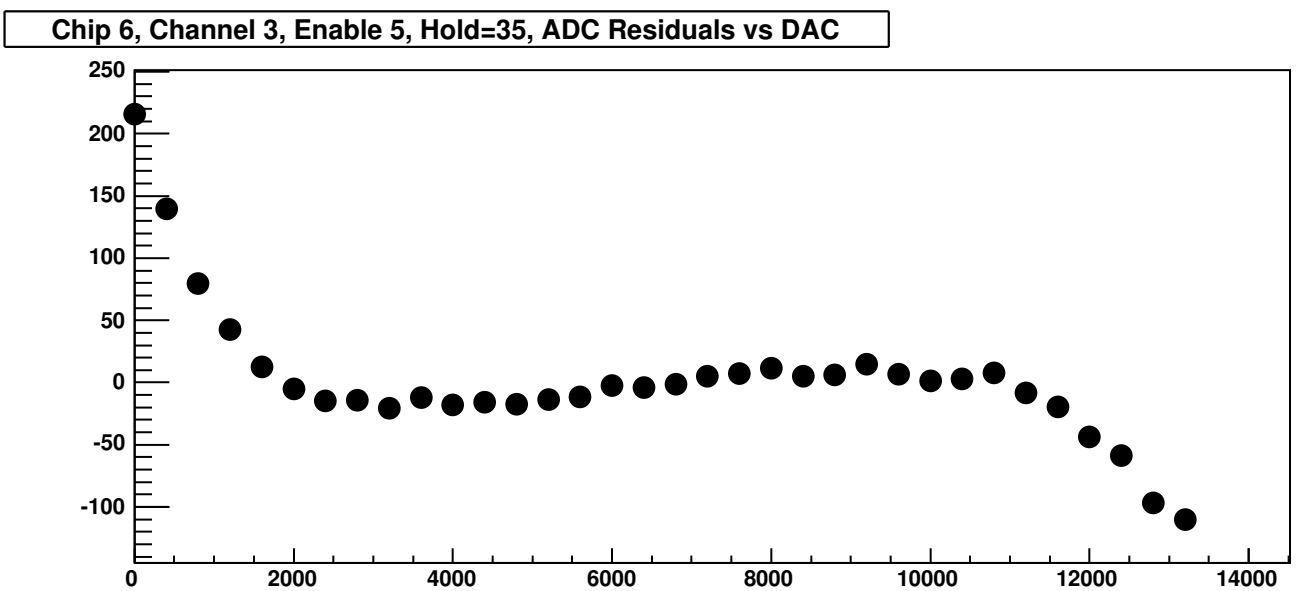

Chip 6, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 35.98 / 23
p0 -236.4 ± 0.8174
p1 0.02694 ± 0.0001221
150
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

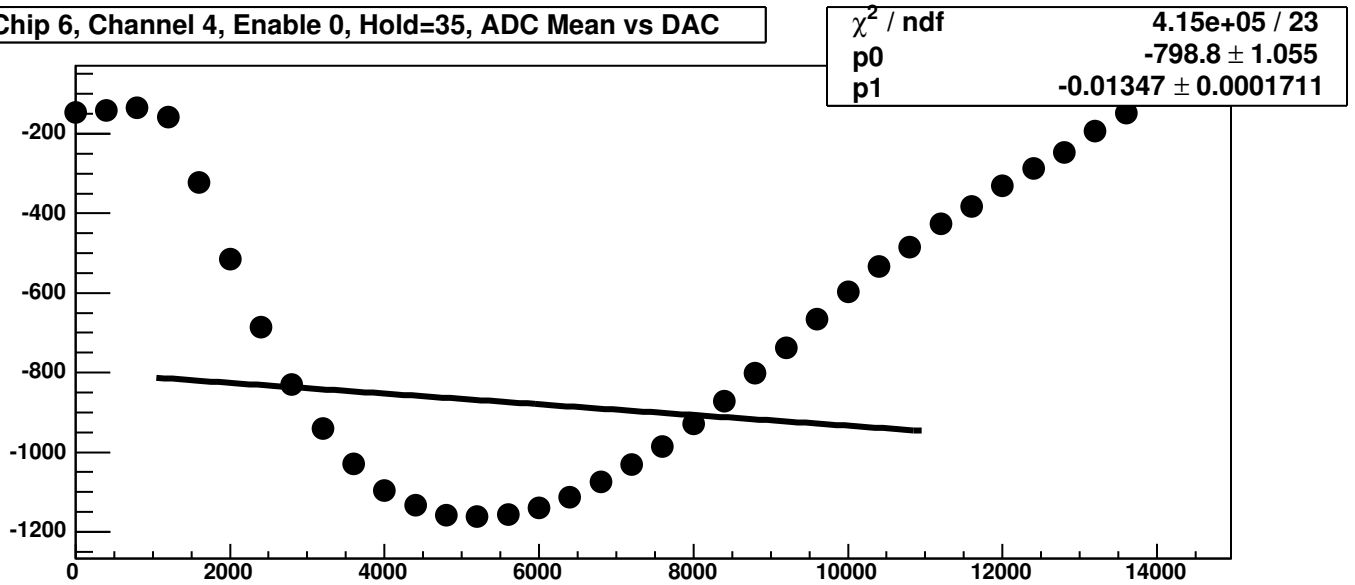

Chip 6, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1311 / 23
p0 -410.7 ± 0.9273
p1 0.1795 ± 0.0001681
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

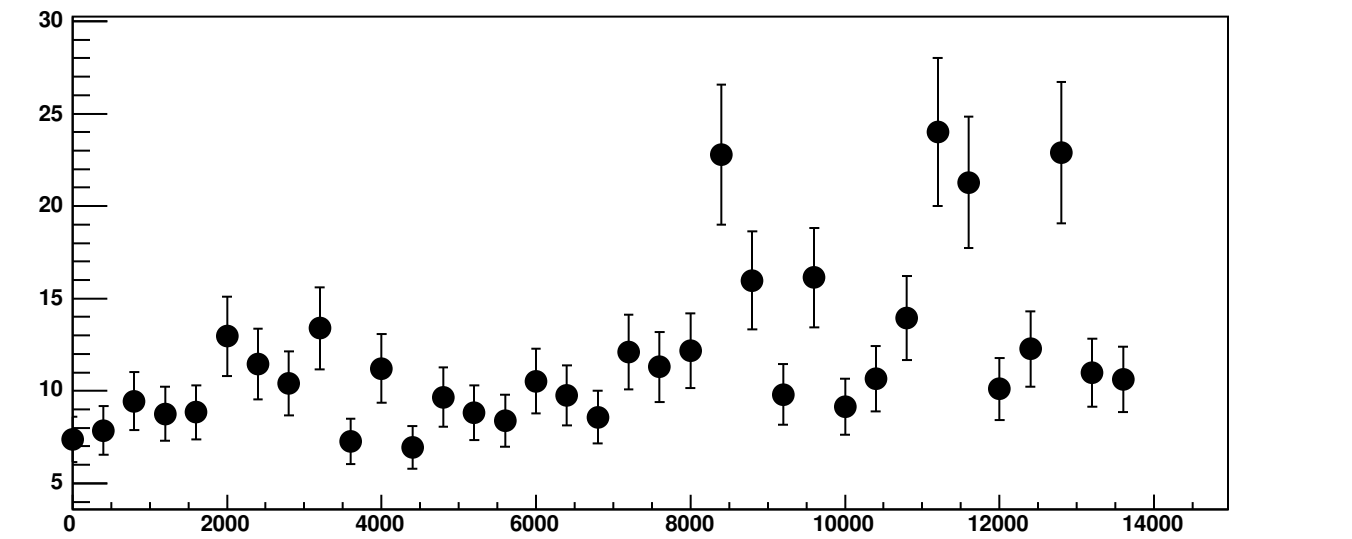

Chip 6, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

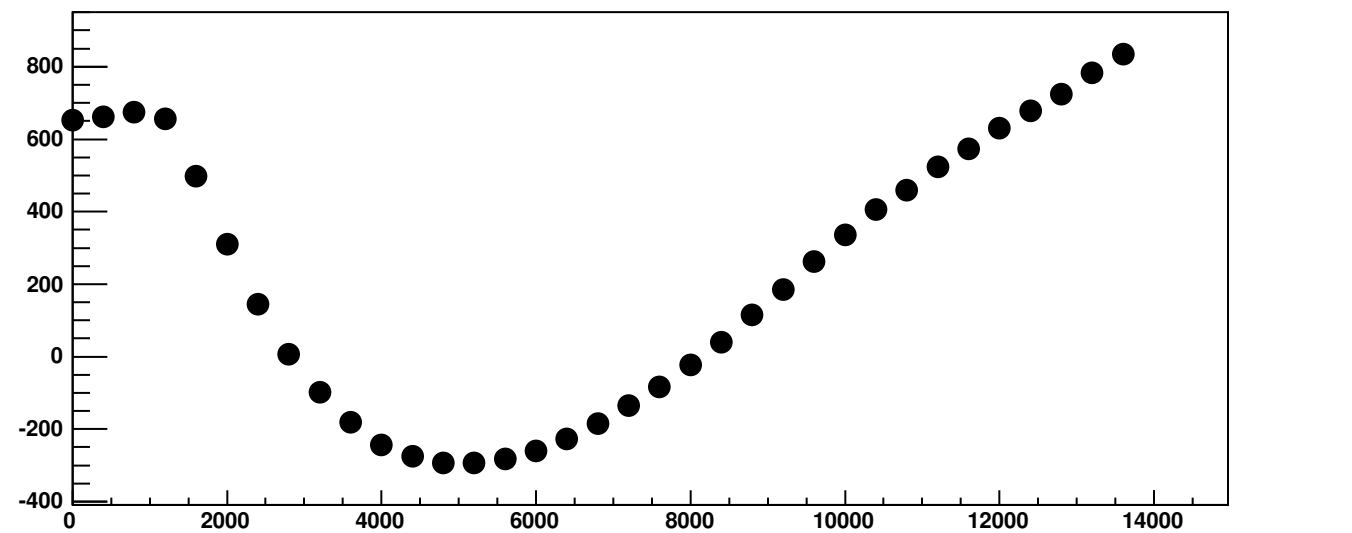
Chip 6, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



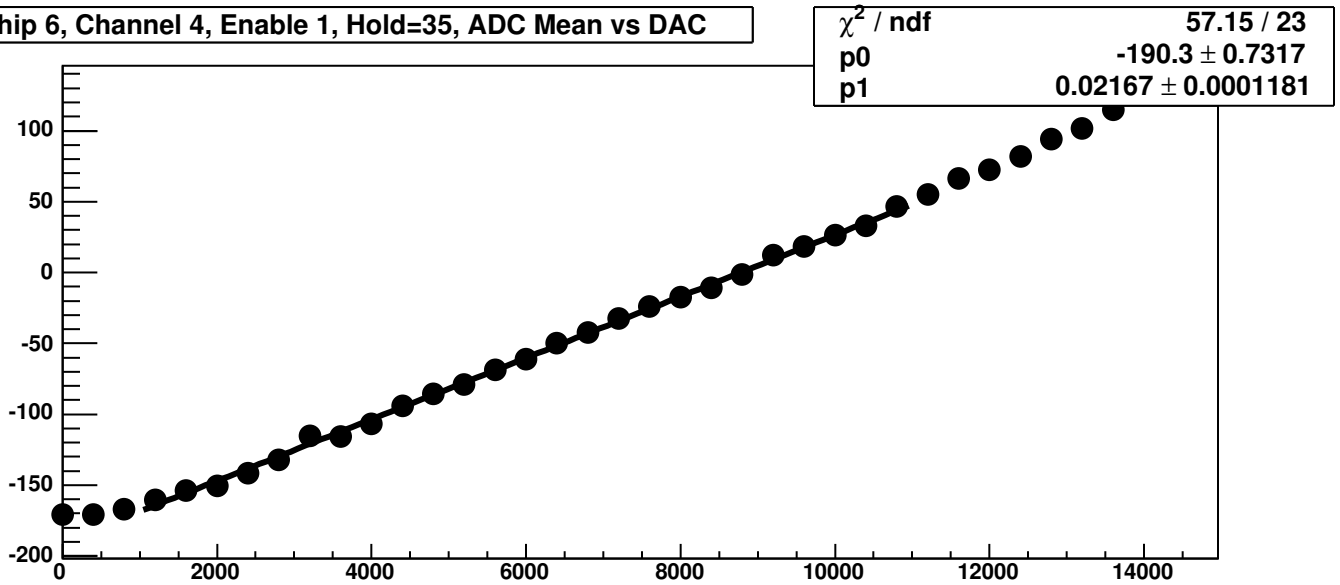
Chip 6, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



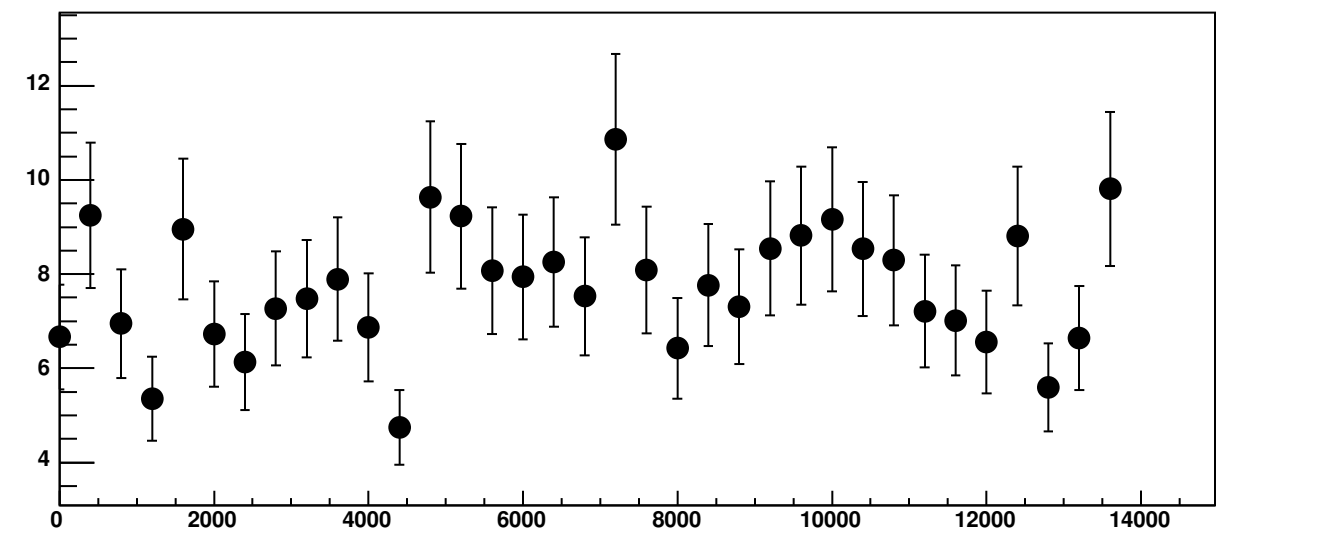
Chip 6, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



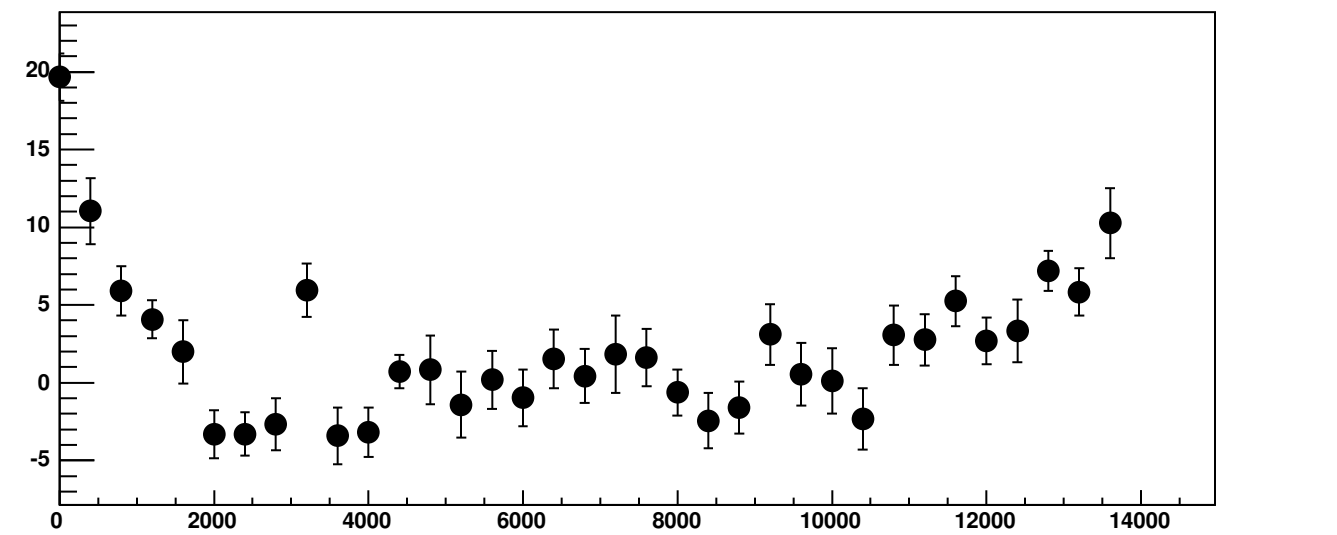
Chip 6, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC

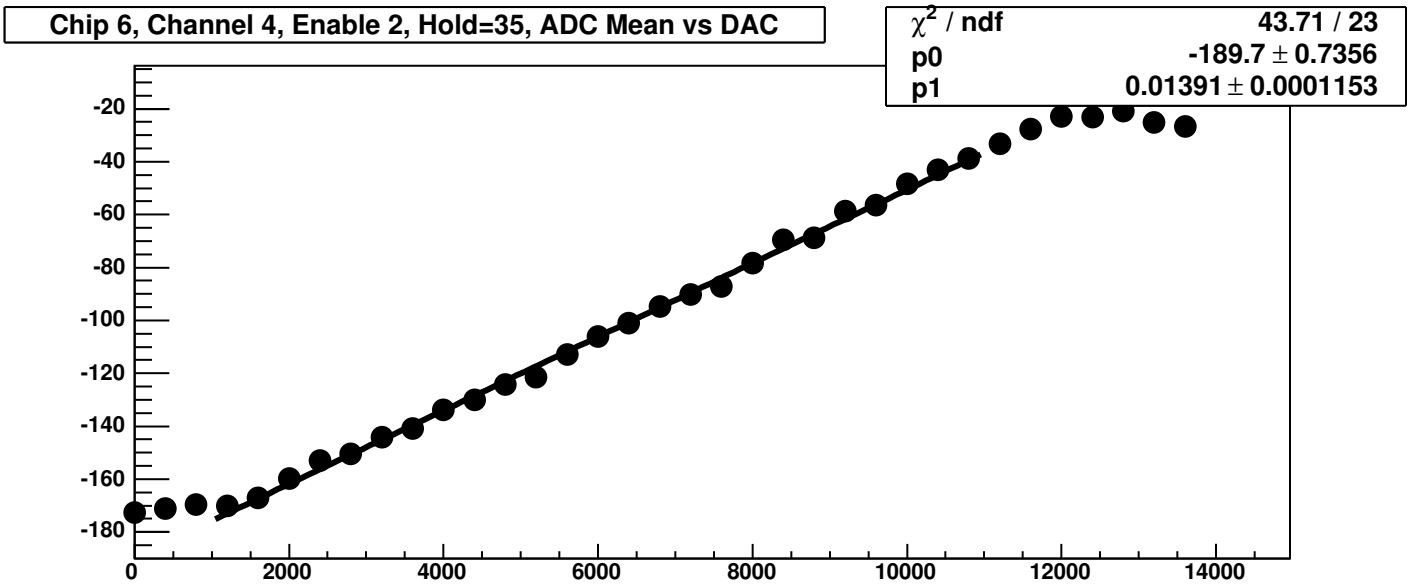


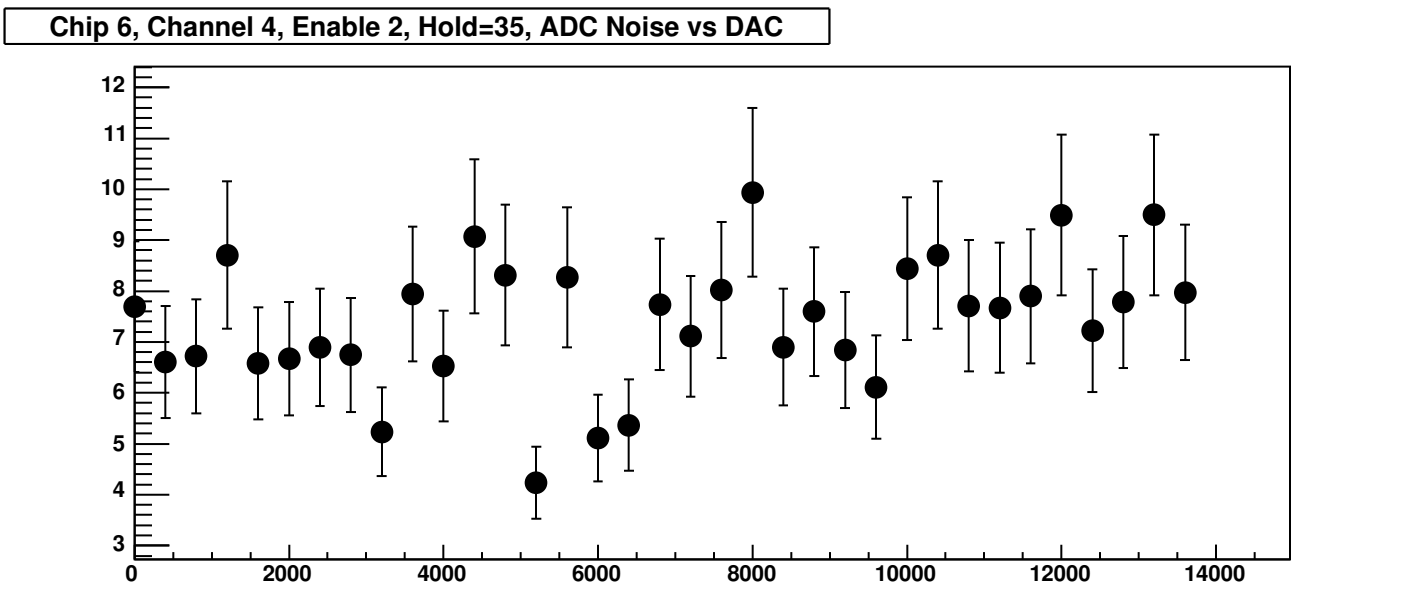
Chip 6, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC

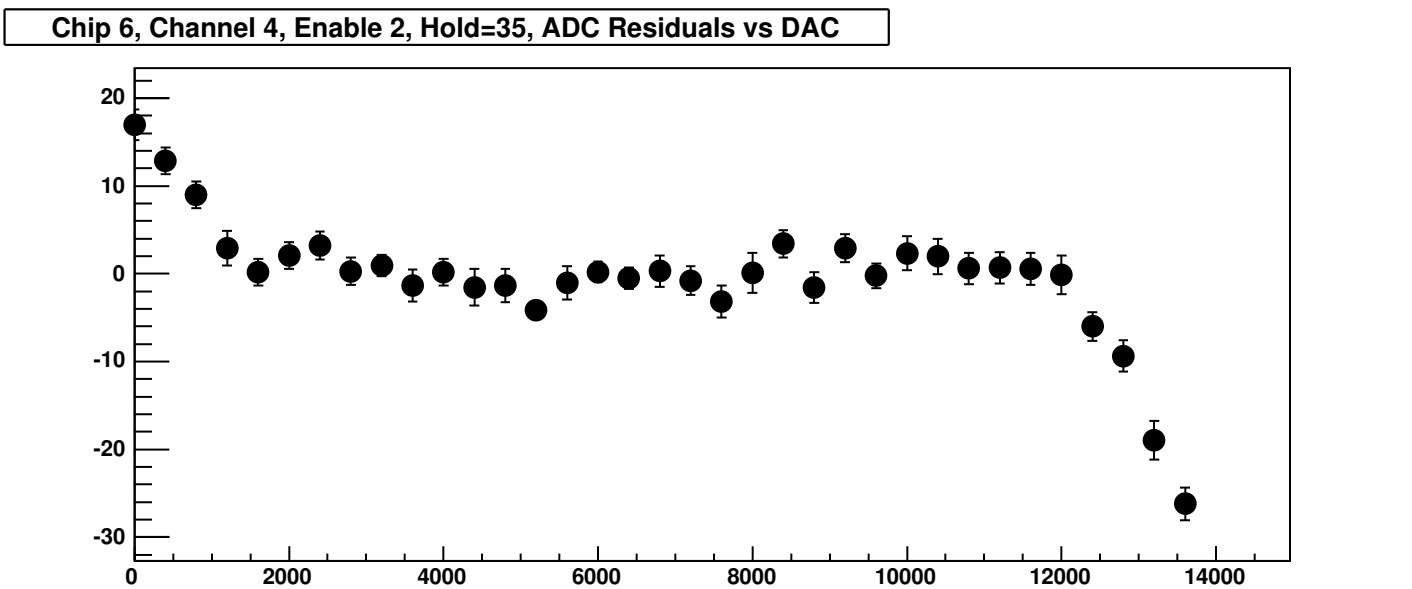


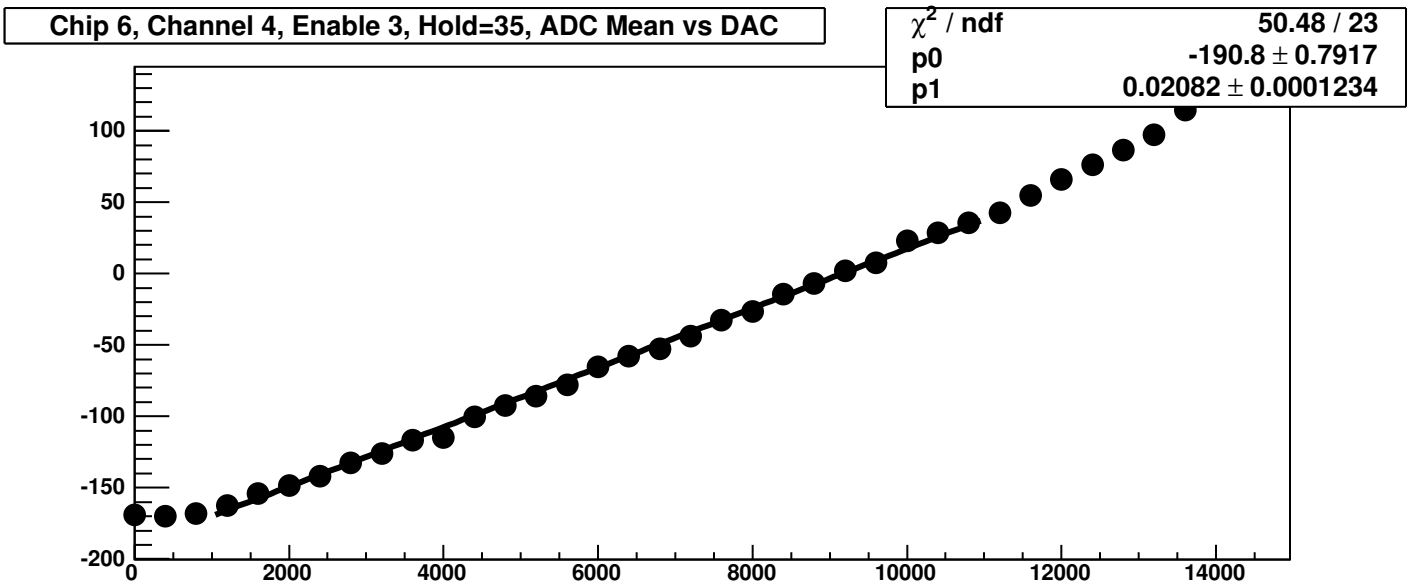
Chip 6, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC

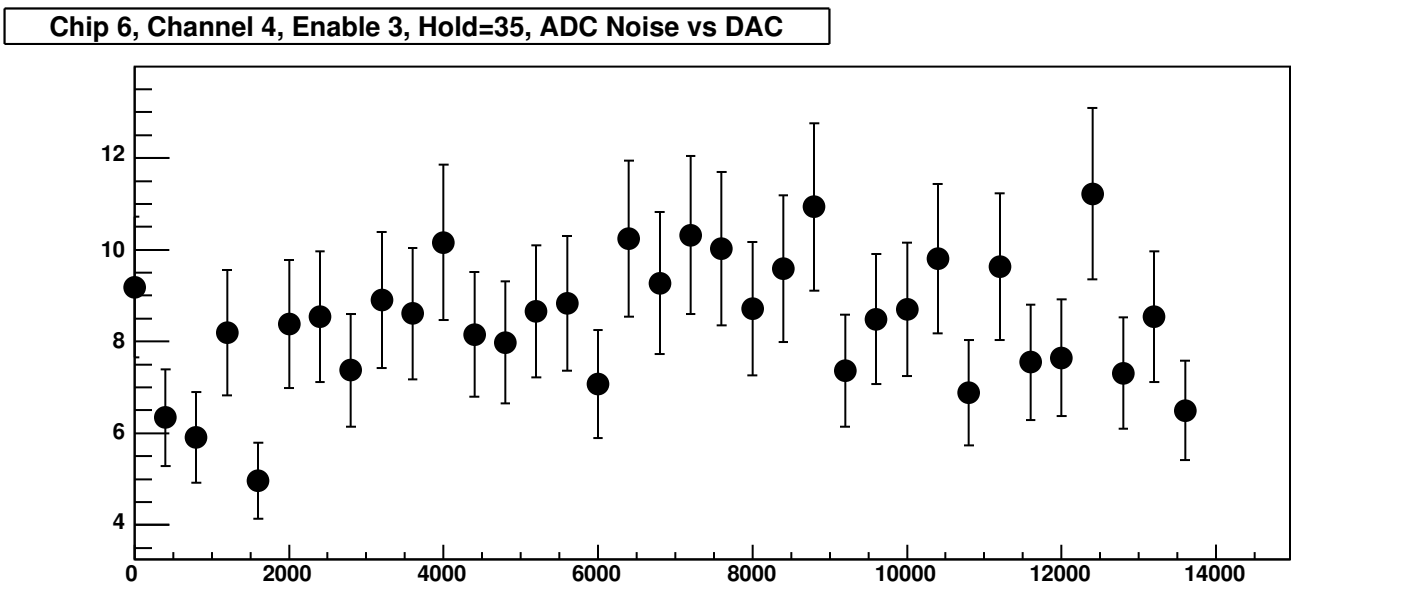


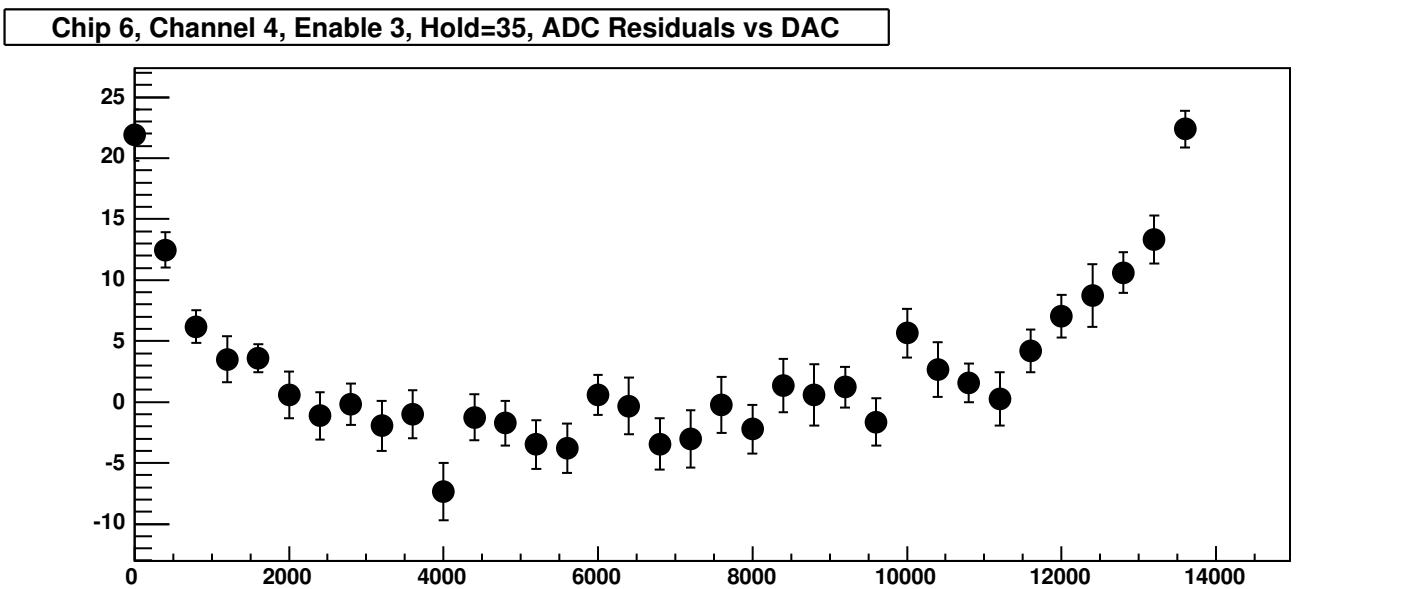

Chip 6, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 43.71 / 23
p0 -189.7 ± 0.7356
p1 0.01391 ± 0.0001153
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000

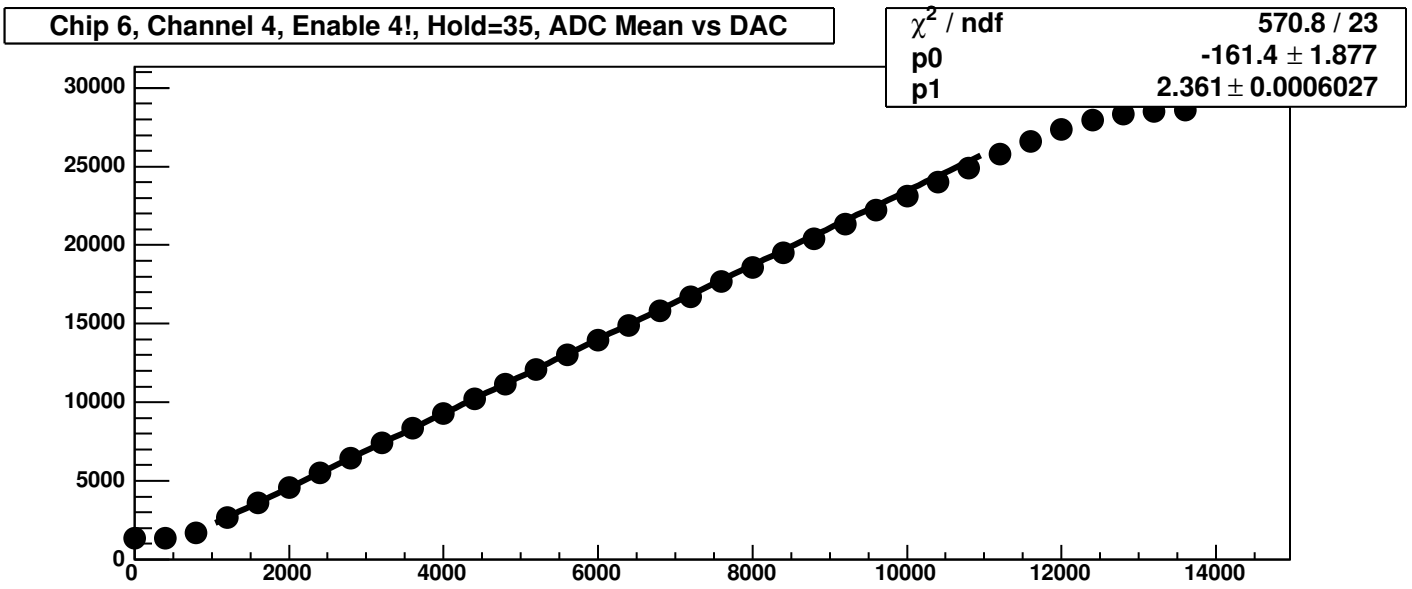

Chip 6, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

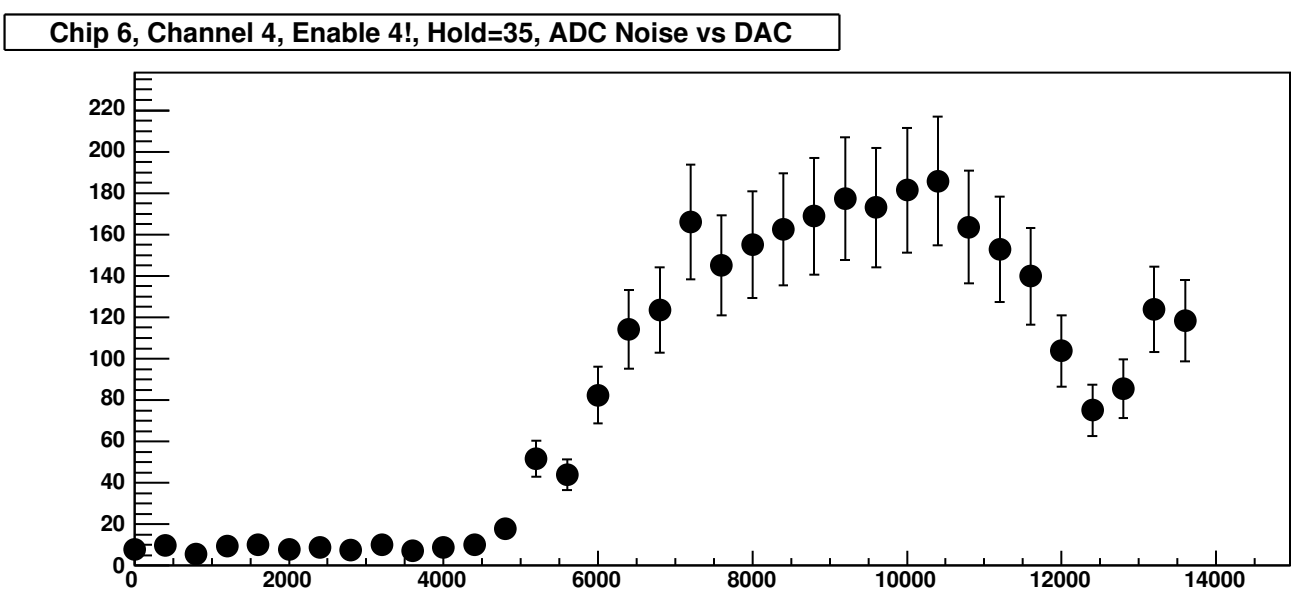

Chip 6, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

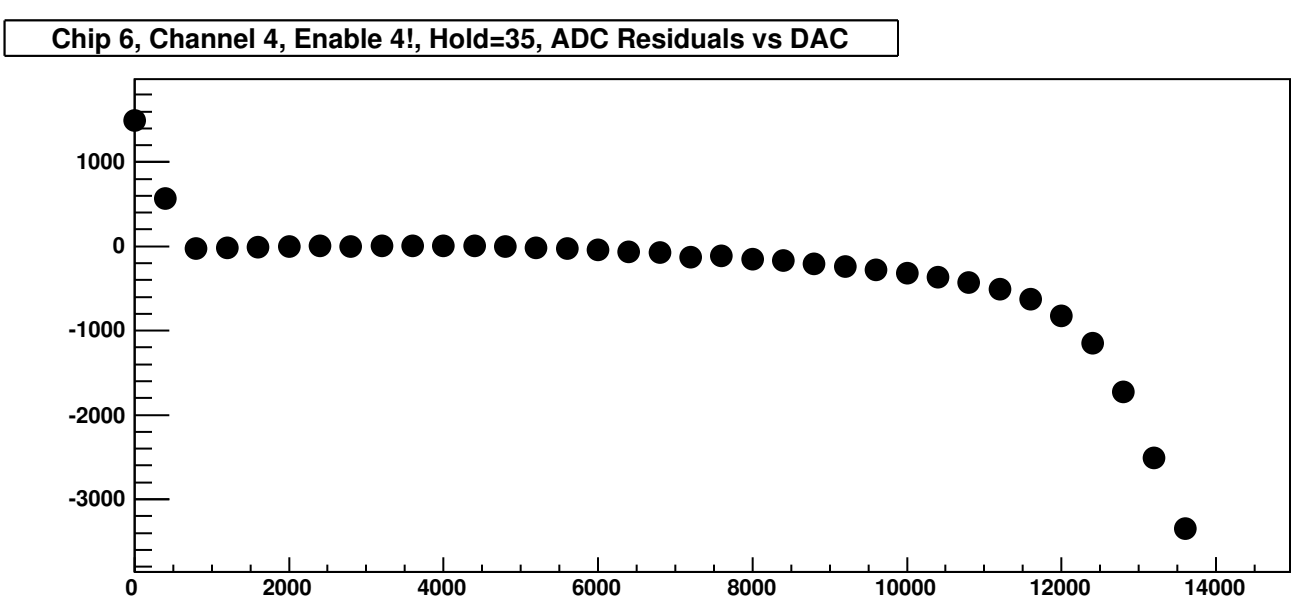

Chip 6, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 50.48 / 23
p0 -190.8 ± 0.7917
p1 0.02082 ± 0.0001234
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

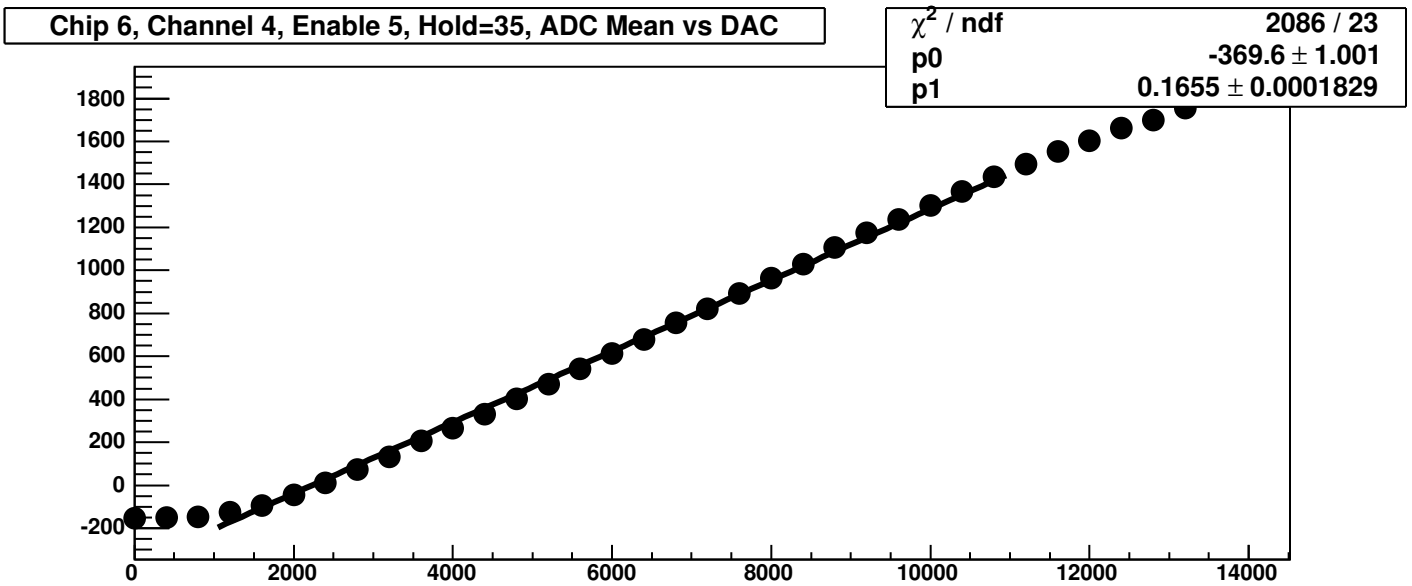

Chip 6, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

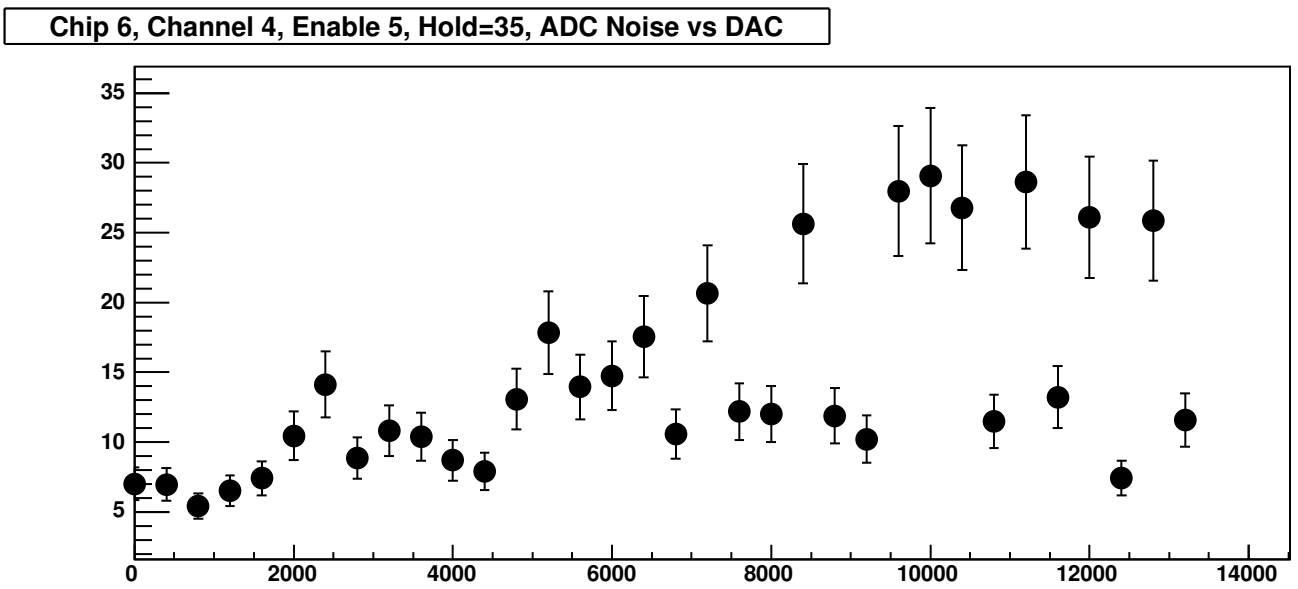

Chip 6, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

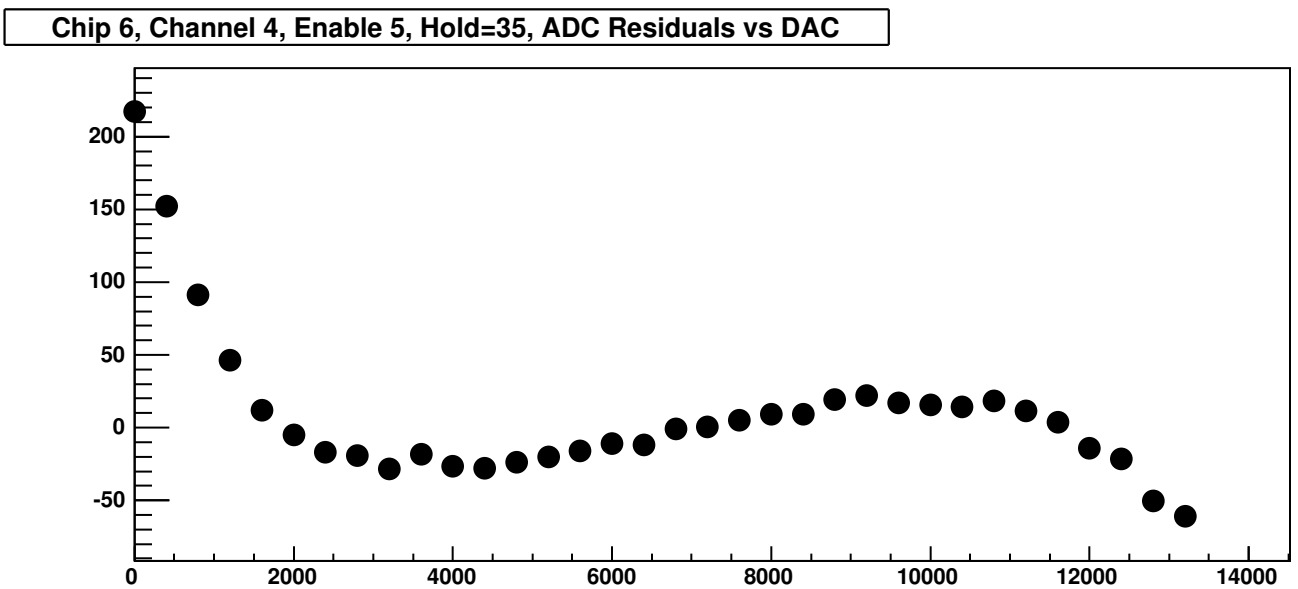

Chip 6, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 570.8 / 23
p0 -161.4 ± 1.877
p1 2.361 ± 0.0006027
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

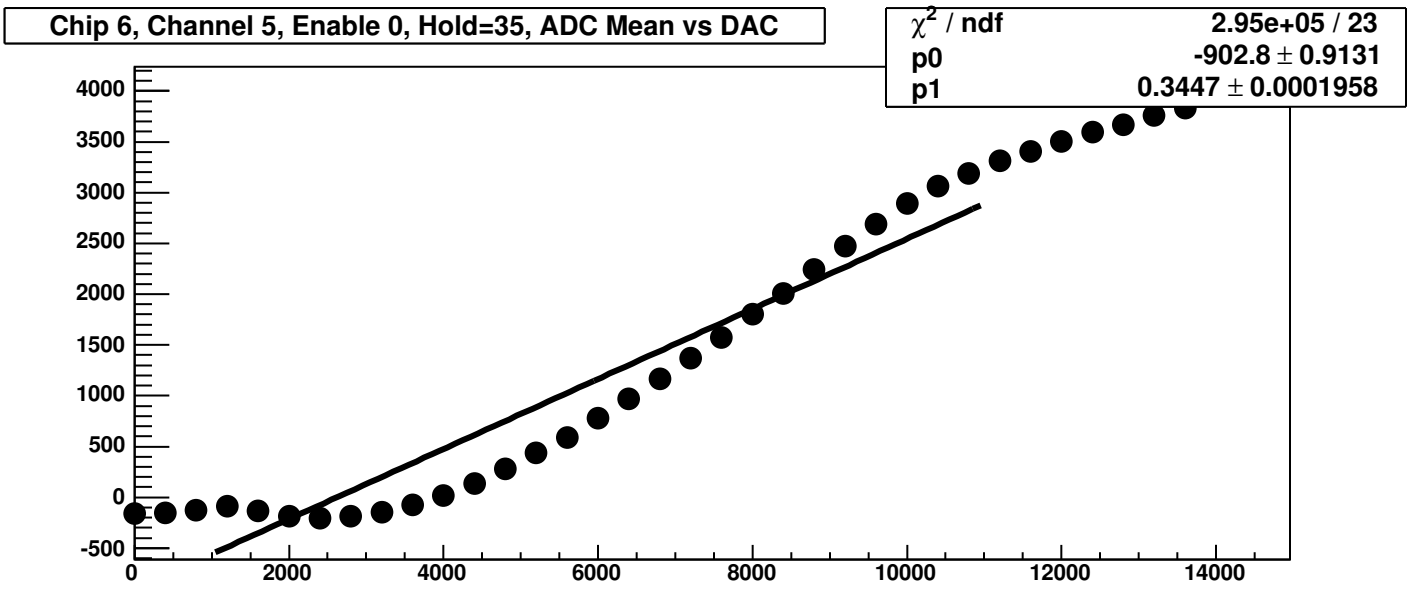

Chip 6, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

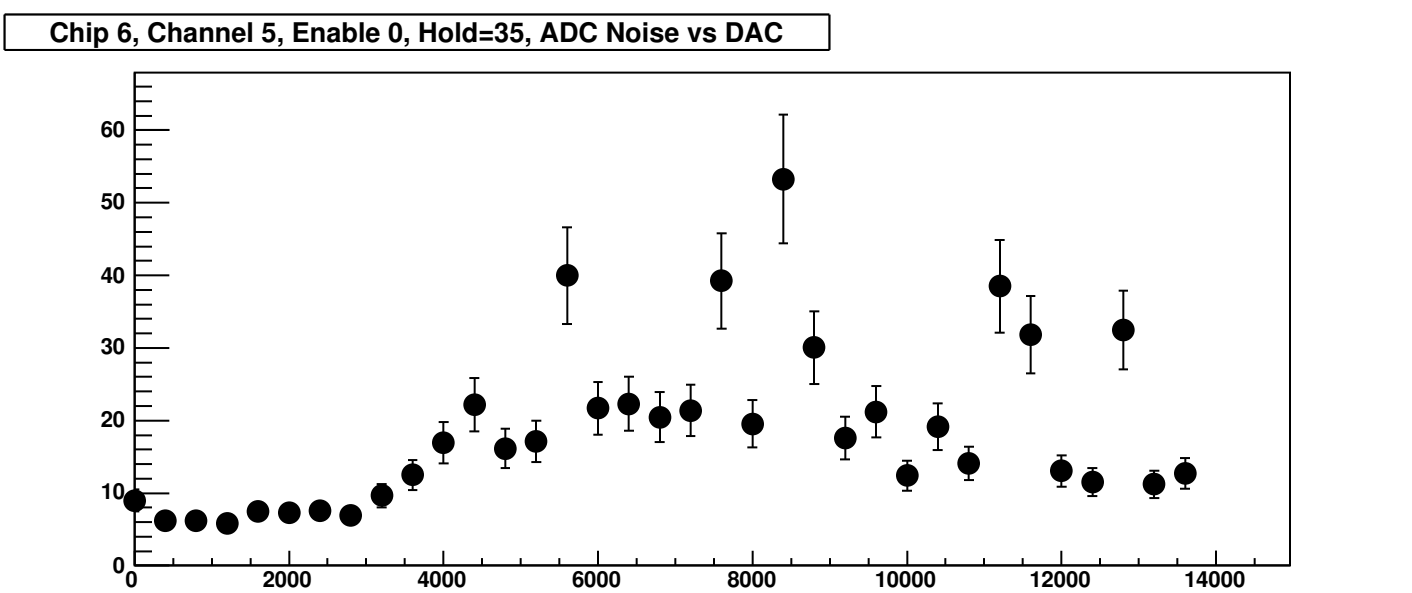

Chip 6, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

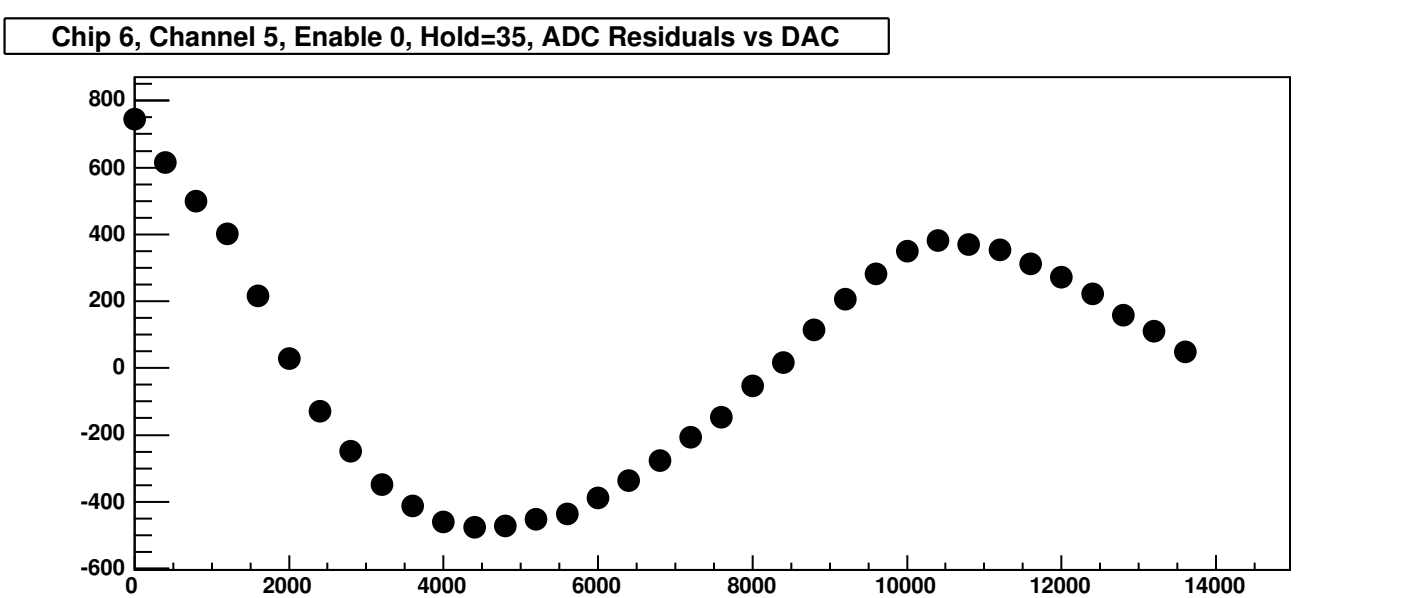

Chip 6, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2086 / 23
p0 -369.6 ± 1.001
p1 0.1655 ± 0.0001829
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

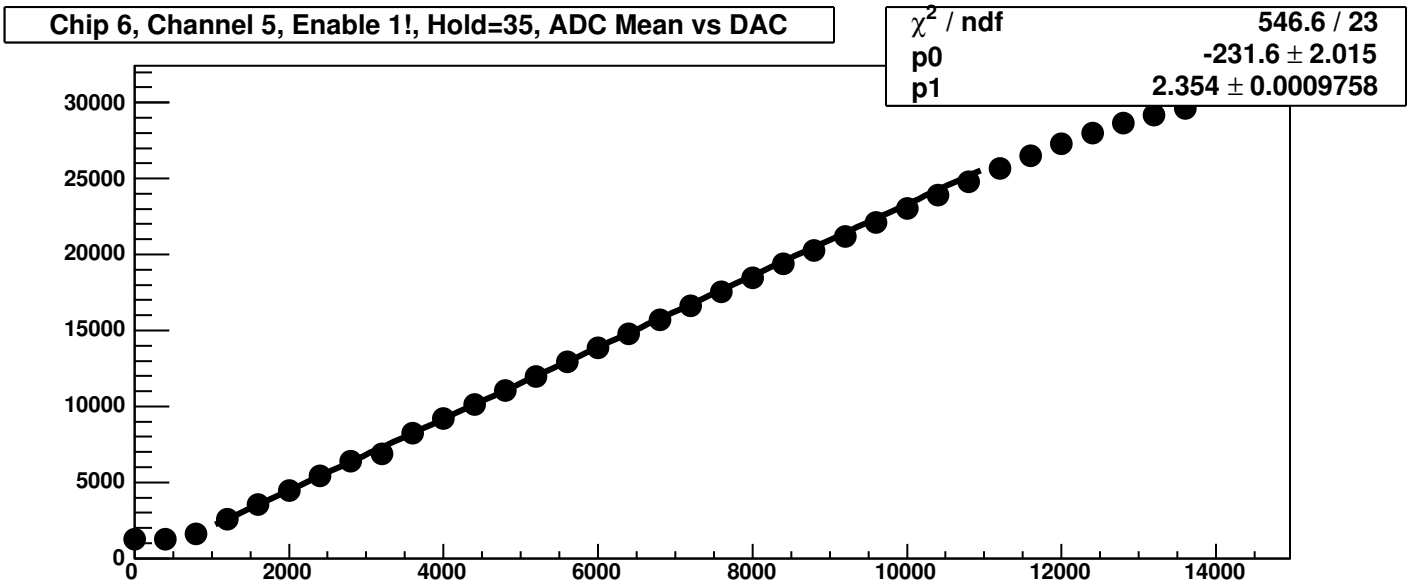

Chip 6, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

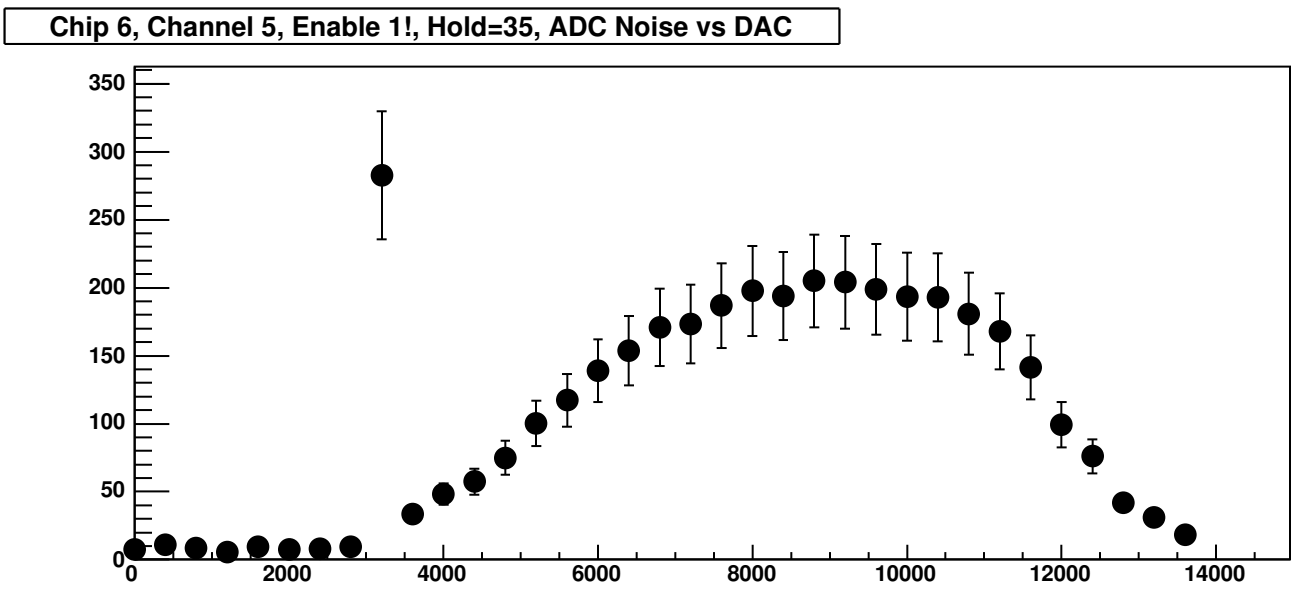

Chip 6, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

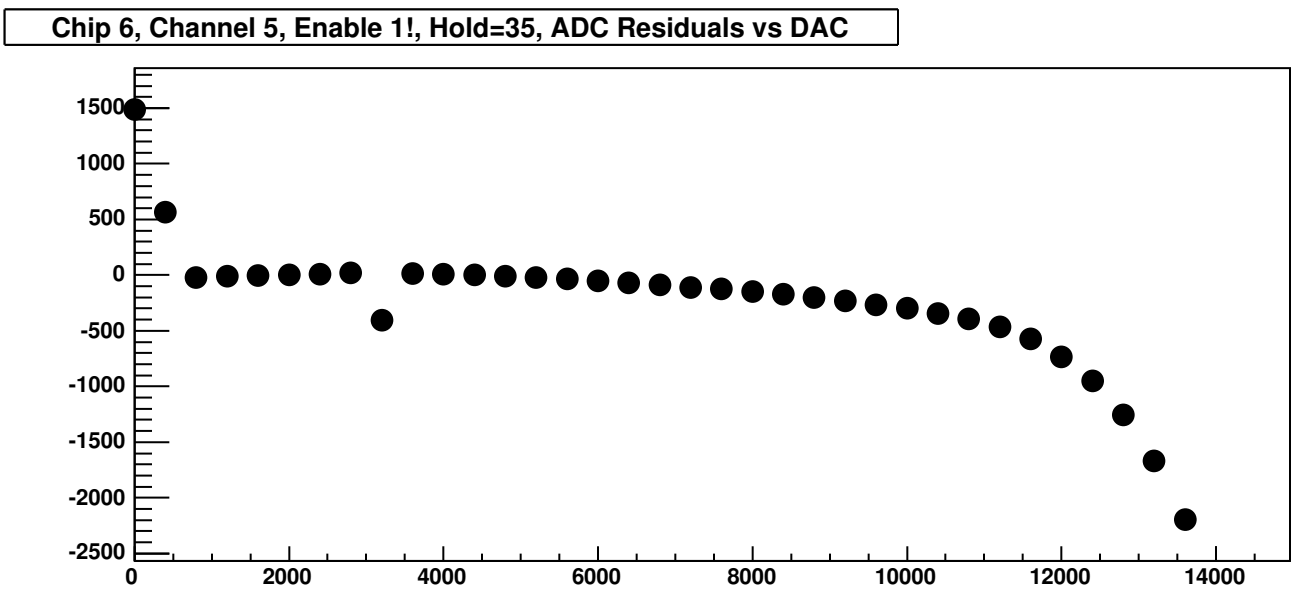

Chip 6, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.95e+05 / 23
p0 -902.8 ± 0.9131
p1 0.3447 ± 0.0001958
4000
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

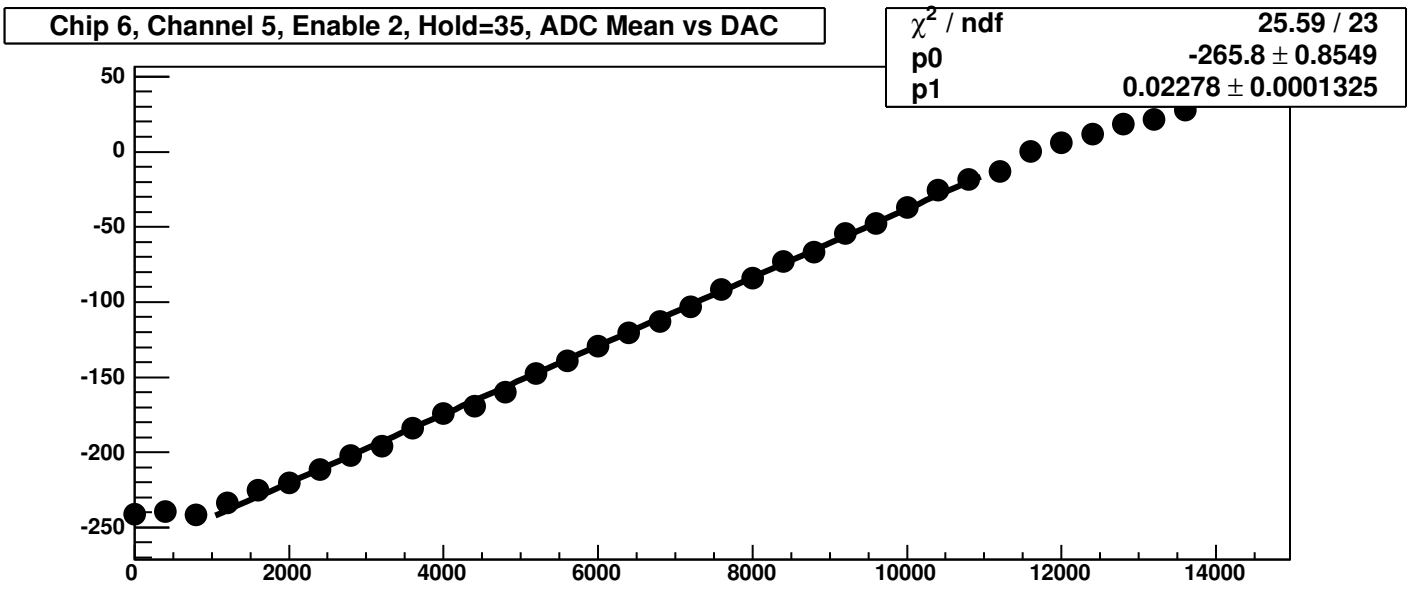

Chip 6, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

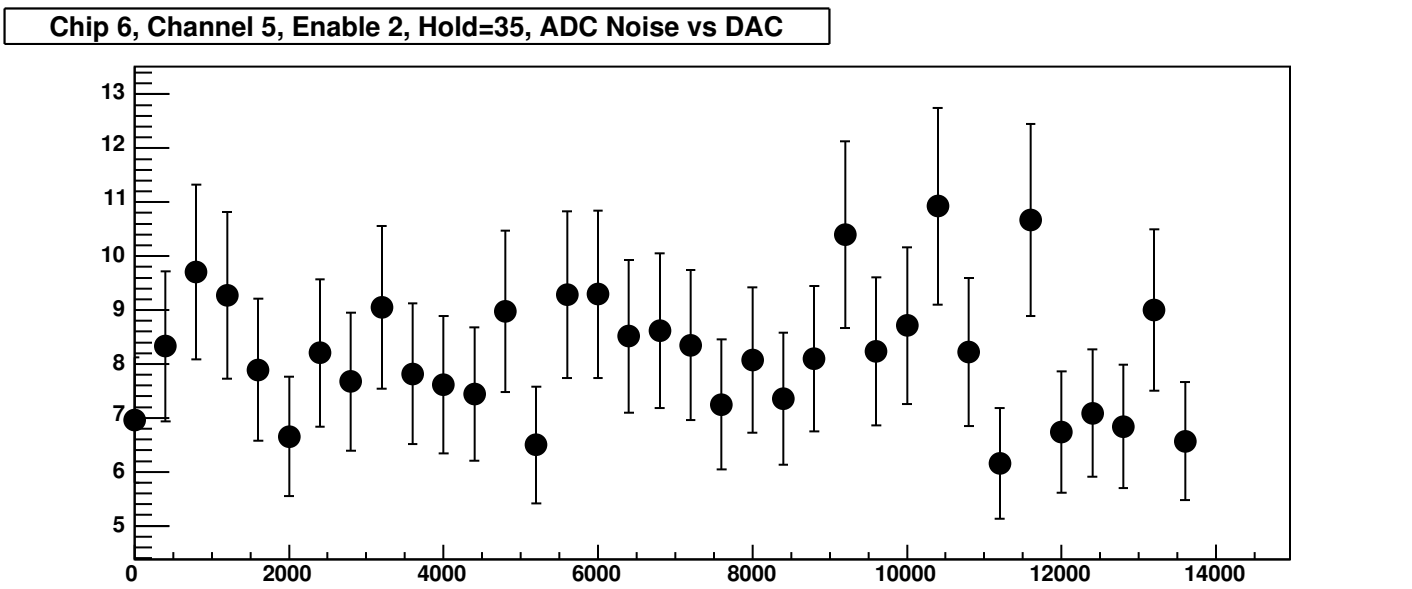

Chip 6, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

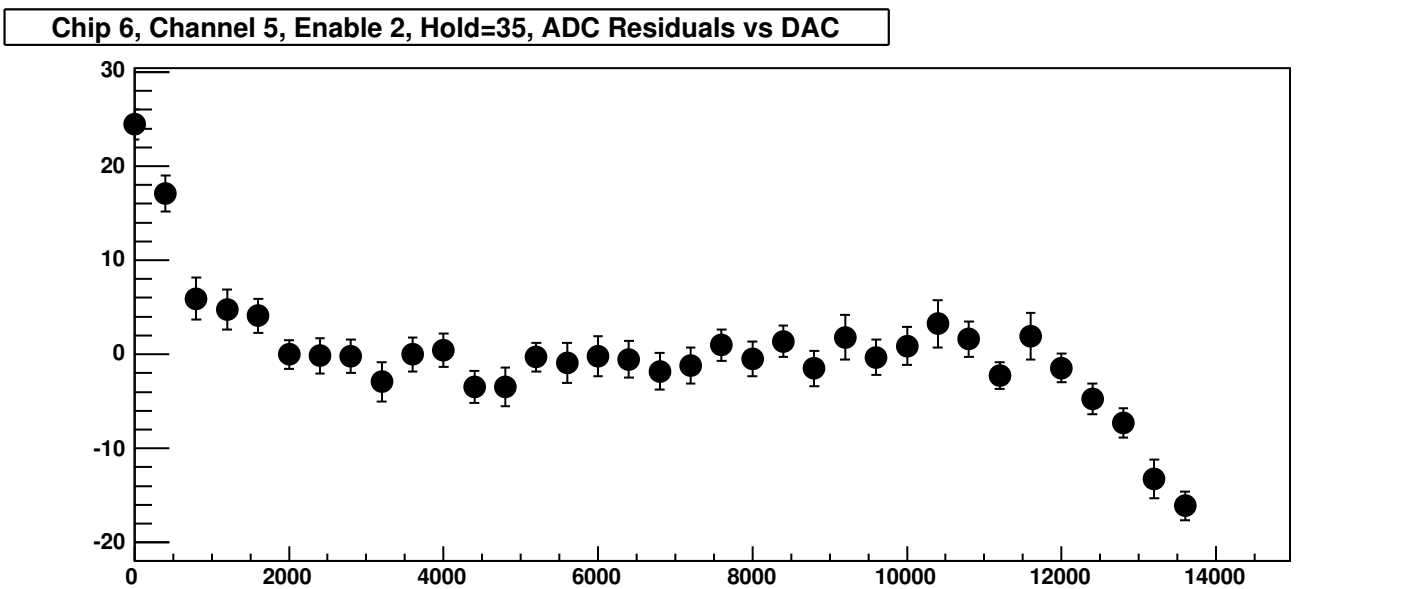

Chip 6, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 546.6 / 23
p0 -231.6 ± 2.015
p1 2.354 ± 0.0009758
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

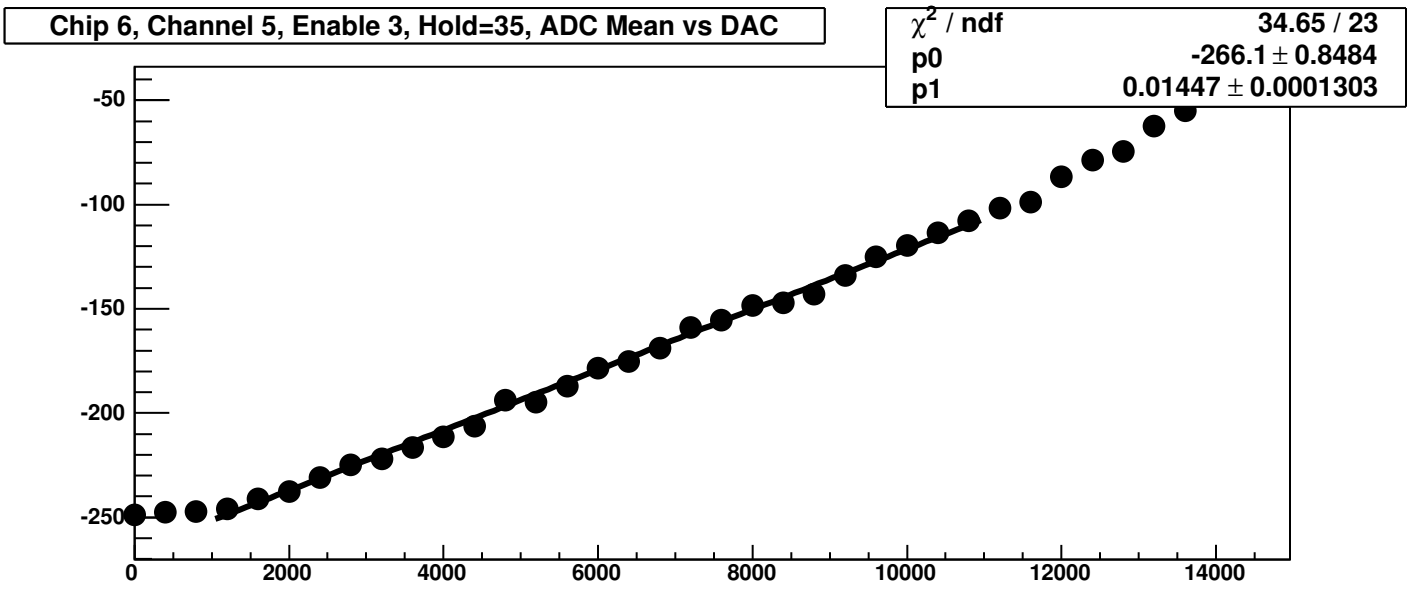

Chip 6, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

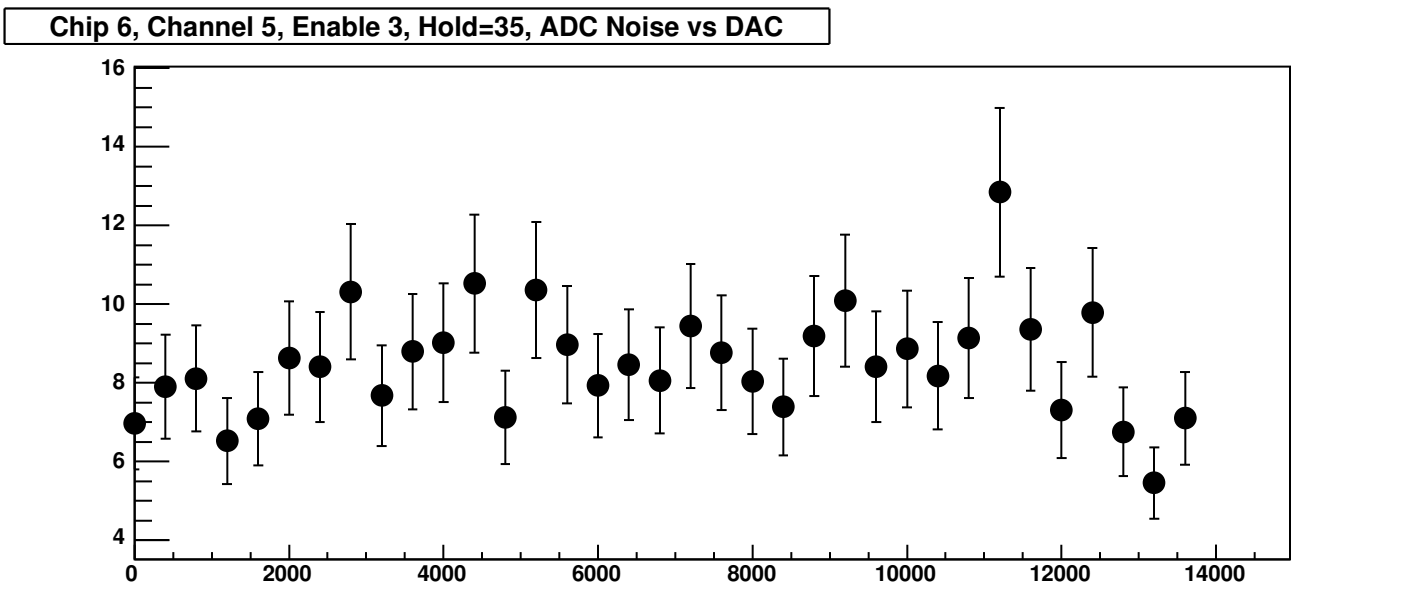

Chip 6, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

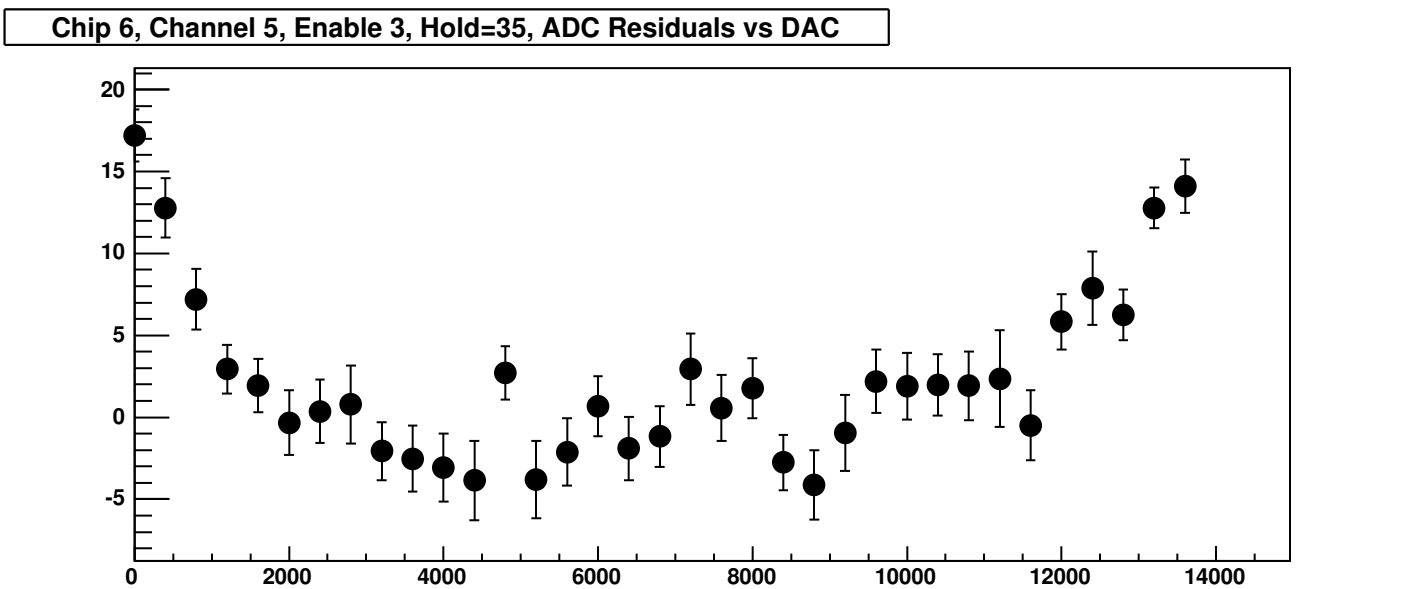

Chip 6, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 25.59 / 23
p0 -265.8 ± 0.8549
p1 0.02278 ± 0.0001325
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

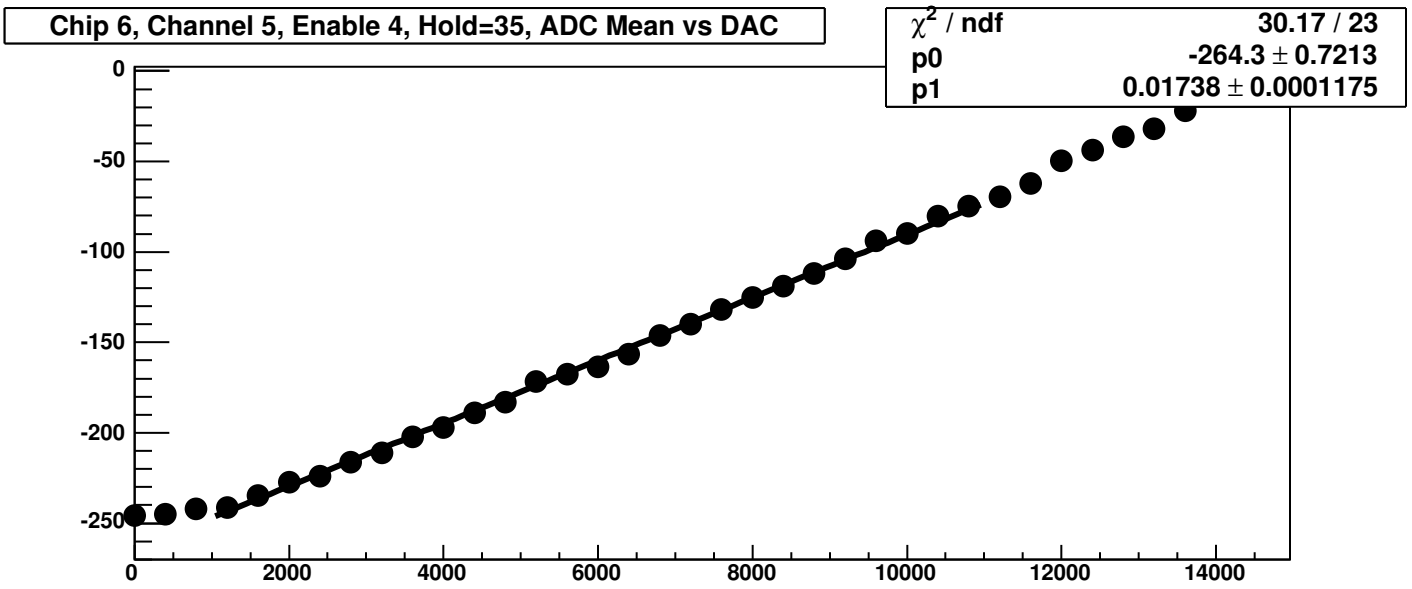

Chip 6, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

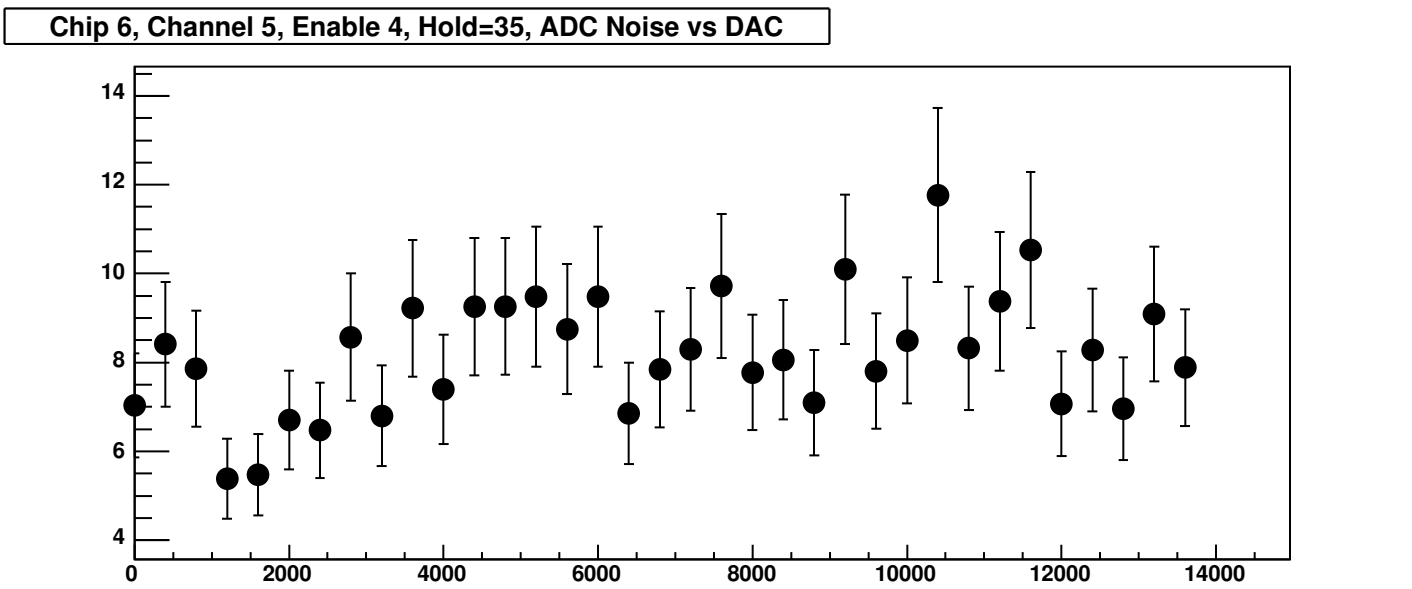

Chip 6, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

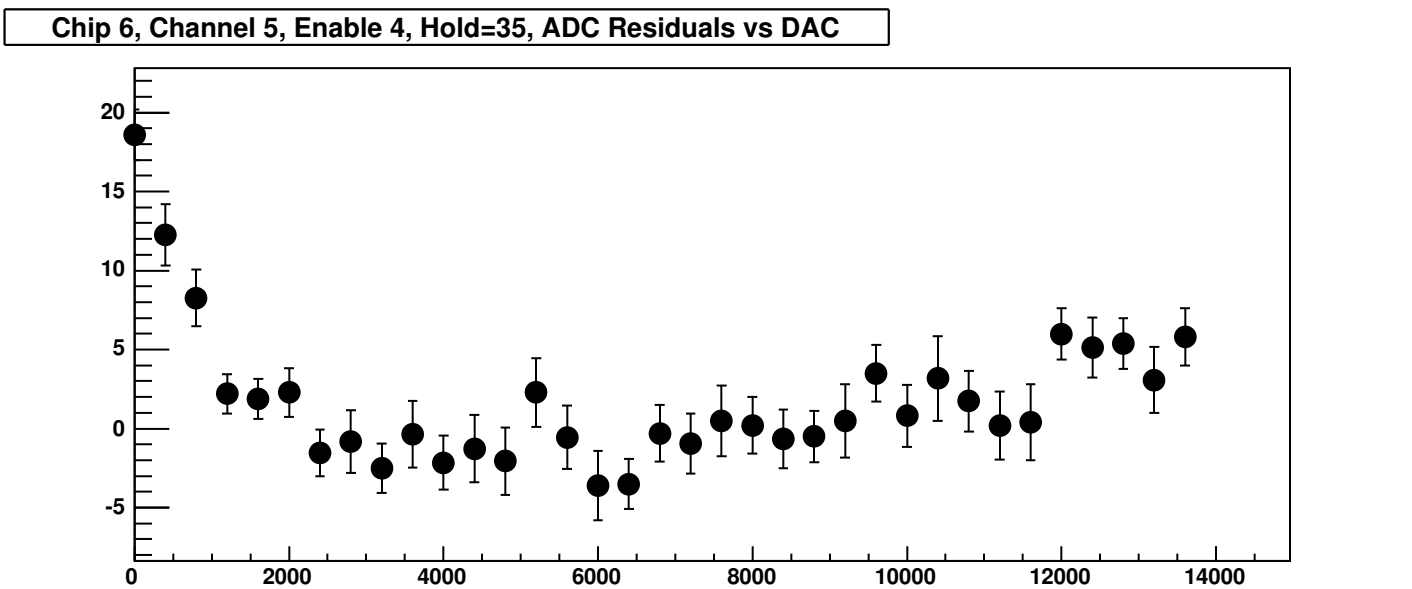

Chip 6, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 34.65 / 23
p0 -266.1 ± 0.8484
p1 0.01447 ± 0.0001303
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

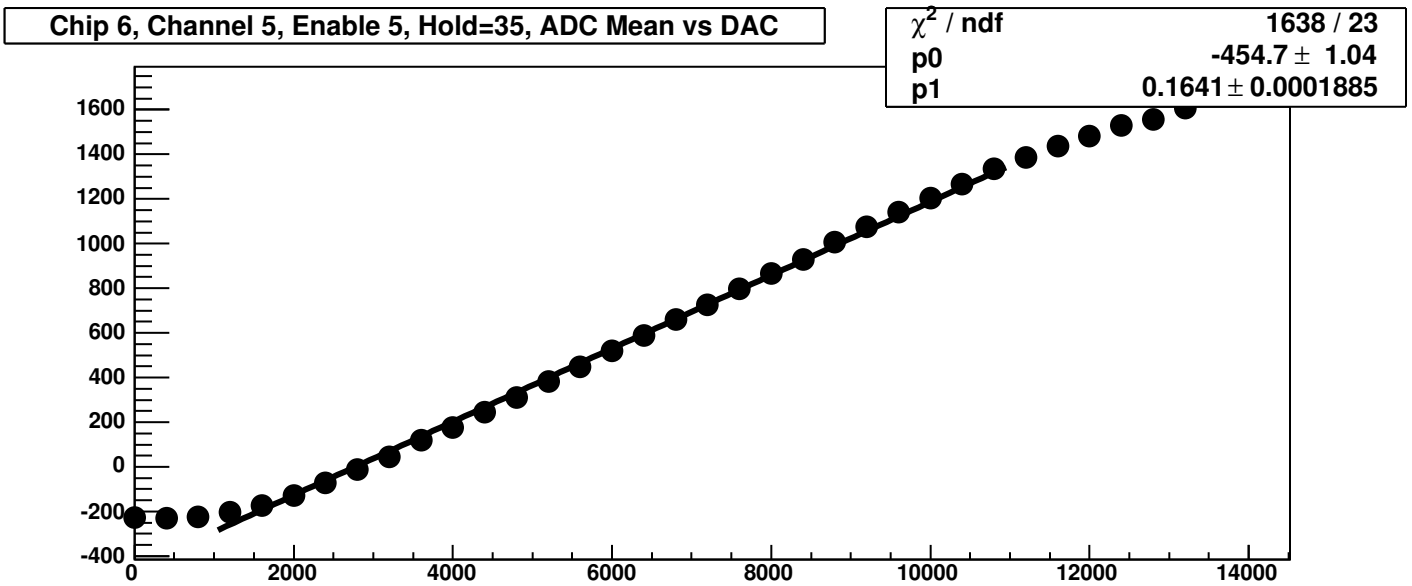

Chip 6, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

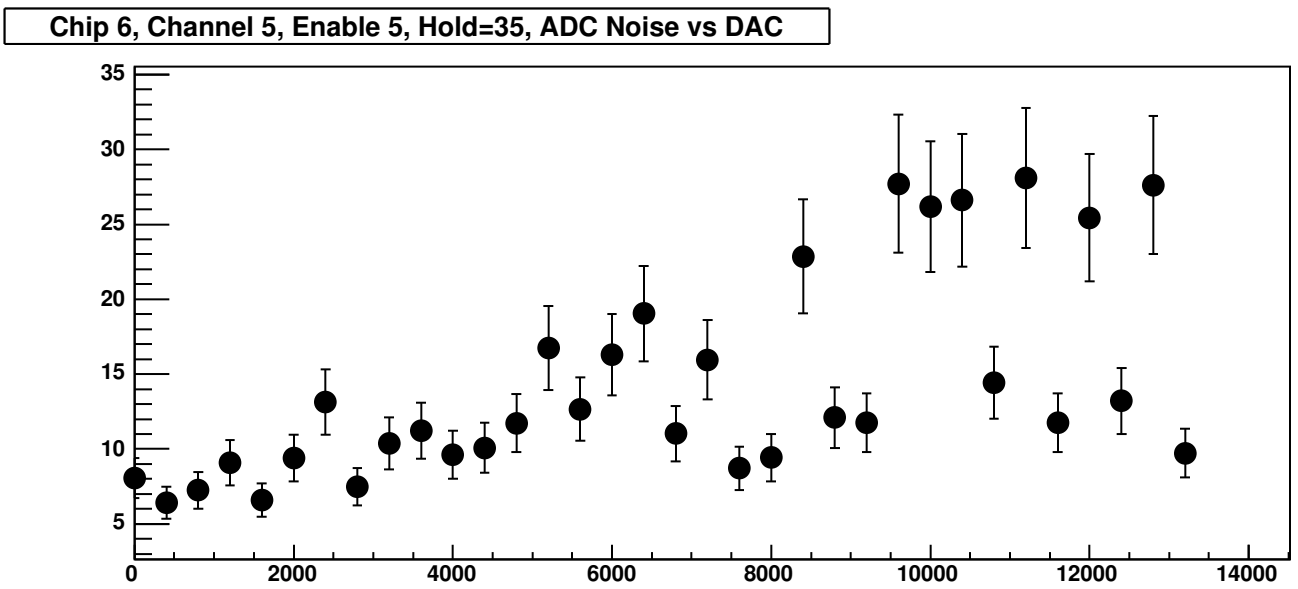

Chip 6, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

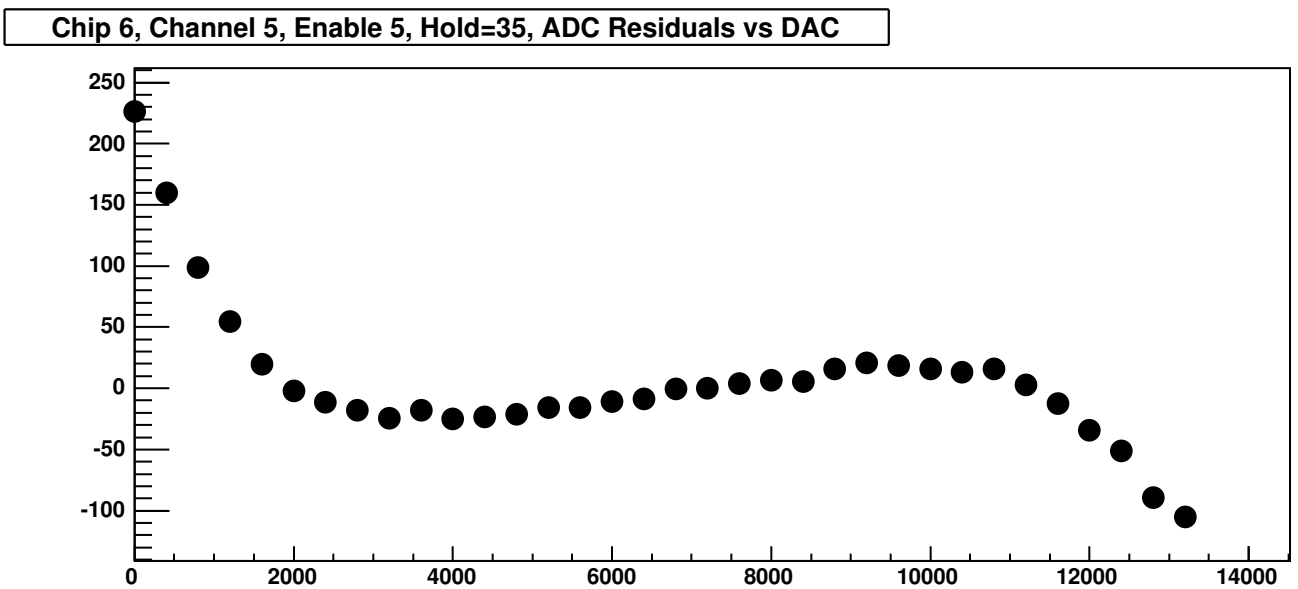

Chip 6, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.17 / 23
p0 -264.3 ± 0.7213
p1 0.01738 ± 0.0001175
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

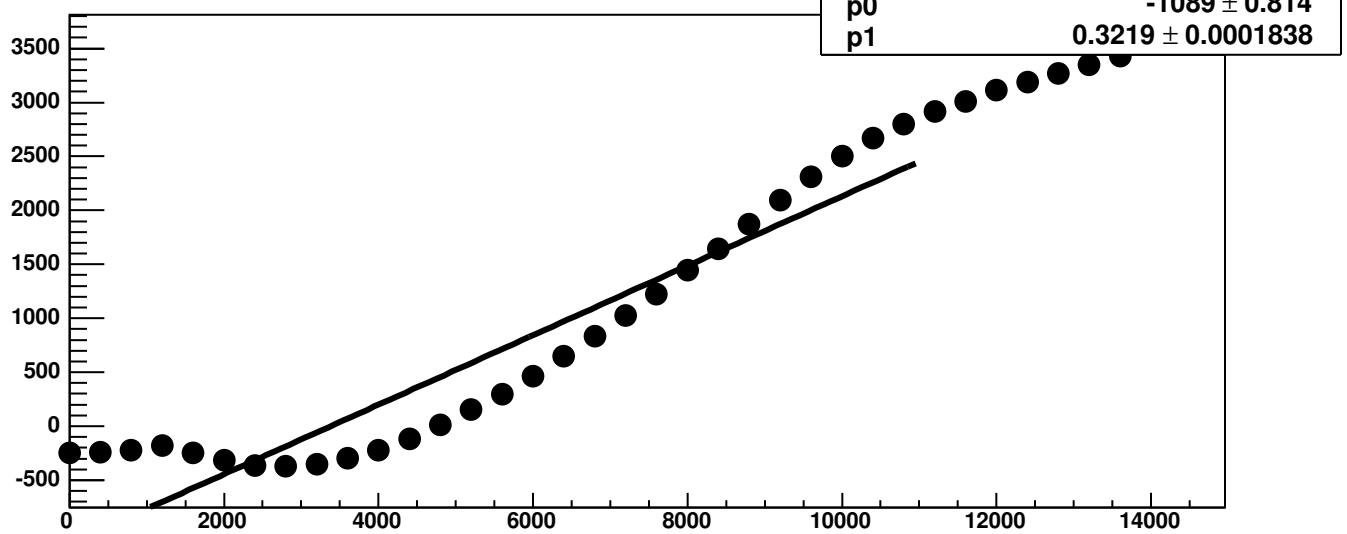

Chip 6, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1638 / 23
p0 -454.7 ± 1.04
p1 0.1641 ± 0.0001885
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

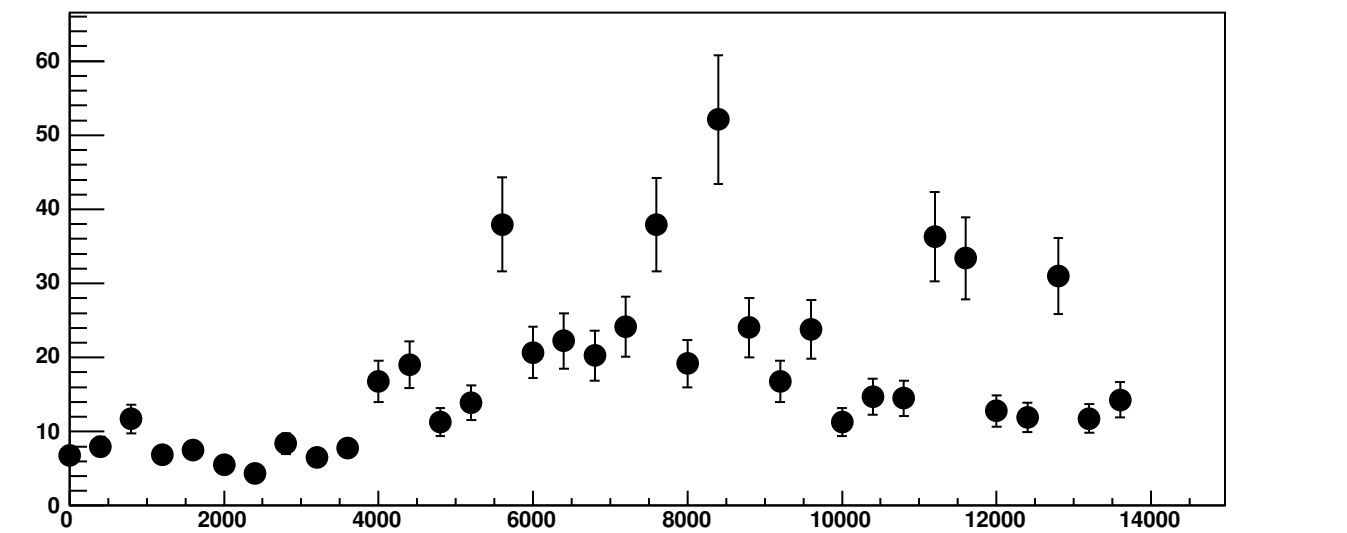

Chip 6, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

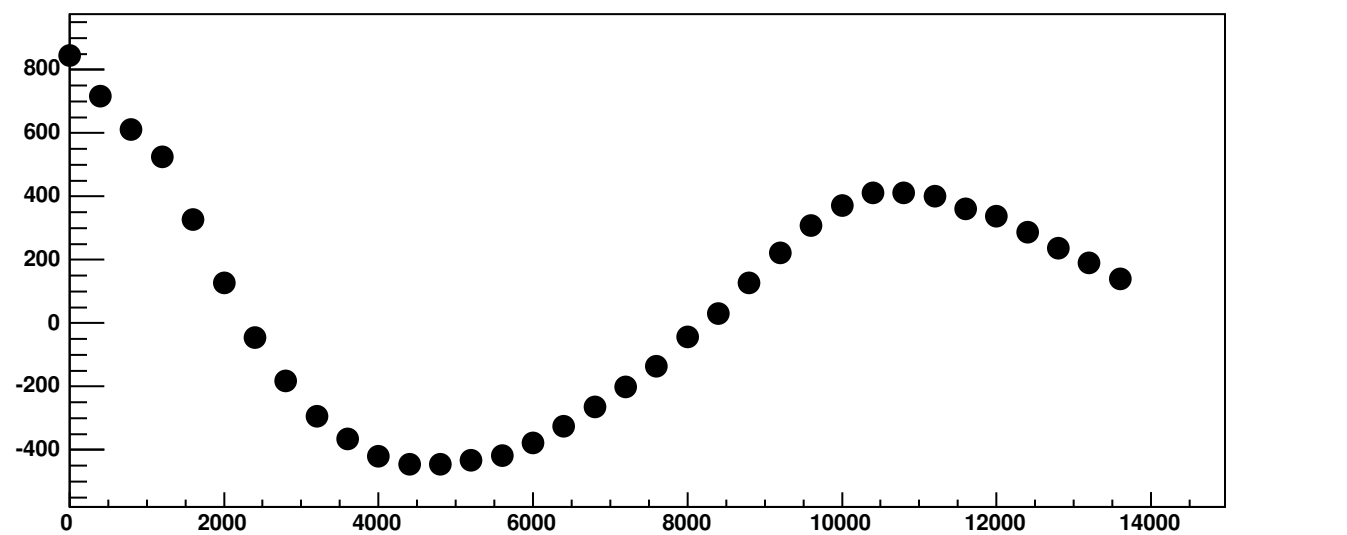
Chip 6, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

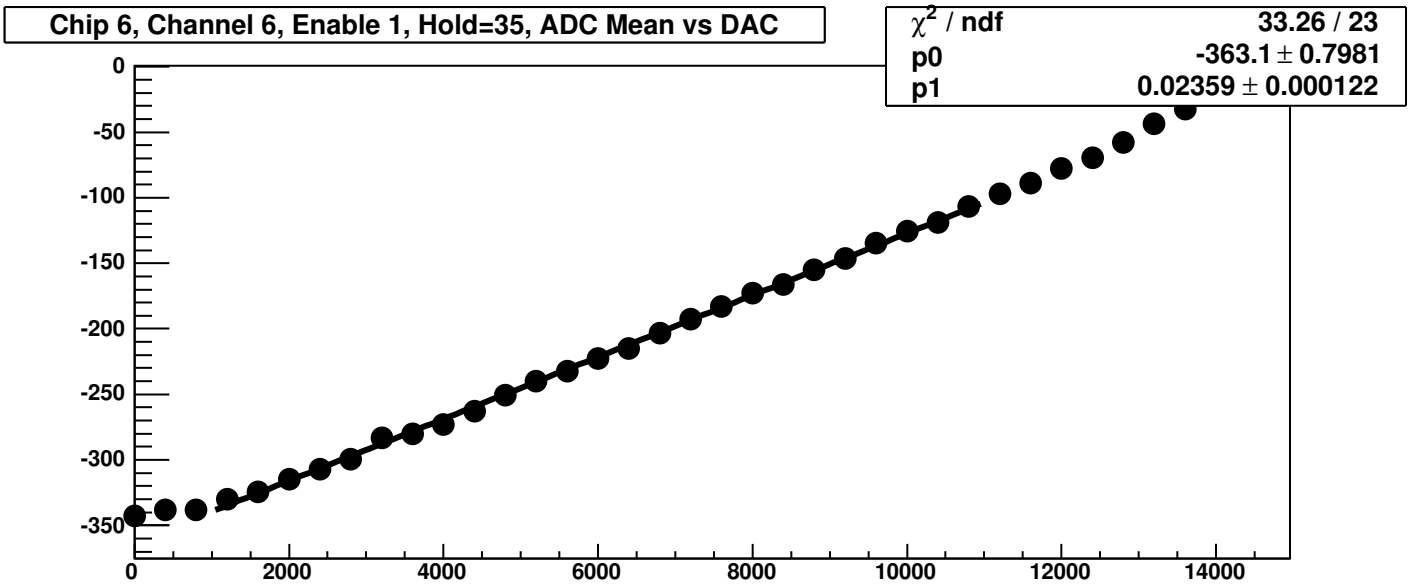


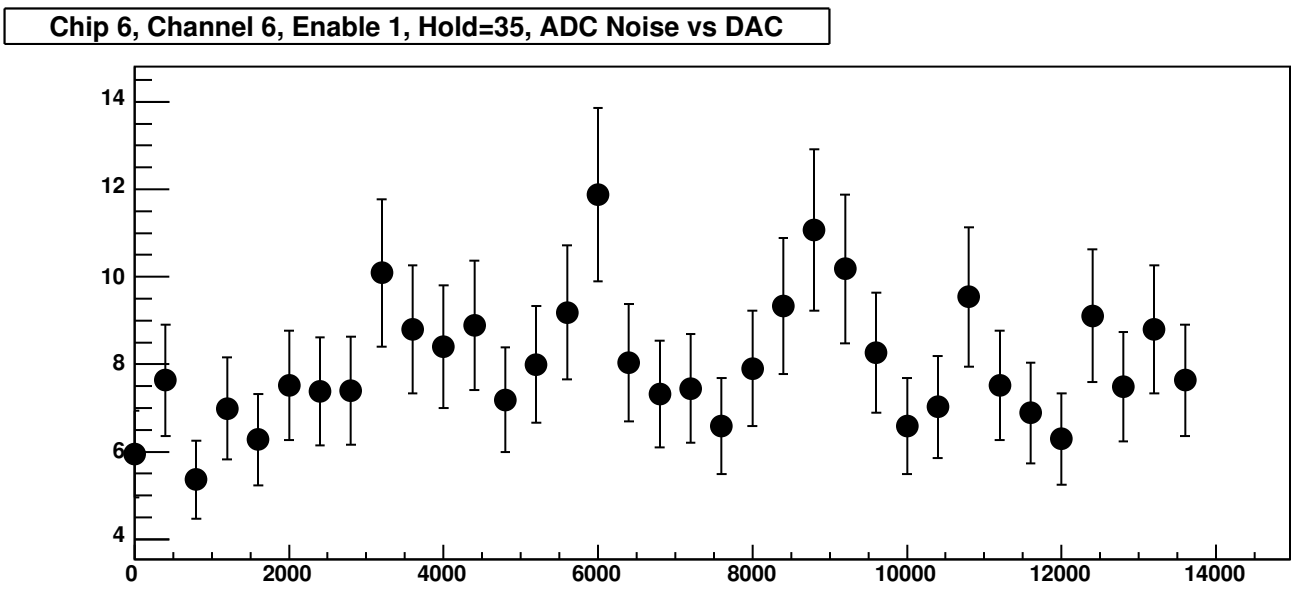
Chip 6, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC

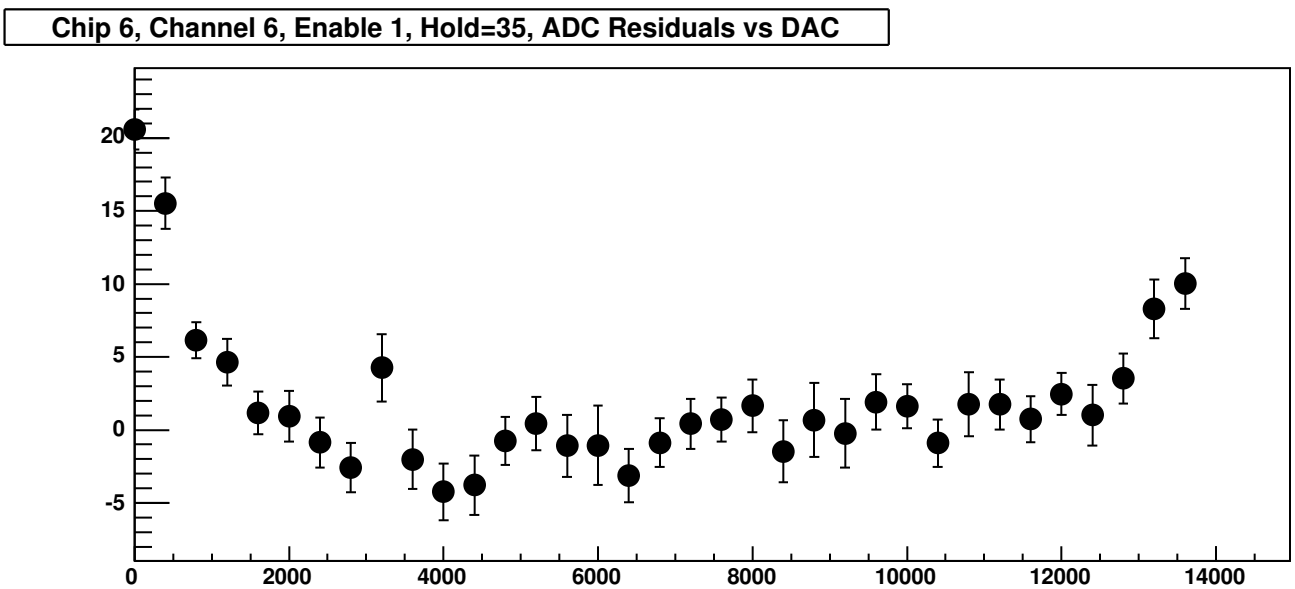


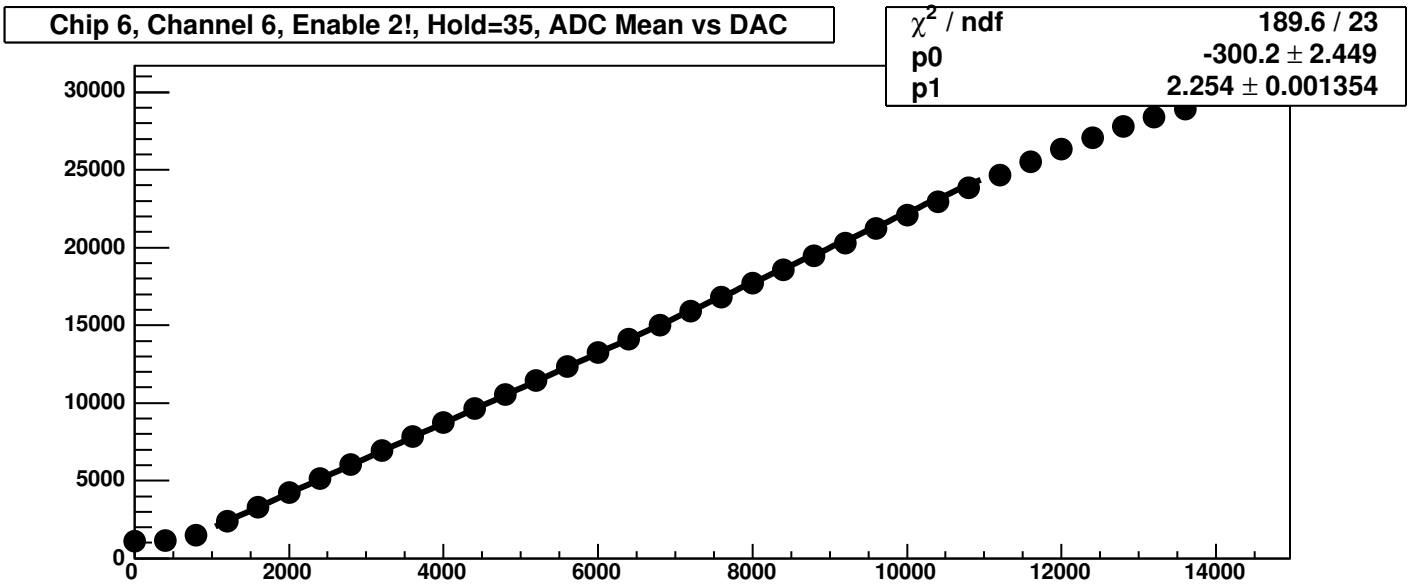
Chip 6, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

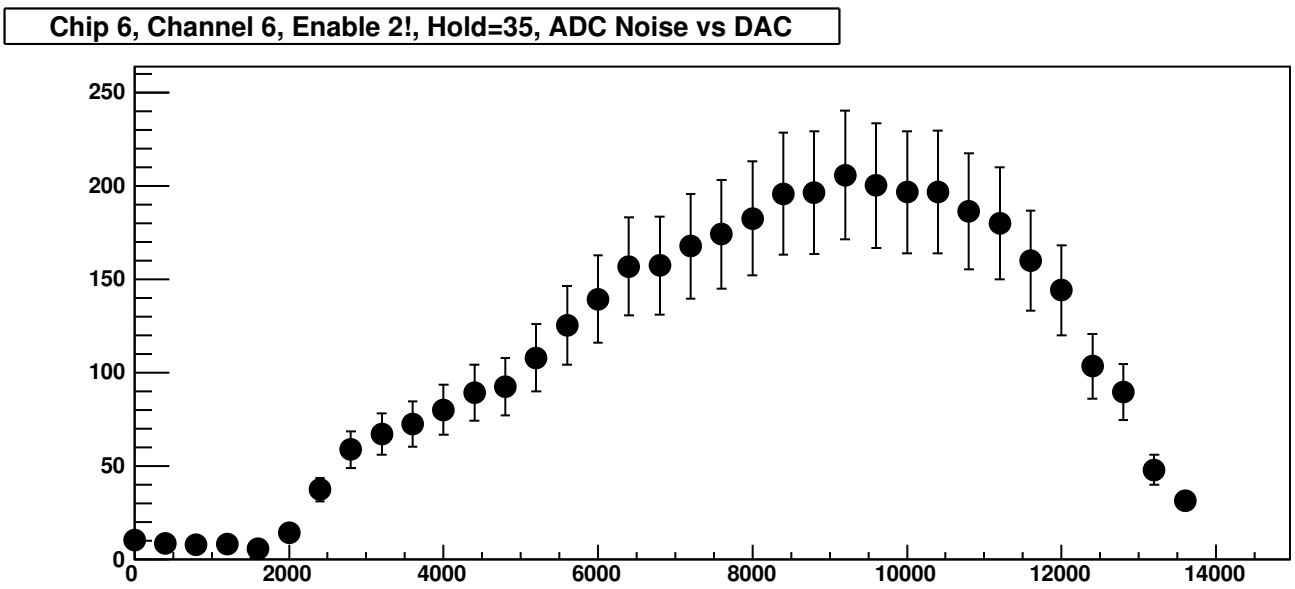


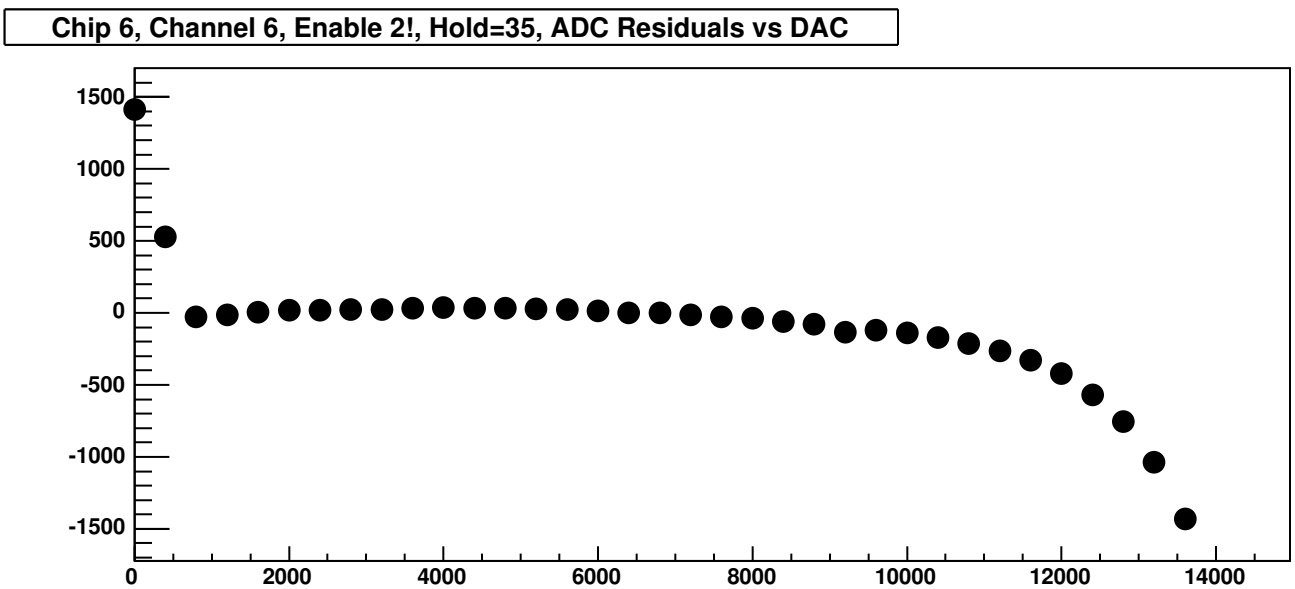

Chip 6, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.26 / 23
p0 -363.1 ± 0.7981
p1 0.02359 ± 0.000122
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

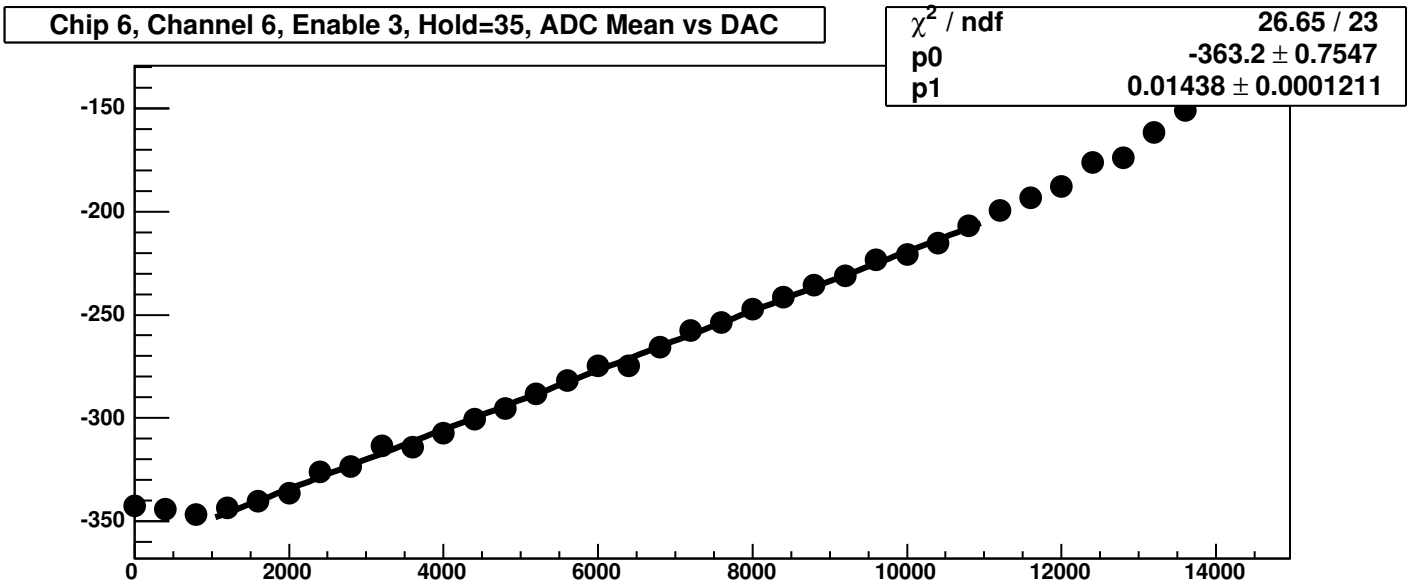

Chip 6, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

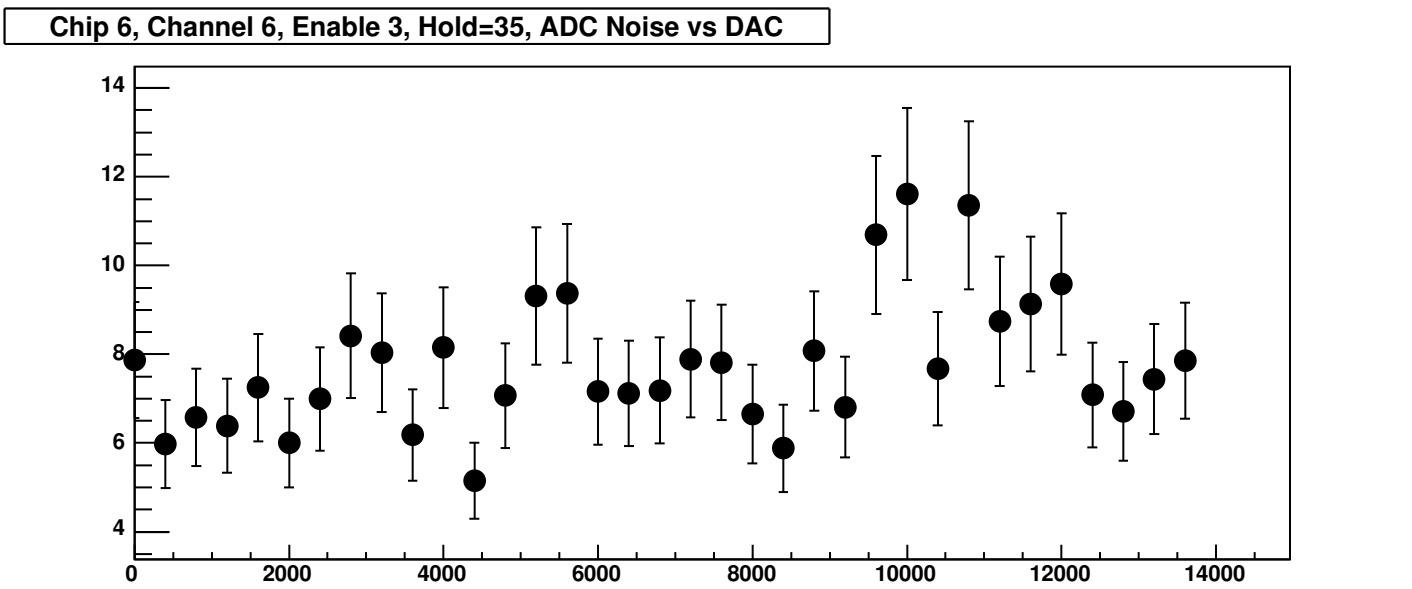

Chip 6, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

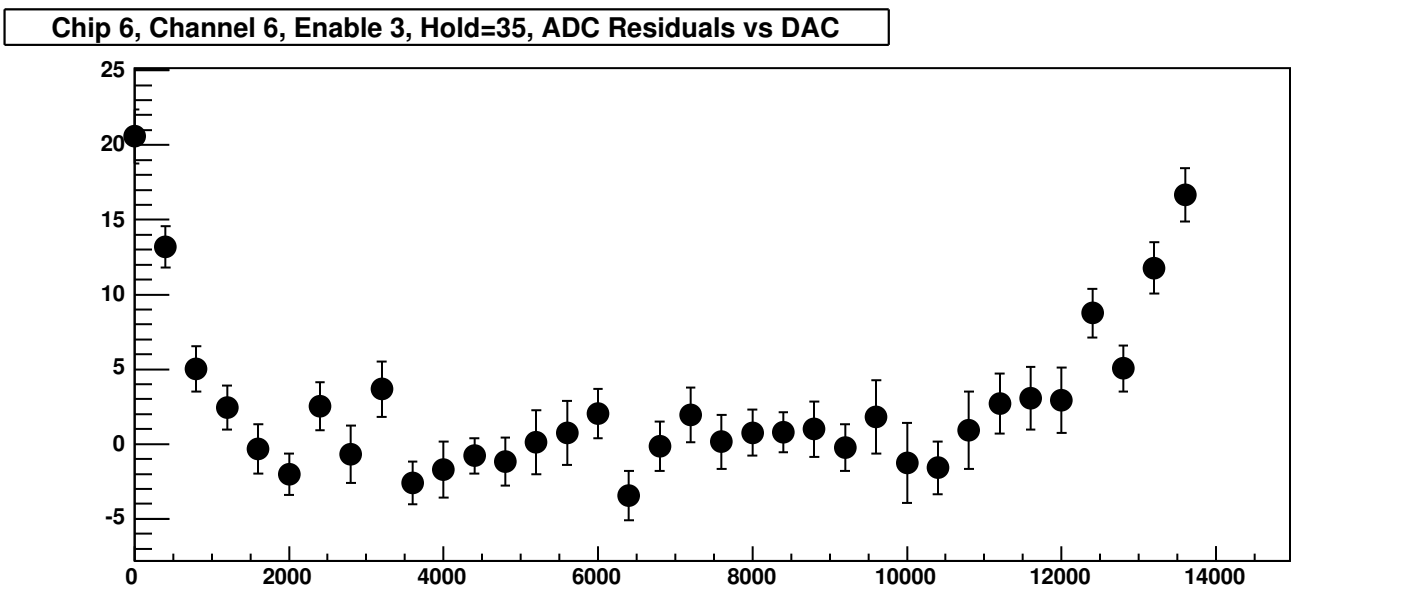

Chip 6, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 189.6 / 23
p0 -300.2 ± 2.449
p1 2.254 ± 0.001354
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

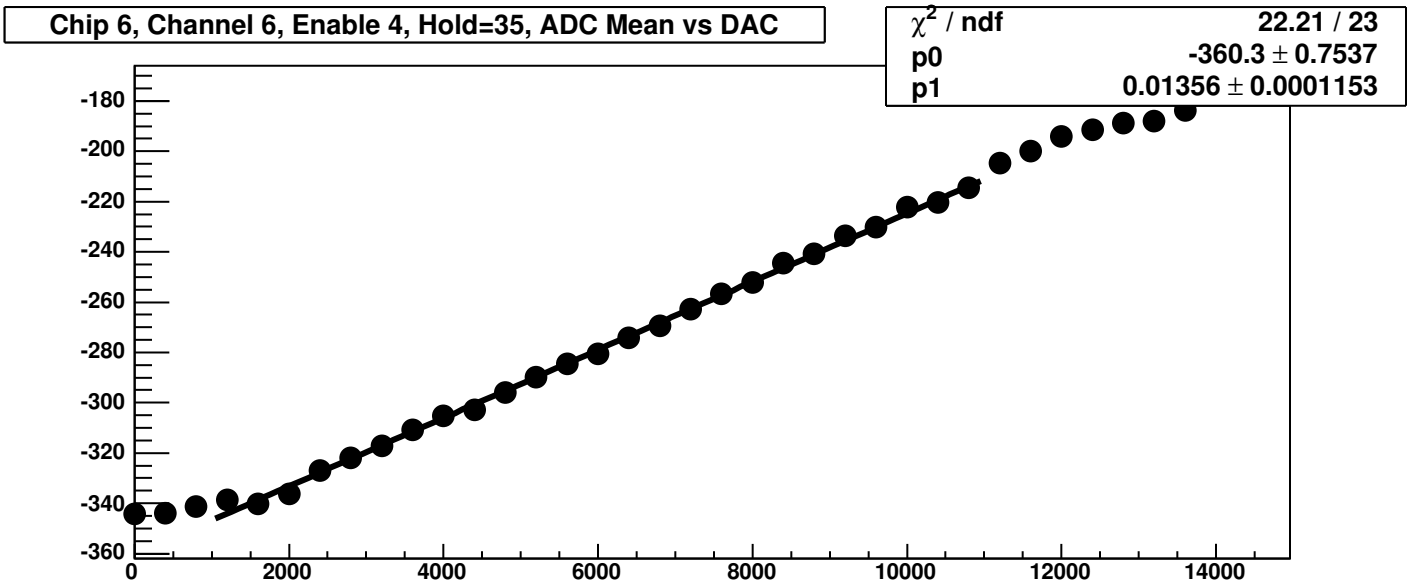

Chip 6, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

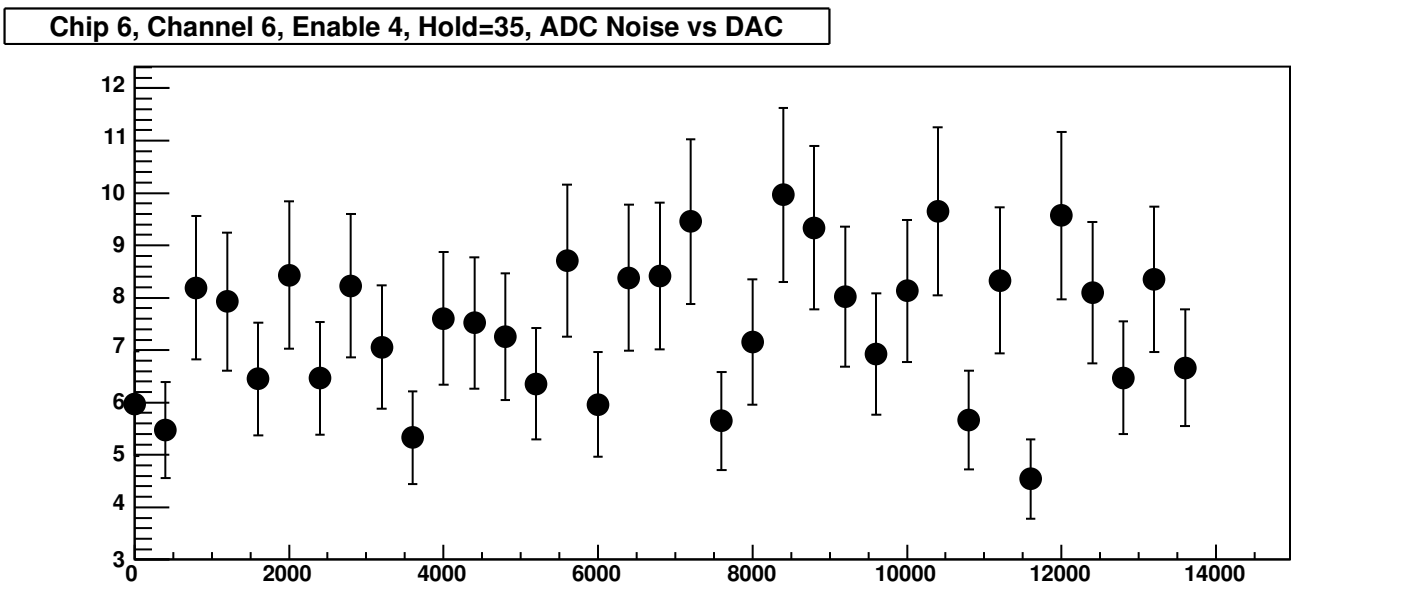

Chip 6, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

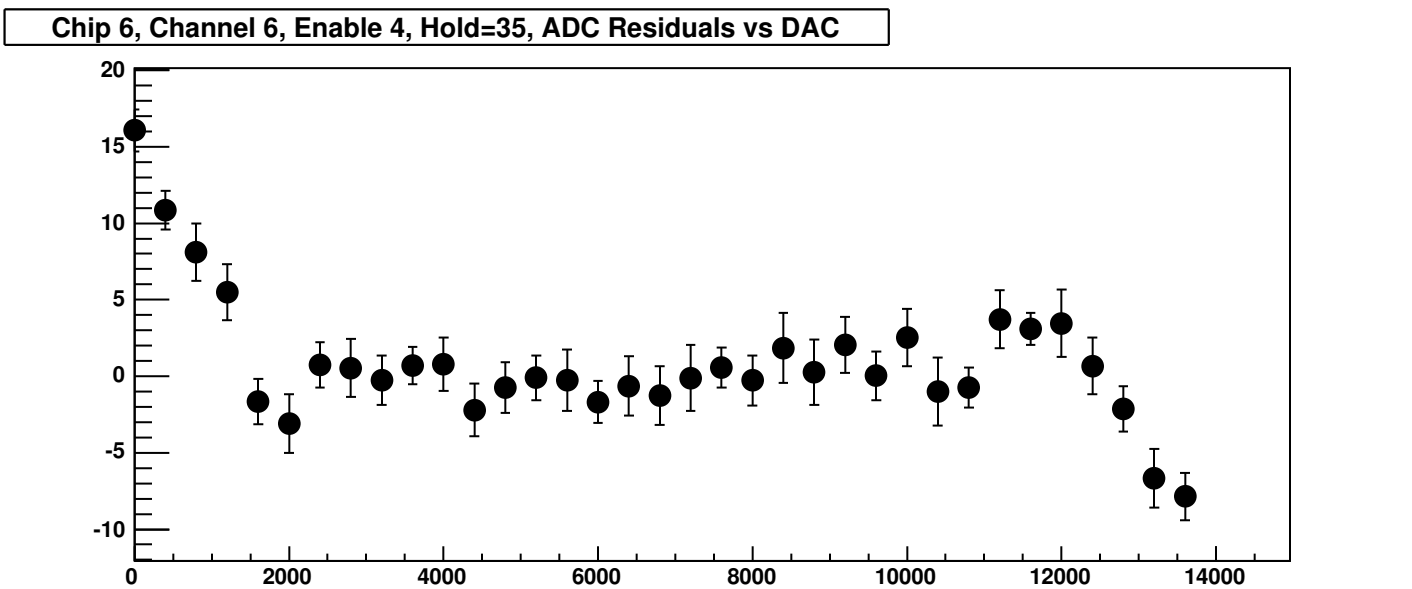

Chip 6, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.65 / 23
p0 -363.2 ± 0.7547
p1 0.01438 ± 0.0001211
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

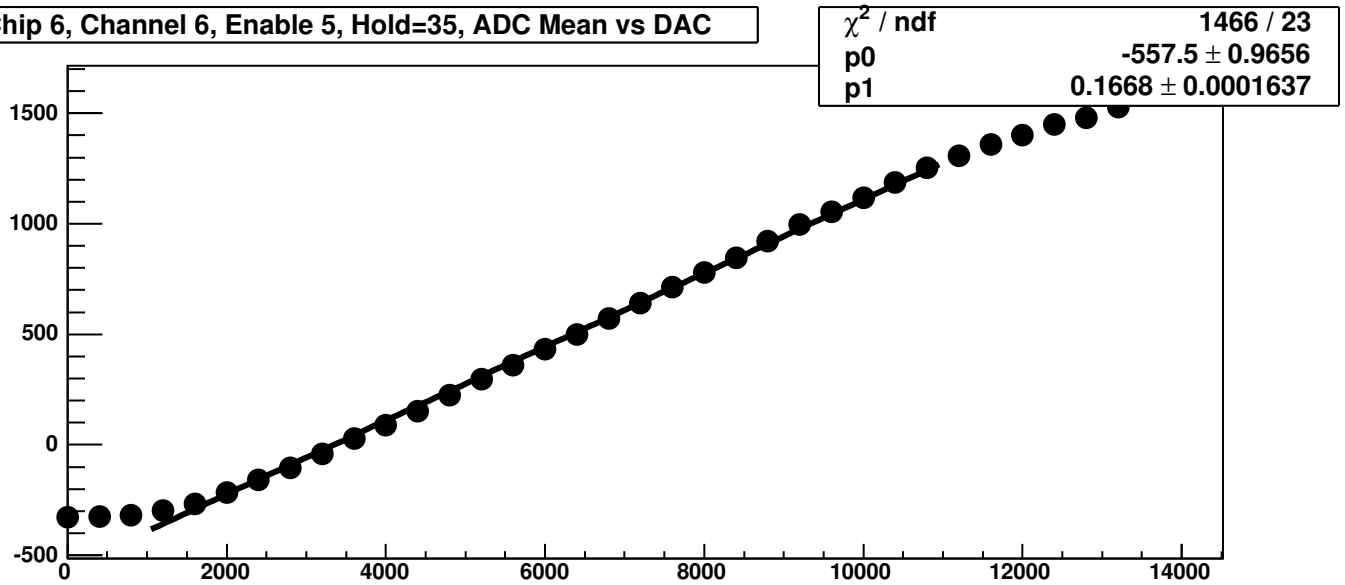

Chip 6, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 22.21 / 23
p0 -360.3 ± 0.7537
p1 0.01356 ± 0.0001153
-180
-200
-220
-240
-260
-280
-300
-320
-340
-360
0
2000
4000
6000
8000
10000
12000
14000

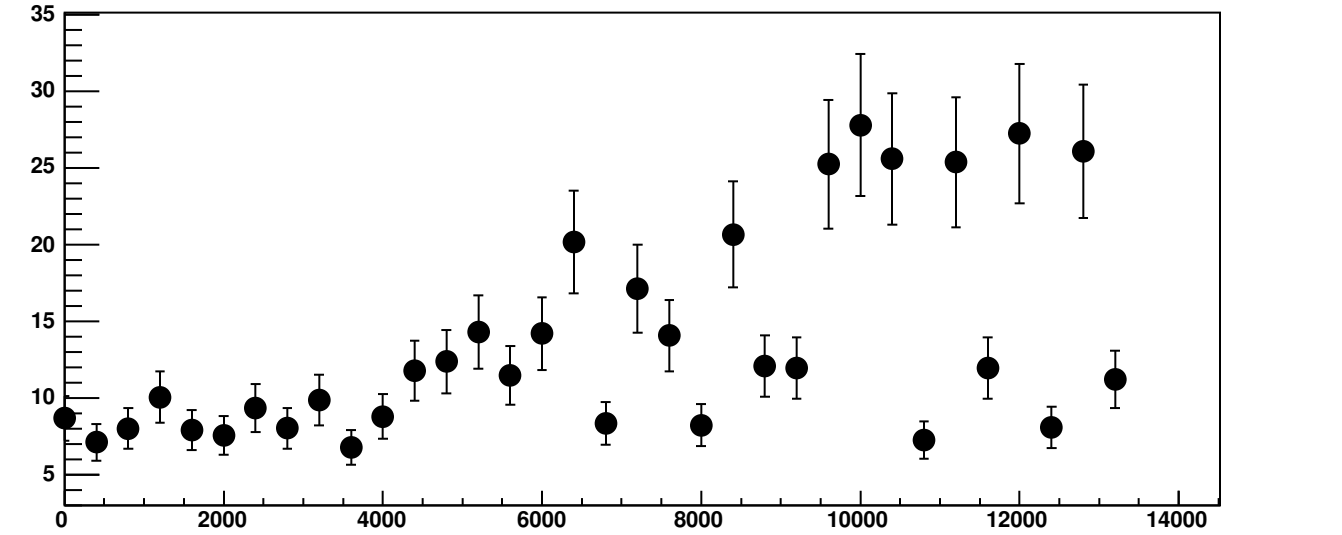

Chip 6, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

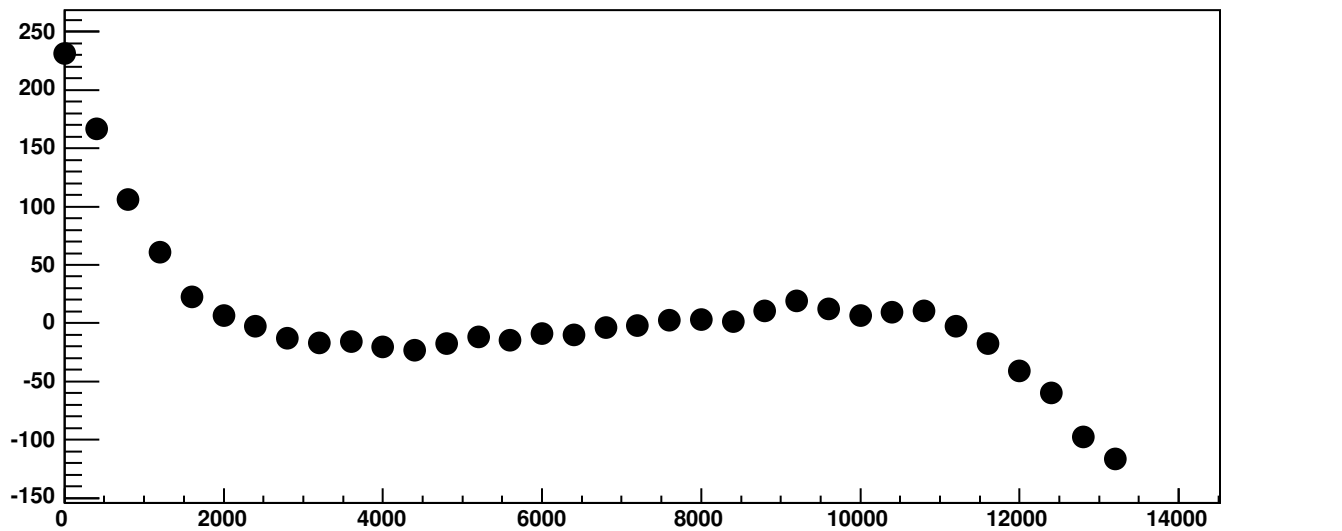
Chip 6, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC

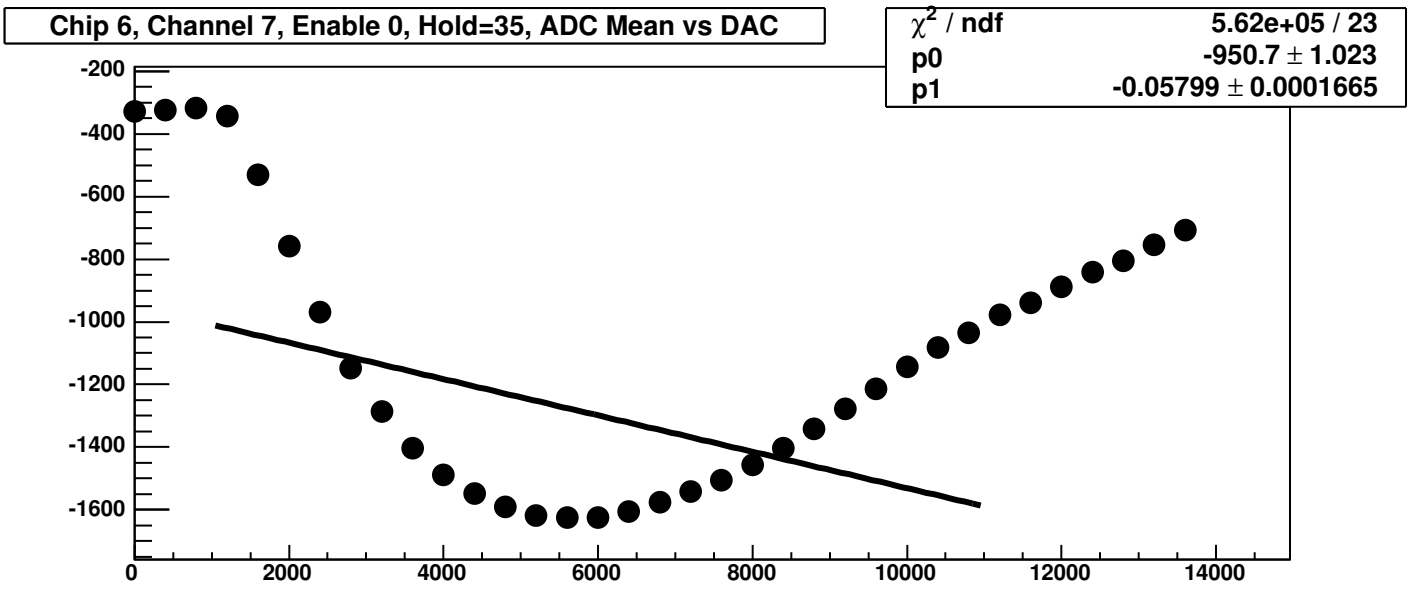


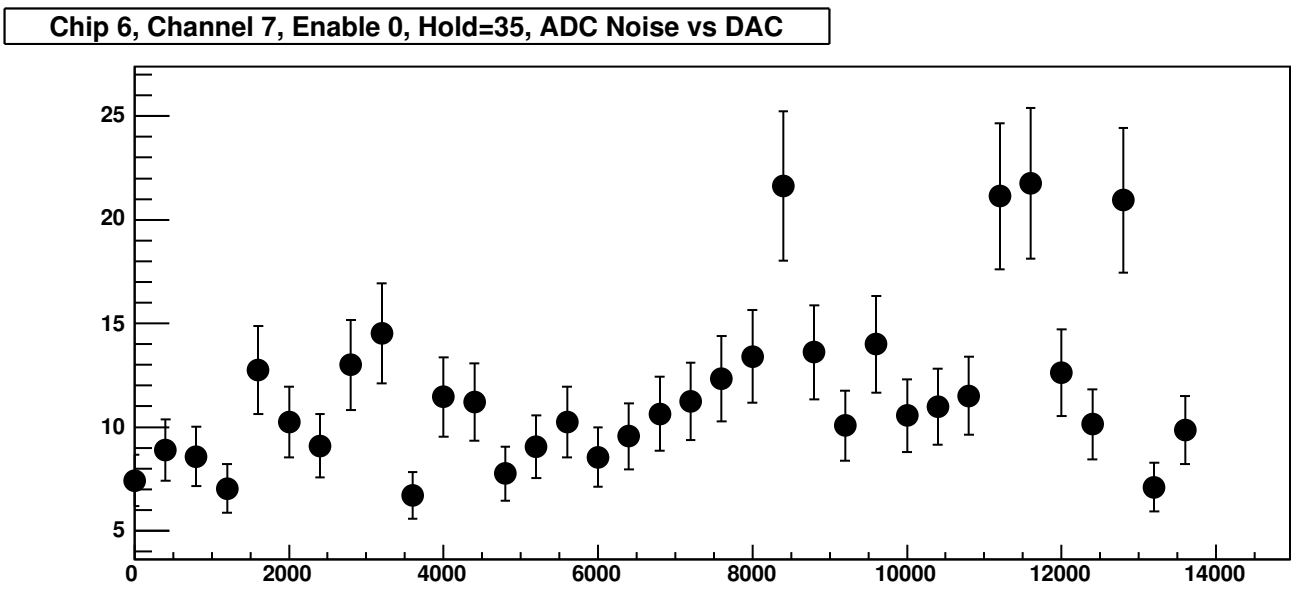
Chip 6, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC

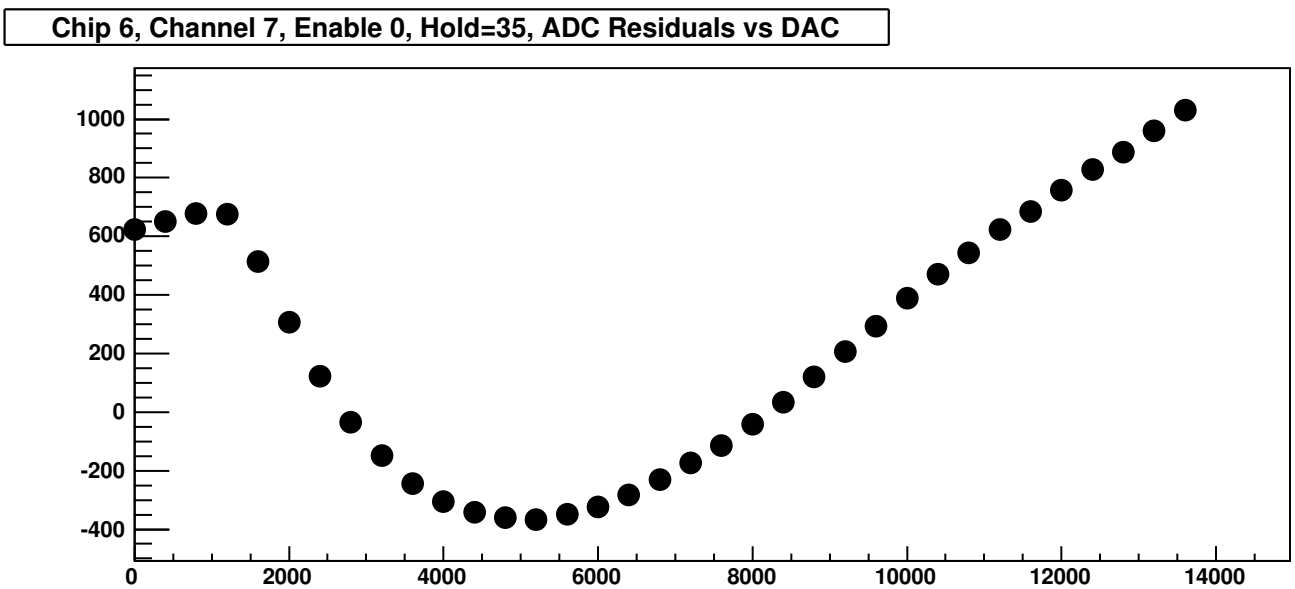


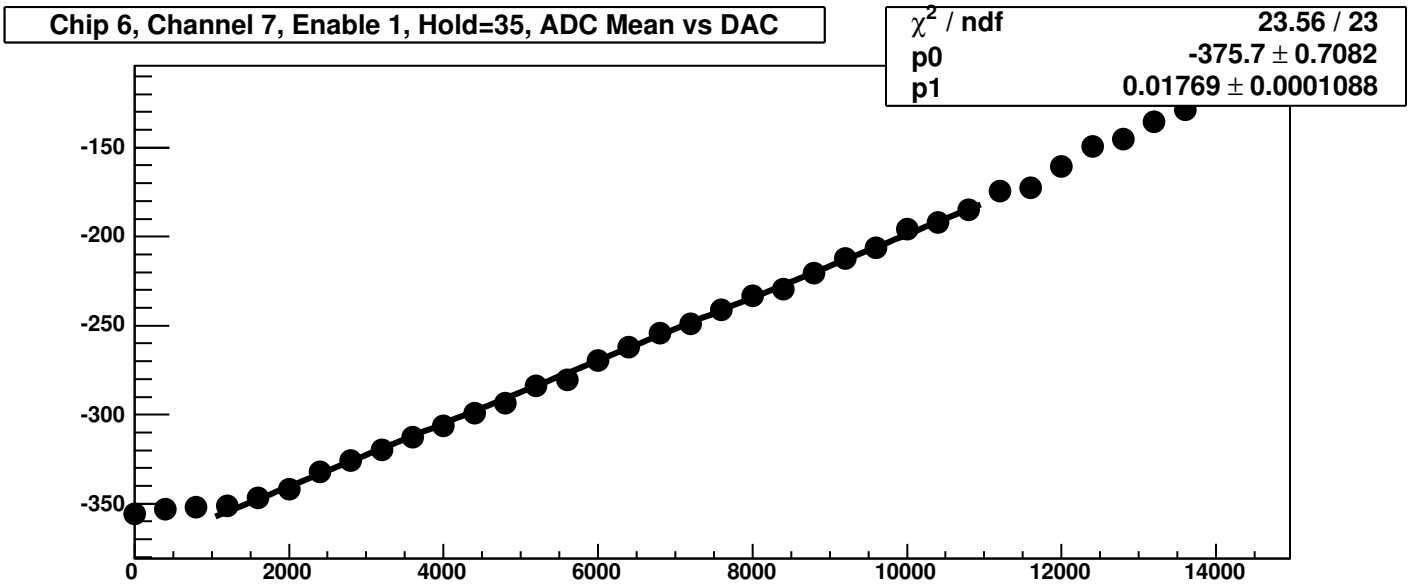
Chip 6, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC

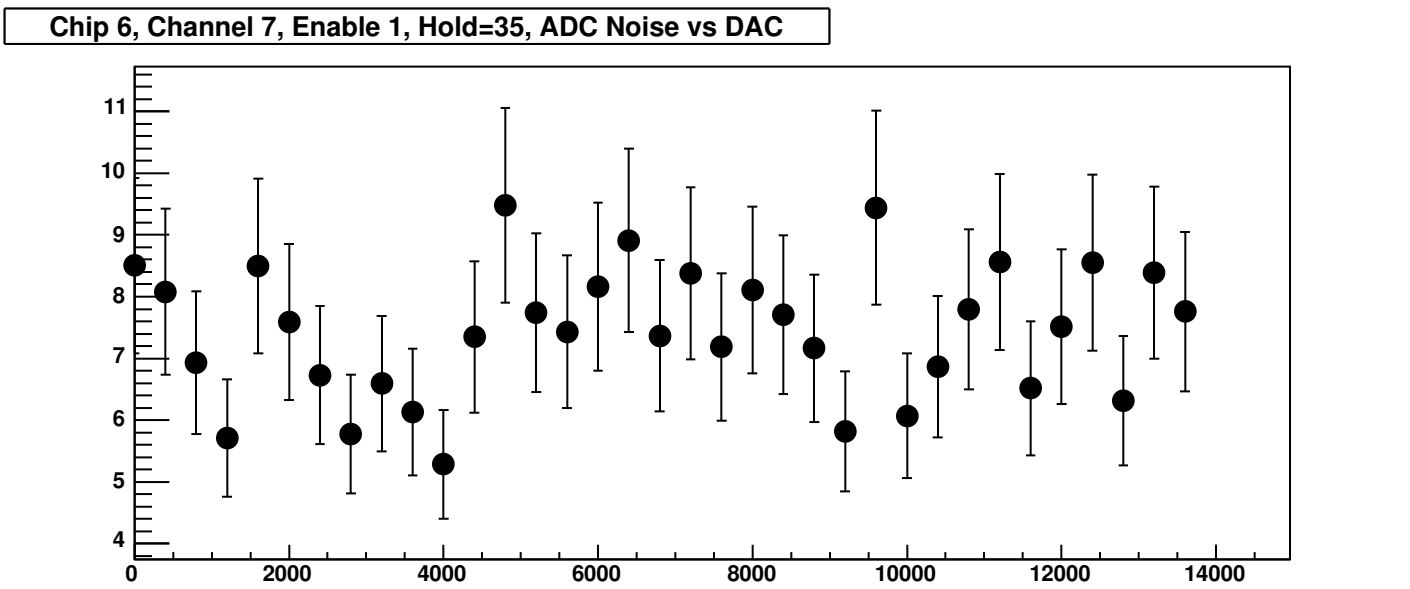


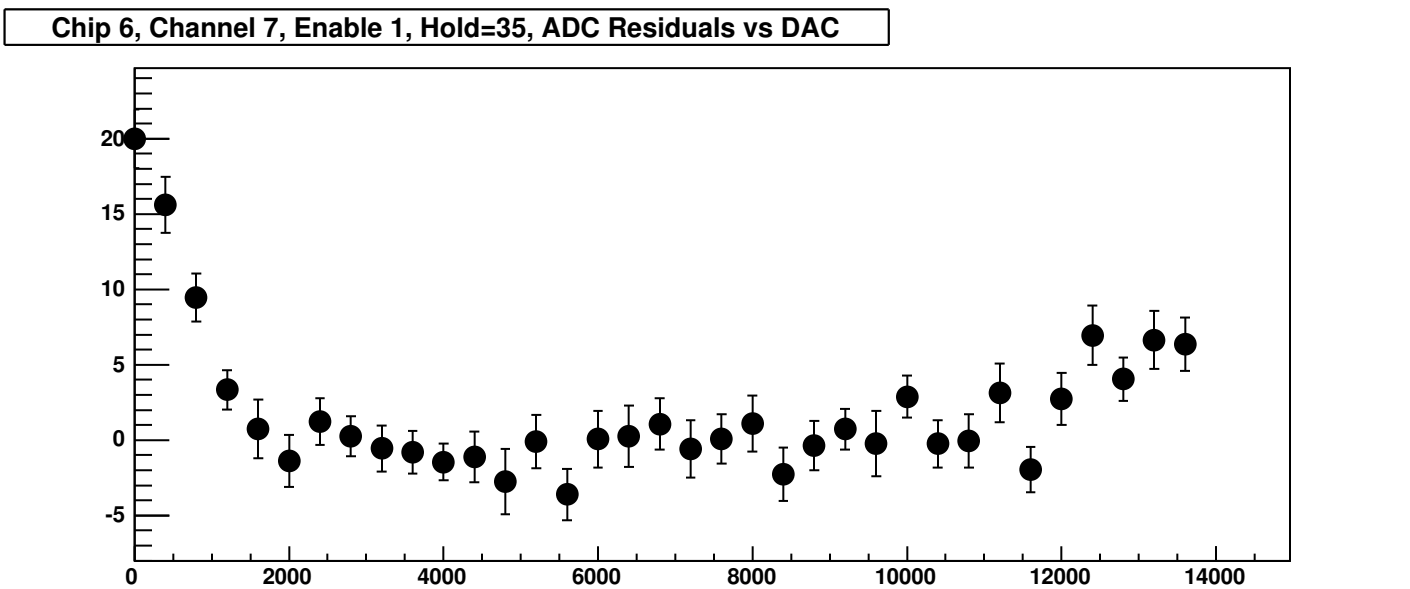

Chip 6, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.62e+05 / 23
p0 -950.7 ± 1.023
p1 -0.05799 ± 0.0001665
-200
-400
-600
-800
-1000
-1200
-1400
-1600
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

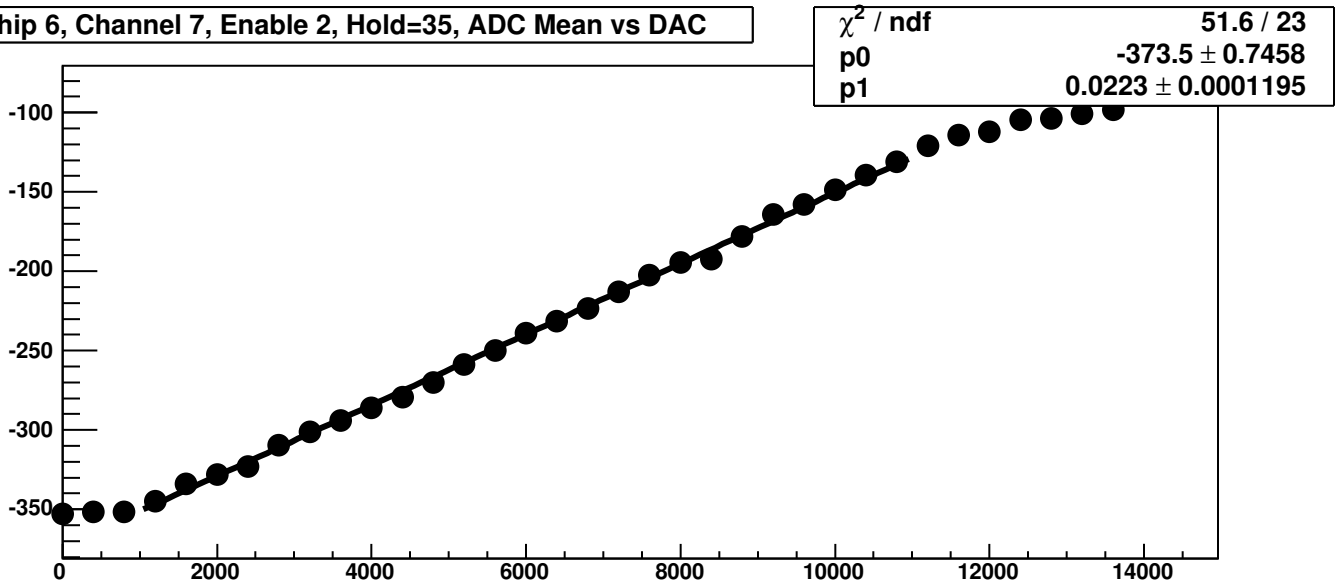

Chip 6, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 23.56 / 23
p0 -375.7 ± 0.7082
p1 0.01769 ± 0.0001088
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

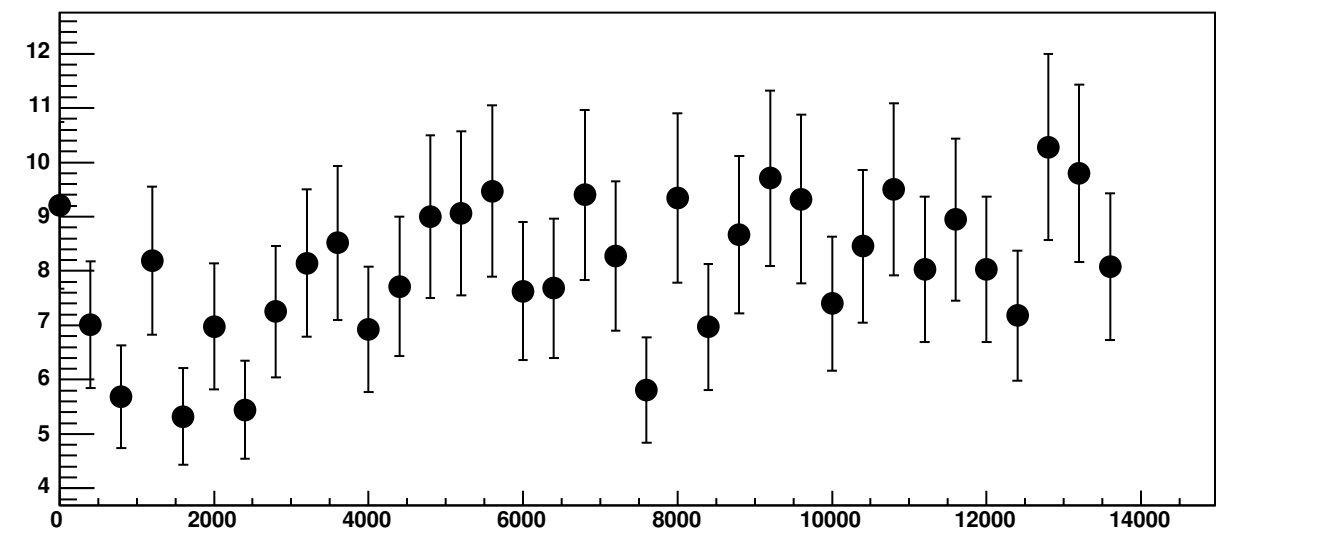

Chip 6, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

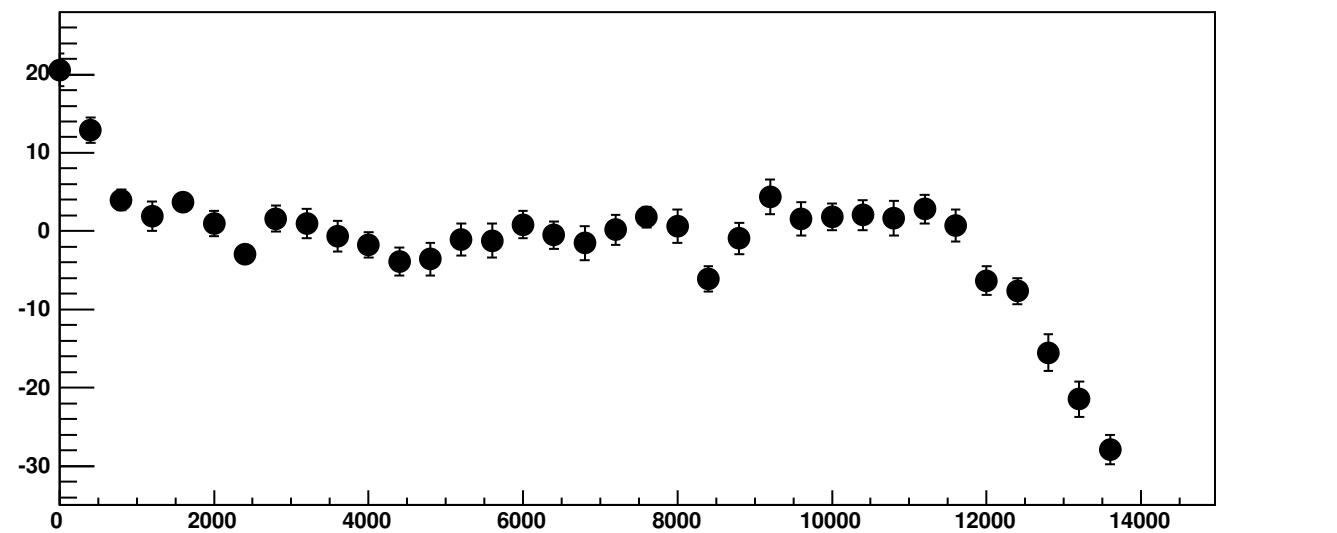
Chip 6, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC

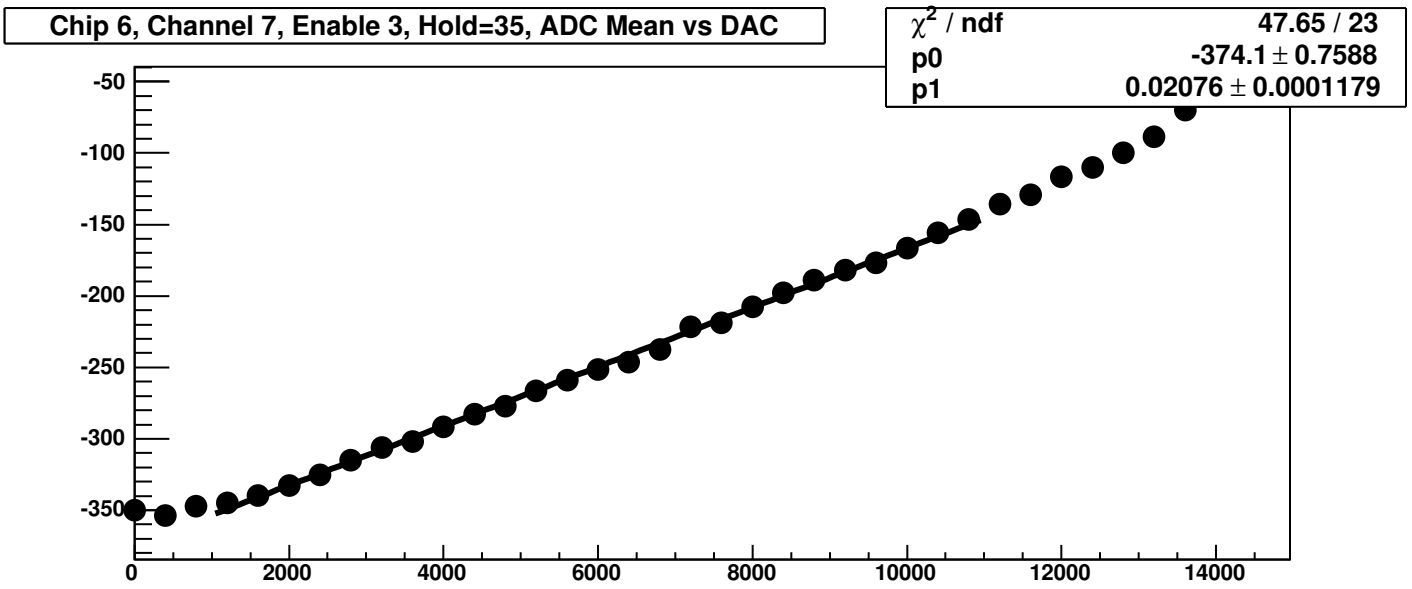


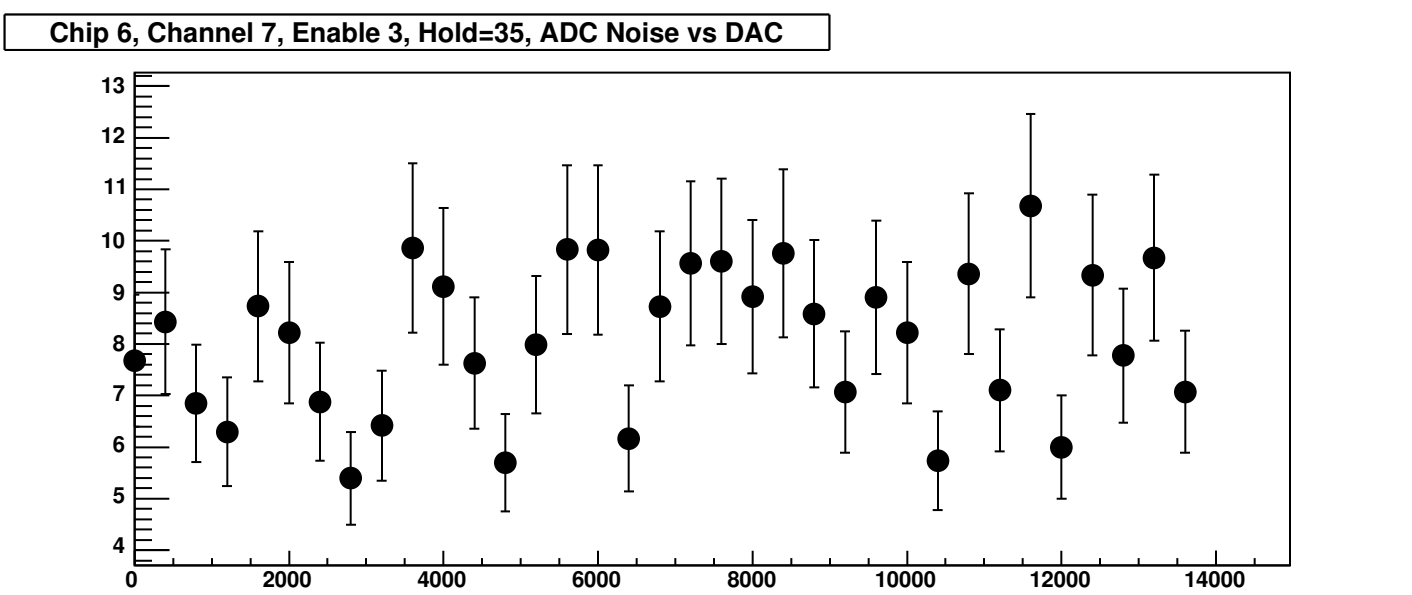
Chip 6, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC

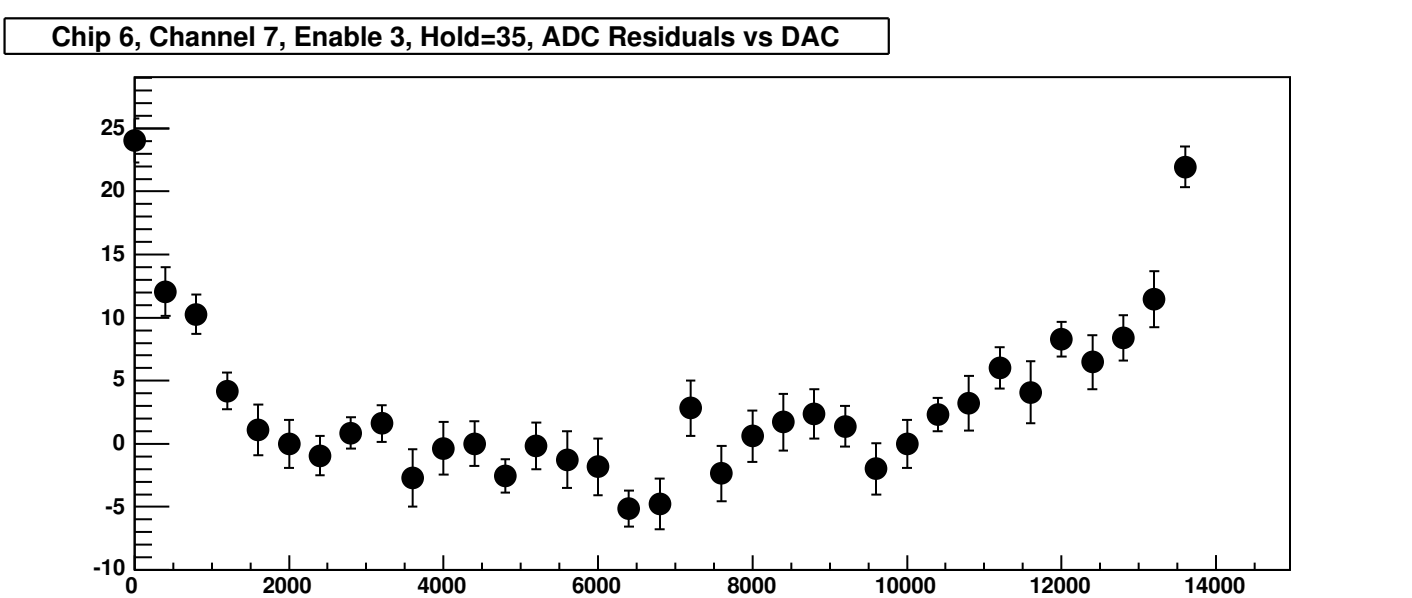


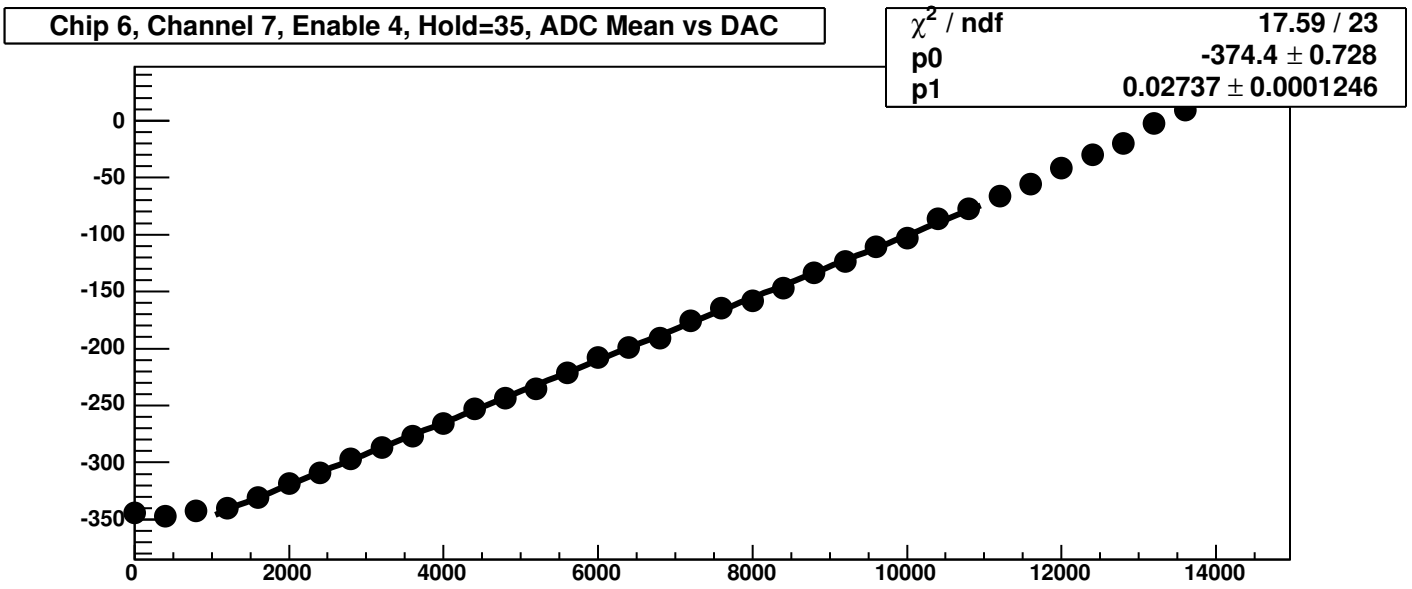
Chip 6, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC

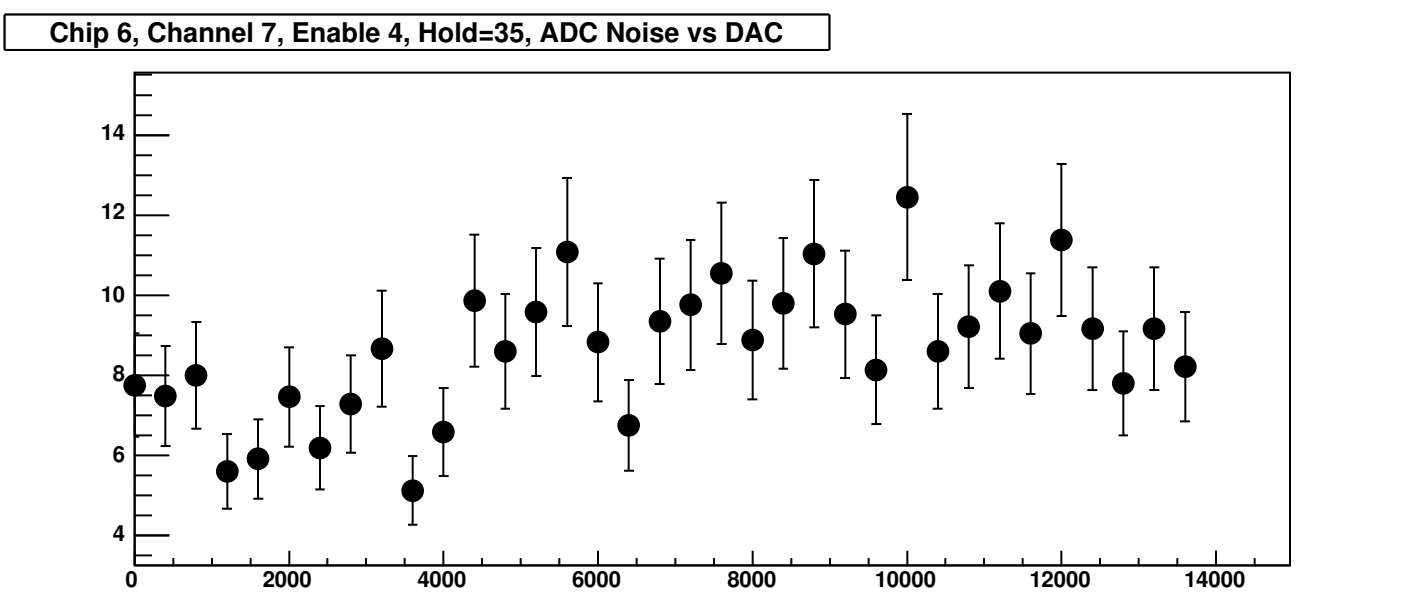


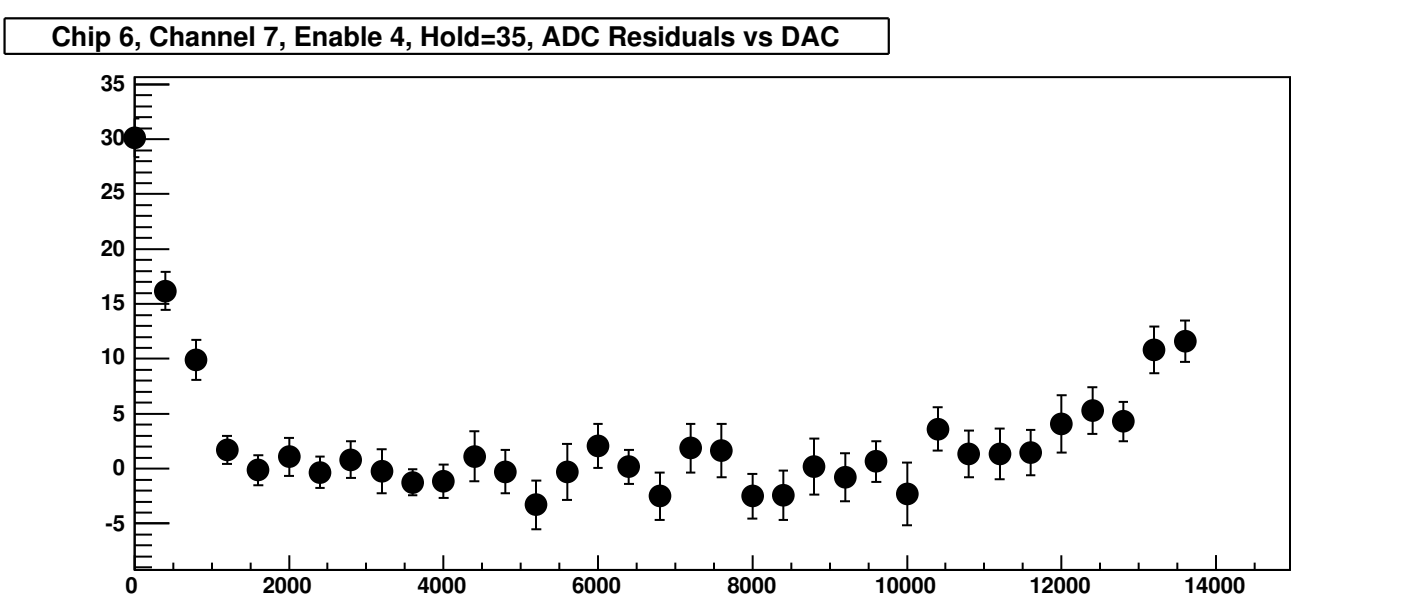

Chip 6, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 47.65 / 23
p0 -374.1 ± 0.7588
p1 0.02076 ± 0.0001179
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

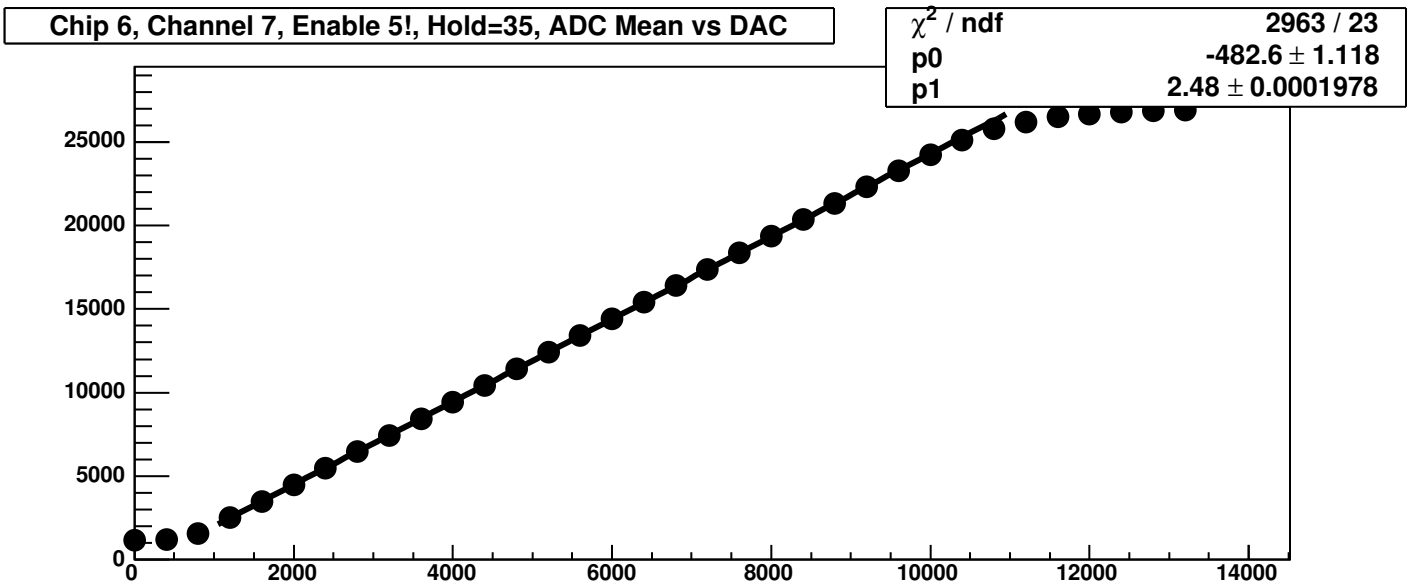

Chip 6, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

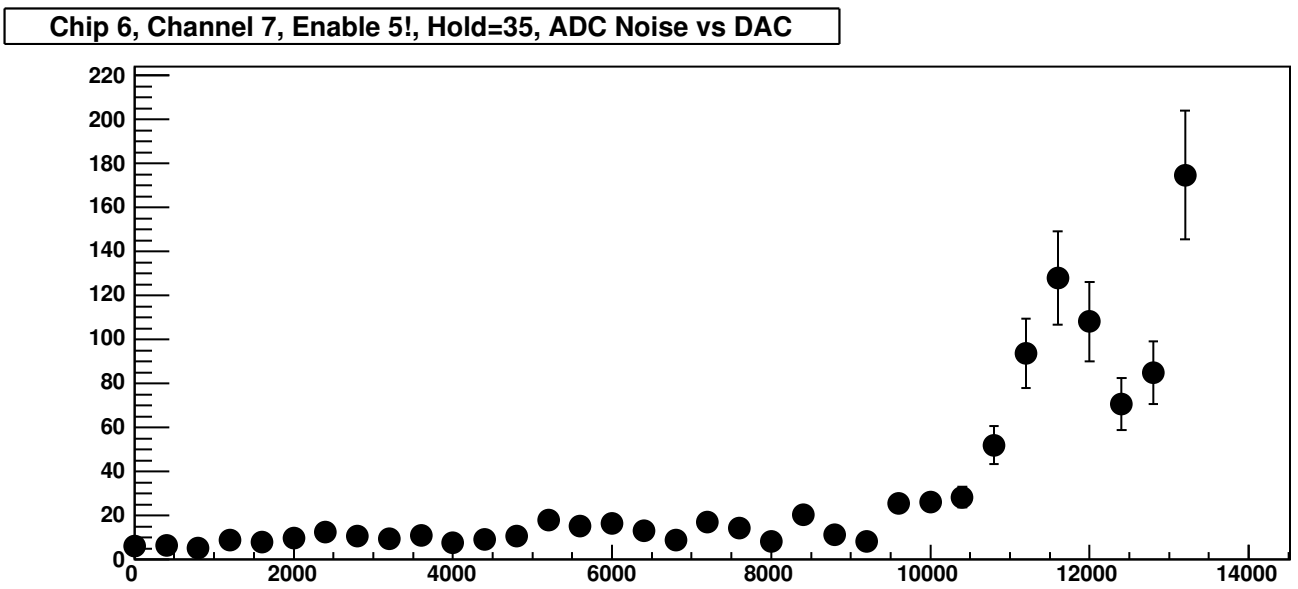

Chip 6, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

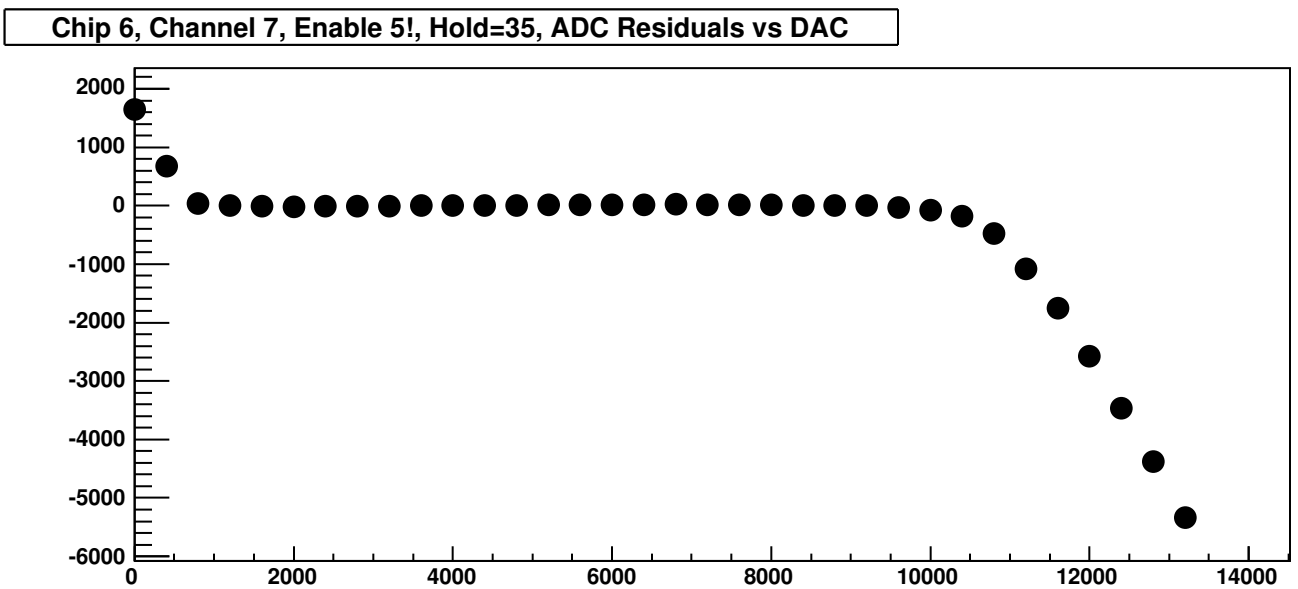

Chip 6, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 17.59 / 23
p0 -374.4 ± 0.728
p1 0.02737 ± 0.0001246
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

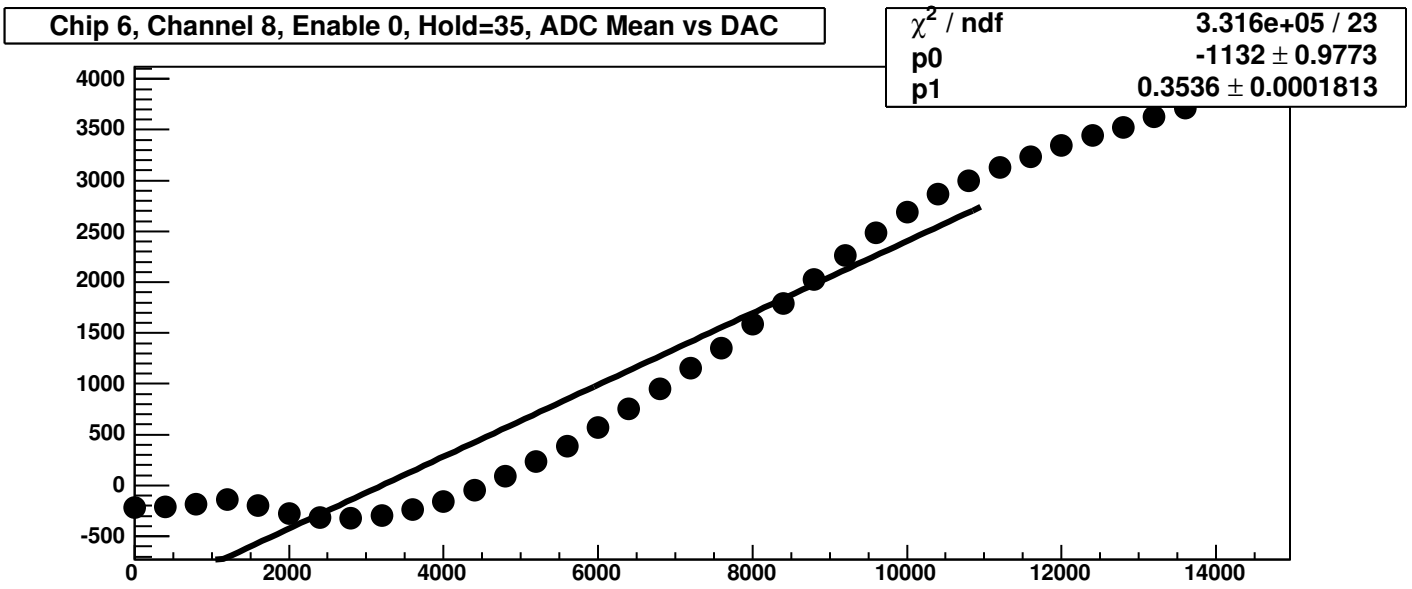

Chip 6, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

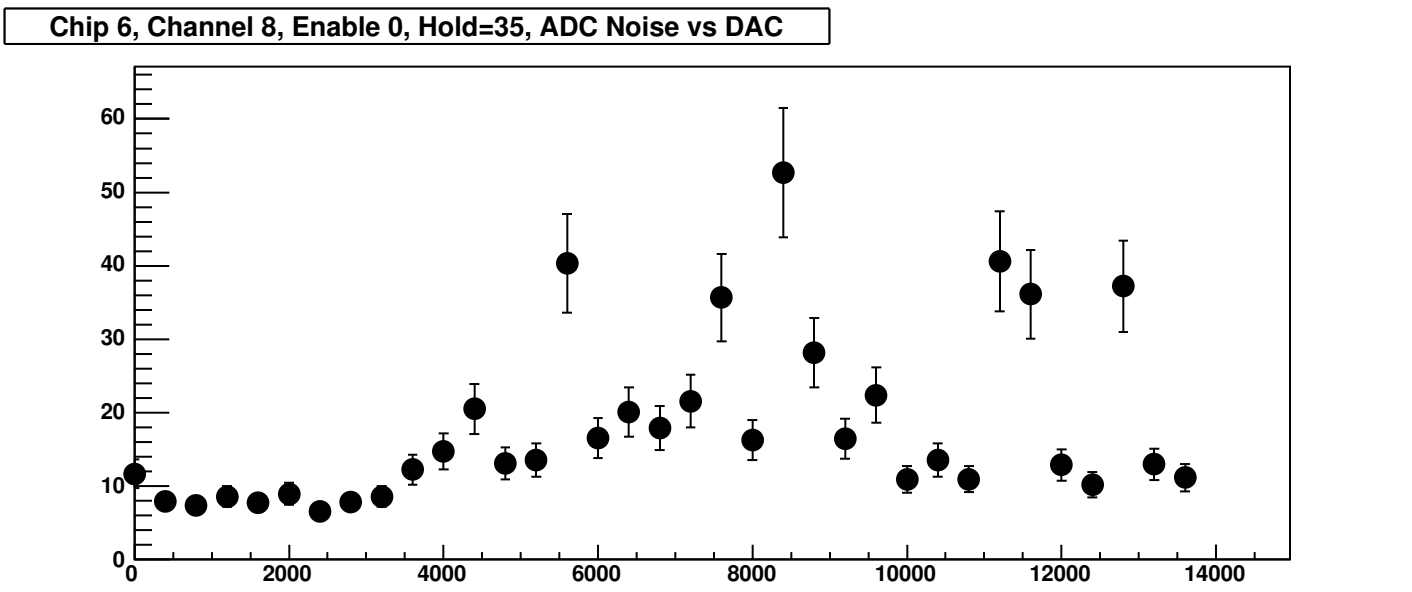

Chip 6, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

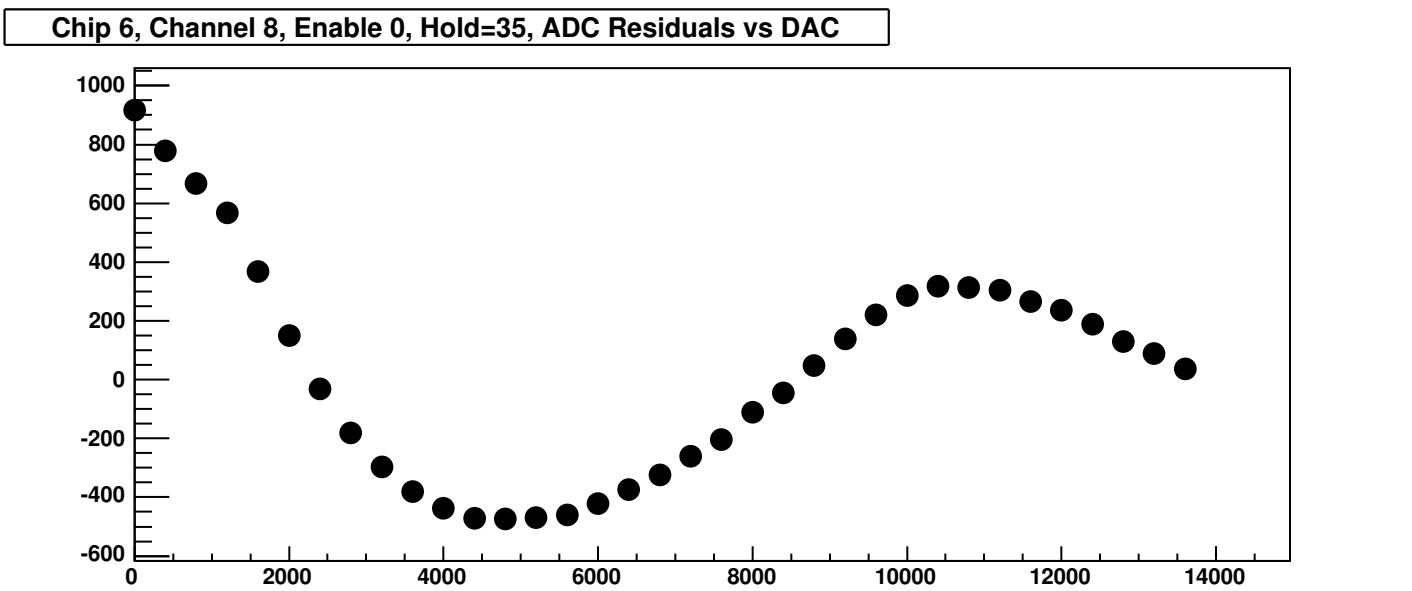

Chip 6, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2963 / 23
p0 -482.6 ± 1.118
p1 2.48 ± 0.0001978
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

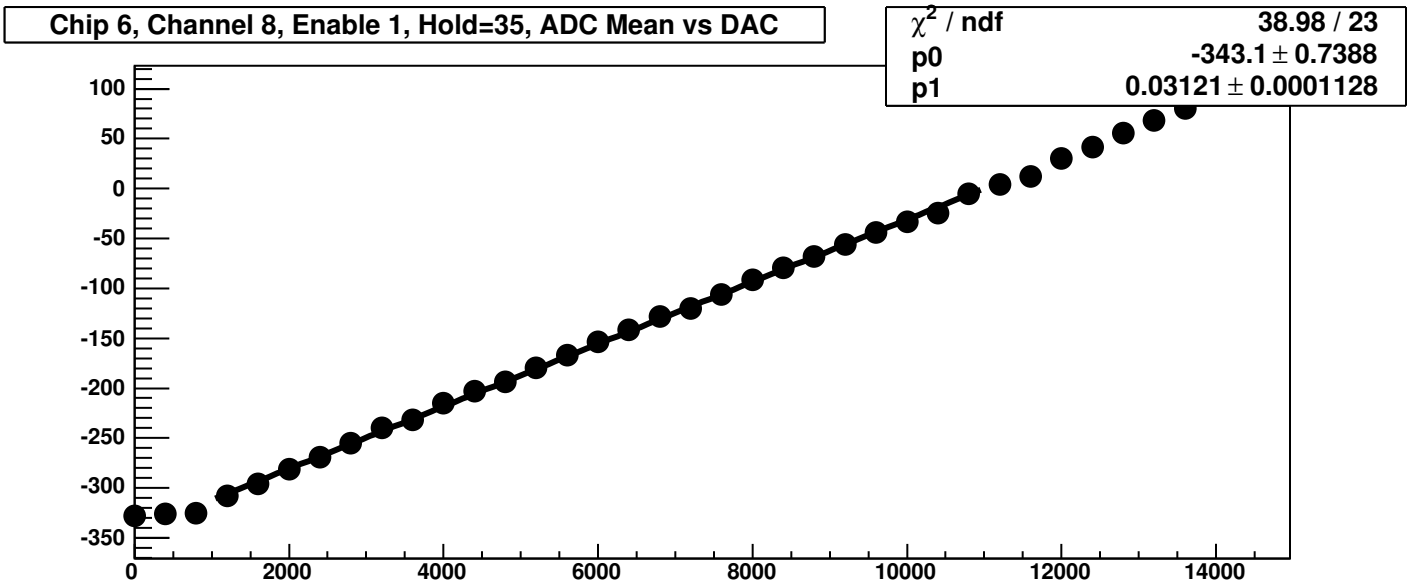

Chip 6, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

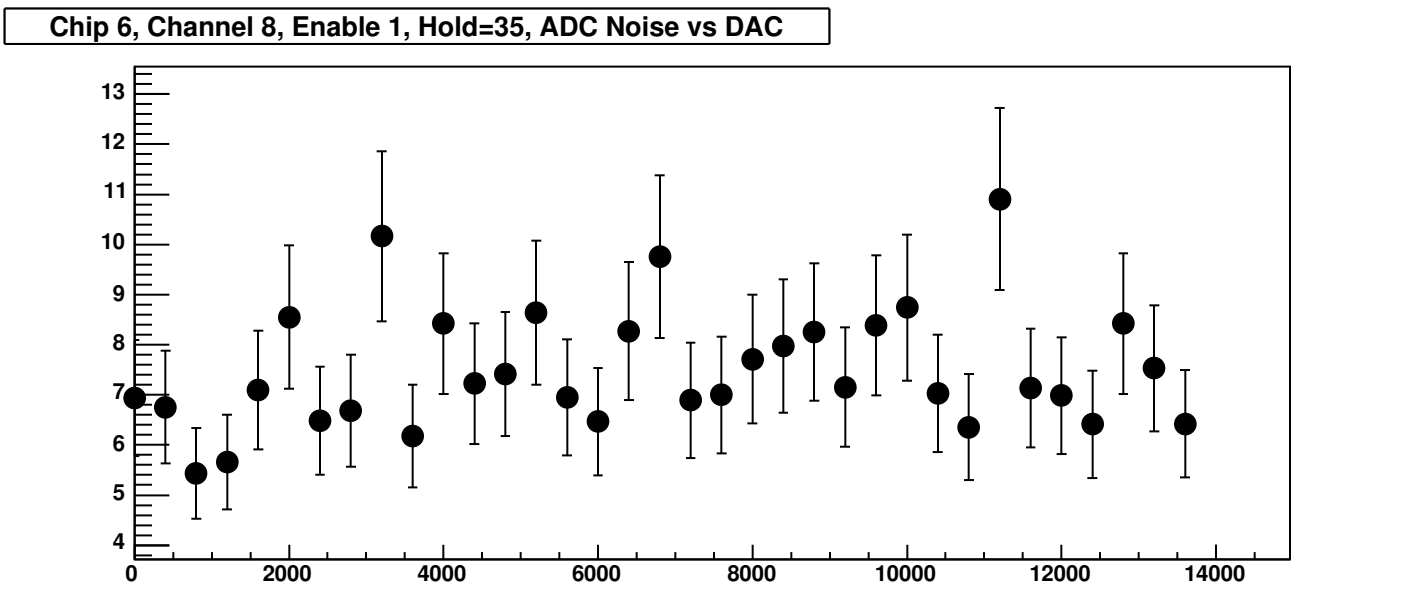

Chip 6, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
2000
4000
6000
8000
10000
12000
14000

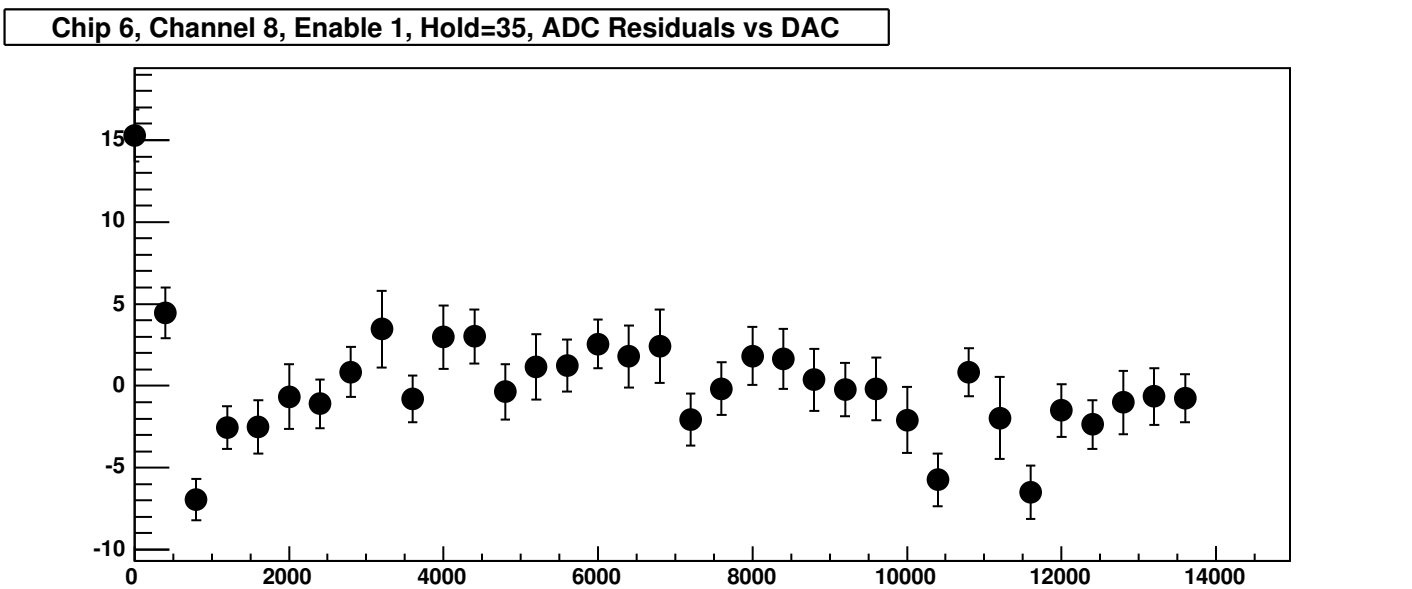

Chip 6, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.316e+05 / 23
p0 -1132 ± 0.9773
p1 0.3536 ± 0.0001813
4000
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

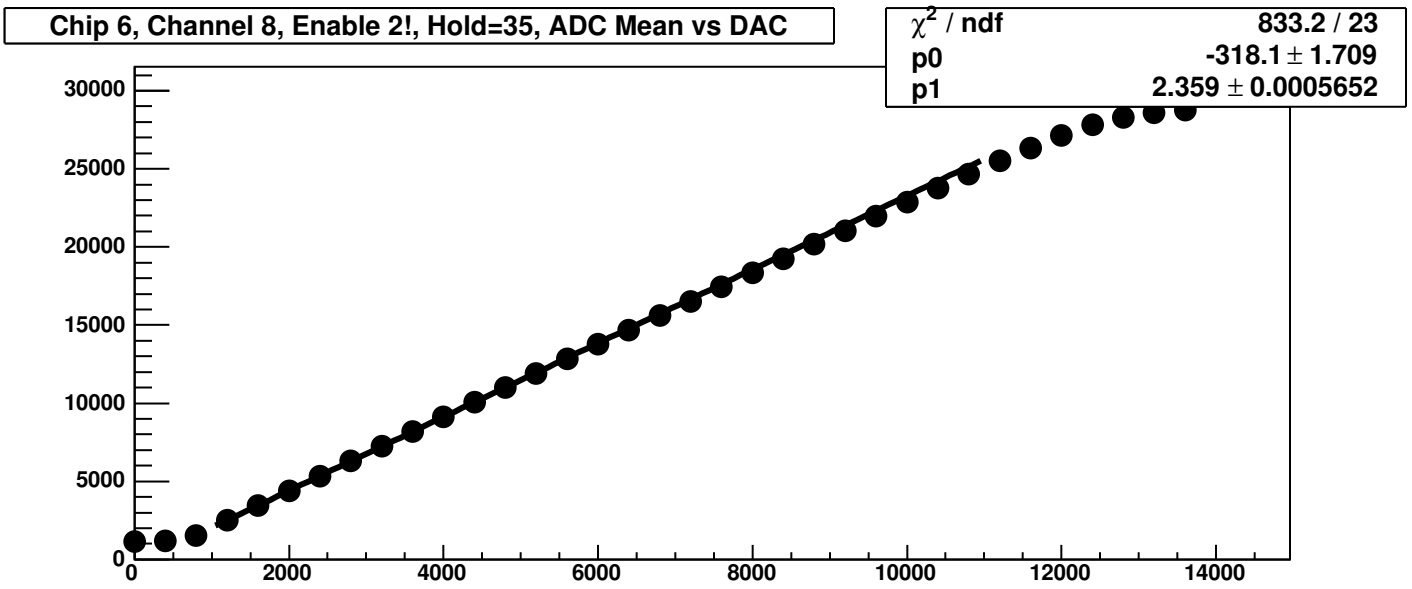

Chip 6, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

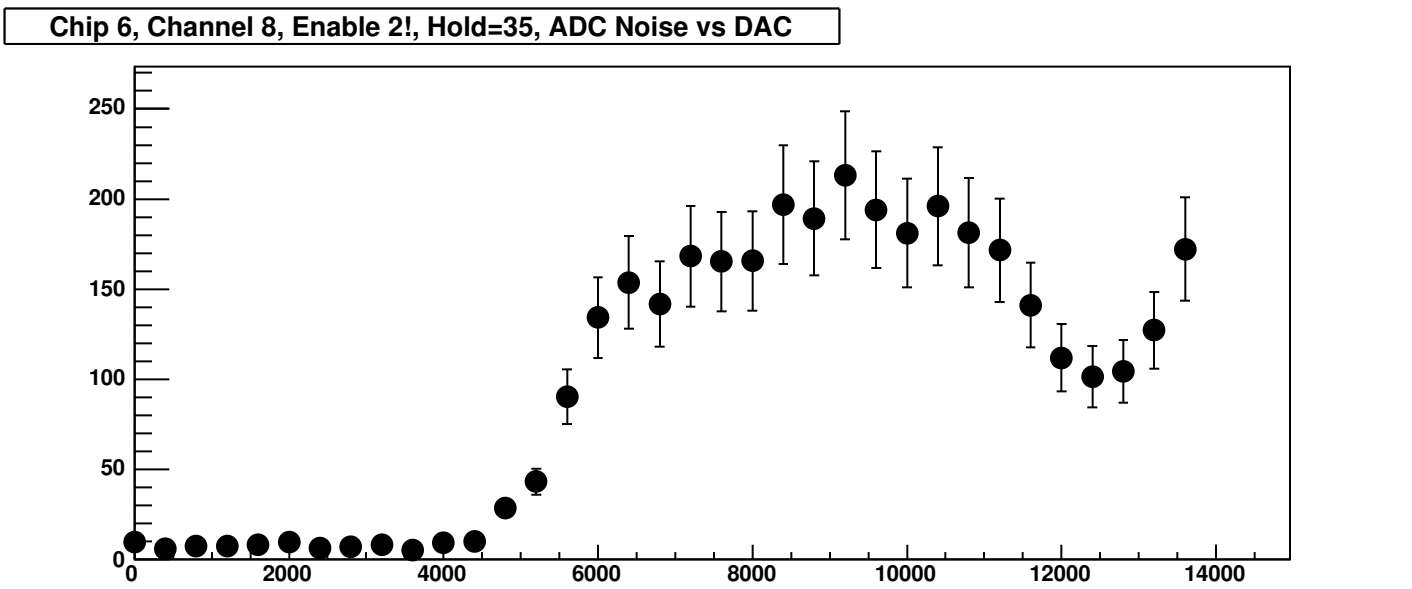

Chip 6, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

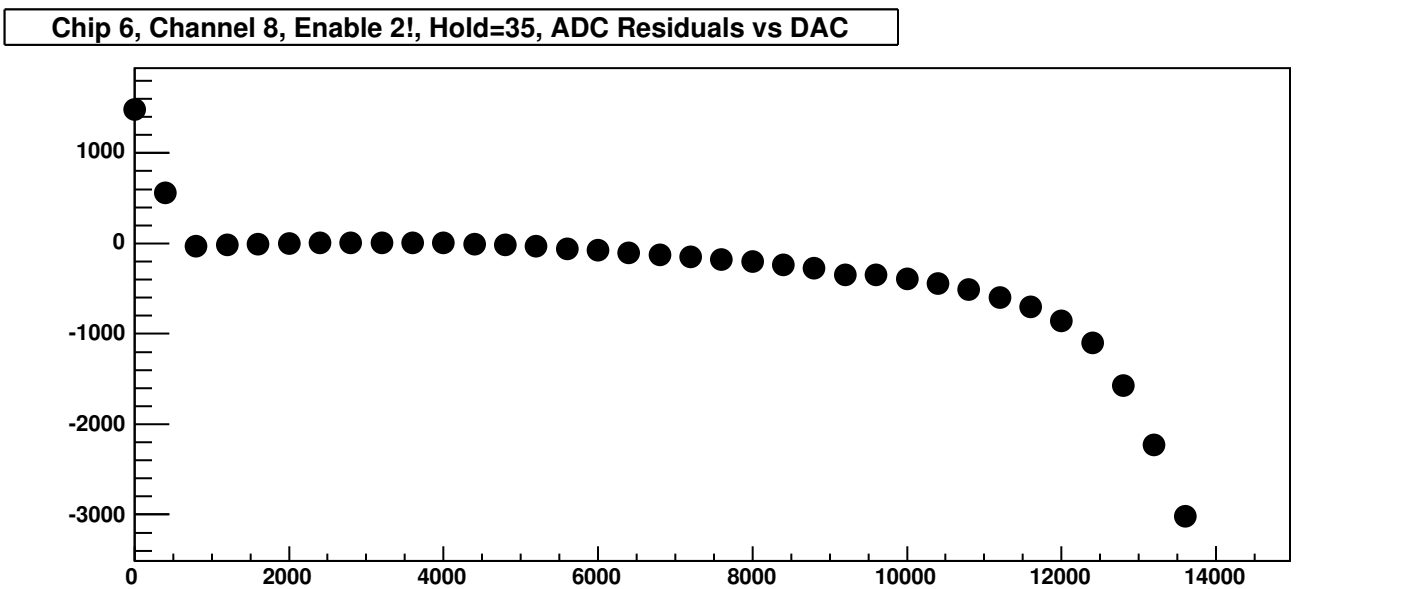

Chip 6, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 38.98 / 23
p0 -343.1 ± 0.7388
p1 0.03121 ± 0.0001128
100
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

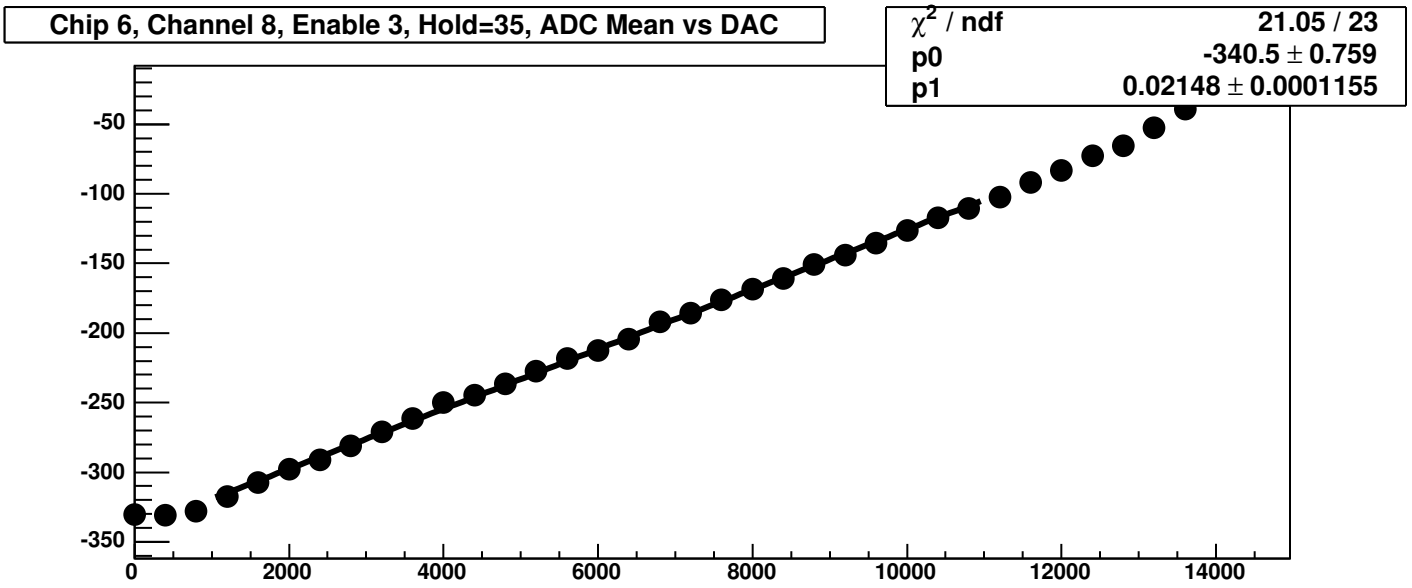

Chip 6, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

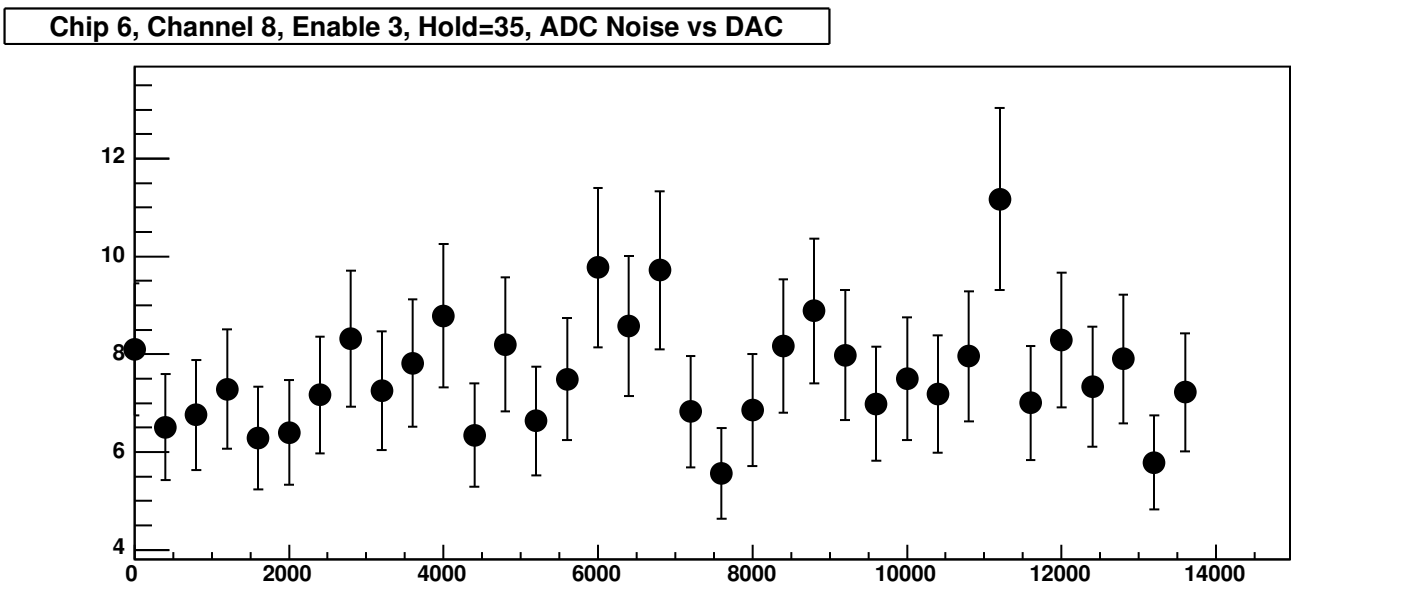

Chip 6, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

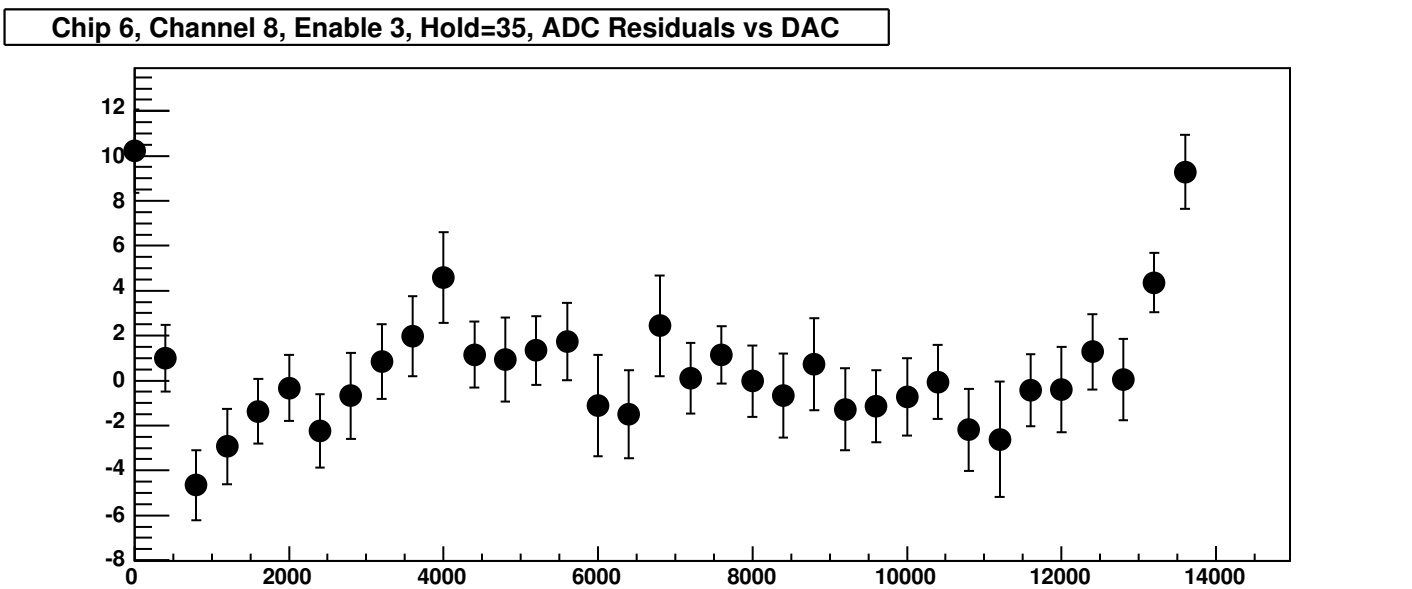

Chip 6, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 833.2 / 23
p0 -318.1 ± 1.709
p1 2.359 ± 0.0005652
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

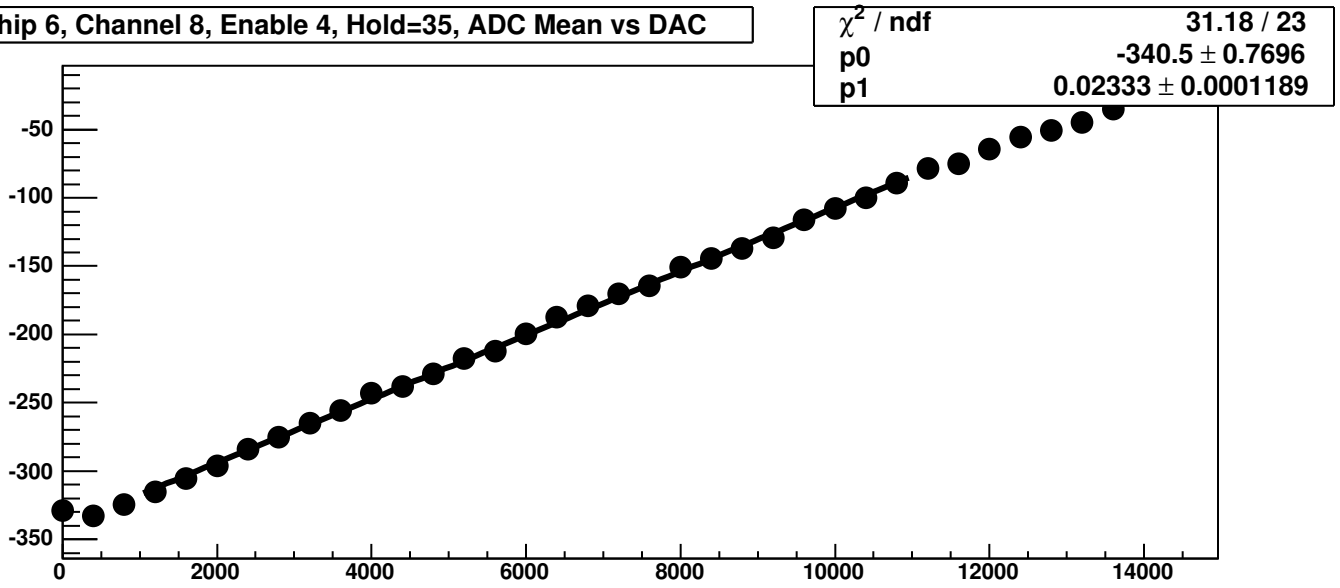

Chip 6, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.05 / 23
p0 -340.5 ± 0.759
p1 0.02148 ± 0.0001155
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

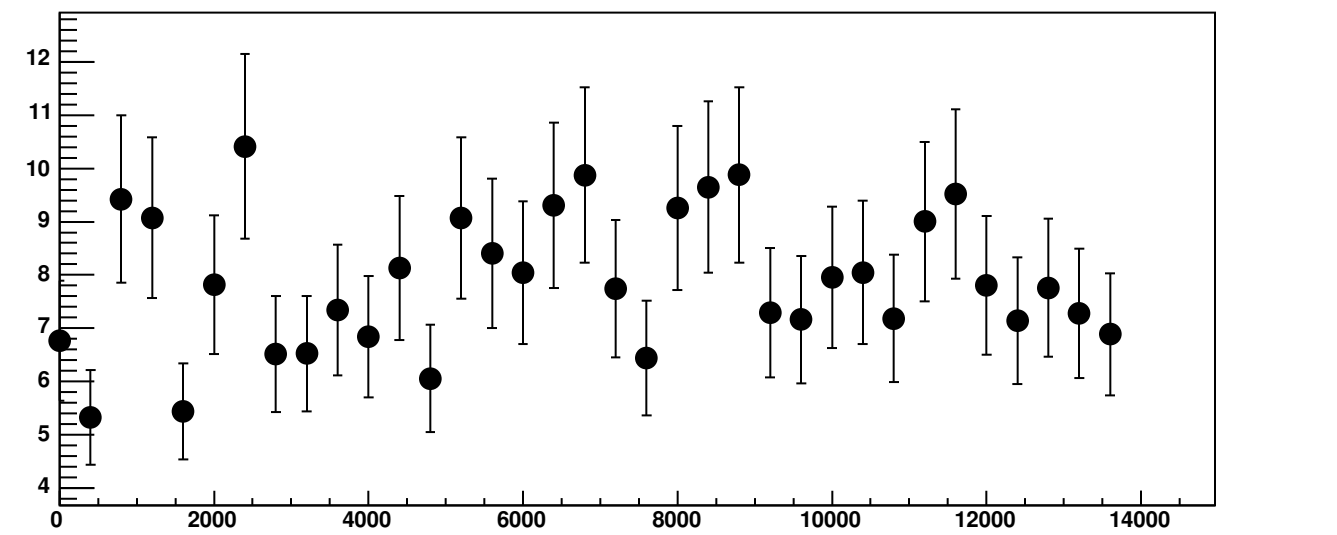

Chip 6, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

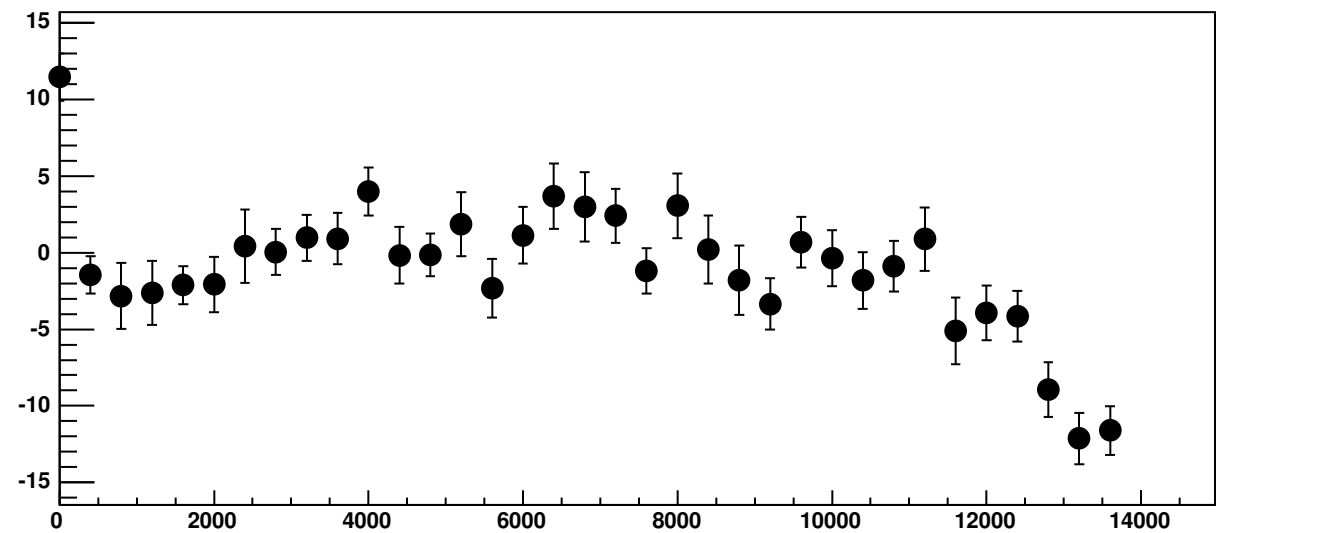
Chip 6, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC



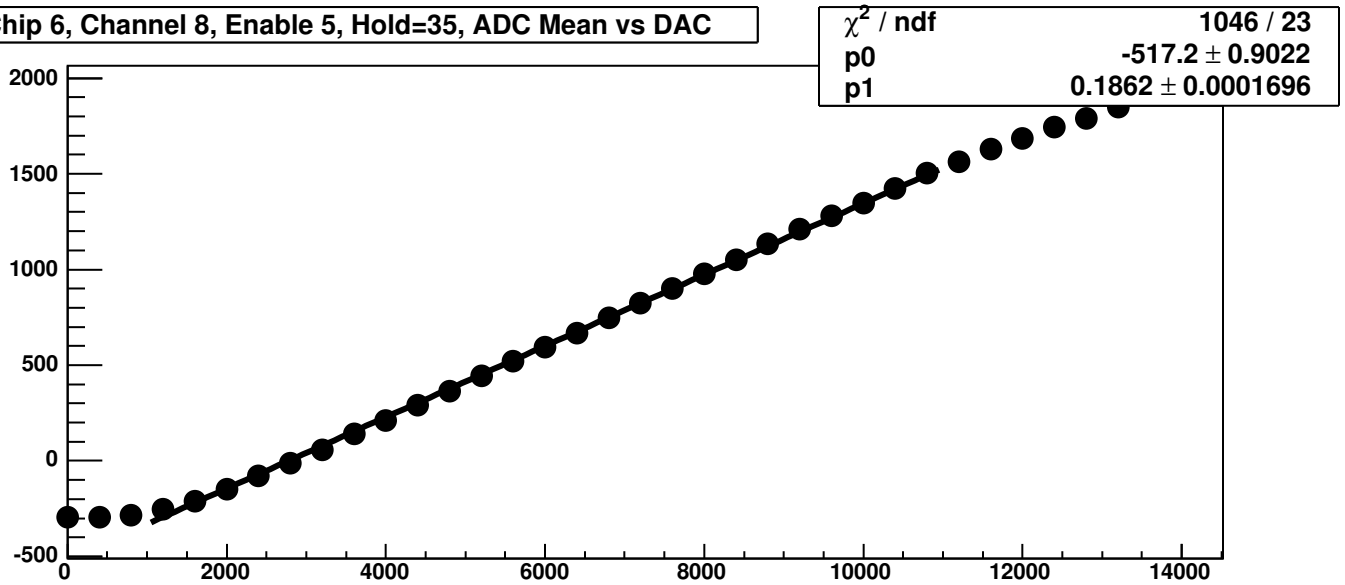
Chip 6, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC



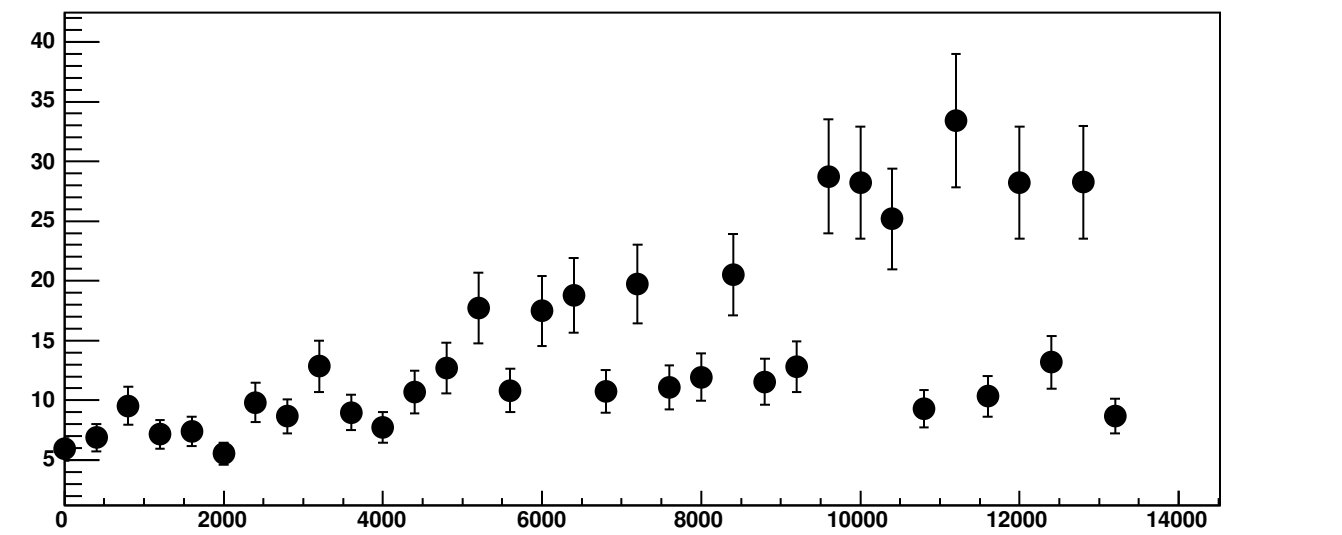
Chip 6, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC



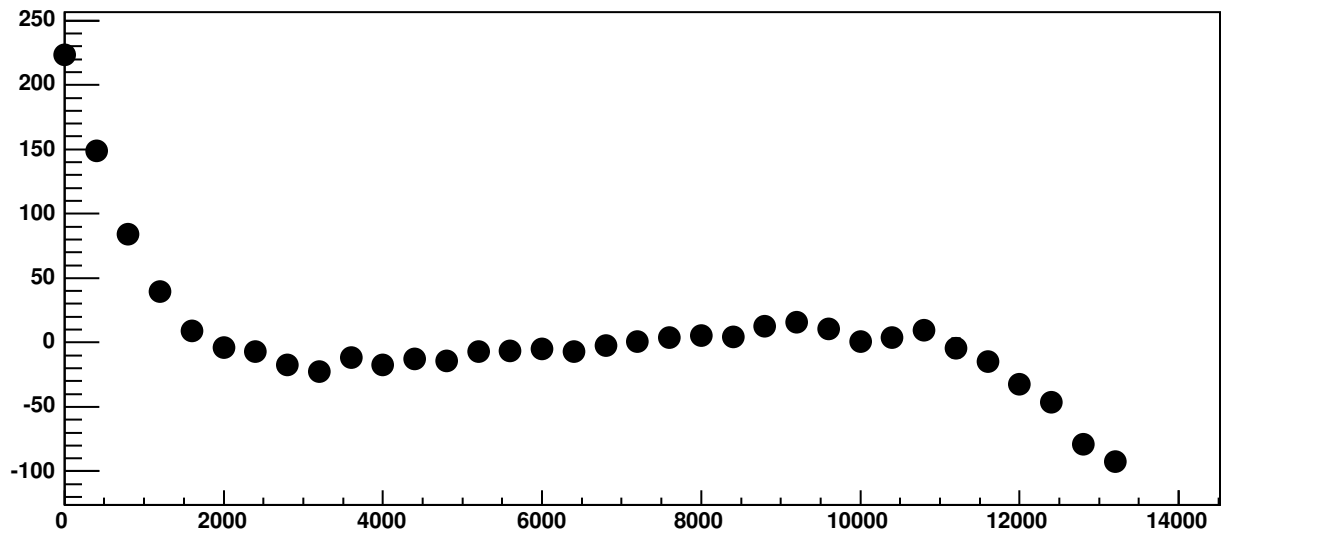
Chip 6, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC

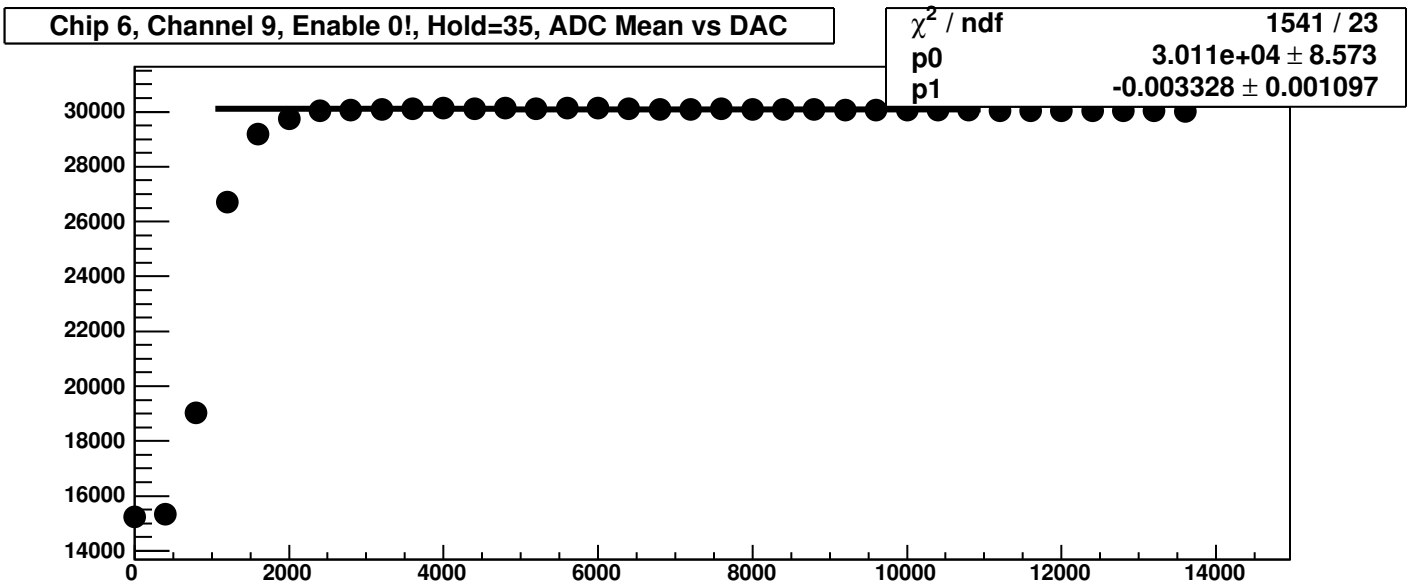


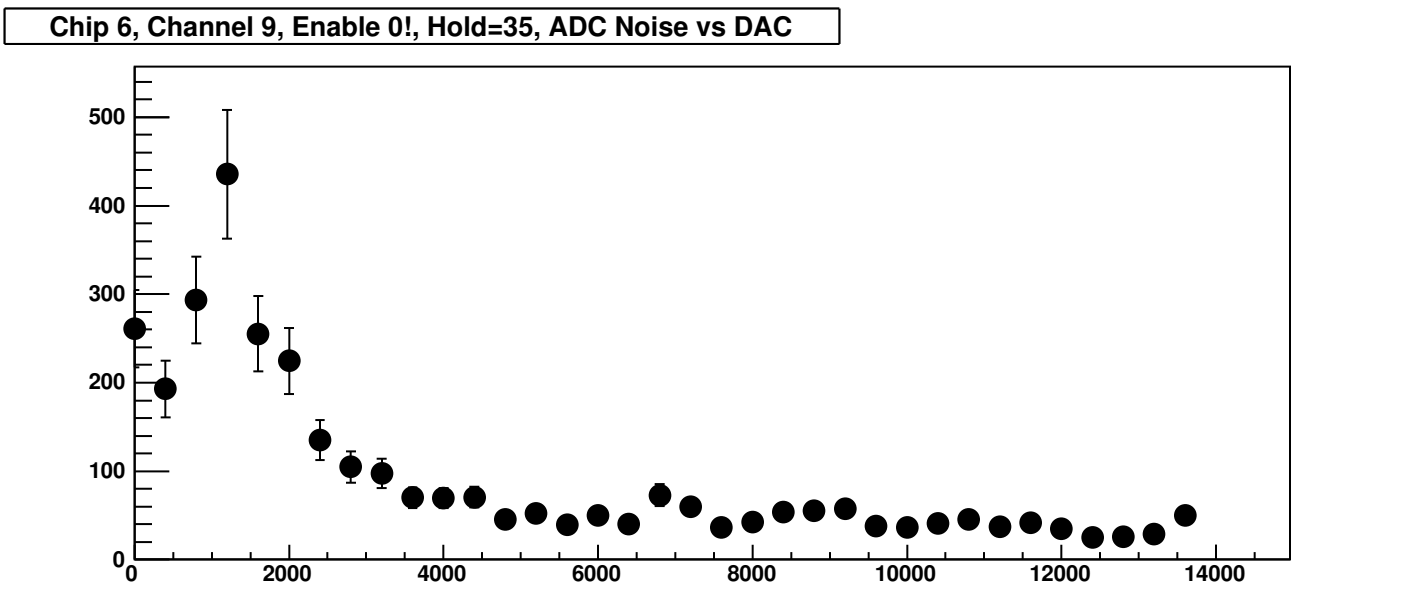
Chip 6, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC

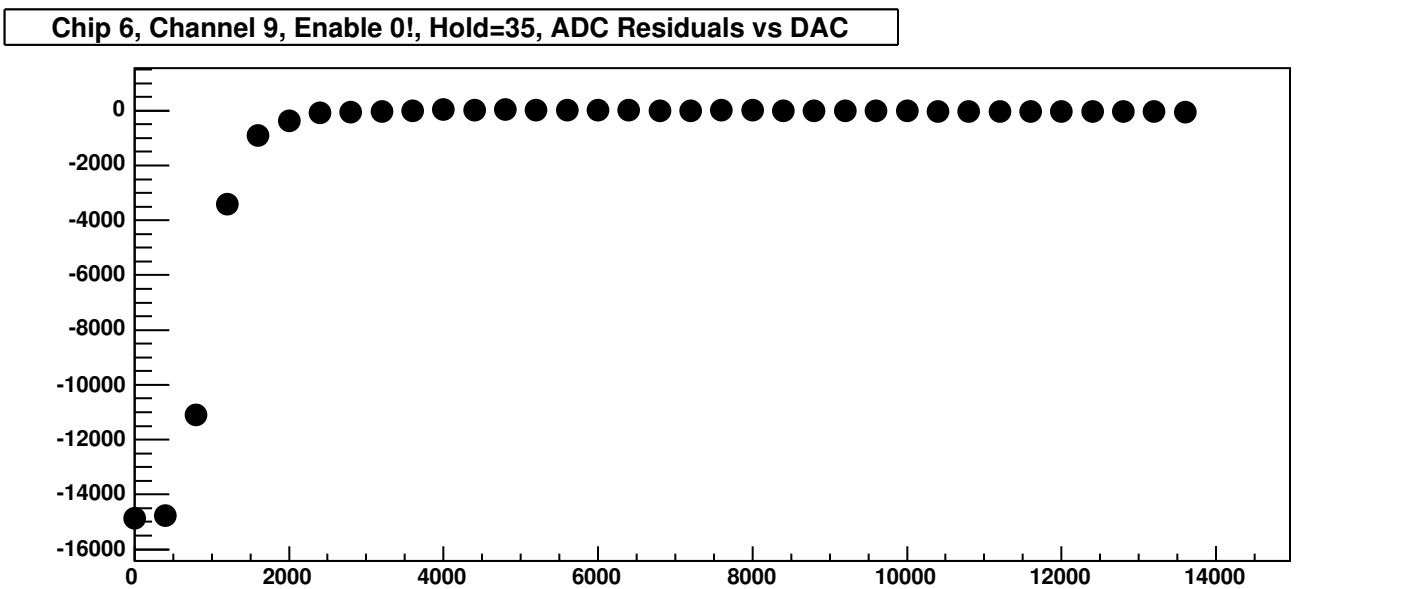


Chip 6, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC

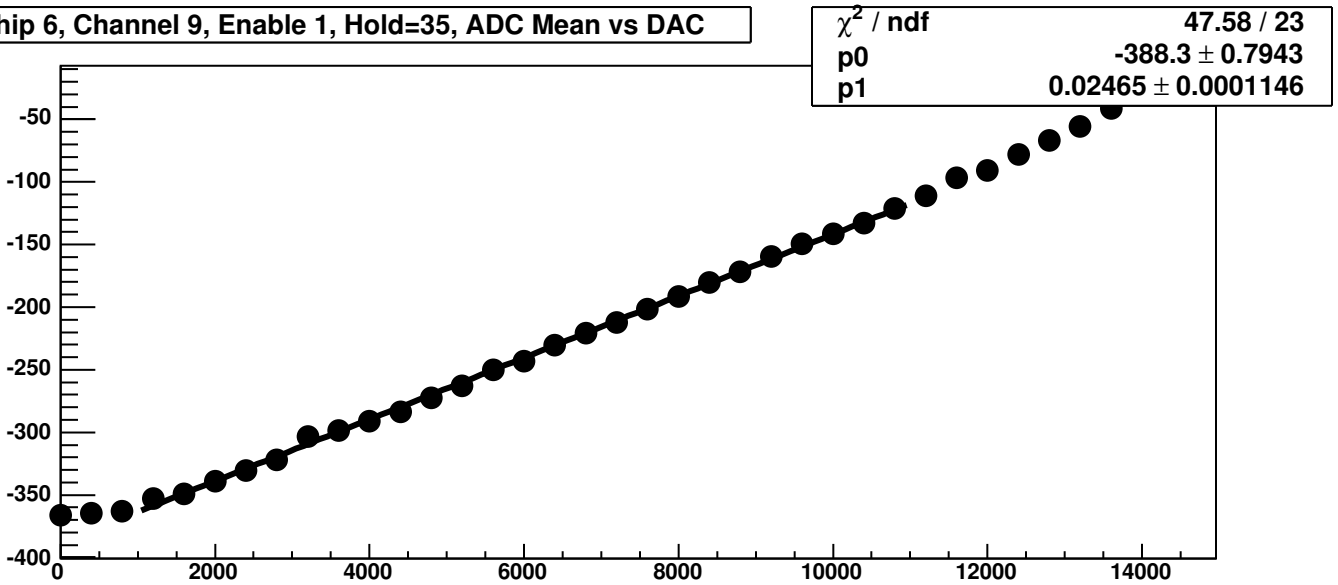



Chip 6, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1541 / 23
p0 3.011e+04 ± 8.573
p1 -0.003328 ± 0.001097
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

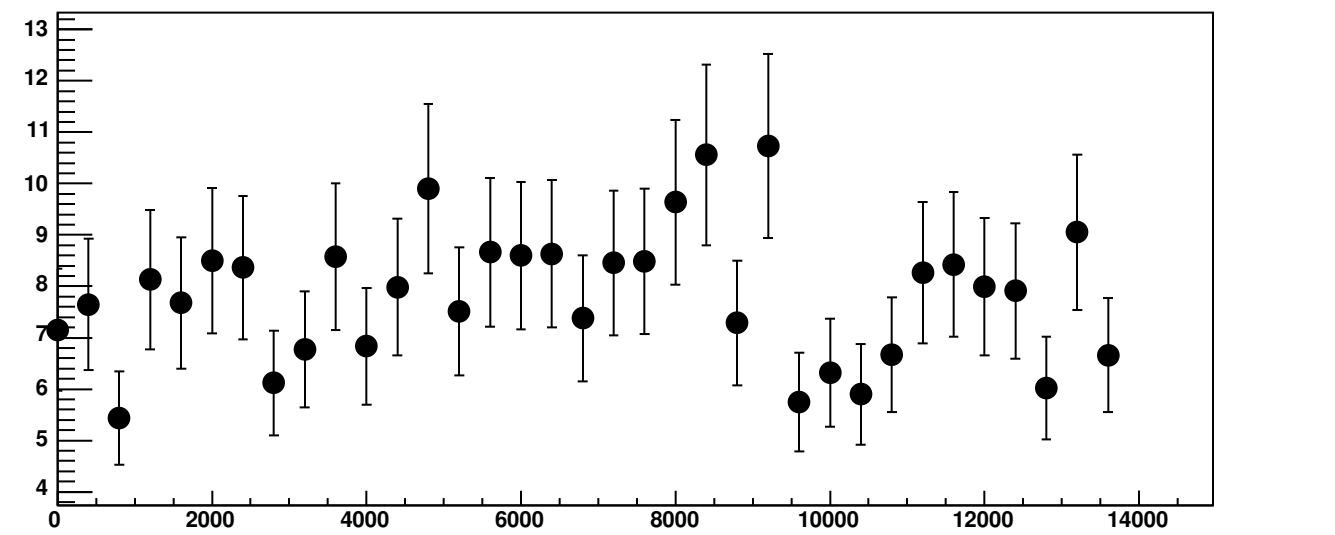

Chip 6, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

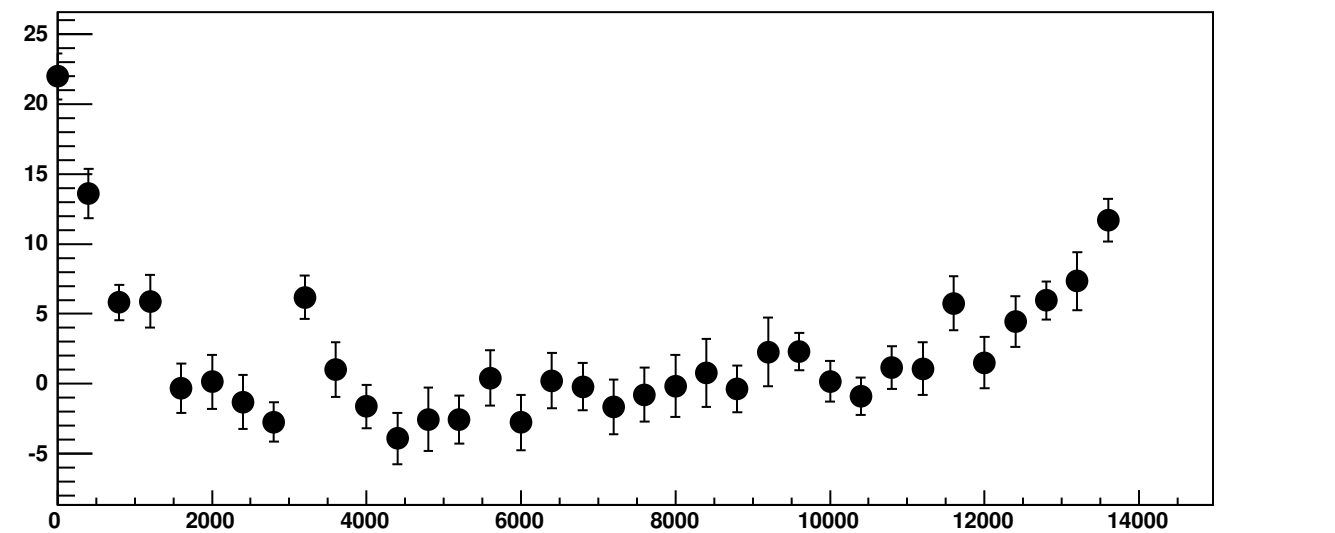
Chip 6, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC



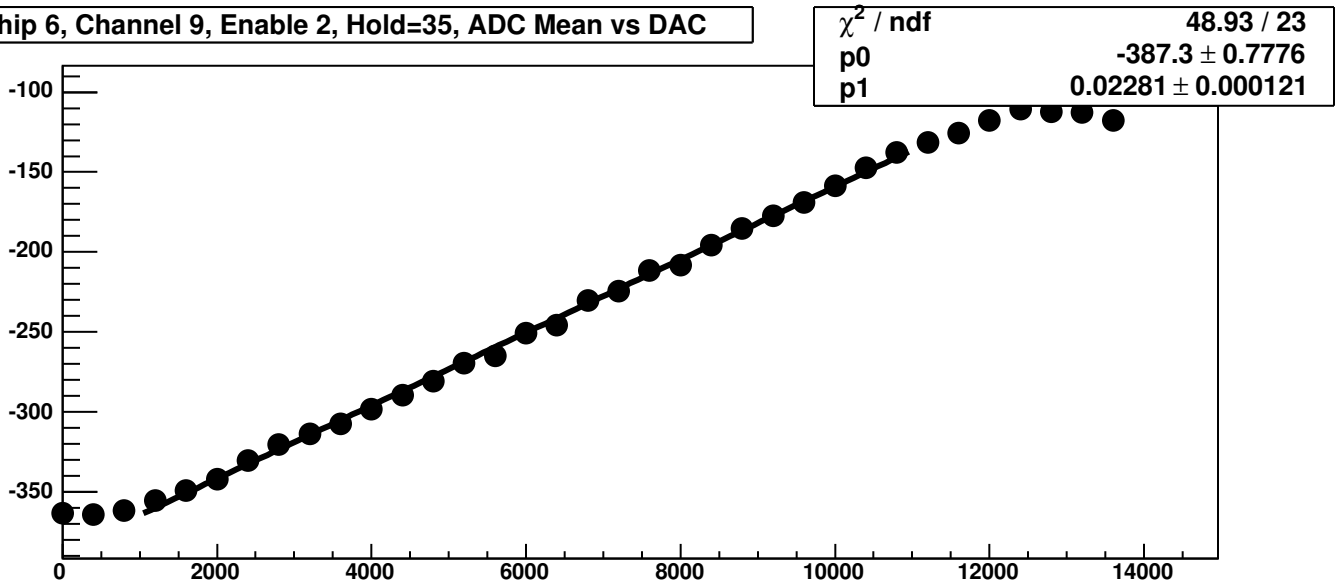
Chip 6, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC



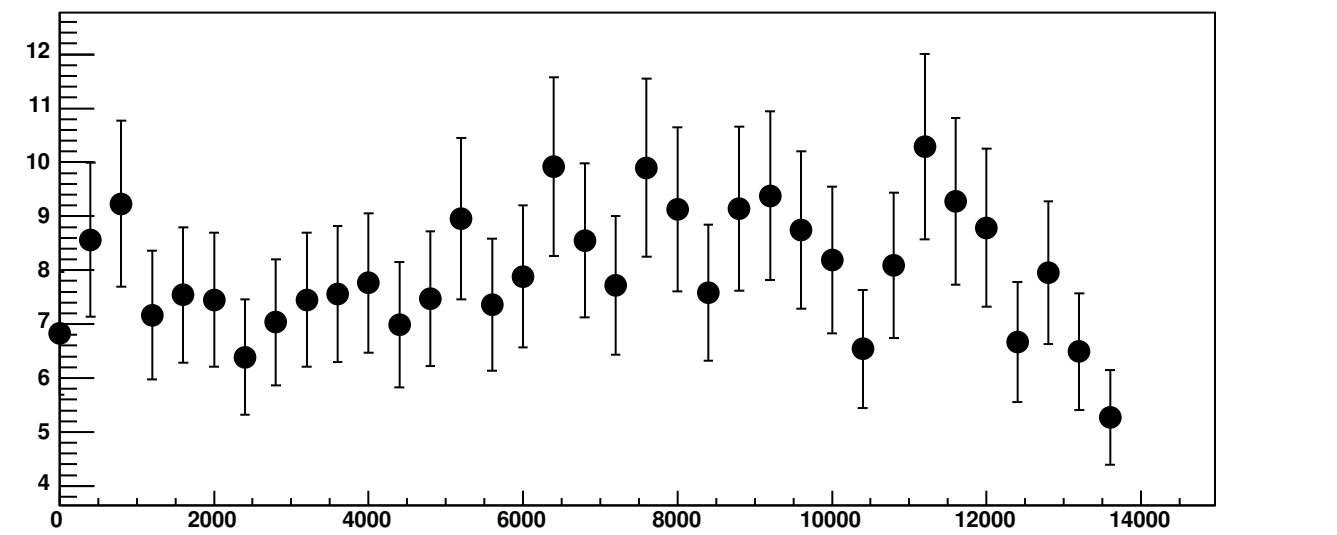
Chip 6, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC



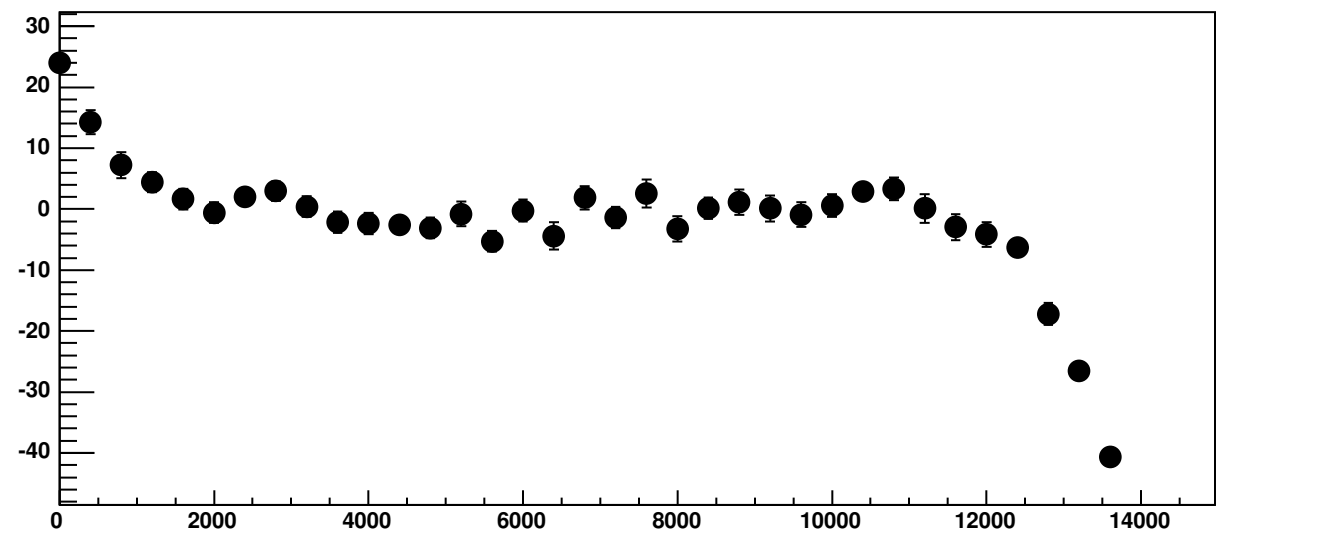
Chip 6, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

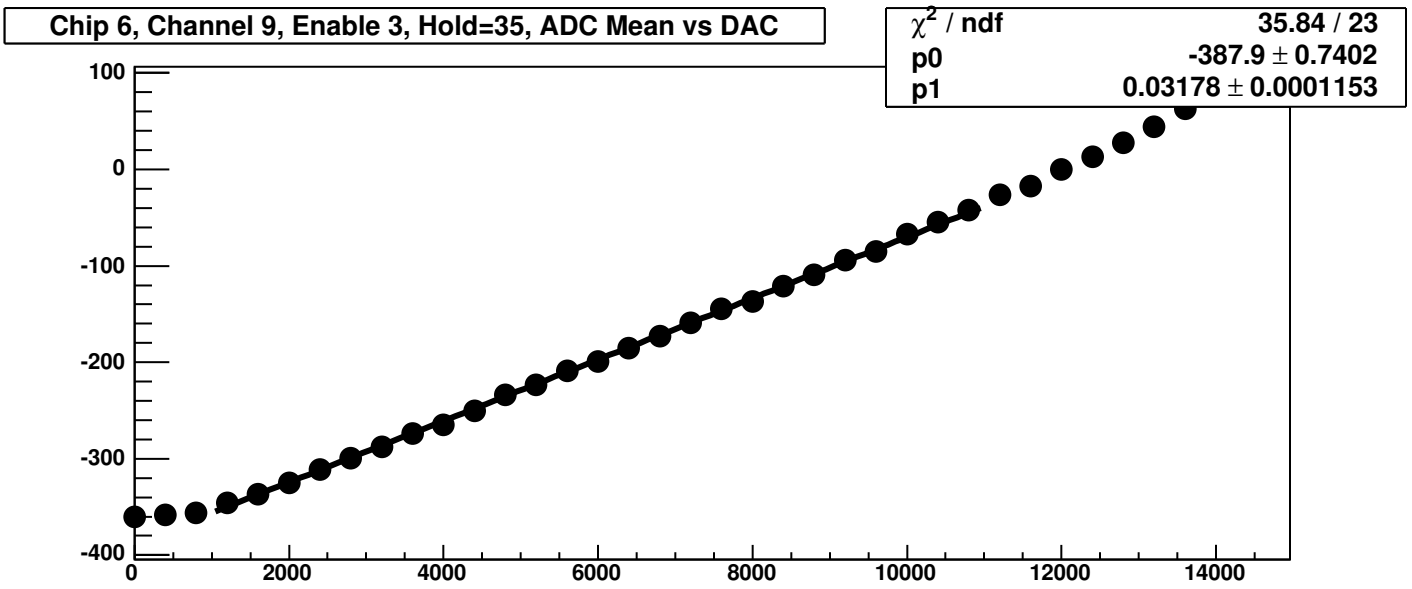


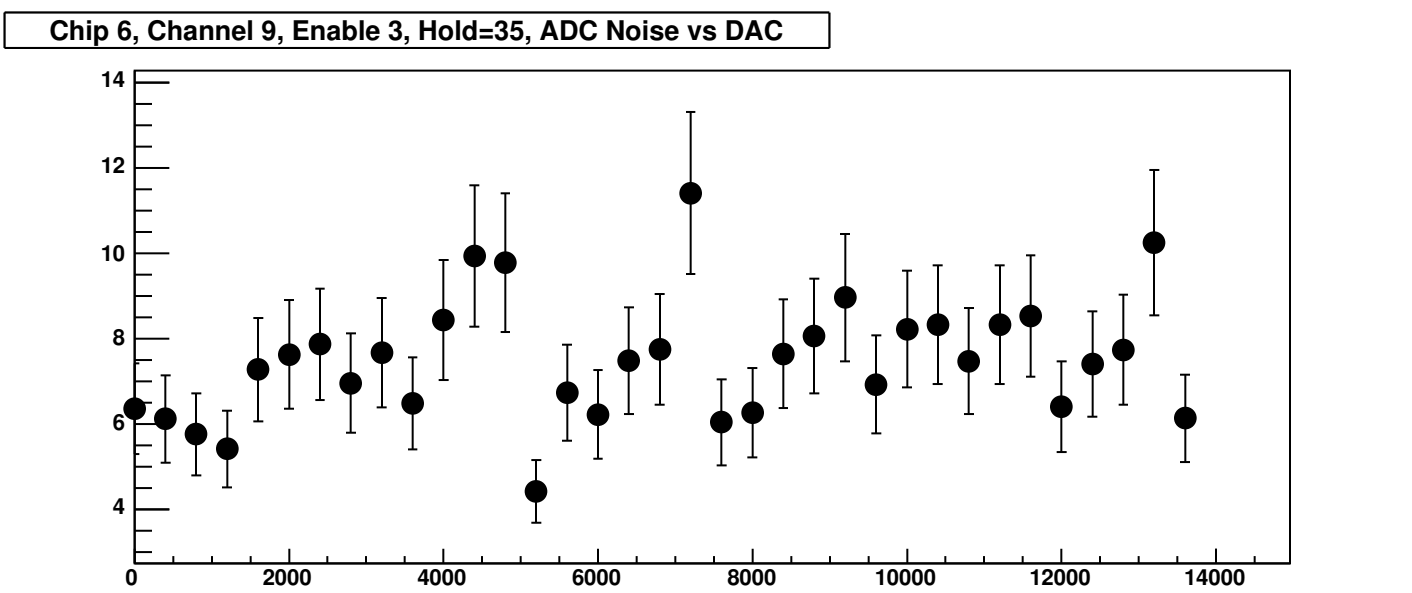
Chip 6, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC

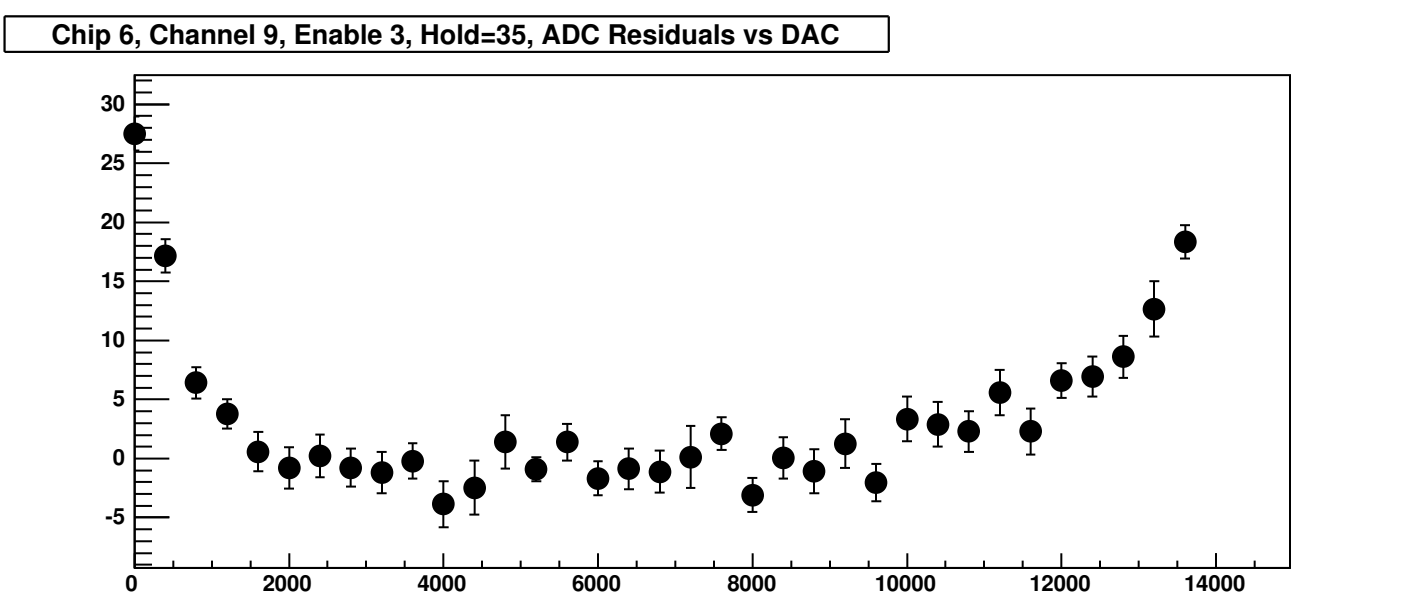


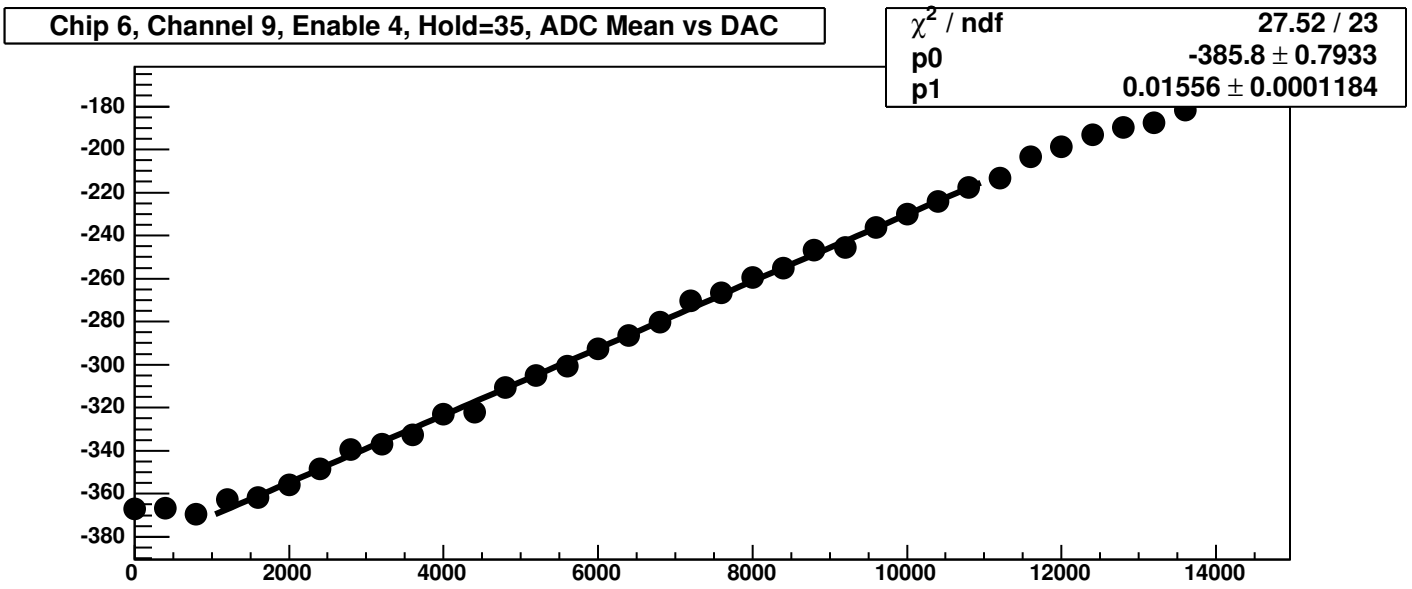
Chip 6, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

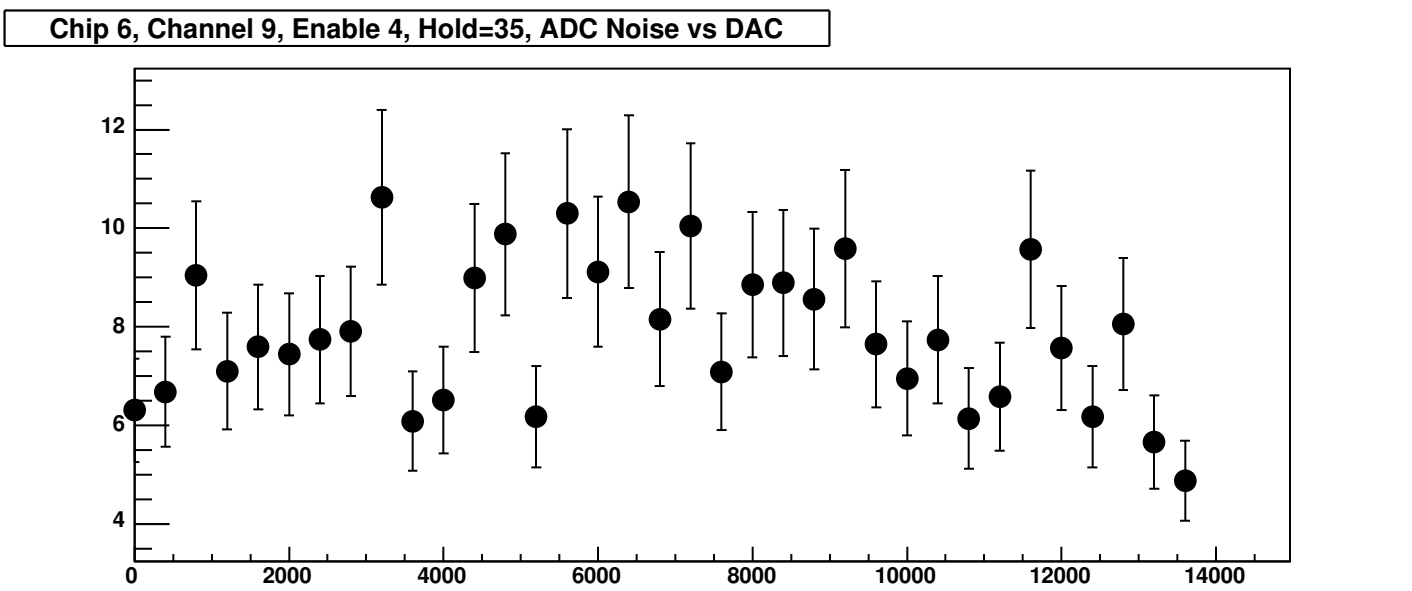


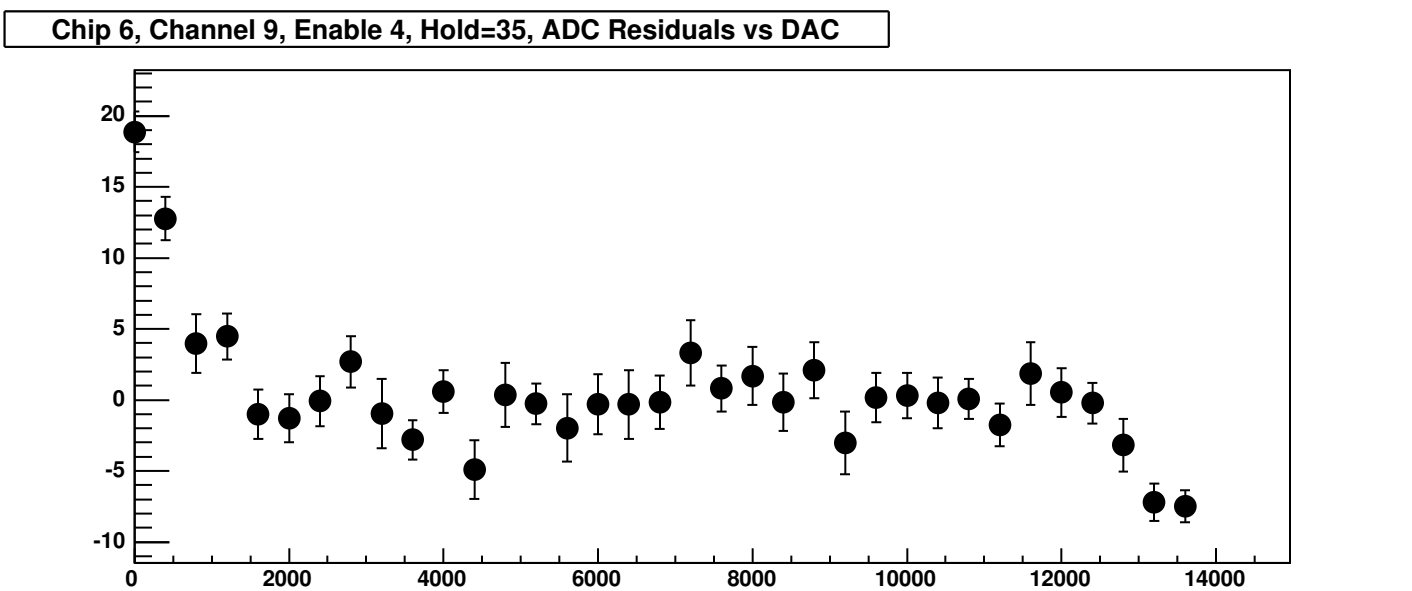

Chip 6, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 35.84 / 23
p0 -387.9 ± 0.7402
p1 0.03178 ± 0.0001153
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

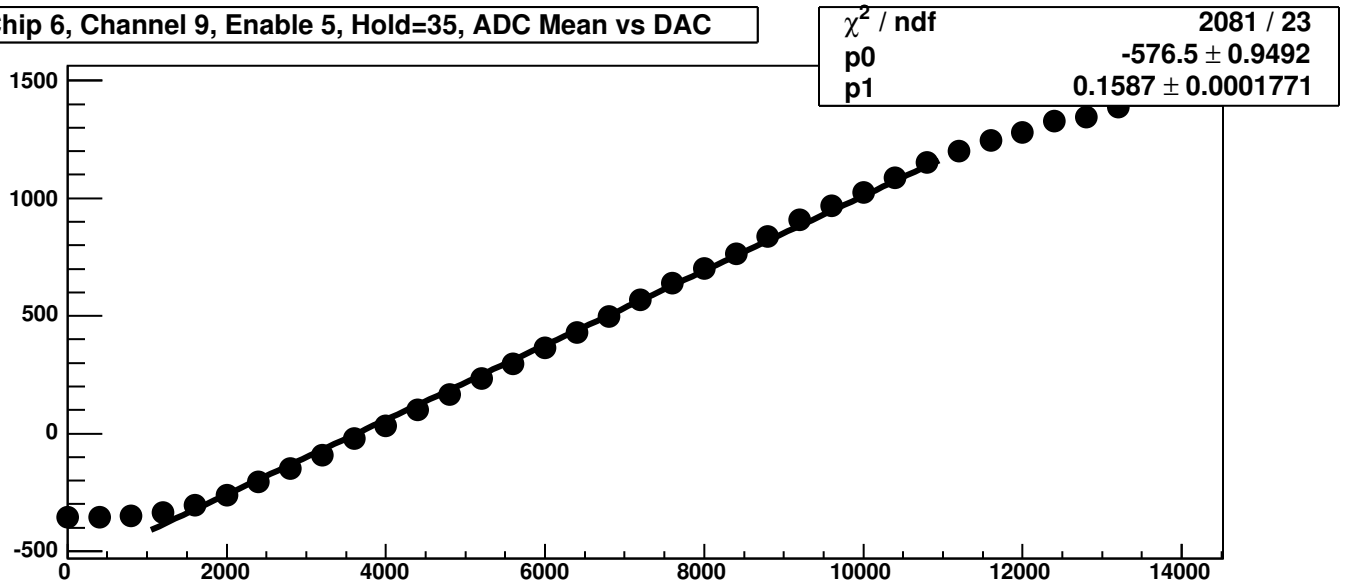

Chip 6, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.52 / 23
p0 -385.8 ± 0.7933
p1 0.01556 ± 0.0001184
-180
-200
-220
-240
-260
-280
-300
-320
-340
-360
-380
0
2000
4000
6000
8000
10000
12000
14000

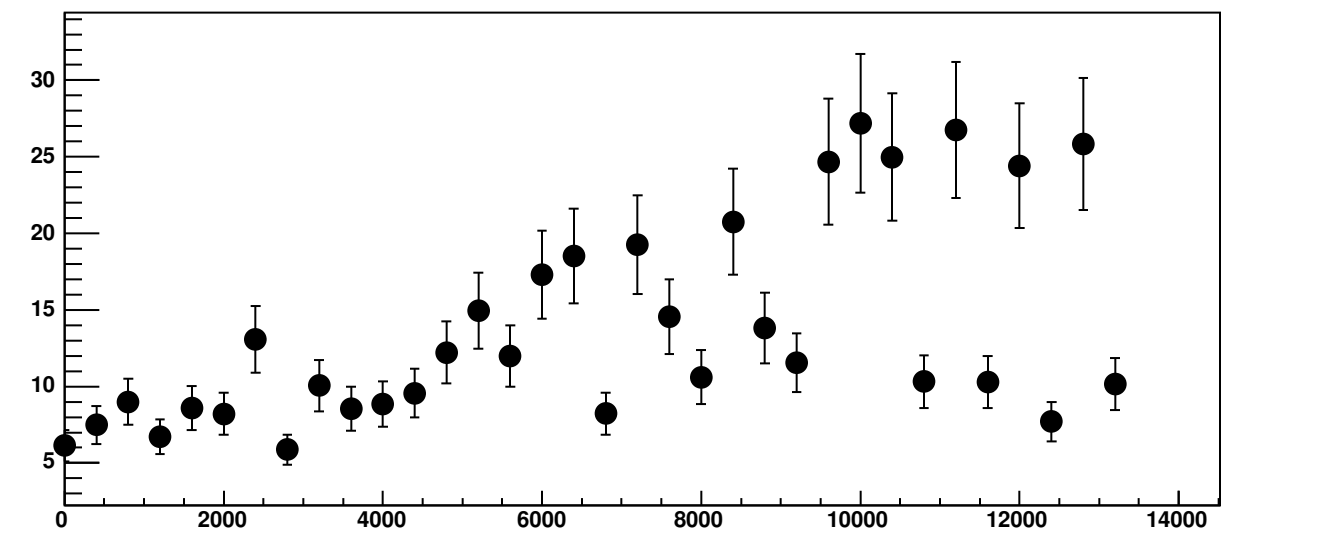

Chip 6, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

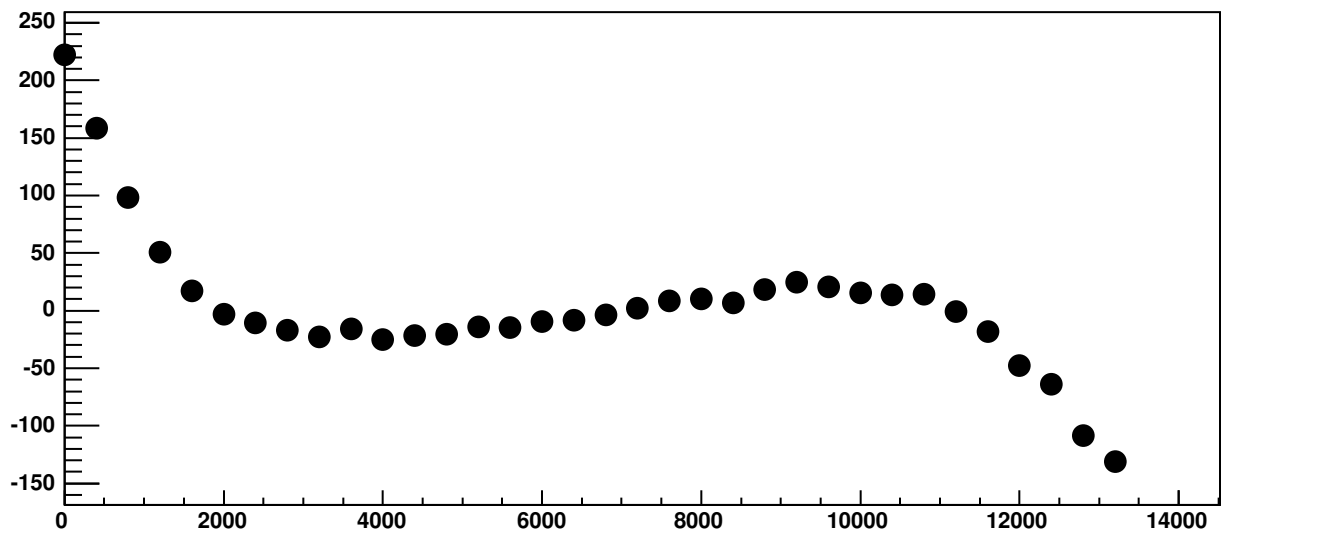
Chip 6, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

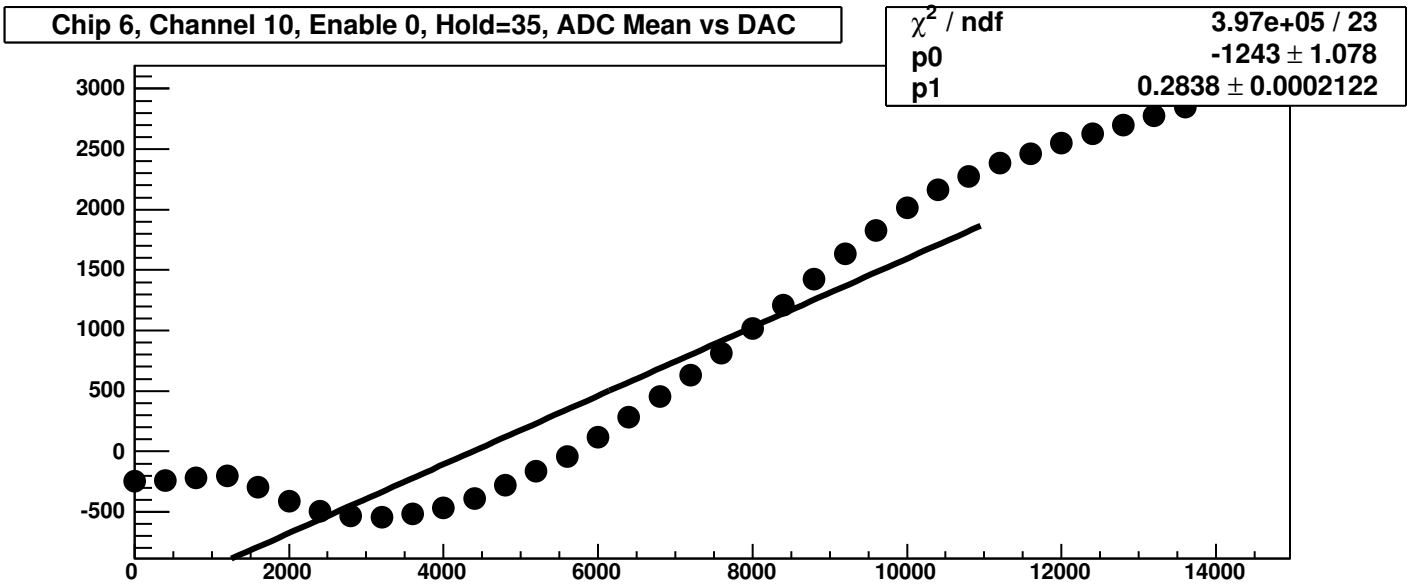


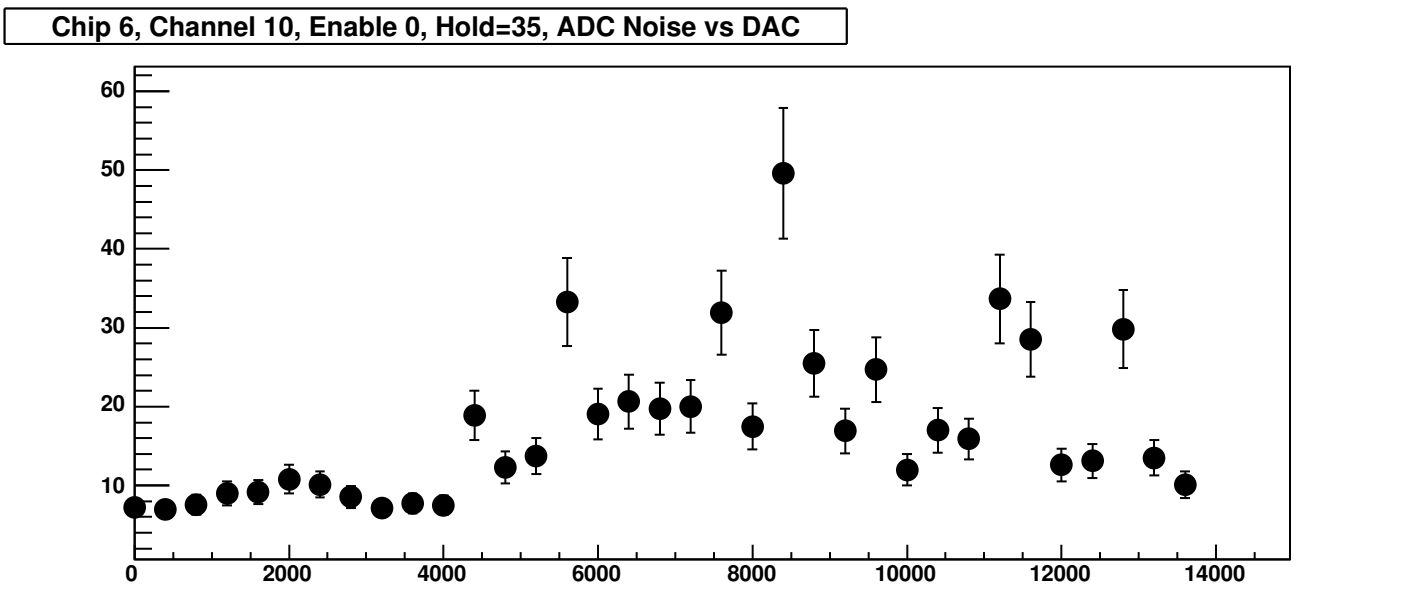
Chip 6, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

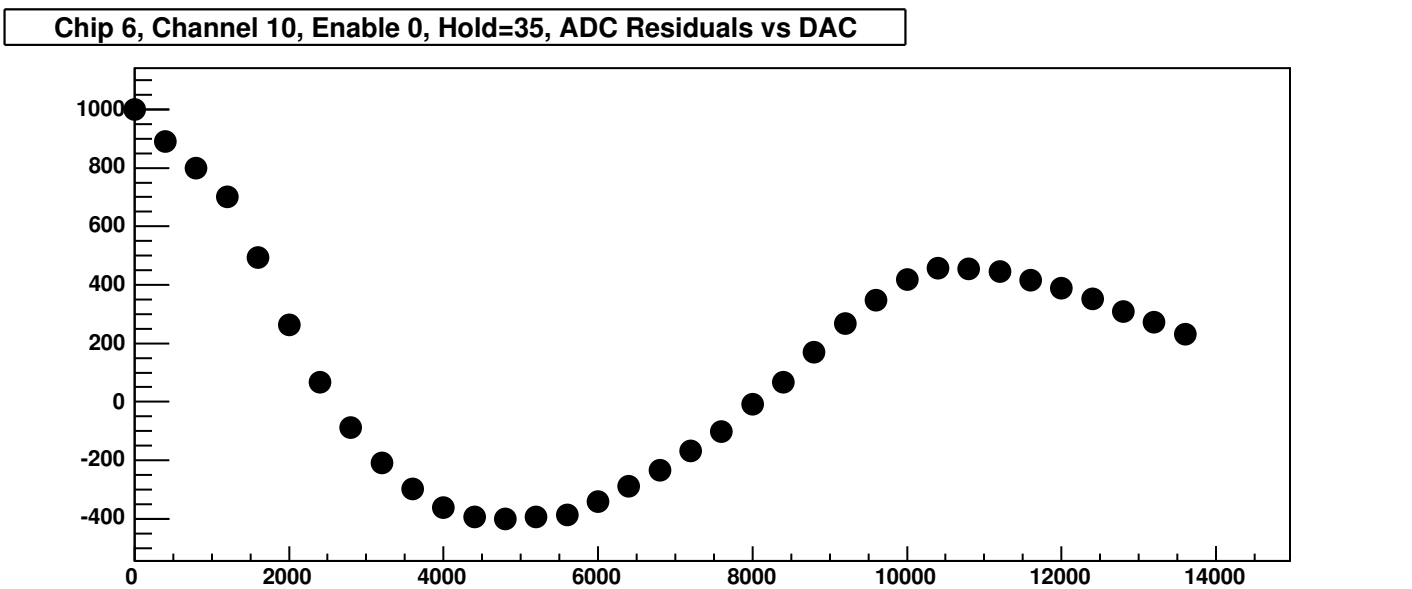


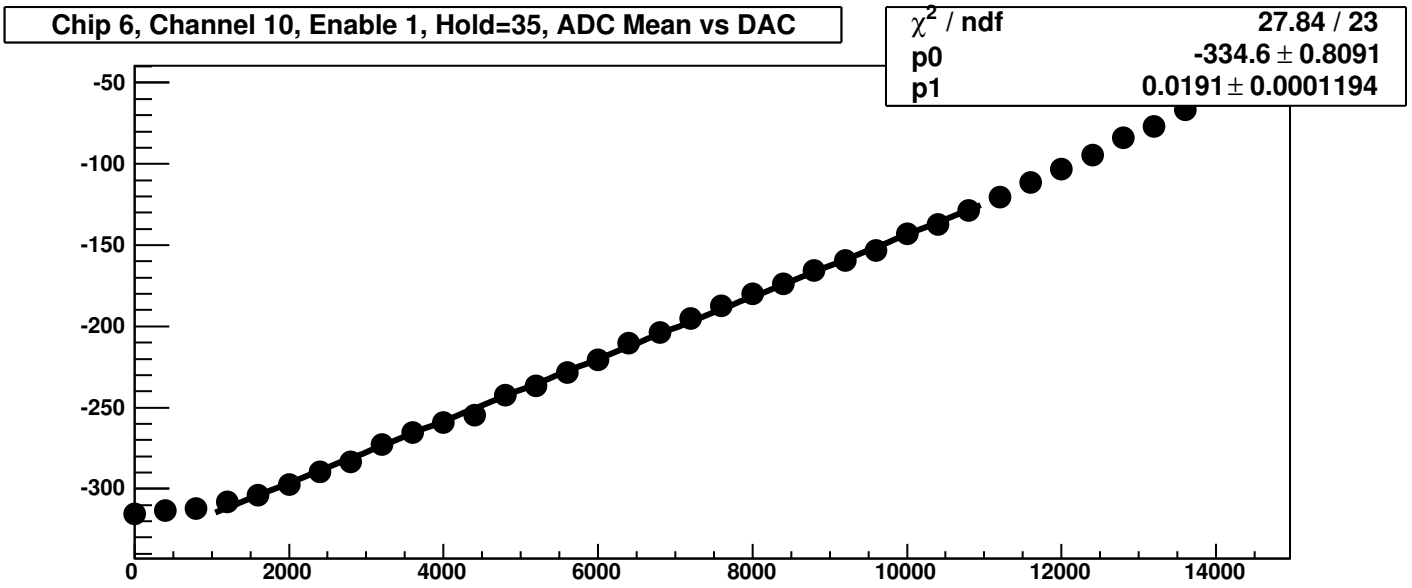
Chip 6, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

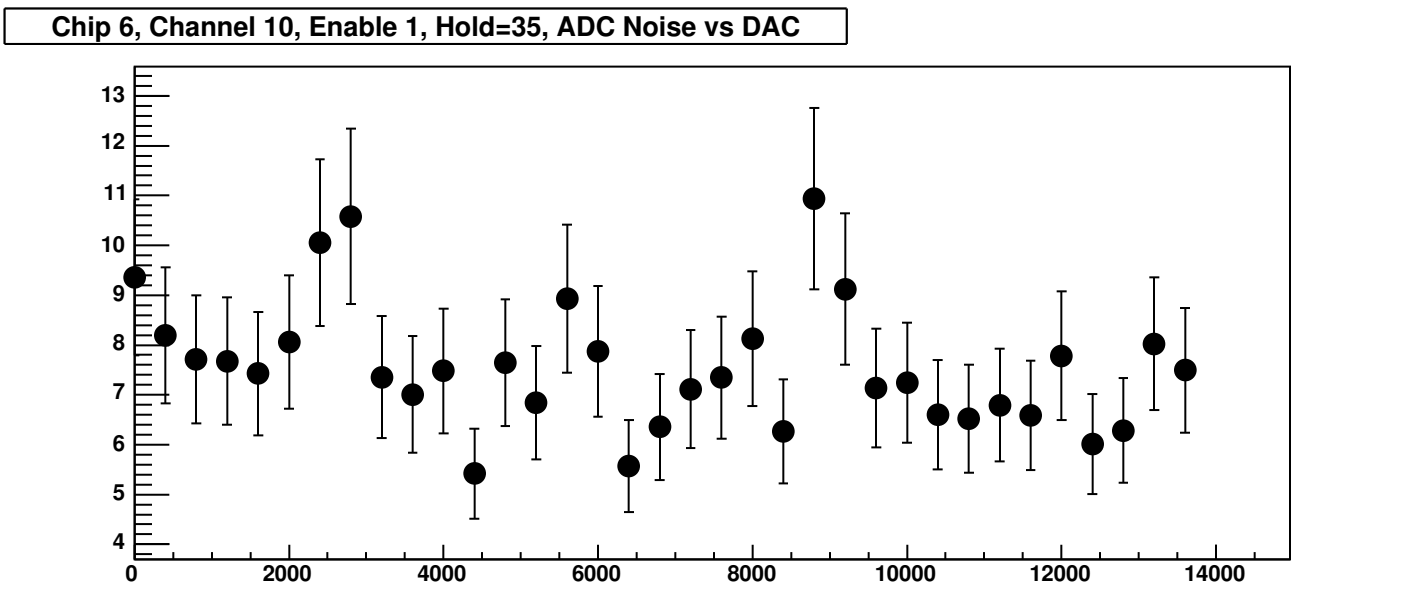


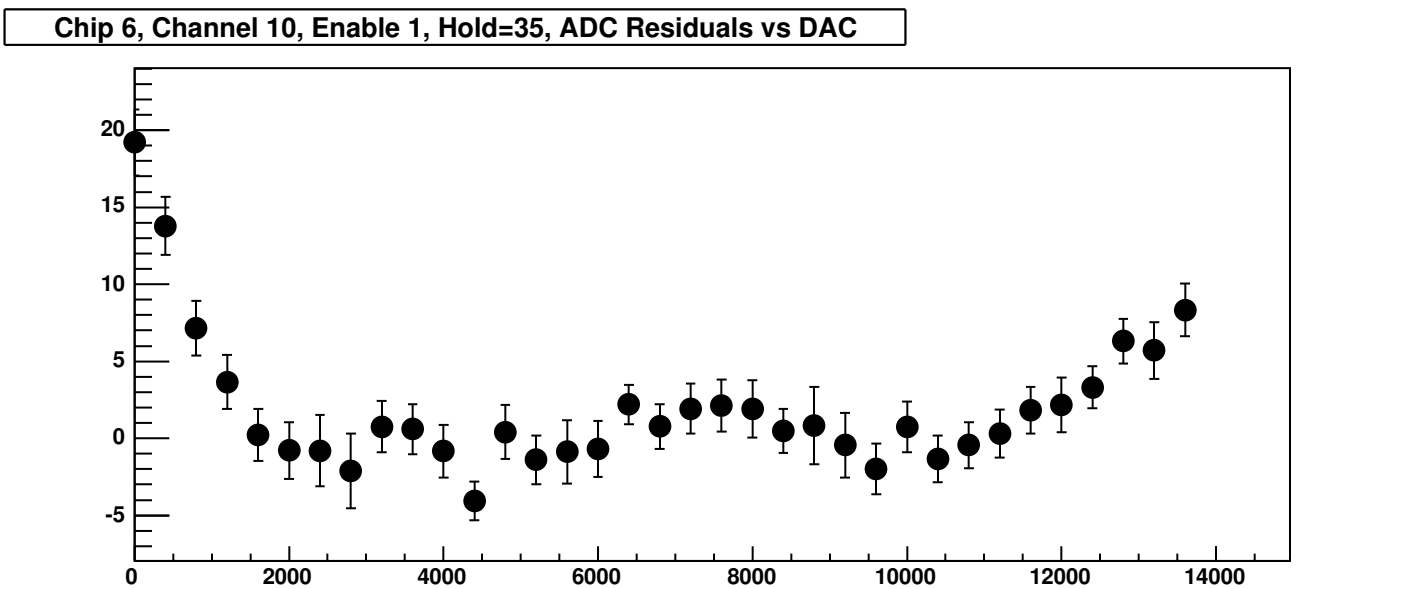

Chip 6, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.97e+05 / 23
p0 -1243 ± 1.078
p1 0.2838 ± 0.0002122
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

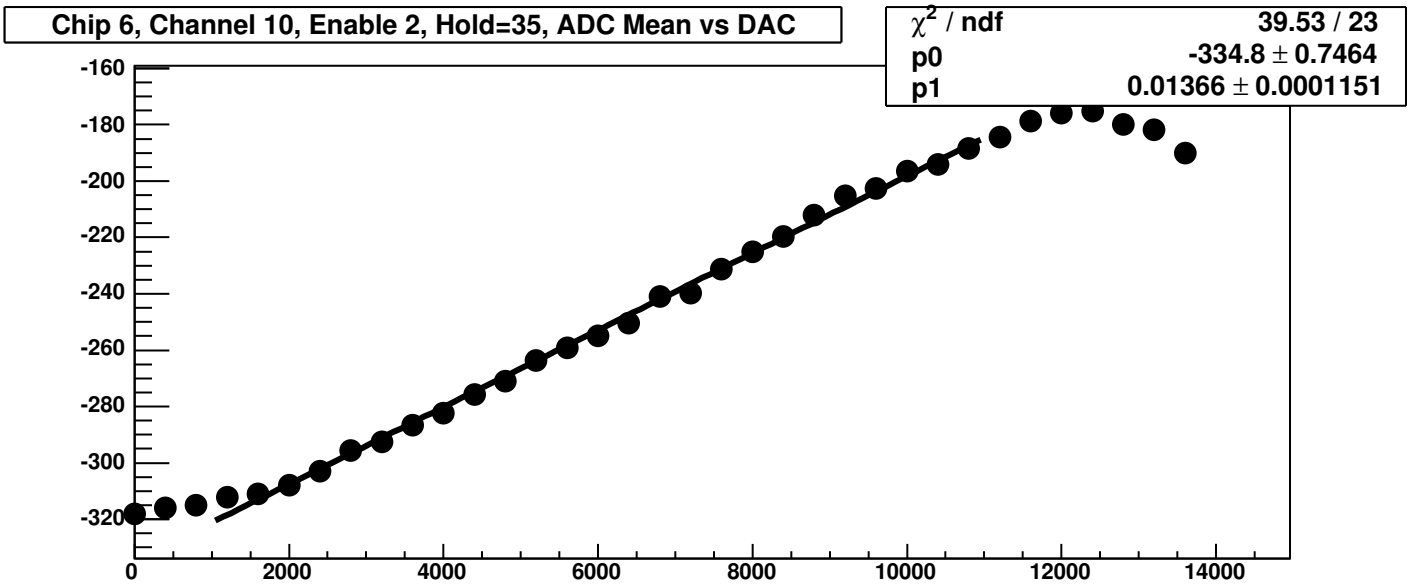

Chip 6, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

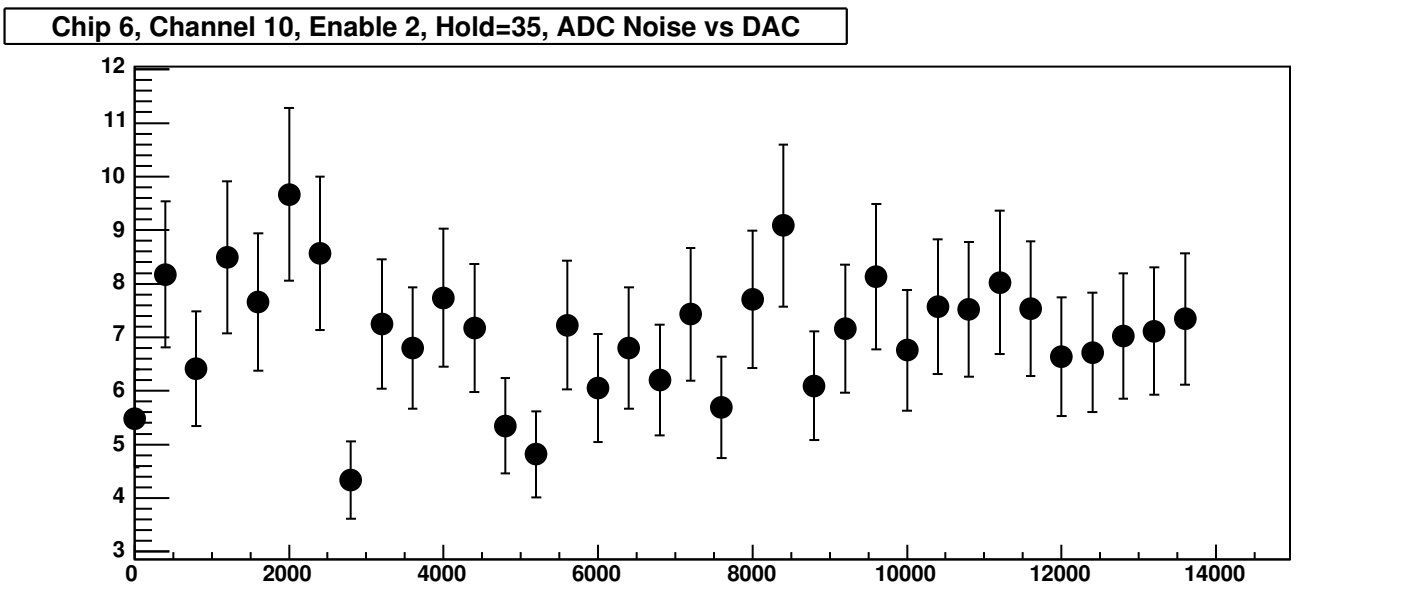

Chip 6, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

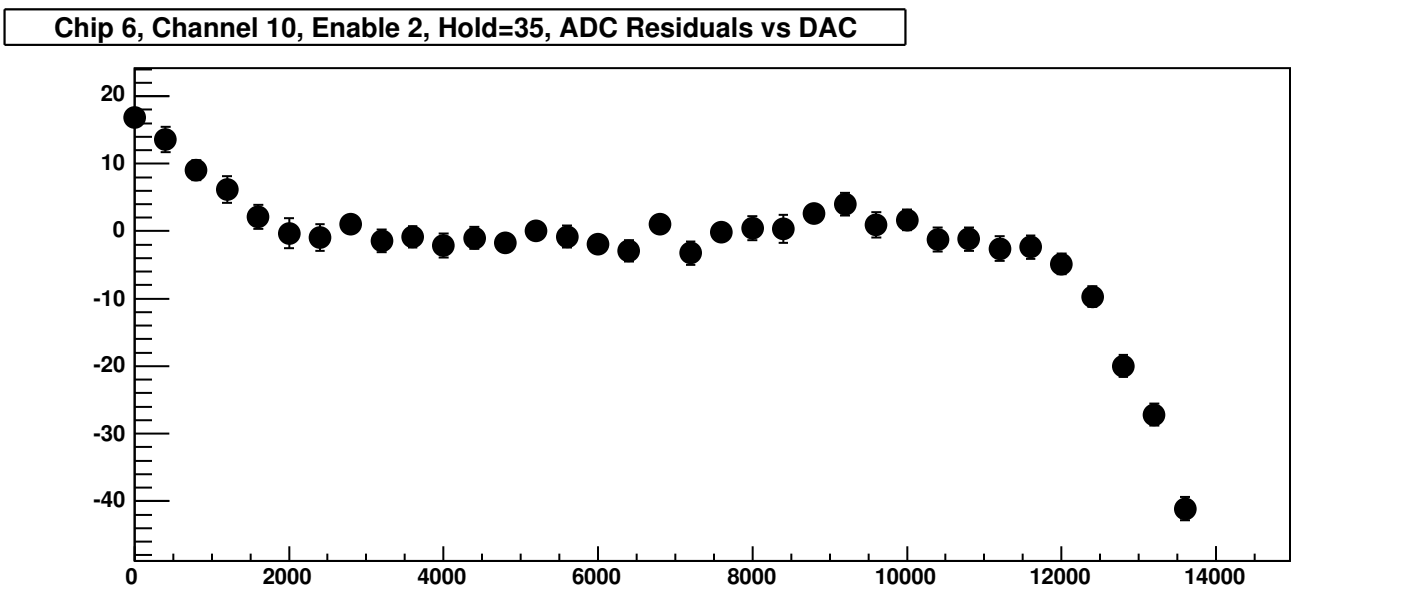

Chip 6, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 27.84 / 23
p0 -334.6 ± 0.8091
p1 0.0191 ± 0.0001194
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

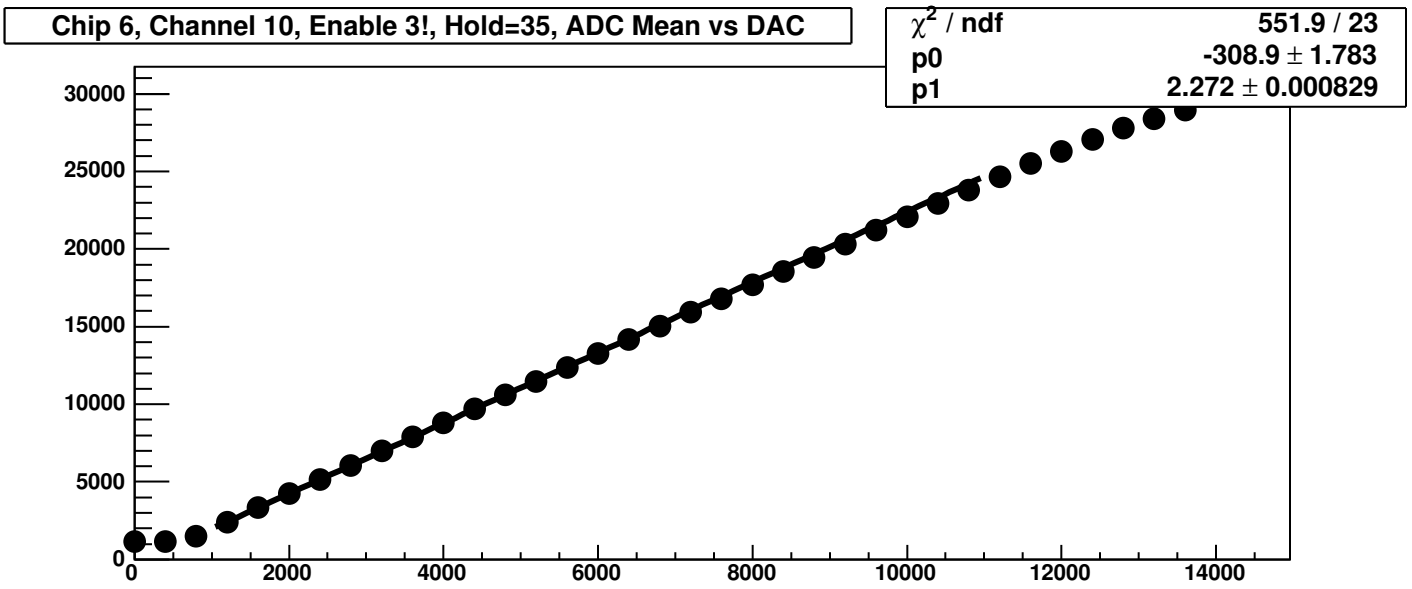

Chip 6, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

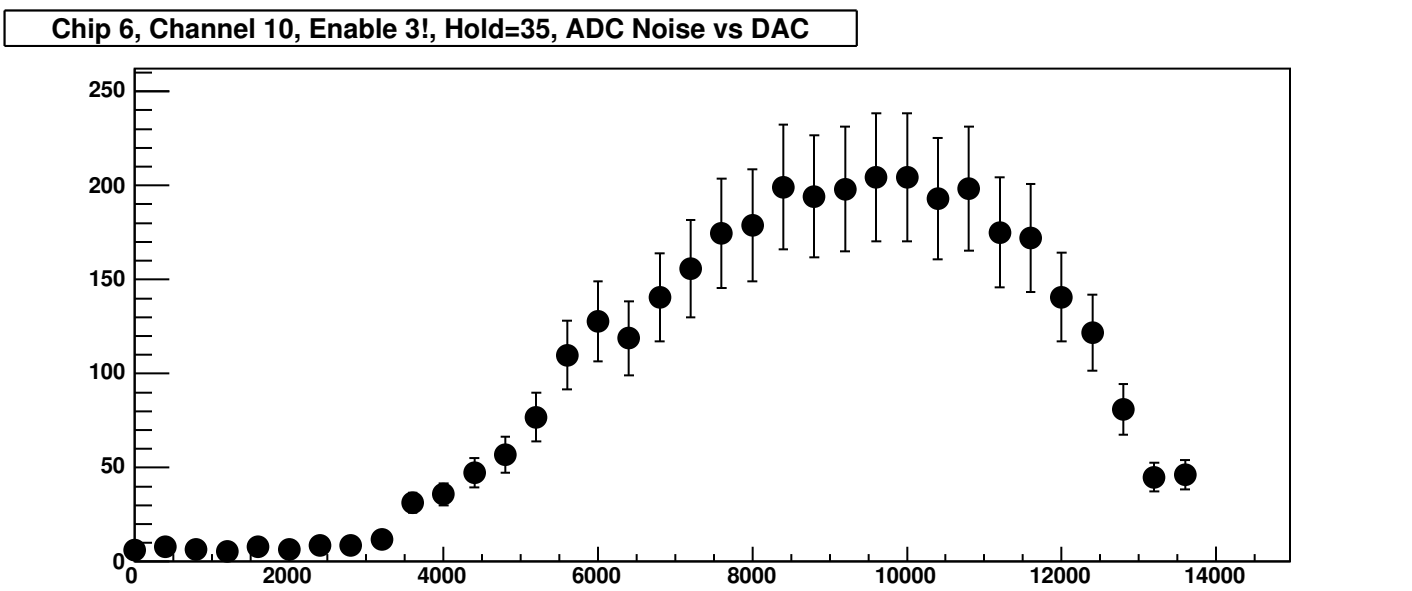

Chip 6, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

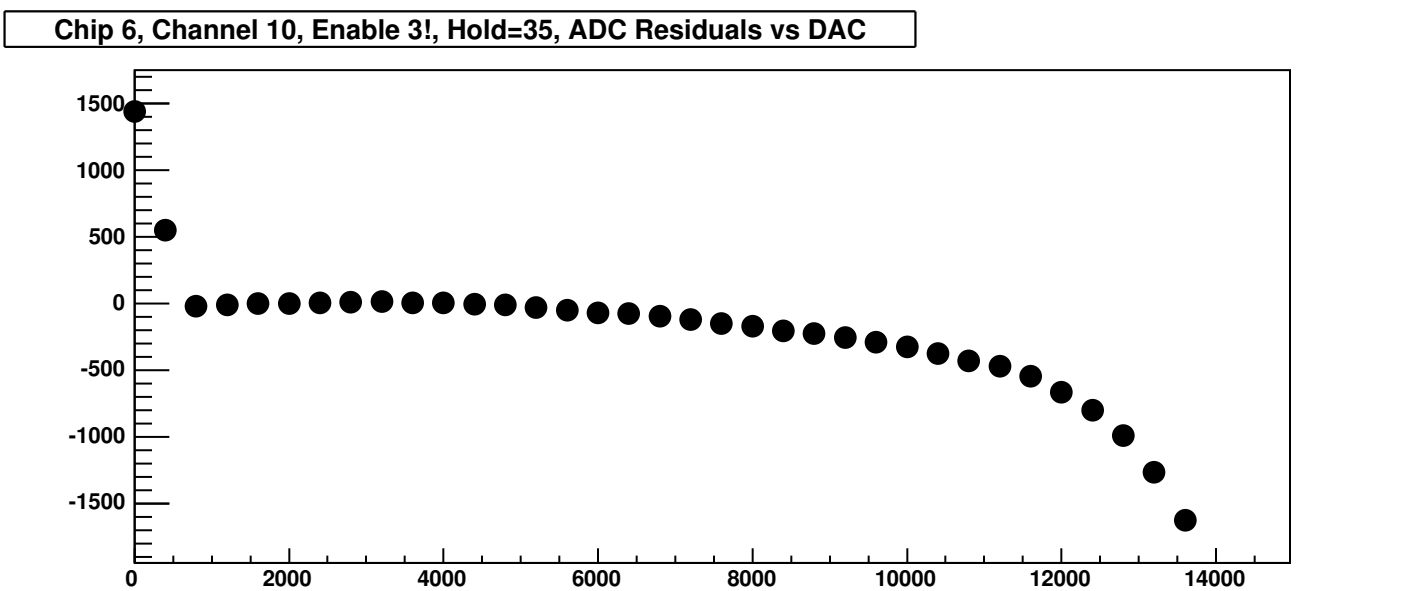

Chip 6, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 39.53 / 23
p0 -334.8 ± 0.7464
p1 0.01366 ± 0.0001151
-160
-180
-200
-220
-240
-260
-280
-300
-320
0
2000
4000
6000
8000
10000
12000
14000

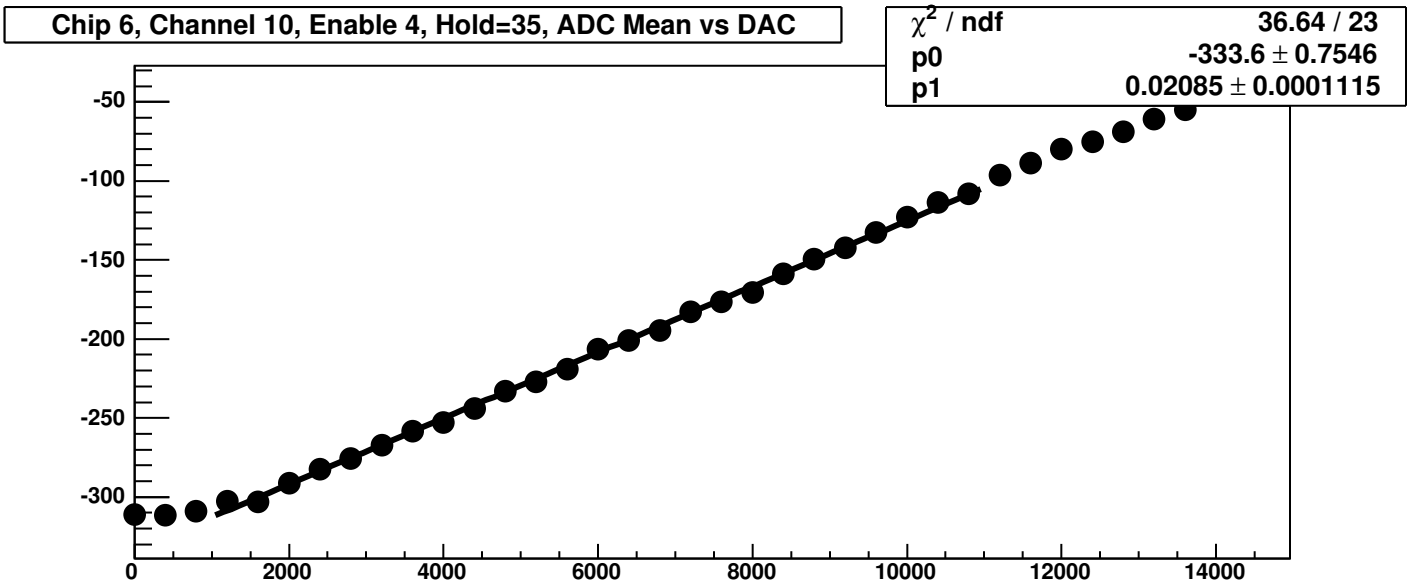

Chip 6, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

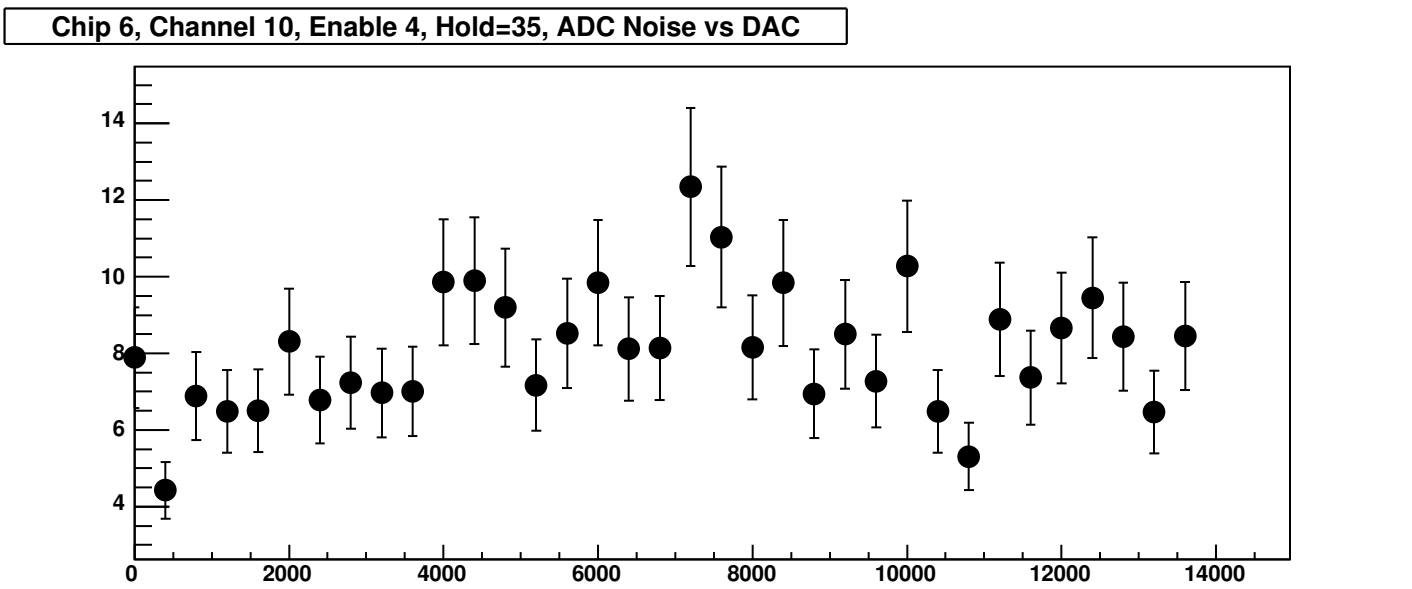

Chip 6, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

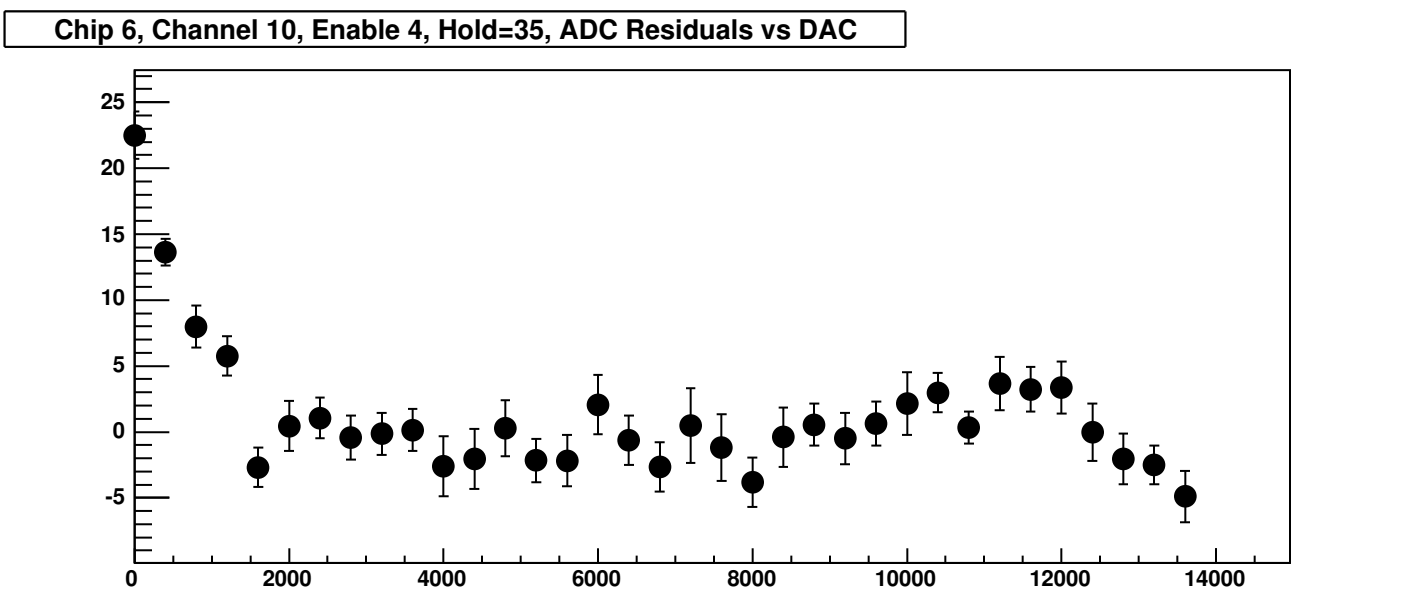

Chip 6, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 551.9 / 23
p0 -308.9 ± 1.783
p1 2.272 ± 0.000829
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

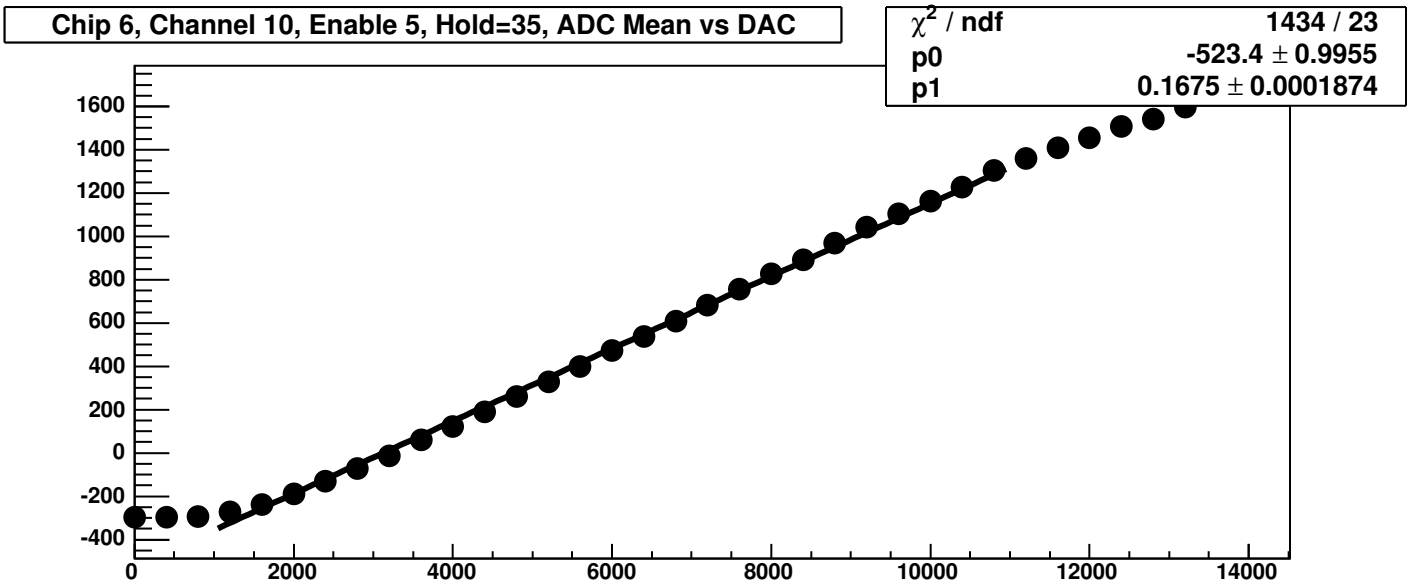

Chip 6, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

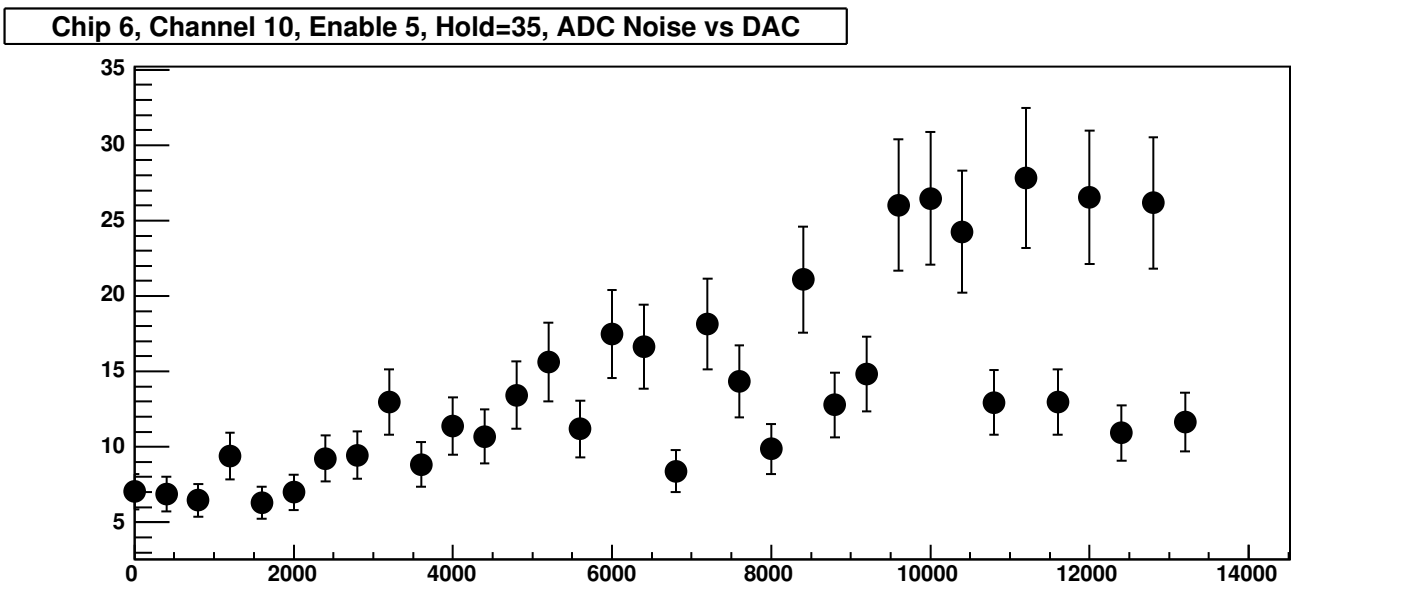

Chip 6, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

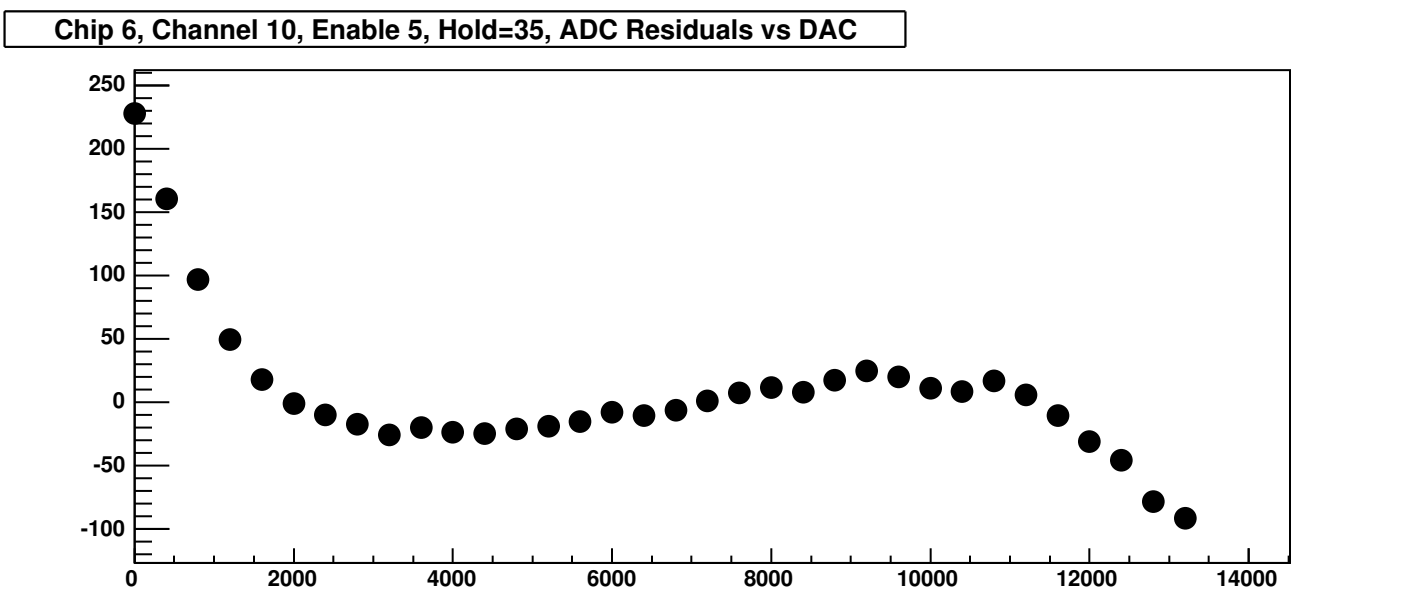

Chip 6, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 36.64 / 23
p0 -333.6 ± 0.7546
p1 0.02085 ± 0.0001115
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

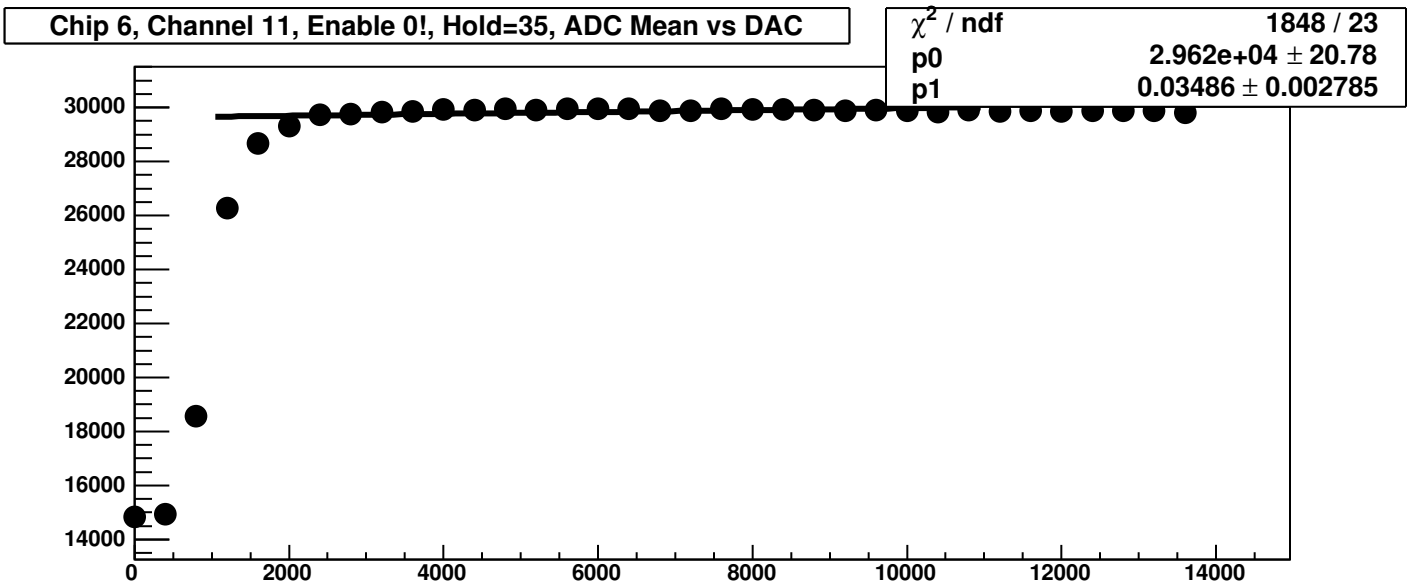

Chip 6, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

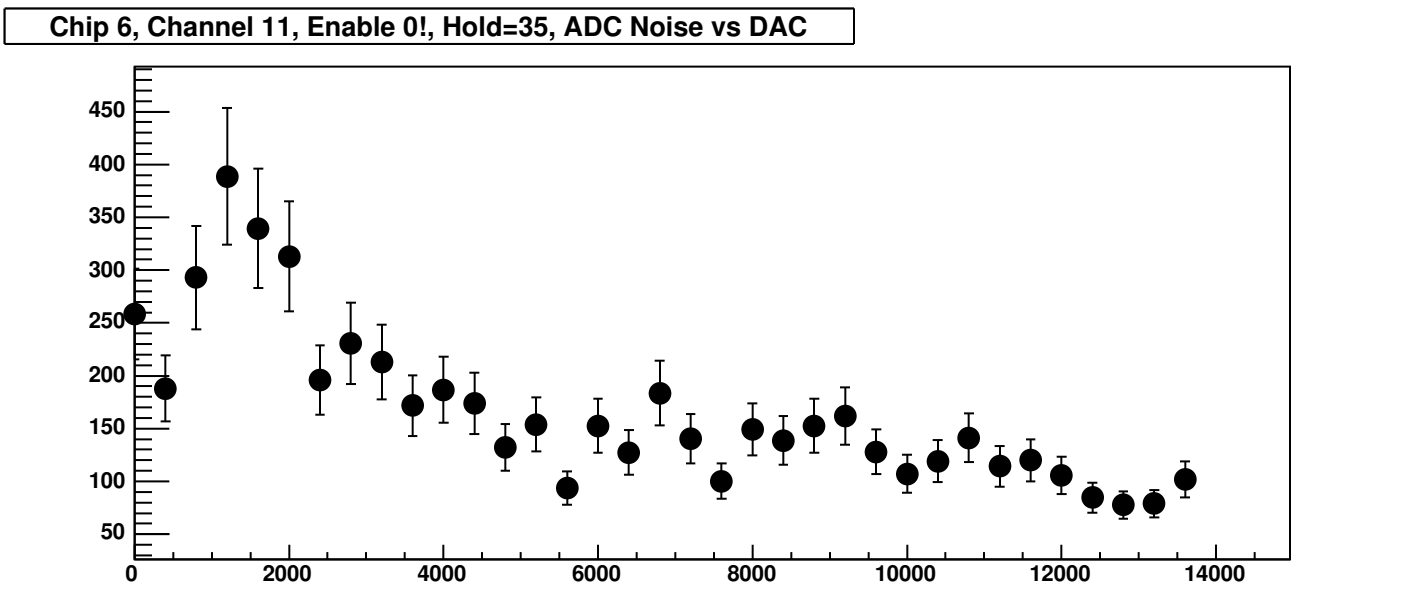

Chip 6, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

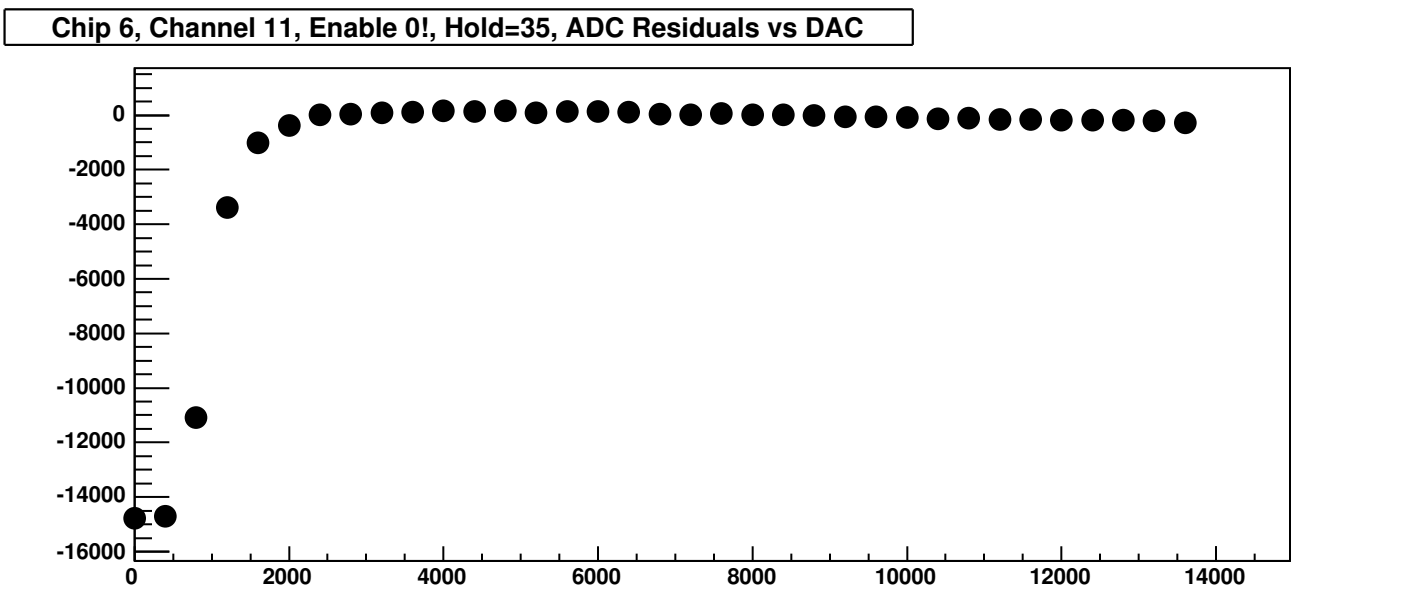

Chip 6, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1434 / 23
p0 -523.4 ± 0.9955
p1 0.1675 ± 0.0001874
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

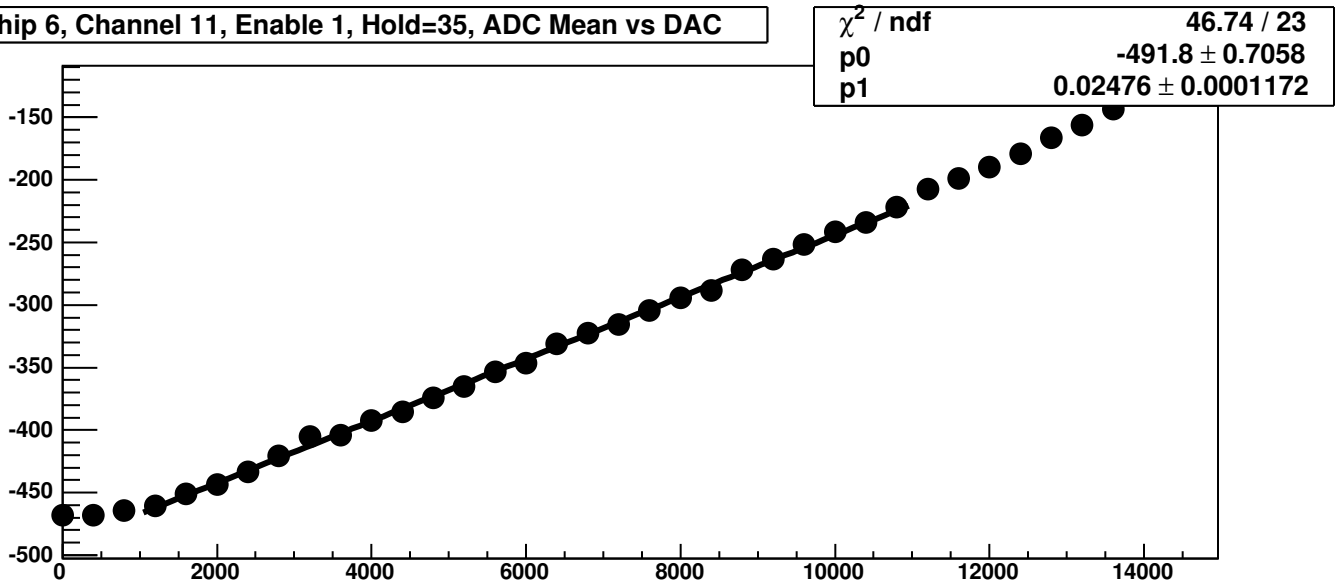

Chip 6, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1848 / 23
p0 2.962e+04 ± 20.78
p1 0.03486 ± 0.002785
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

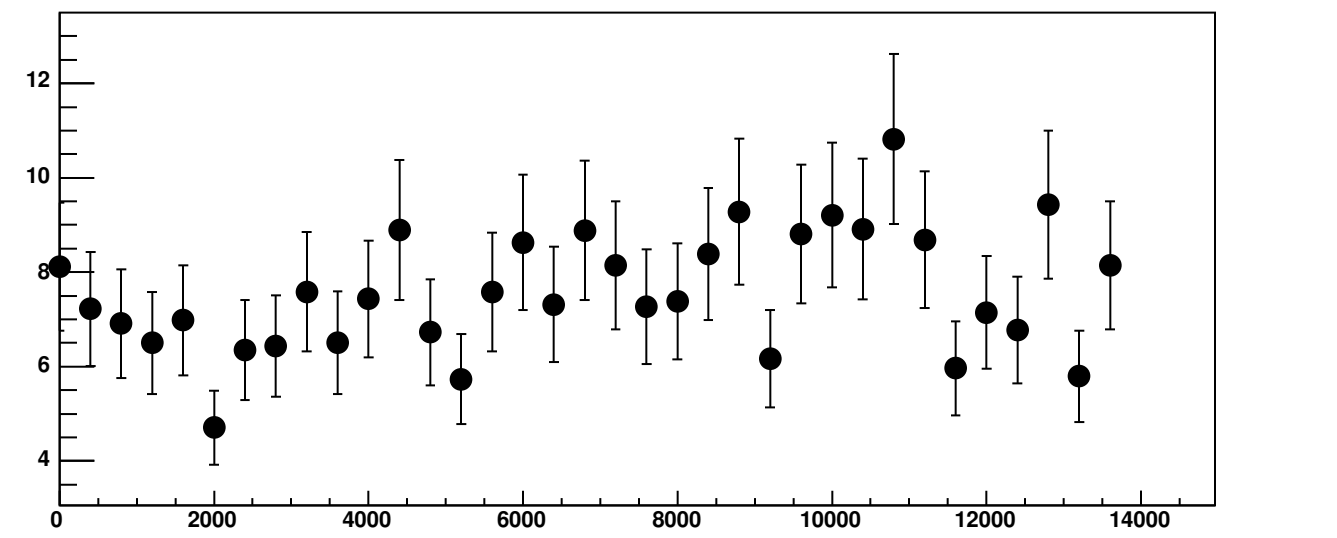

Chip 6, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

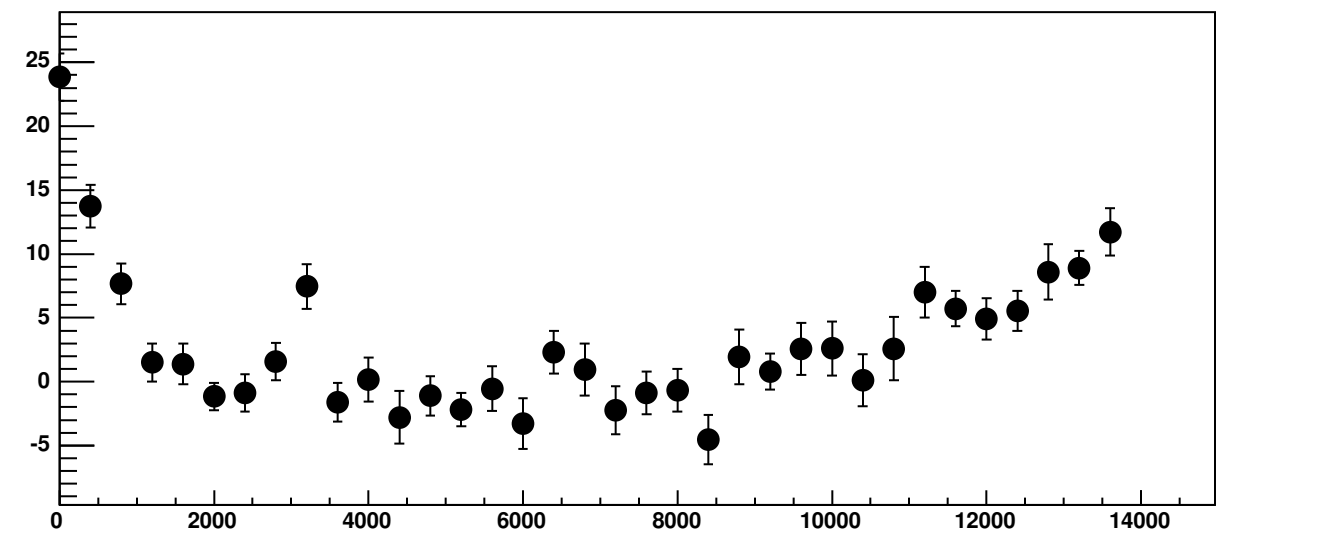
Chip 6, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

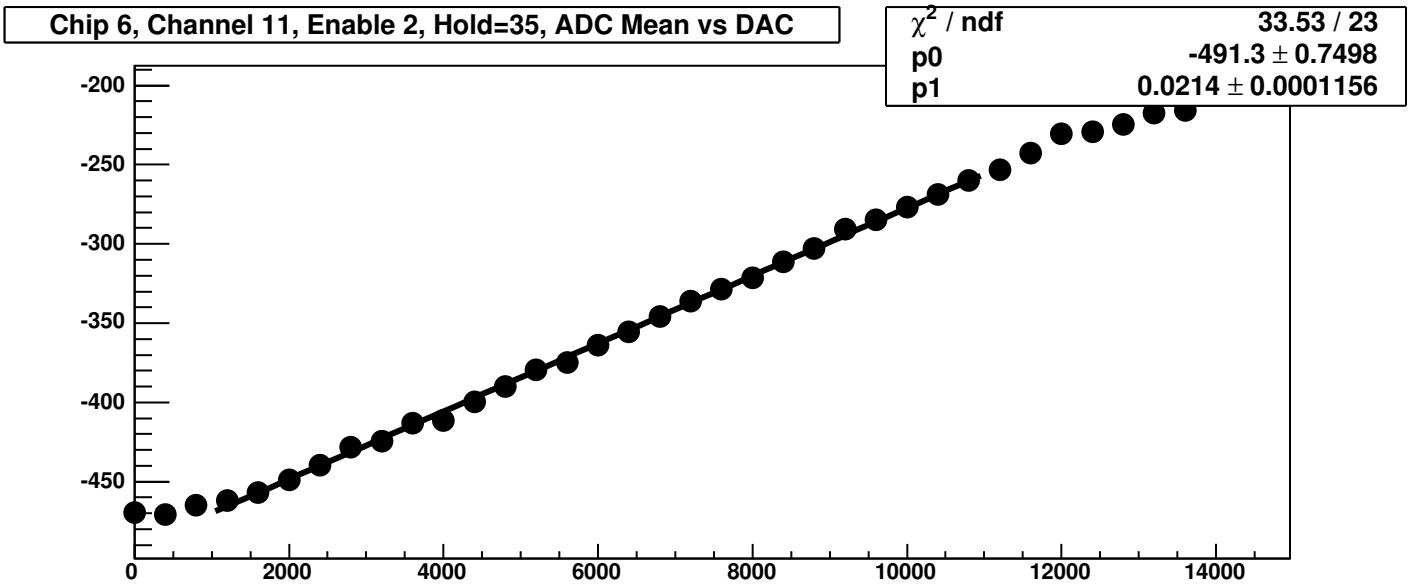


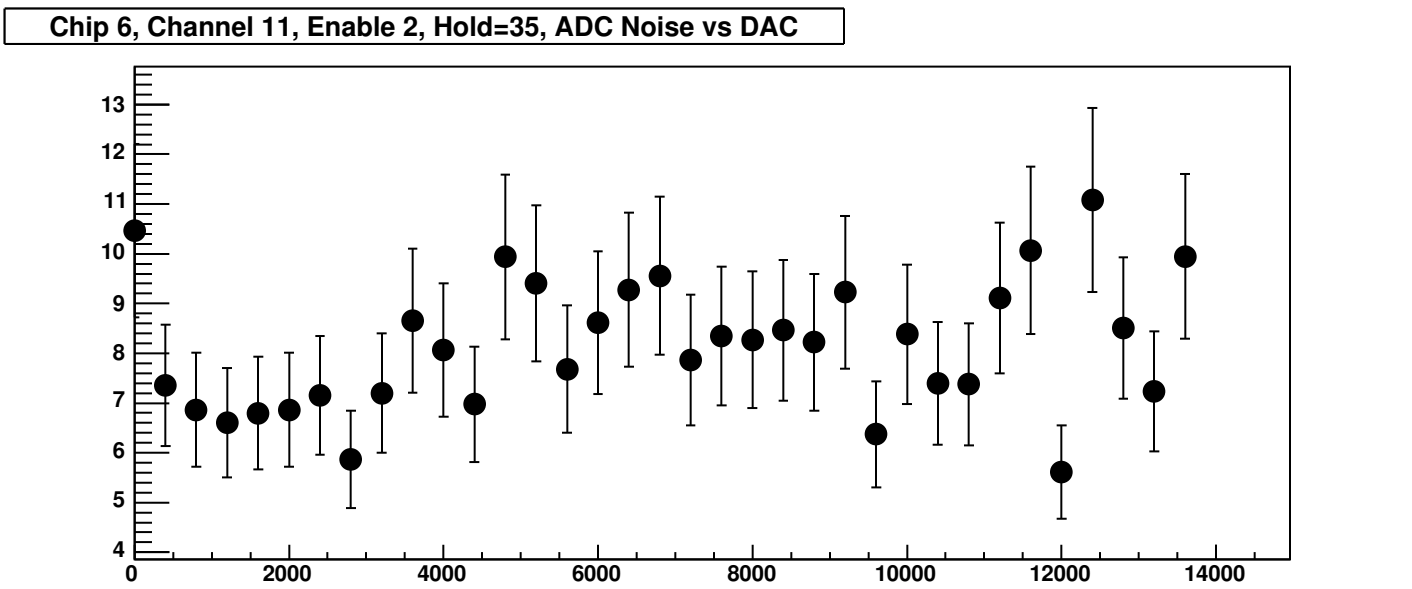
Chip 6, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

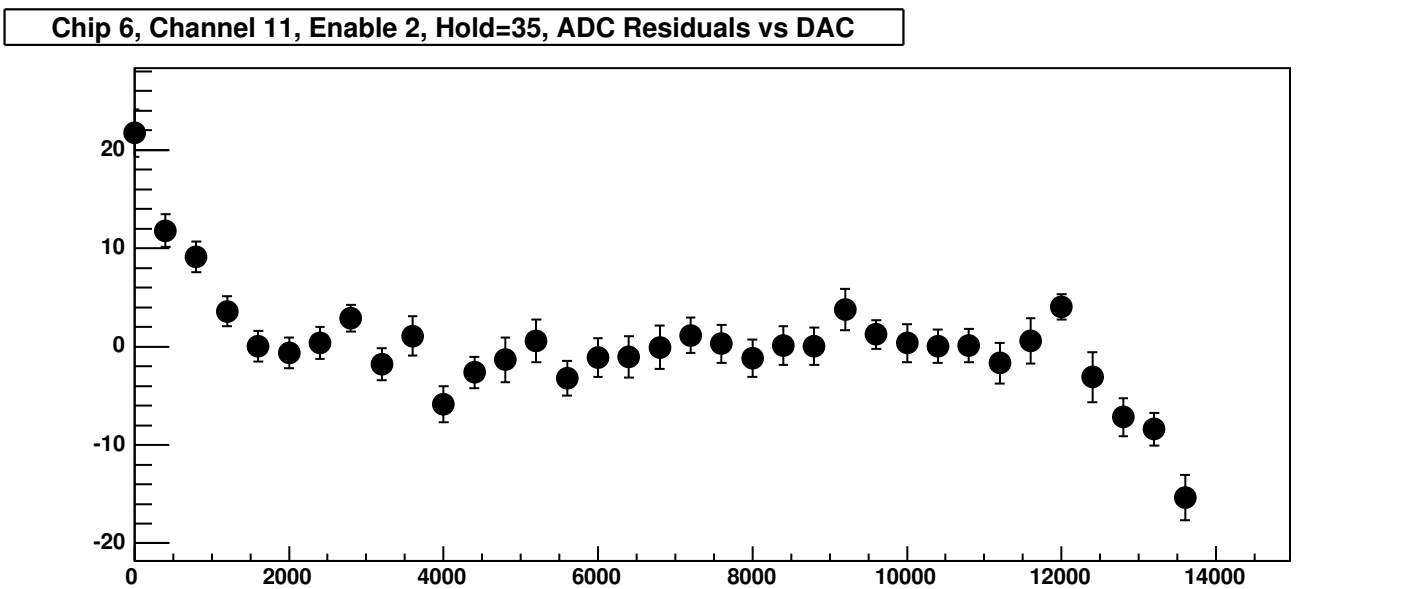


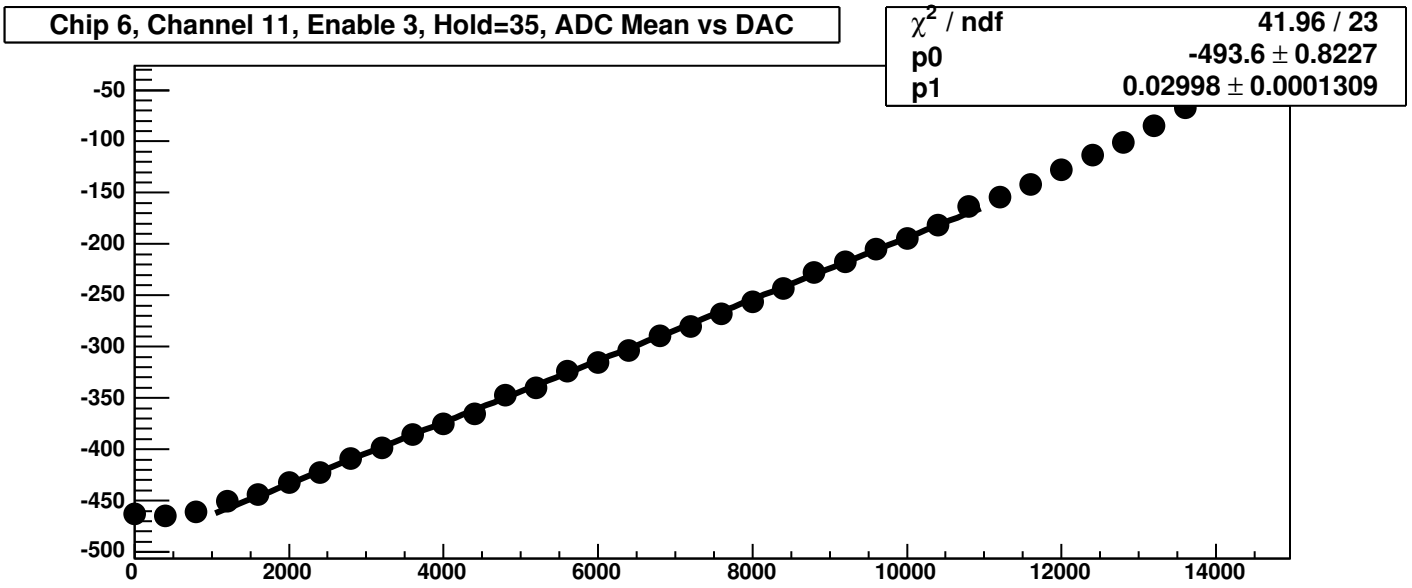
Chip 6, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

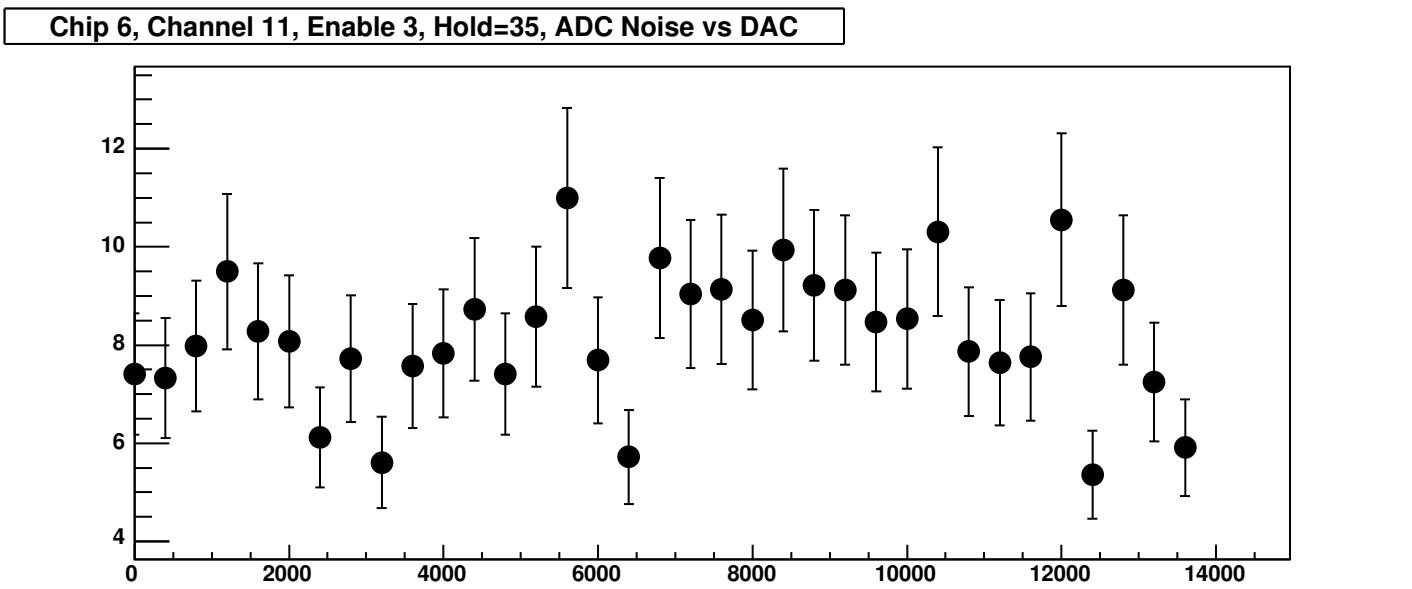


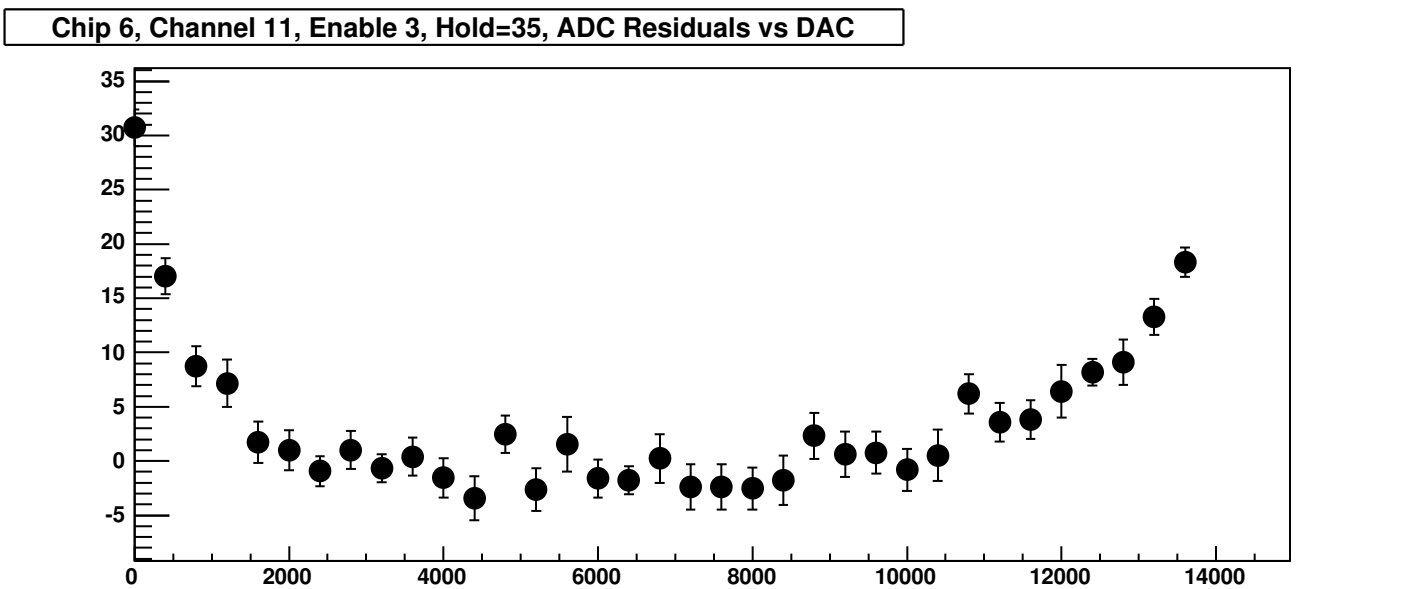

Chip 6, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 33.53 / 23
p0 -491.3 ± 0.7498
p1 0.0214 ± 0.0001156
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

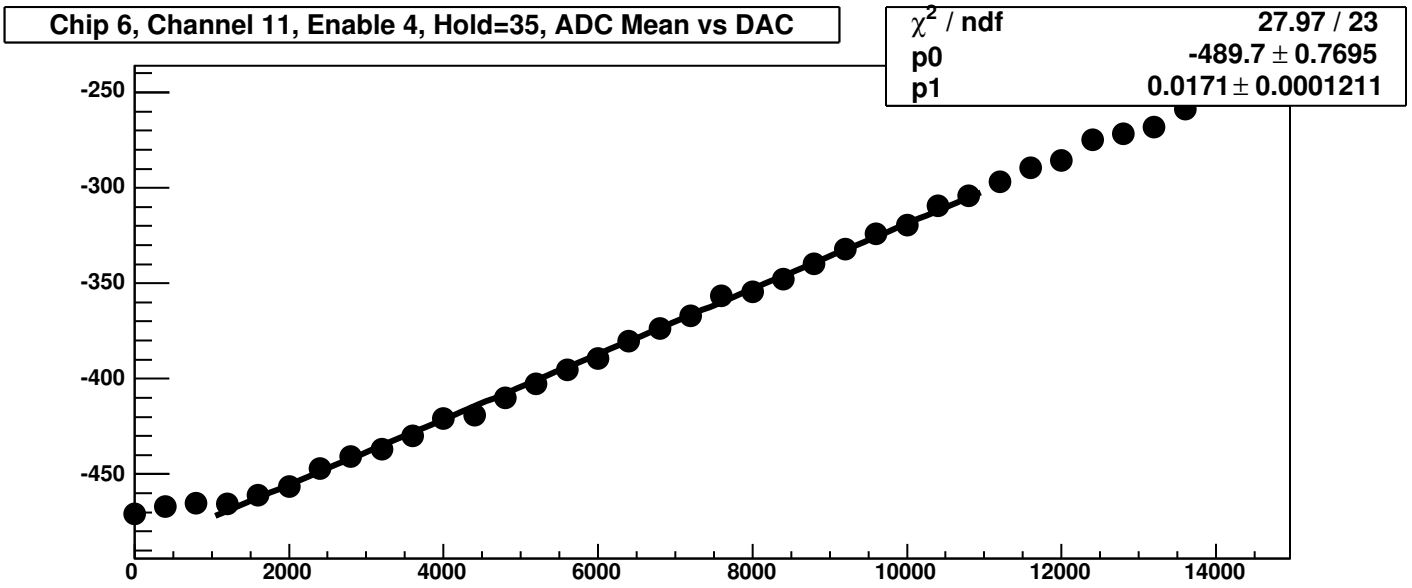

Chip 6, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

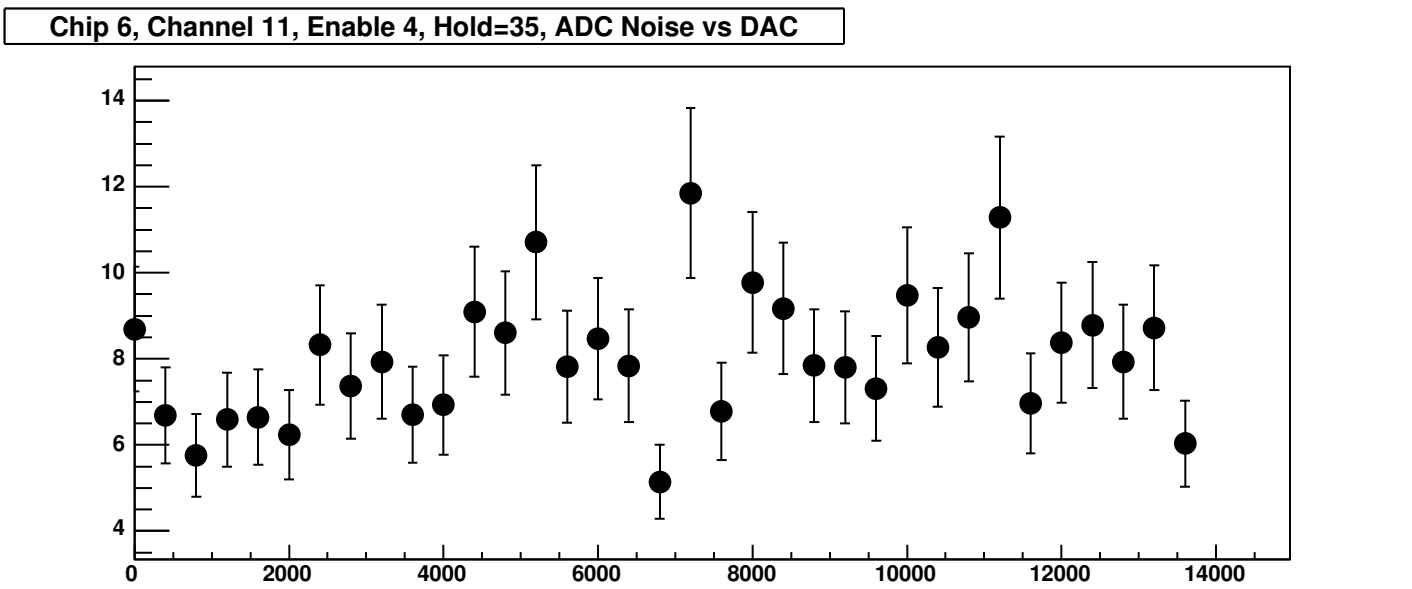

Chip 6, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

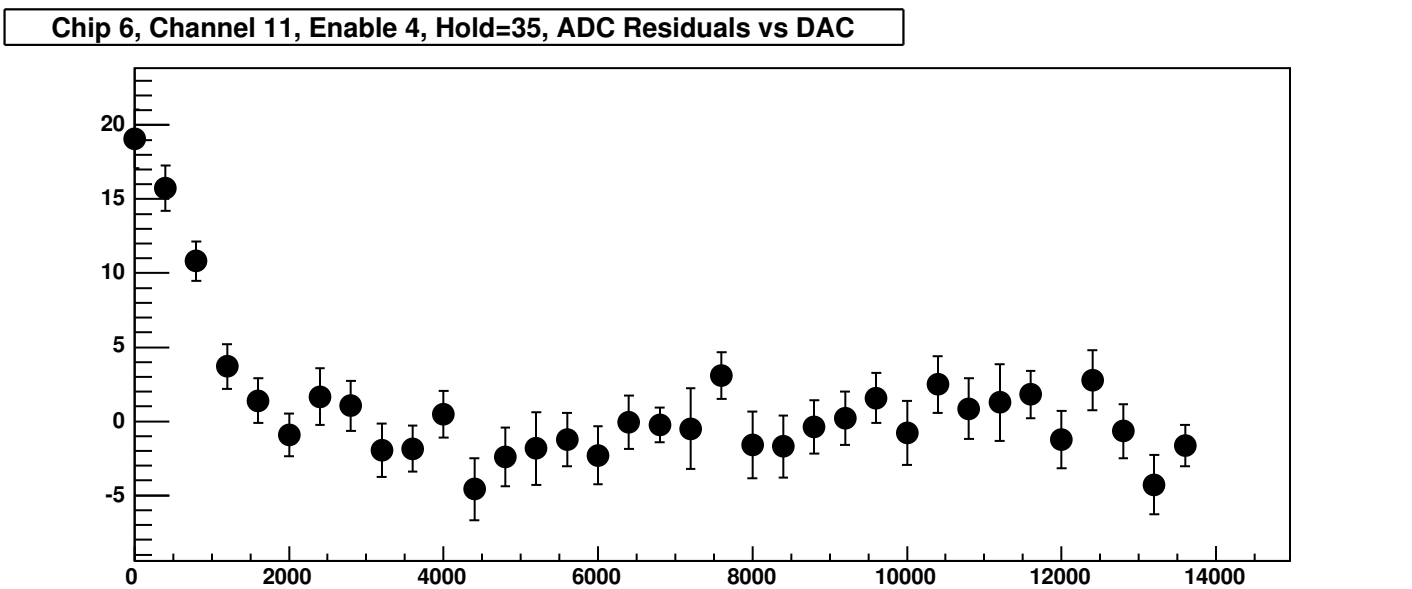

Chip 6, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 41.96 / 23
p0 -493.6 ± 0.8227
p1 0.02998 ± 0.0001309
-50
-100
-150
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

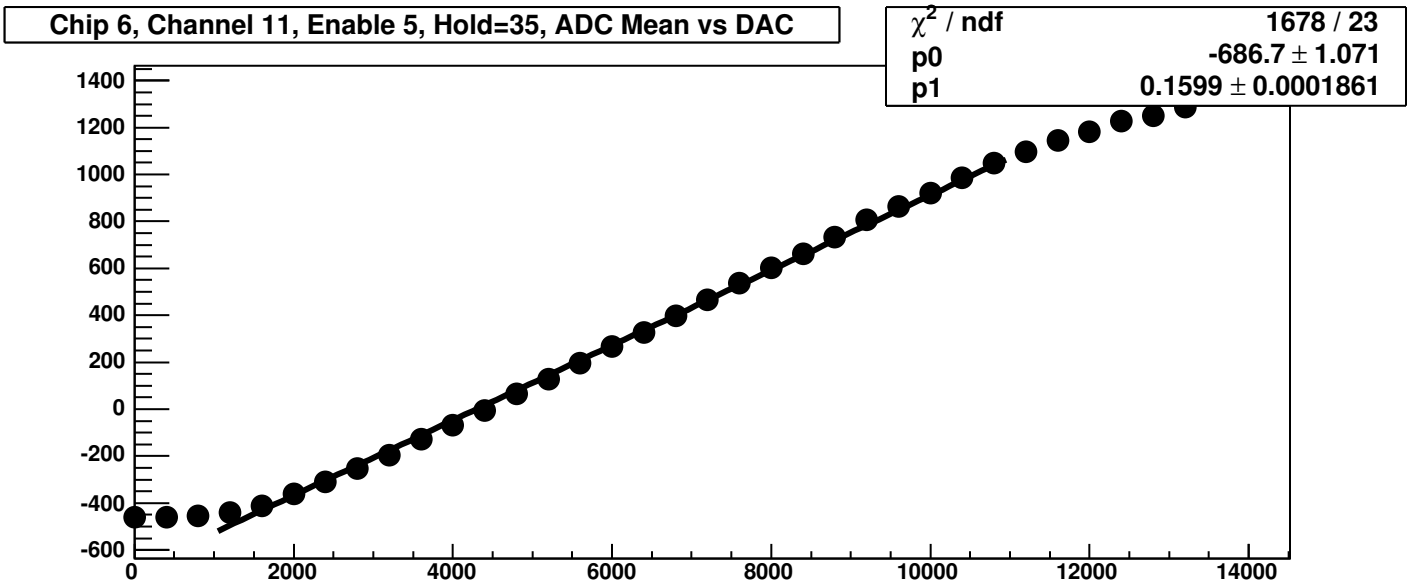

Chip 6, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

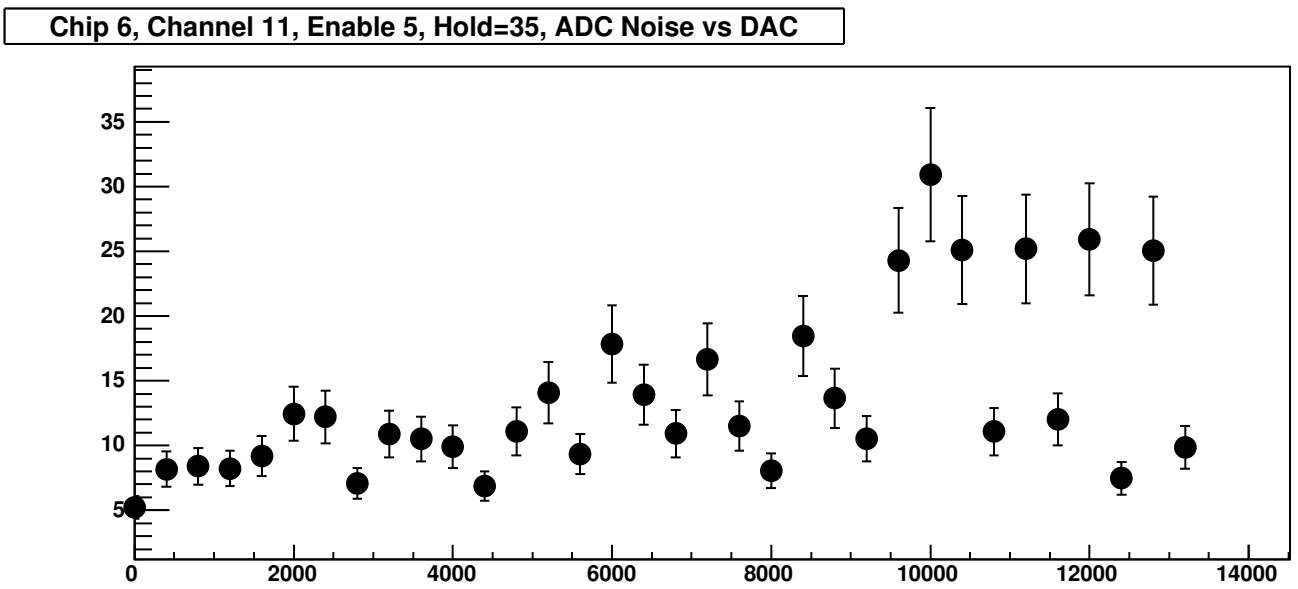

Chip 6, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

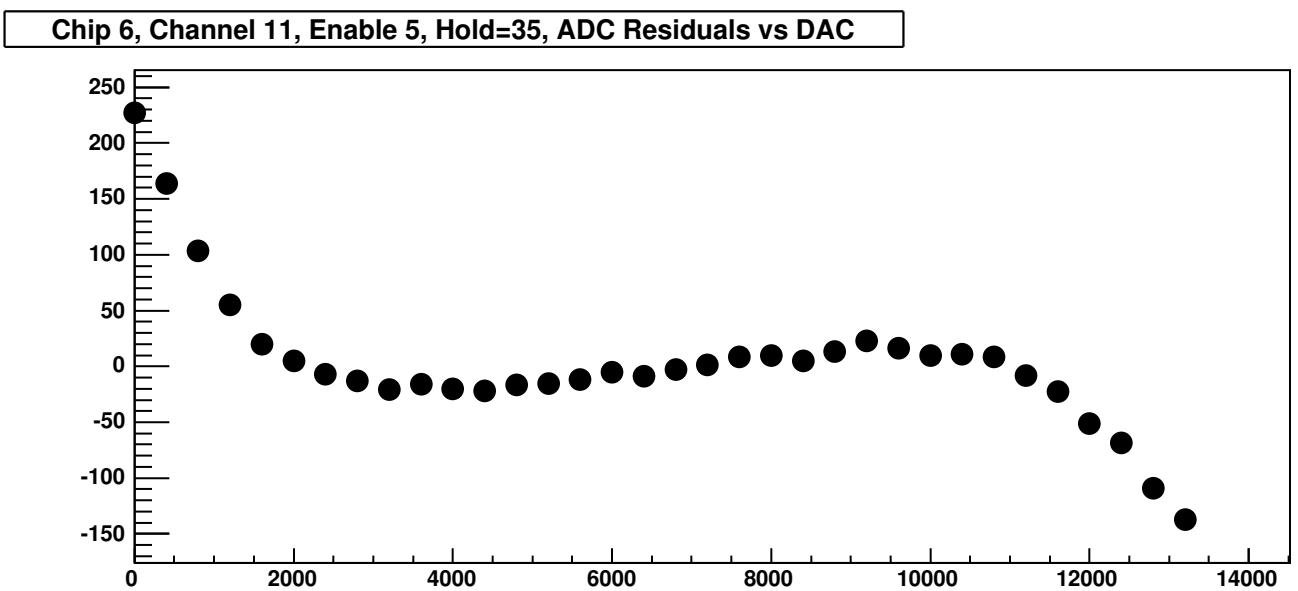

Chip 6, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.97 / 23
p0 -489.7 ± 0.7695
p1 0.0171 ± 0.0001211
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

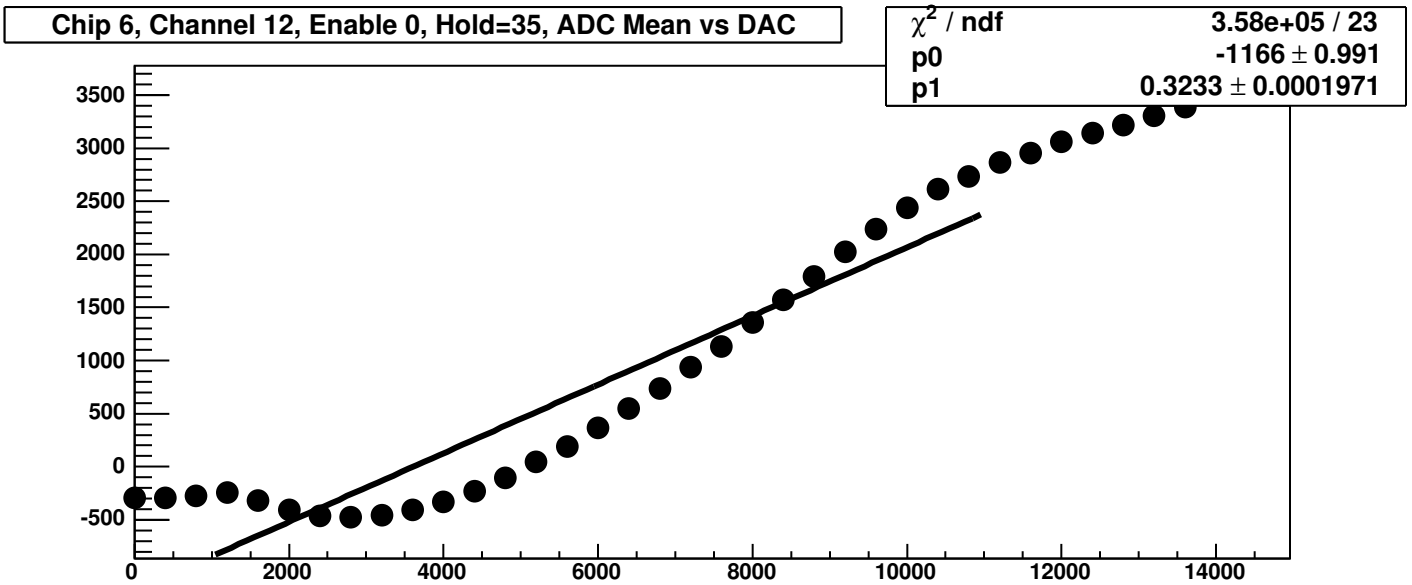

Chip 6, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

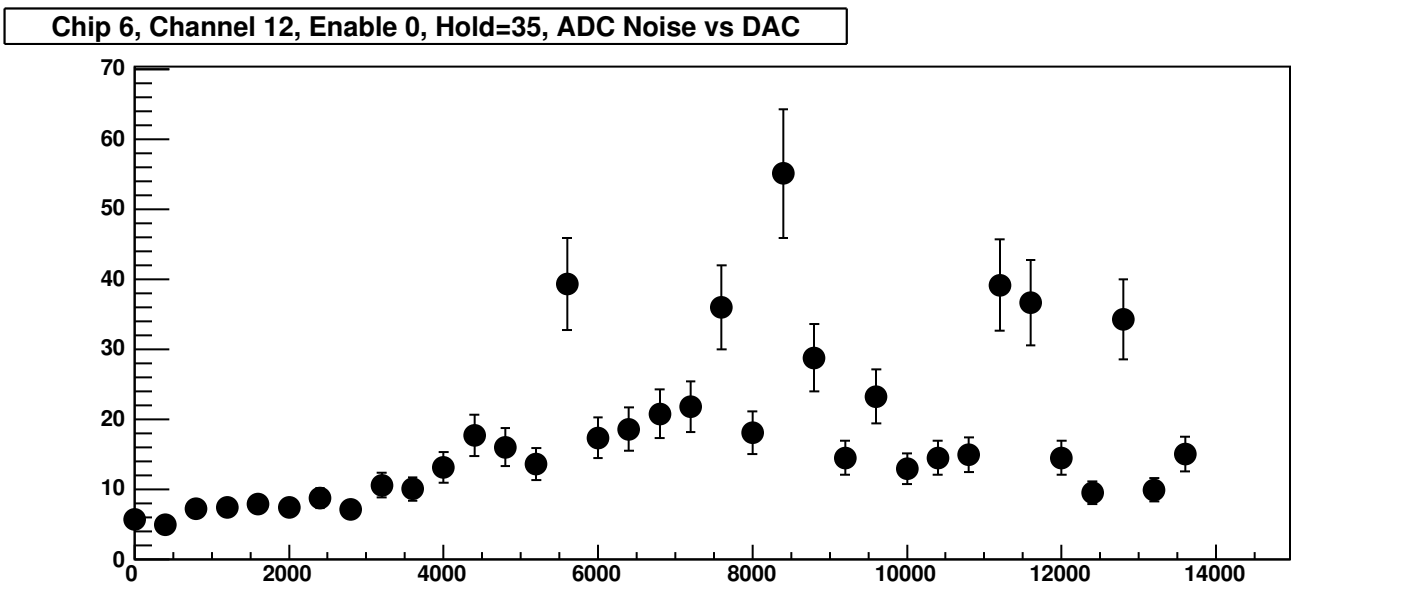

Chip 6, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

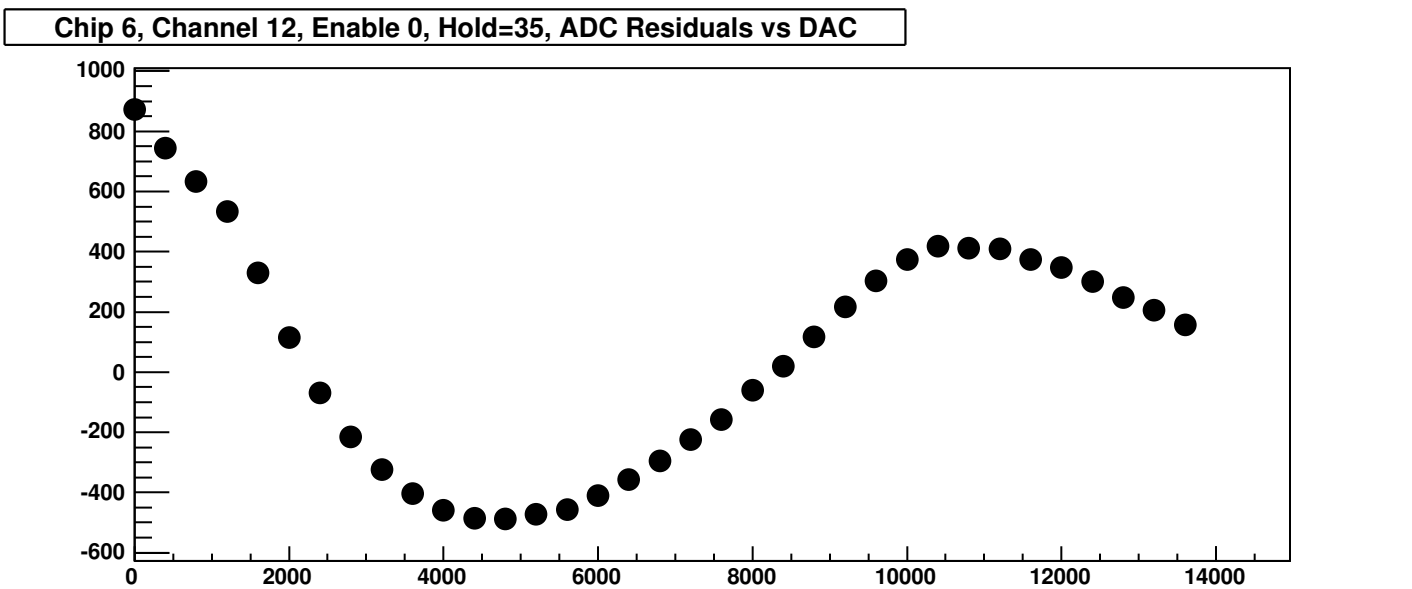

Chip 6, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1678 / 23
p0 -686.7 ± 1.071
p1 0.1599 ± 0.0001861
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

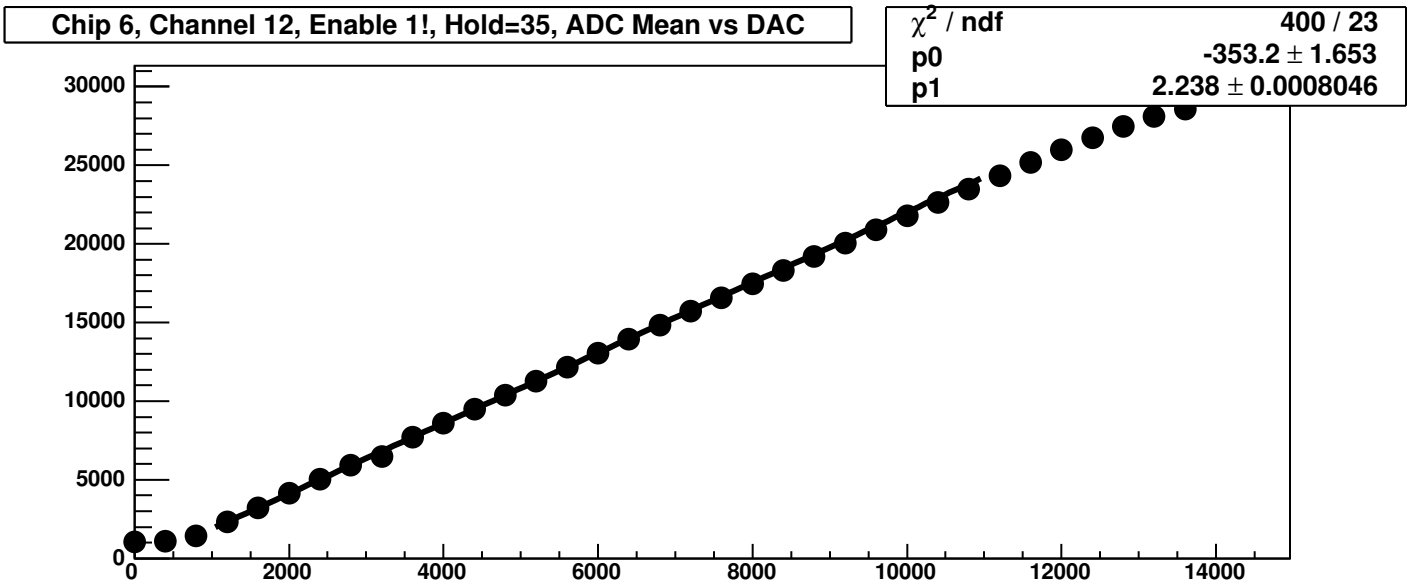

Chip 6, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

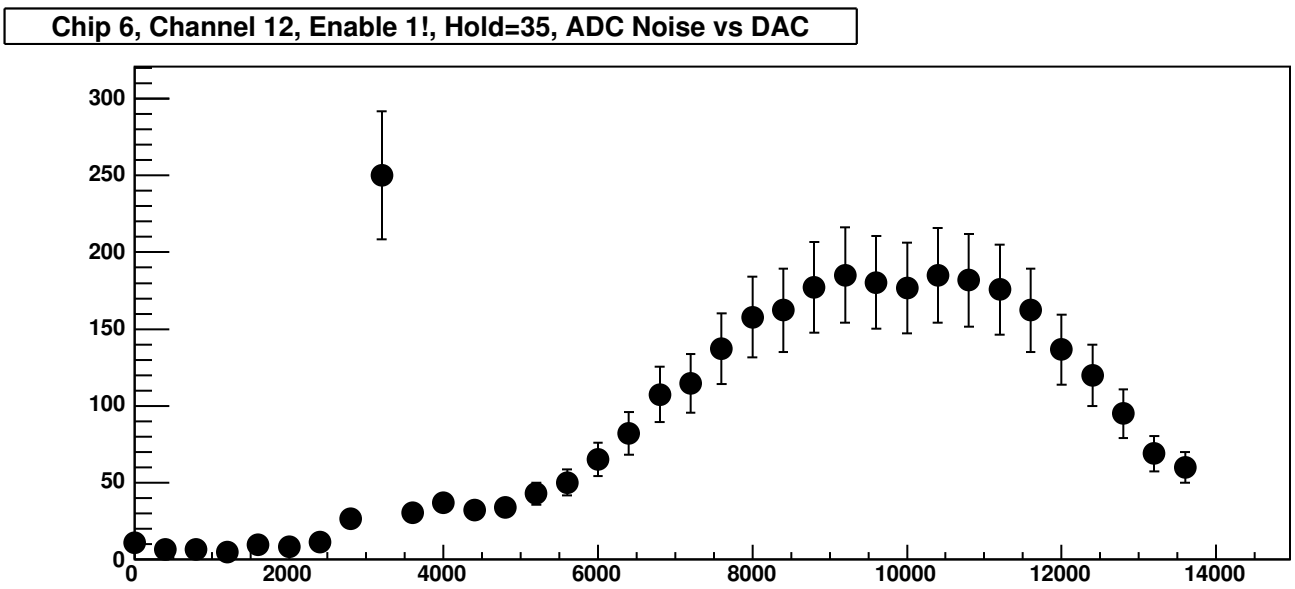

Chip 6, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

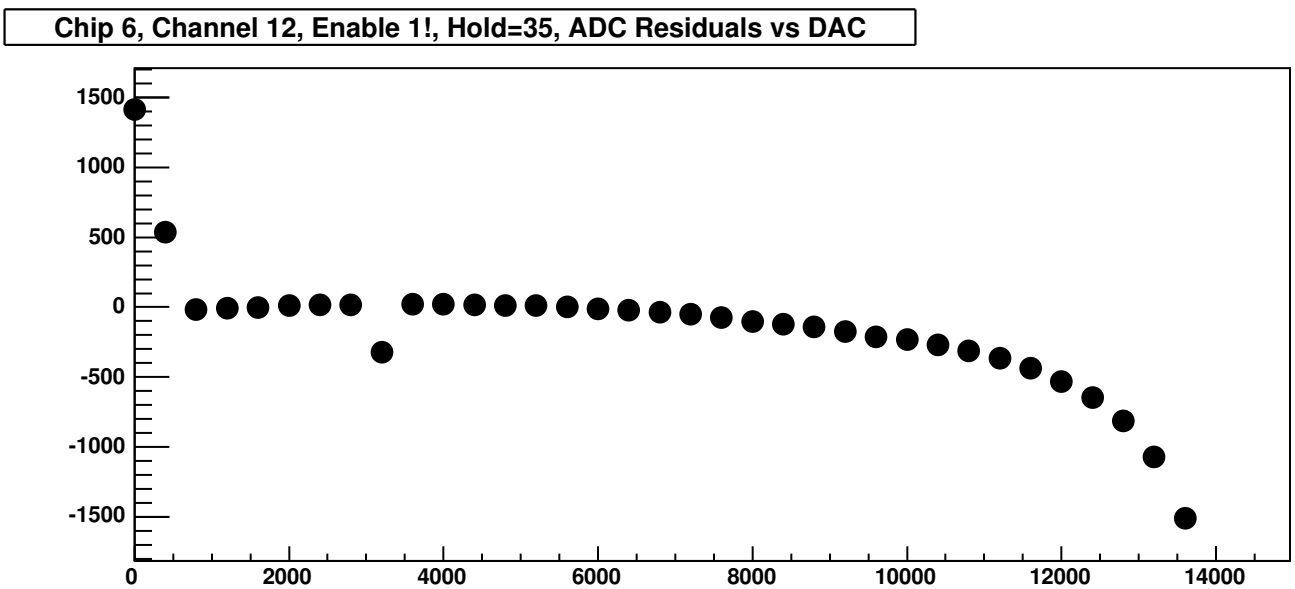

Chip 6, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.58e+05 / 23
p0 -1166 ± 0.991
p1 0.3233 ± 0.0001971
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

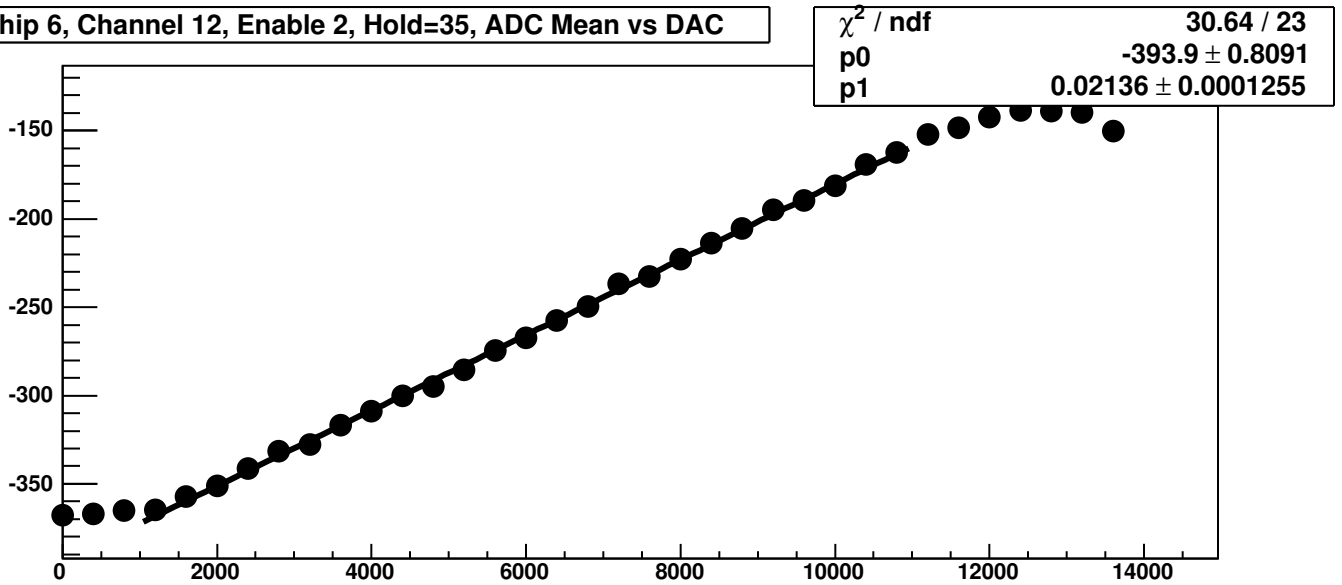

Chip 6, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 400 / 23
p0 -353.2 ± 1.653
p1 2.238 ± 0.0008046
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

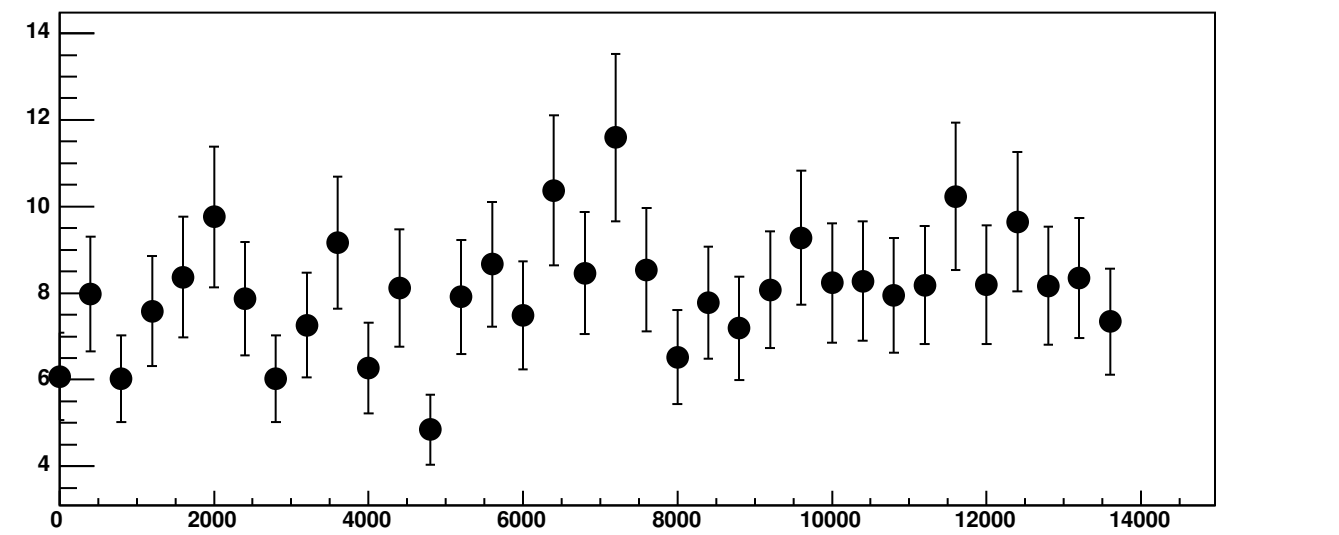

Chip 6, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

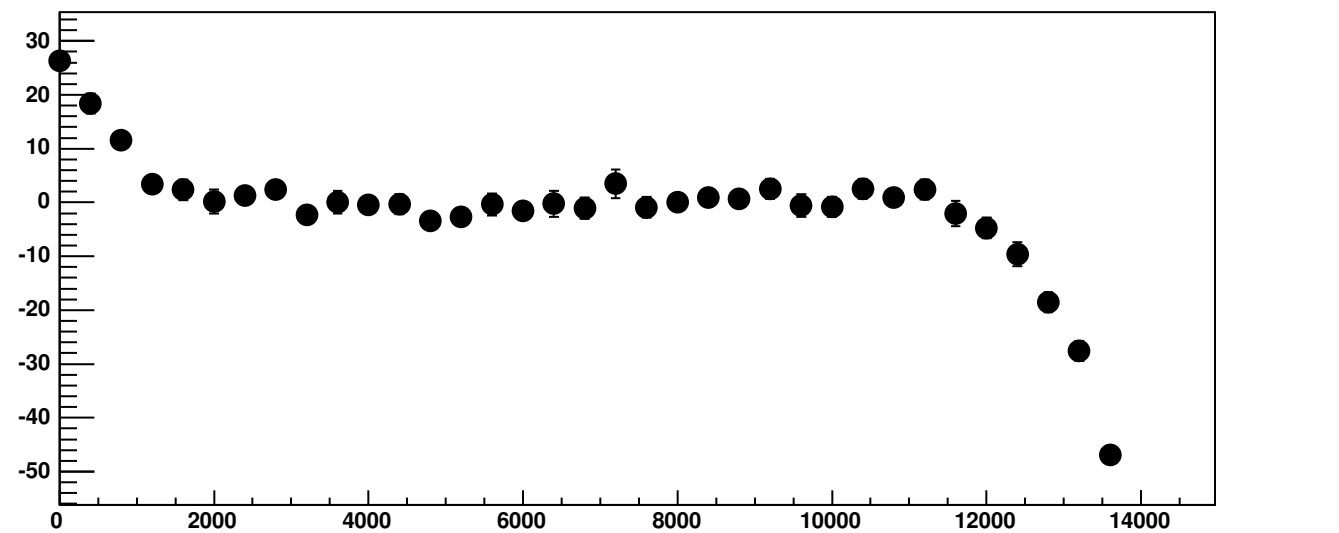
Chip 6, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC

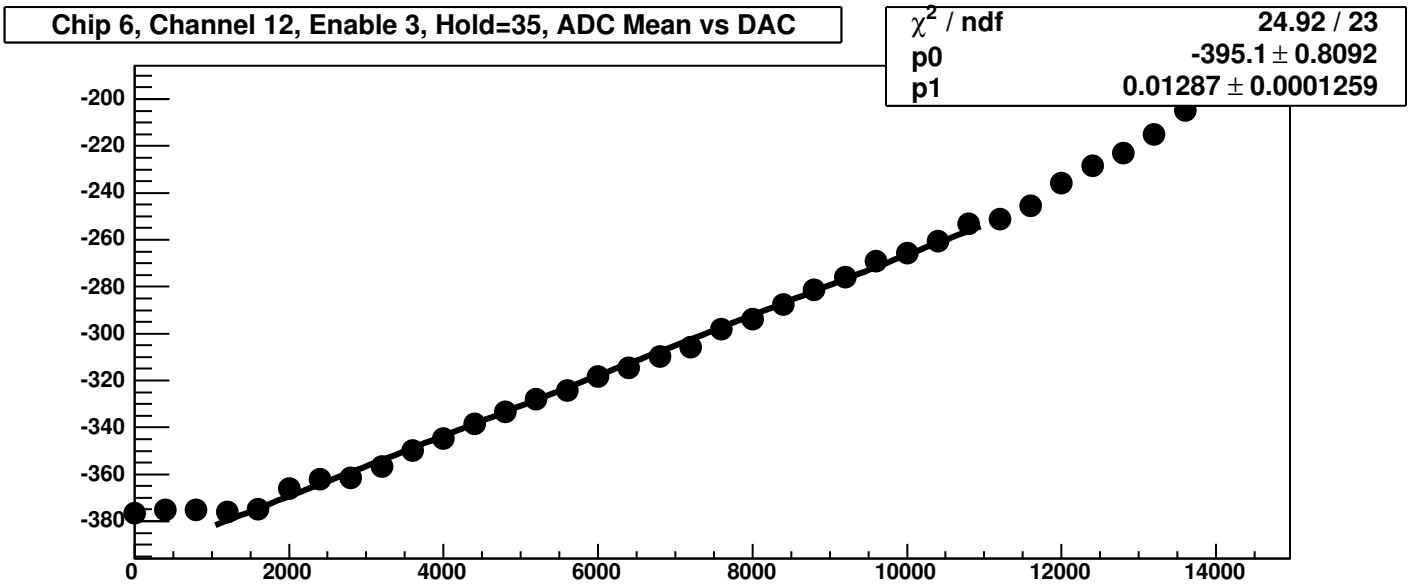


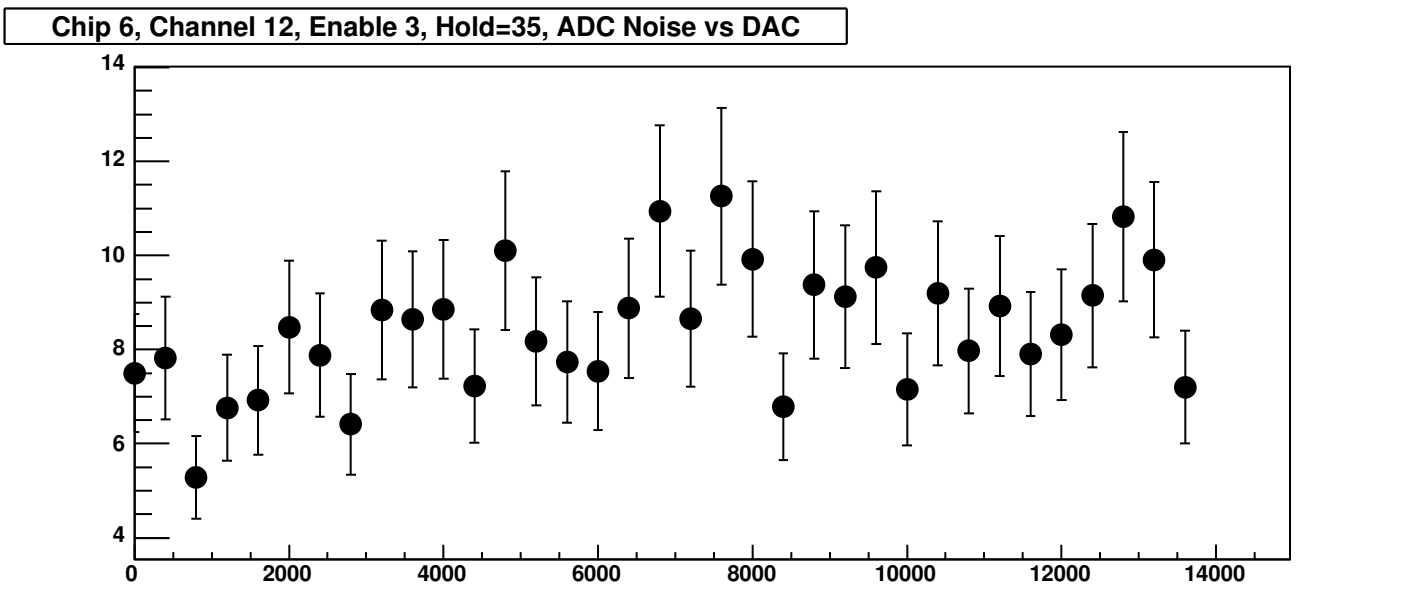
Chip 6, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC

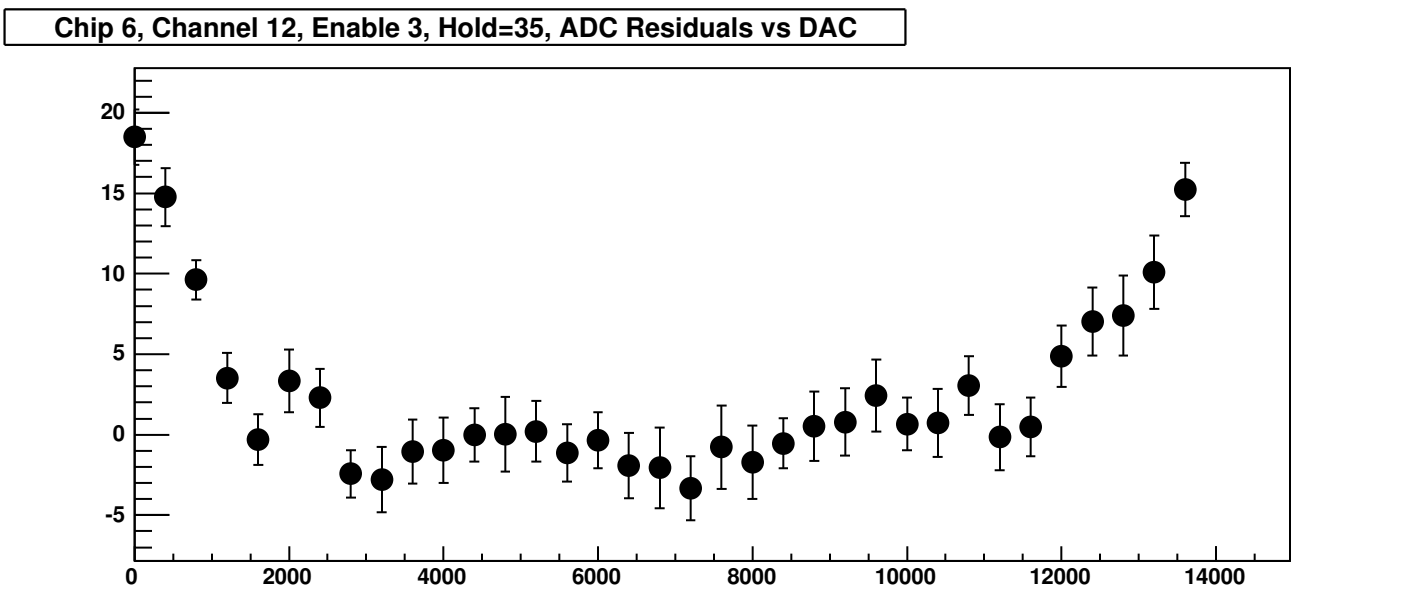


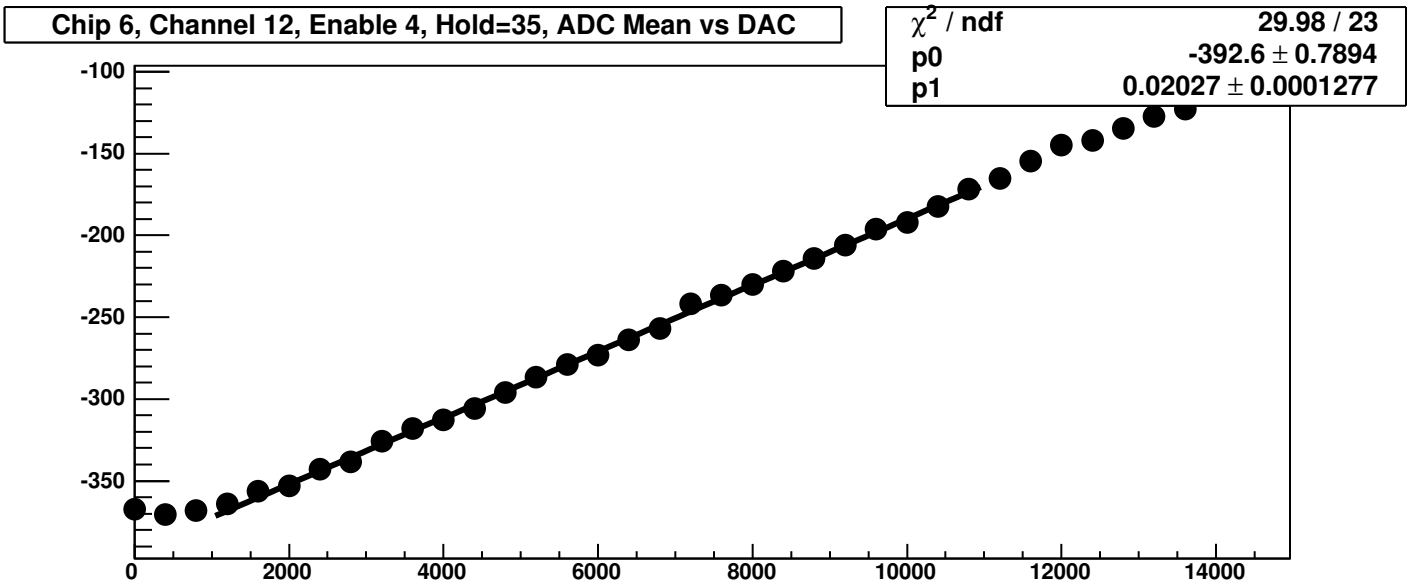
Chip 6, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC

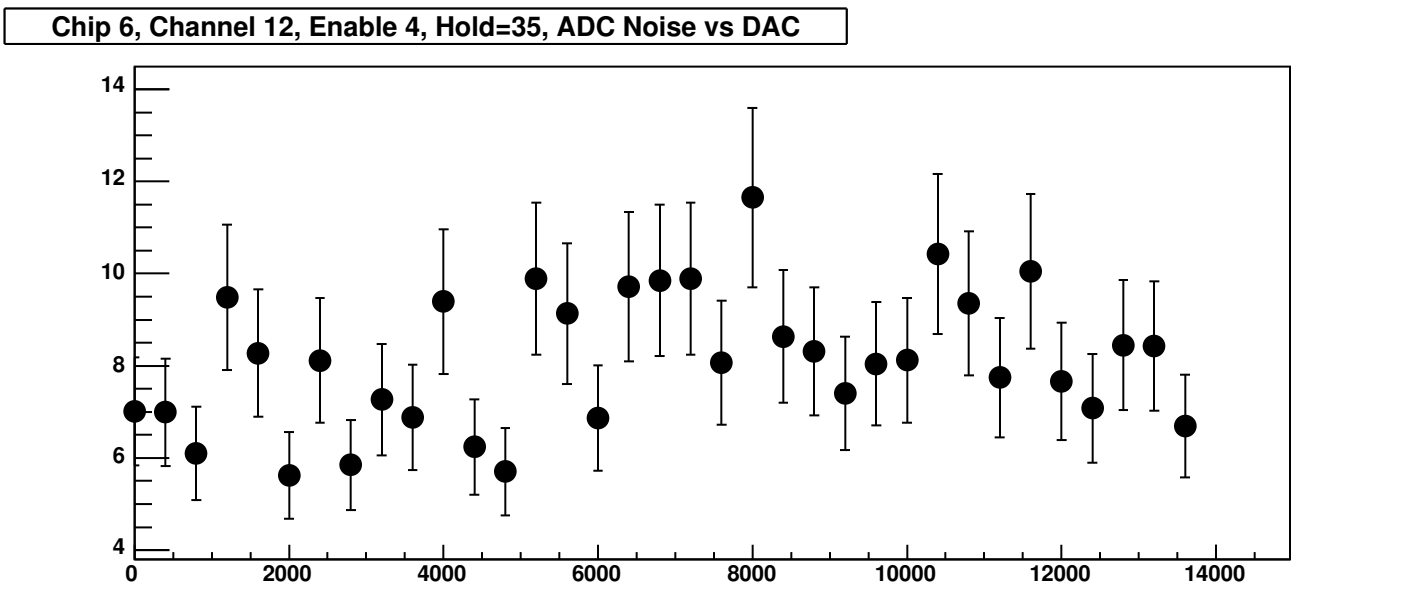


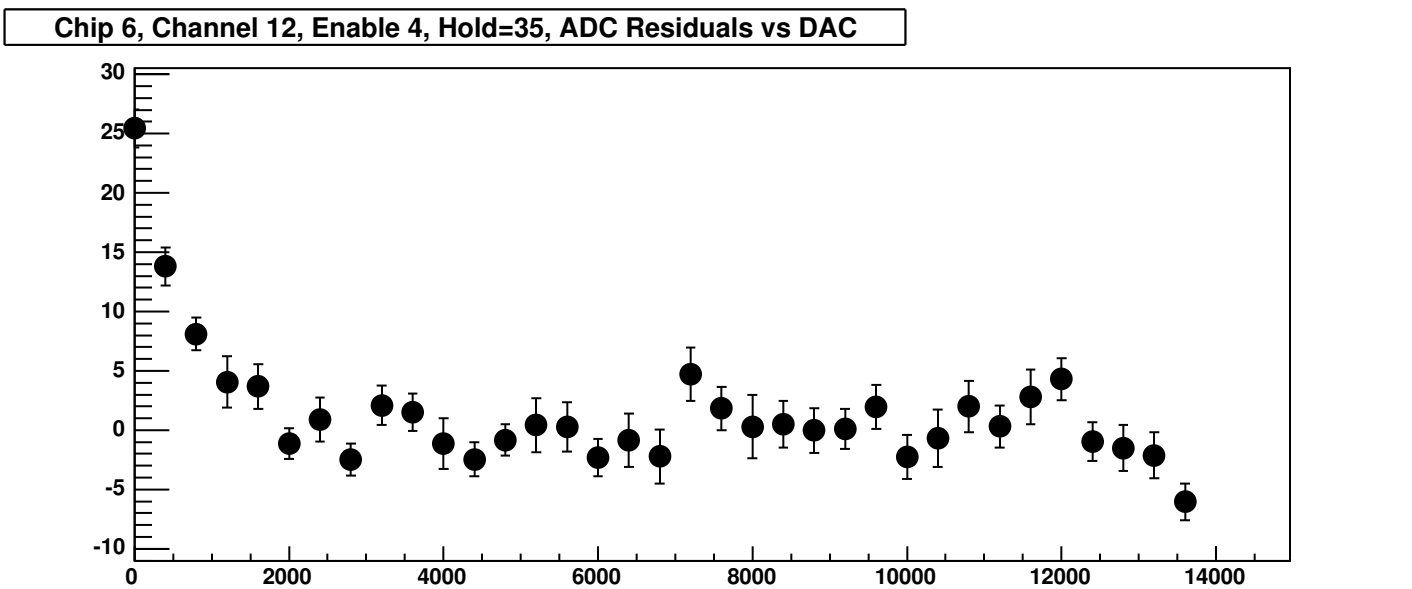

Chip 6, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.92 / 23
p0 -395.1 ± 0.8092
p1 0.01287 ± 0.0001259
-200
-220
-240
-260
-280
-300
-320
-340
-360
-380
0
2000
4000
6000
8000
10000
12000
14000

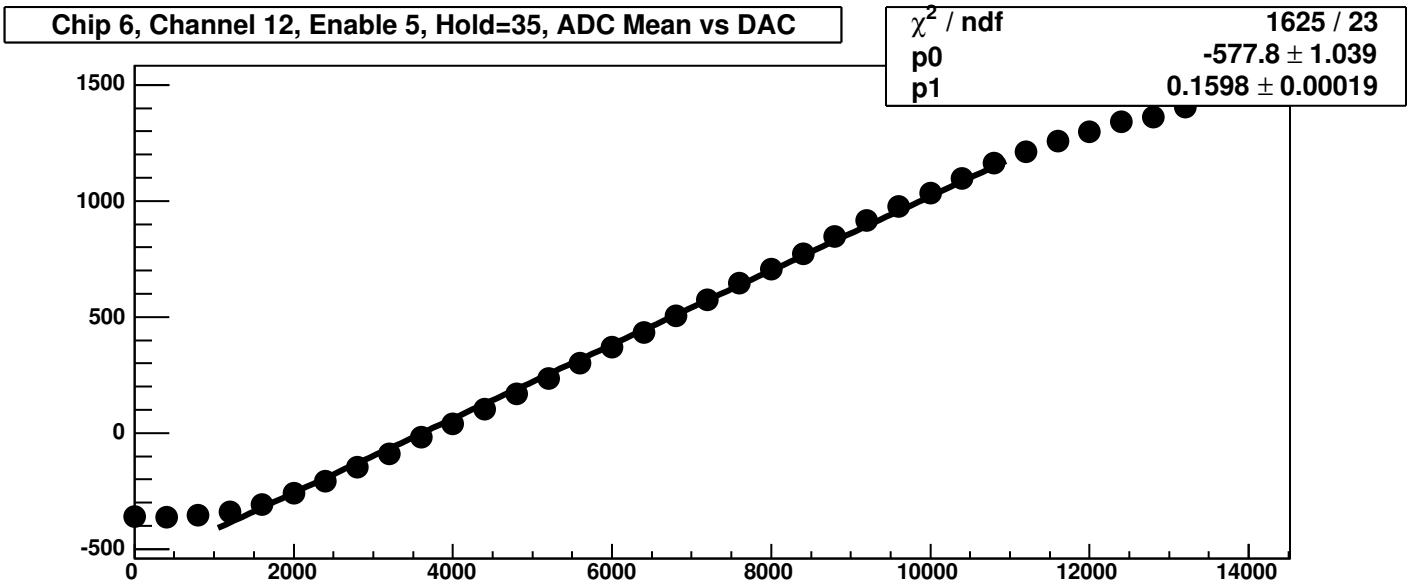

Chip 6, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

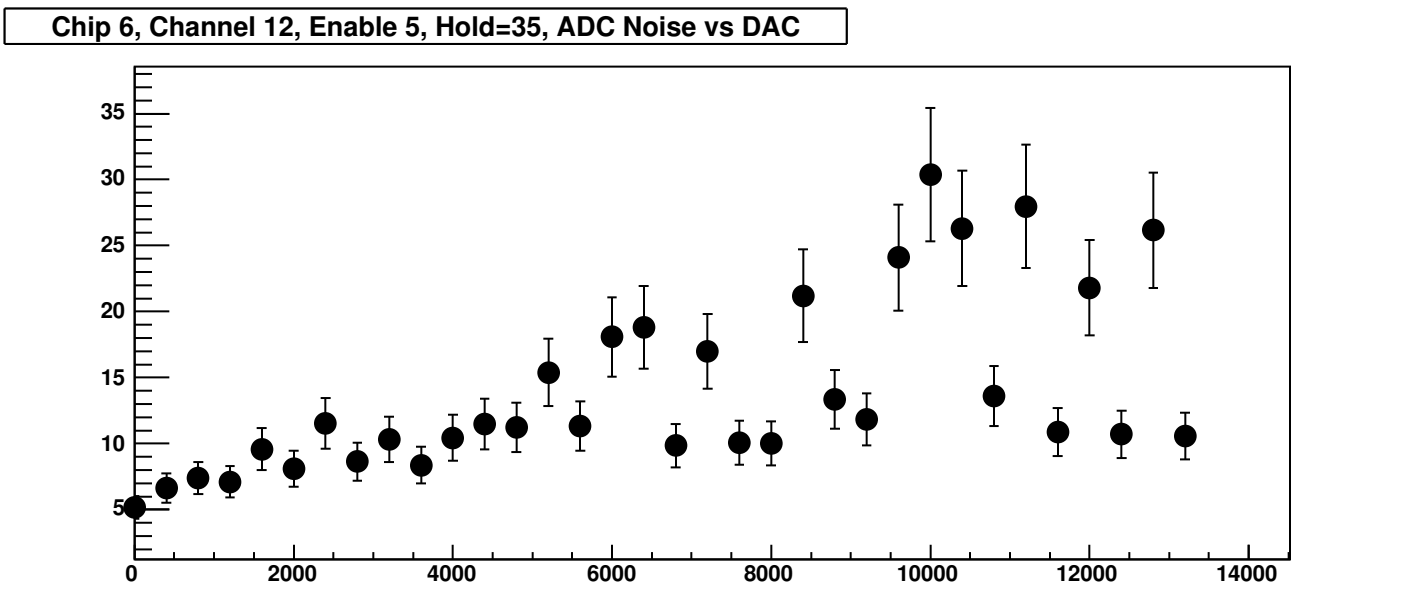

Chip 6, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

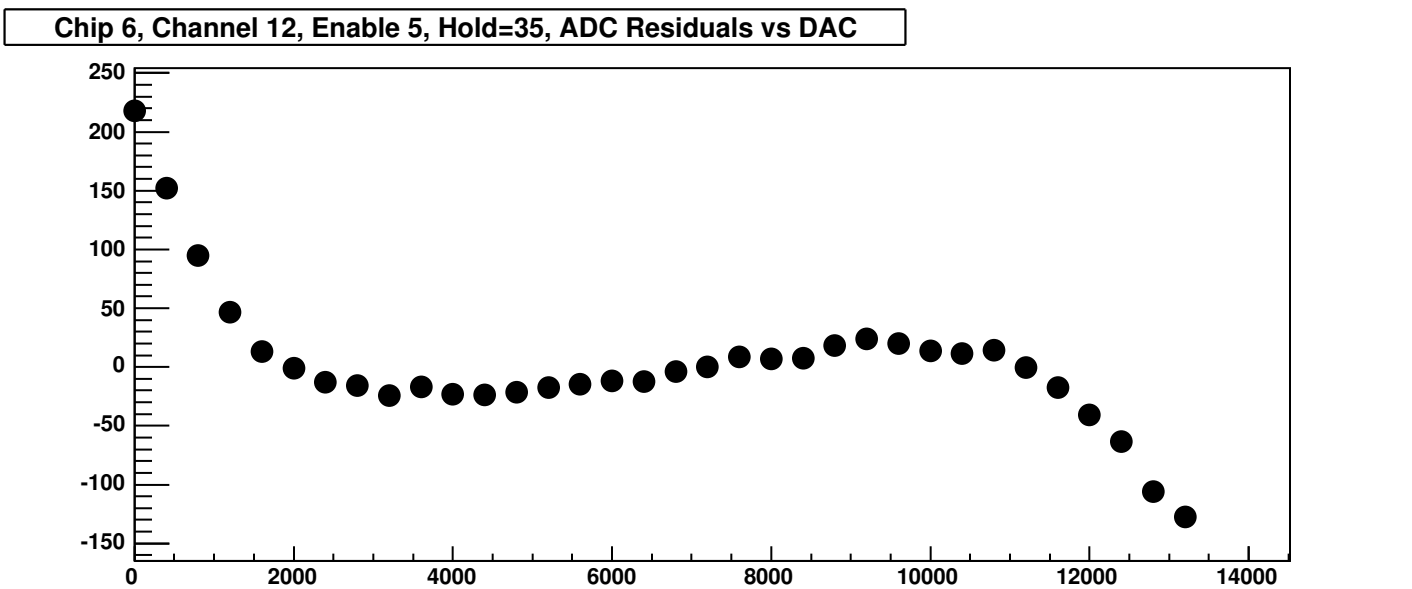

Chip 6, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.98 / 23
p0 -392.6 ± 0.7894
p1 0.02027 ± 0.0001277
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

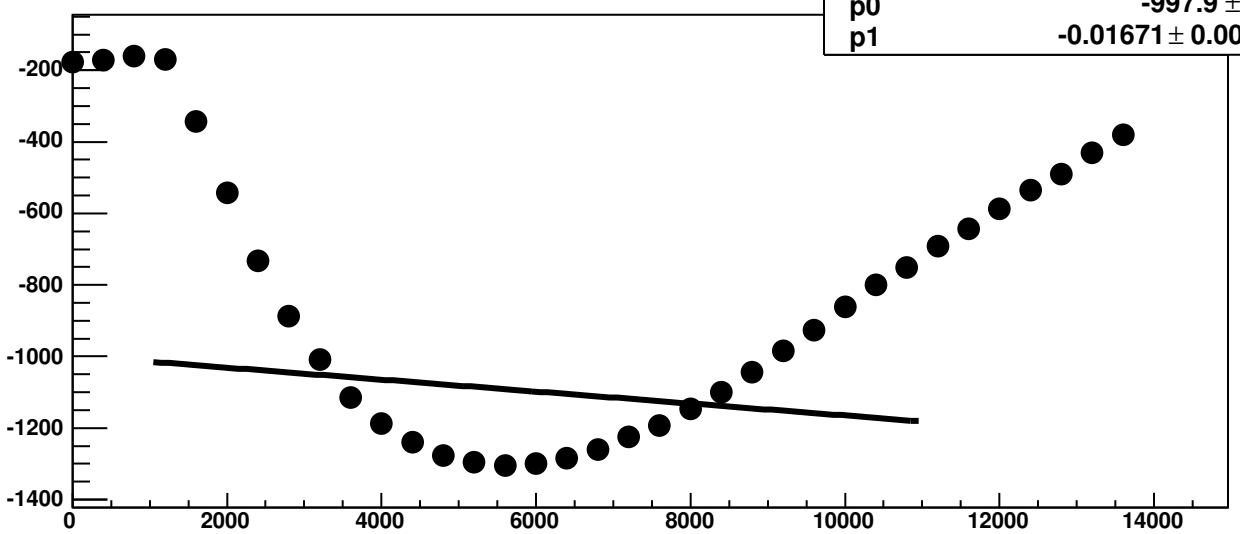

Chip 6, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1625 / 23
p0 -577.8 ± 1.039
p1 0.1598 ± 0.00019
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

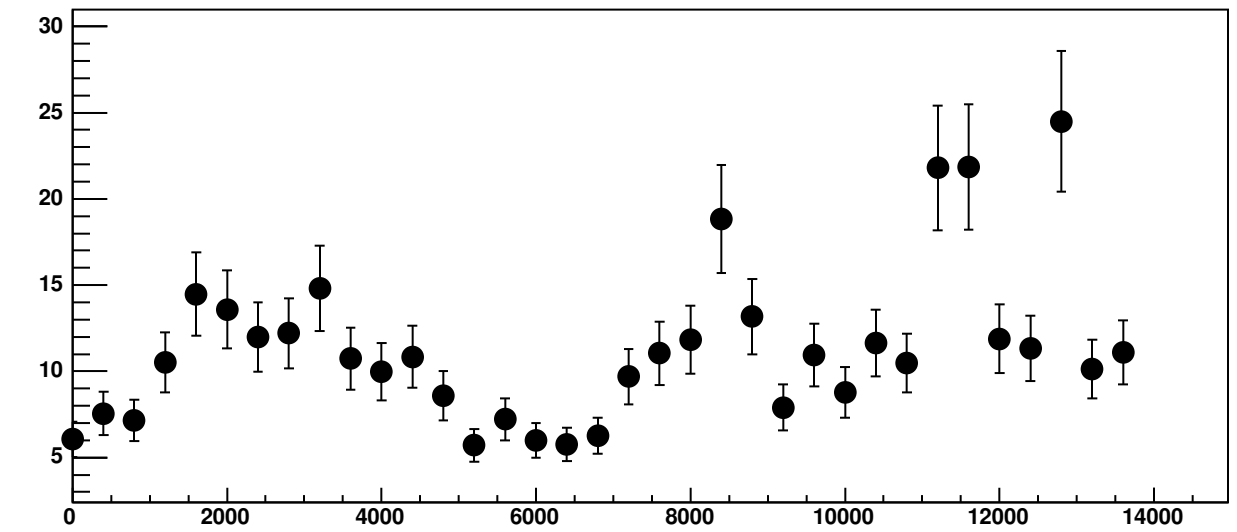

Chip 6, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

Chip 6, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

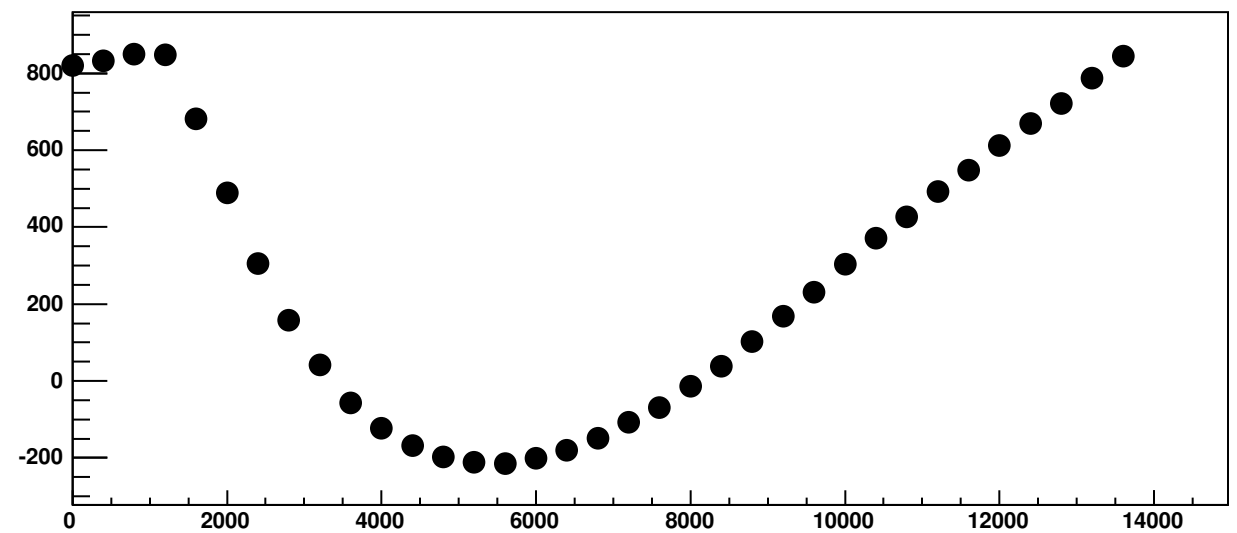
χ^2 / ndf	4.146e+05 / 23
p0	-997.9 ± 1.163
p1	-0.01671 ± 0.0001751

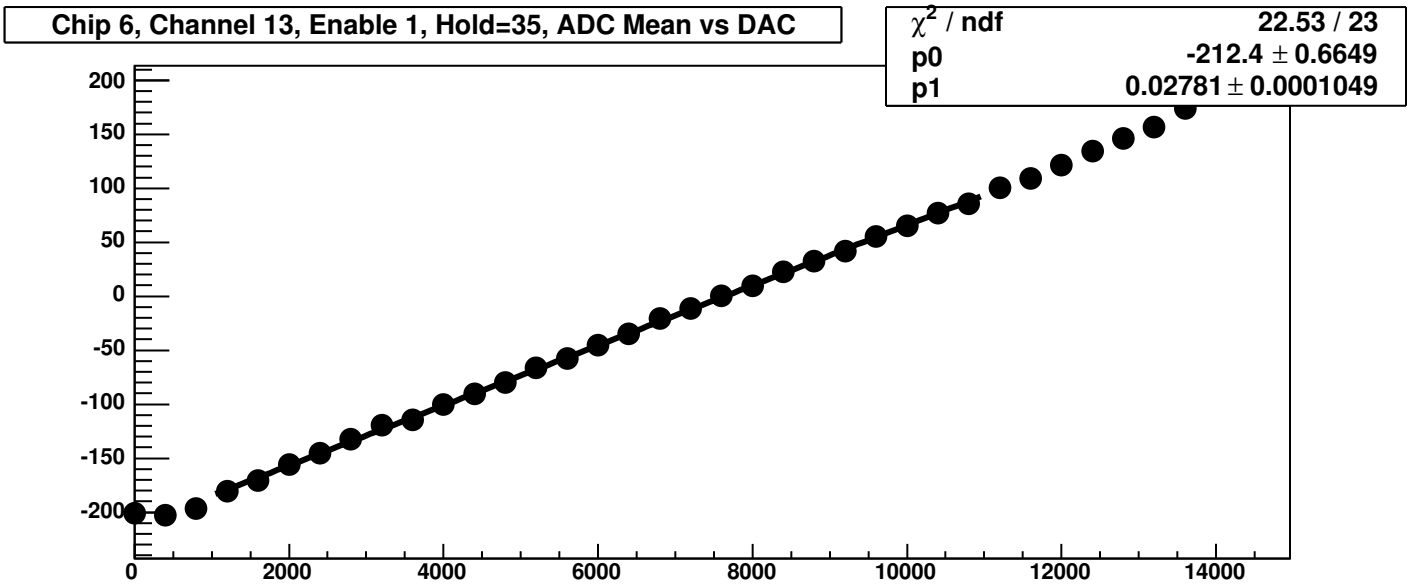


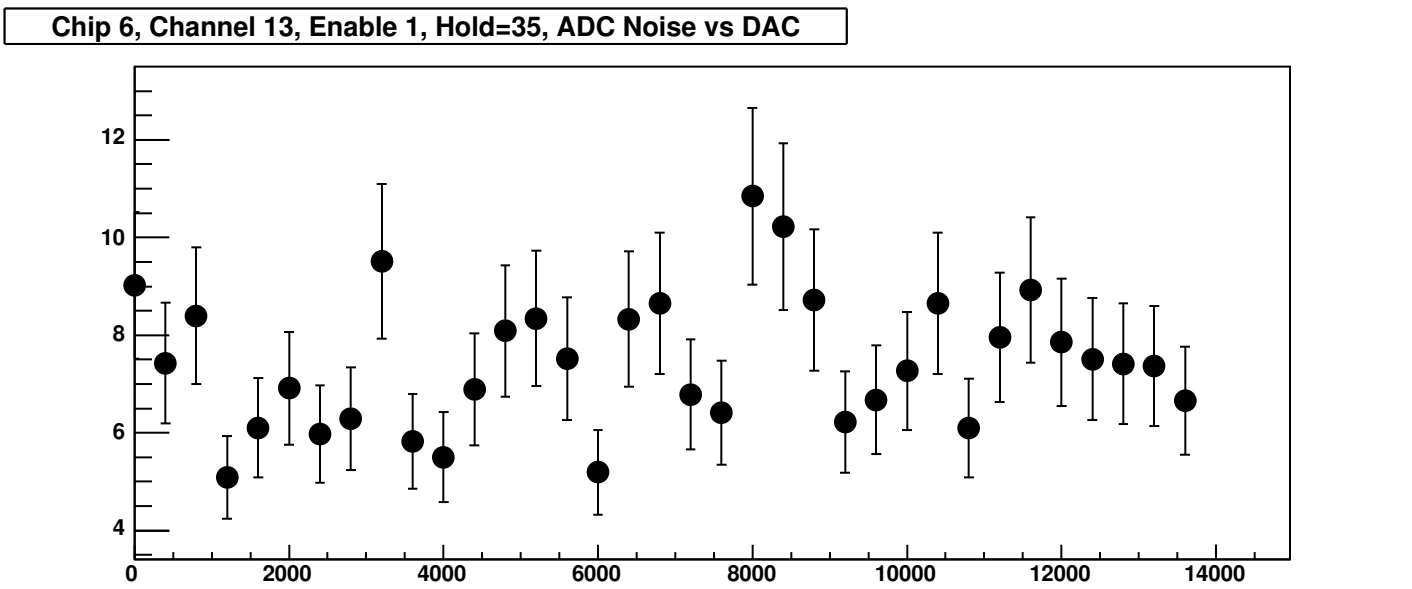
Chip 6, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

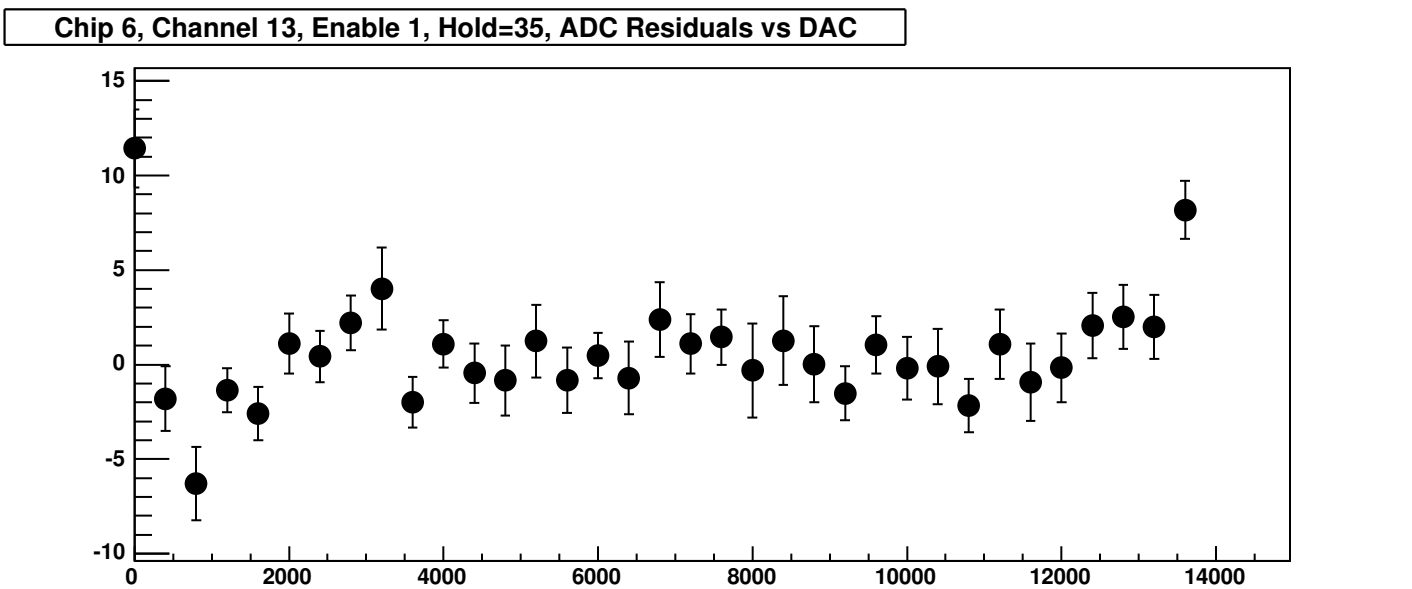


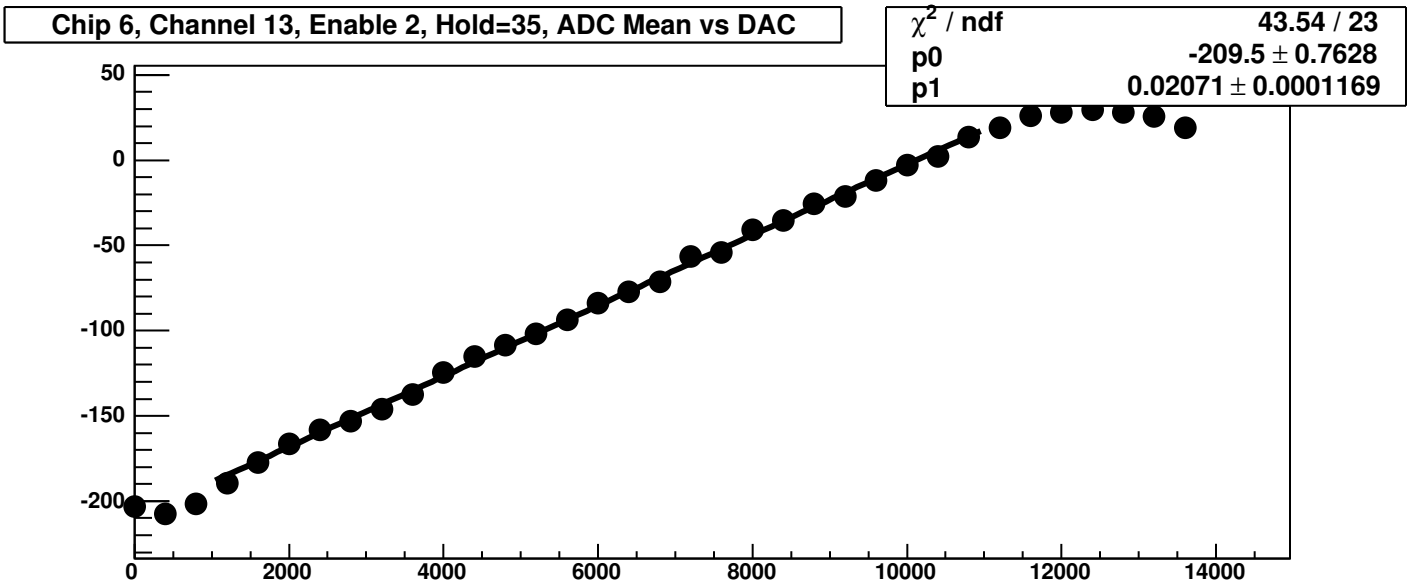
Chip 6, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

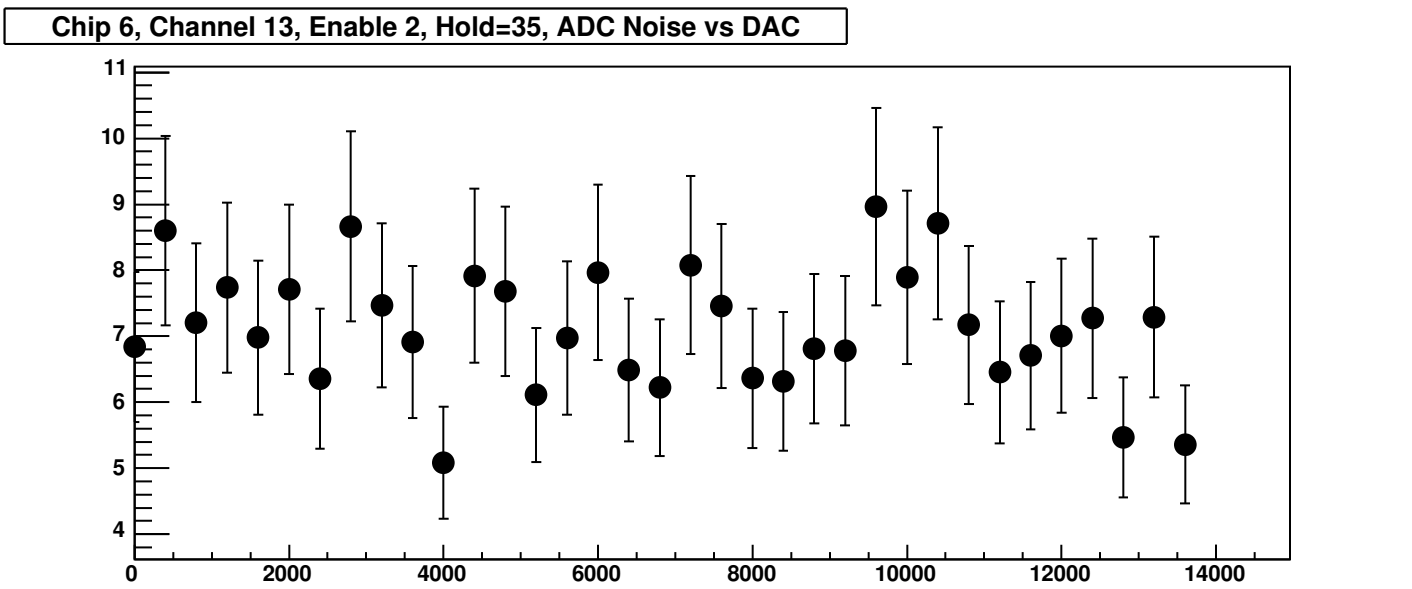


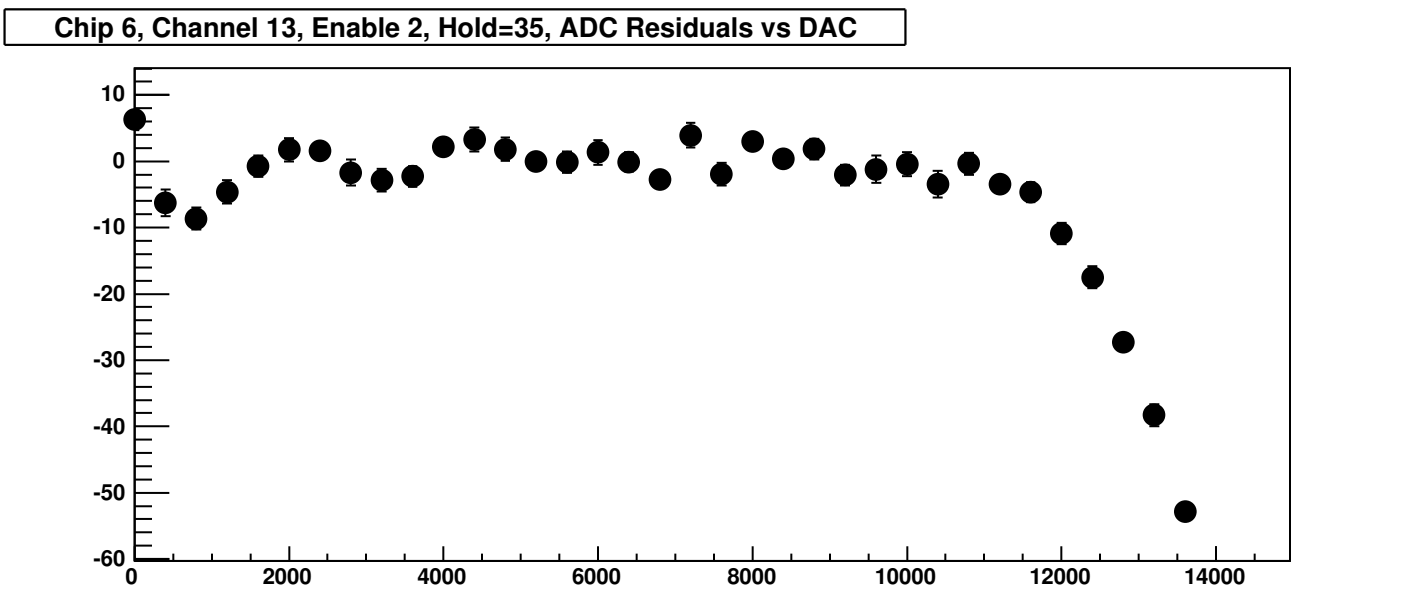

Chip 6, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 22.53 / 23
p0 -212.4 ± 0.6649
p1 0.02781 ± 0.0001049
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

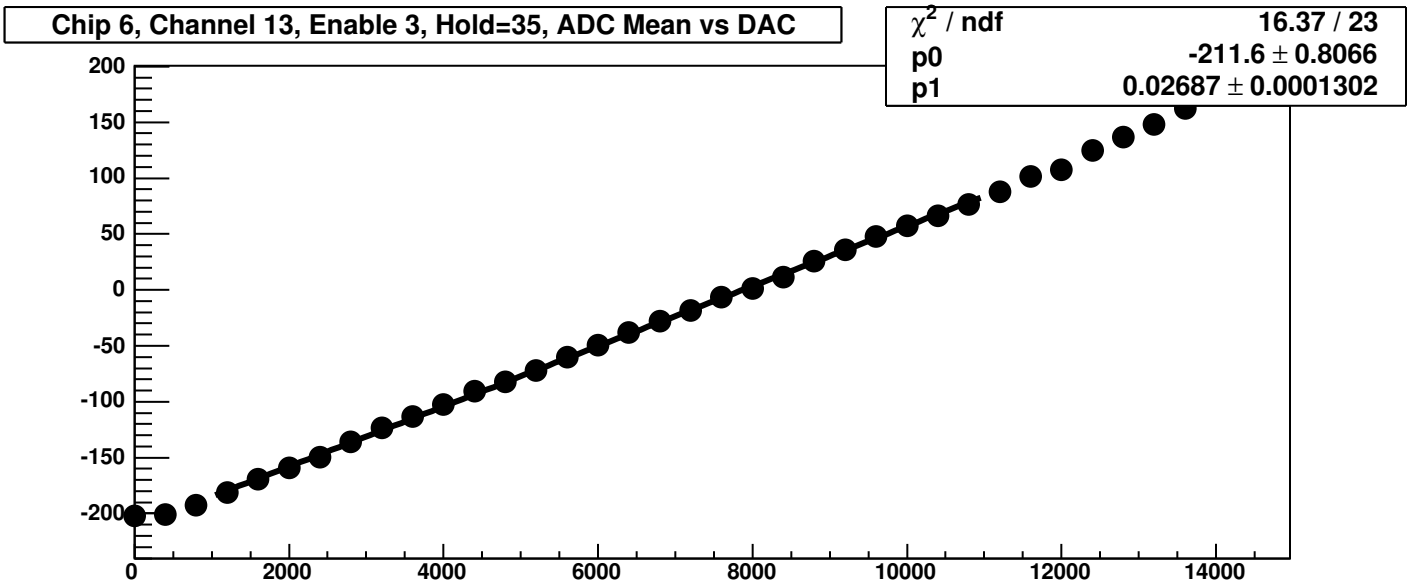

Chip 6, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

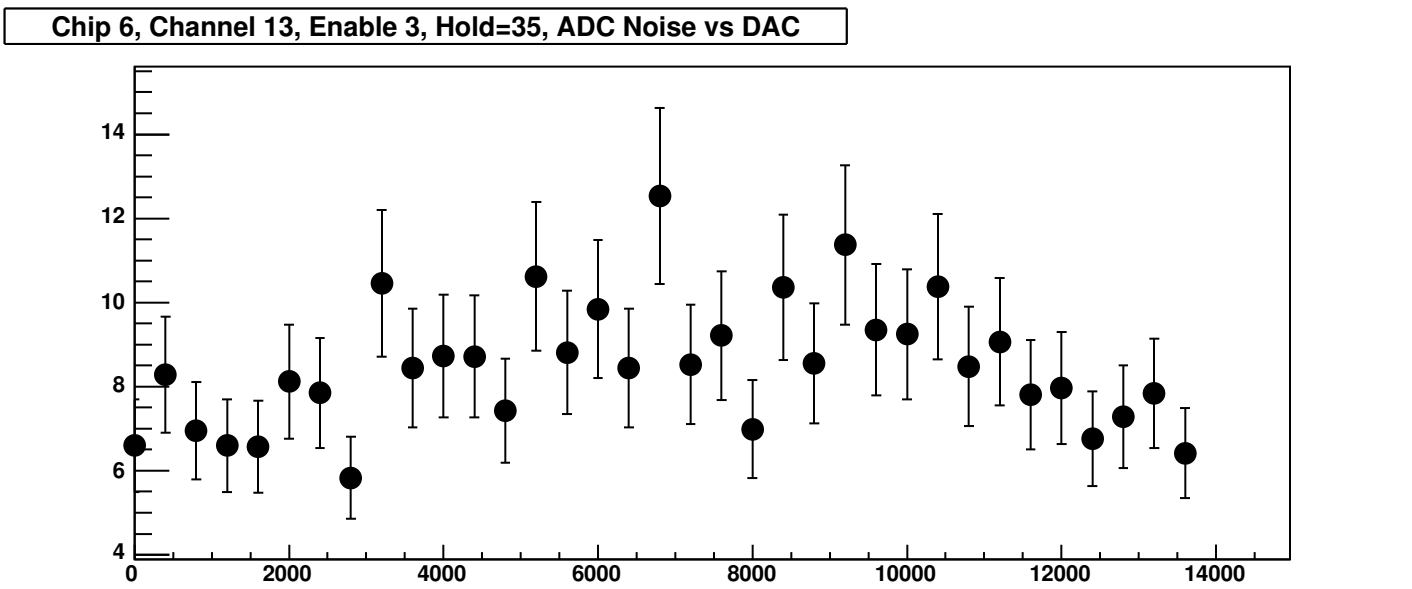

Chip 6, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

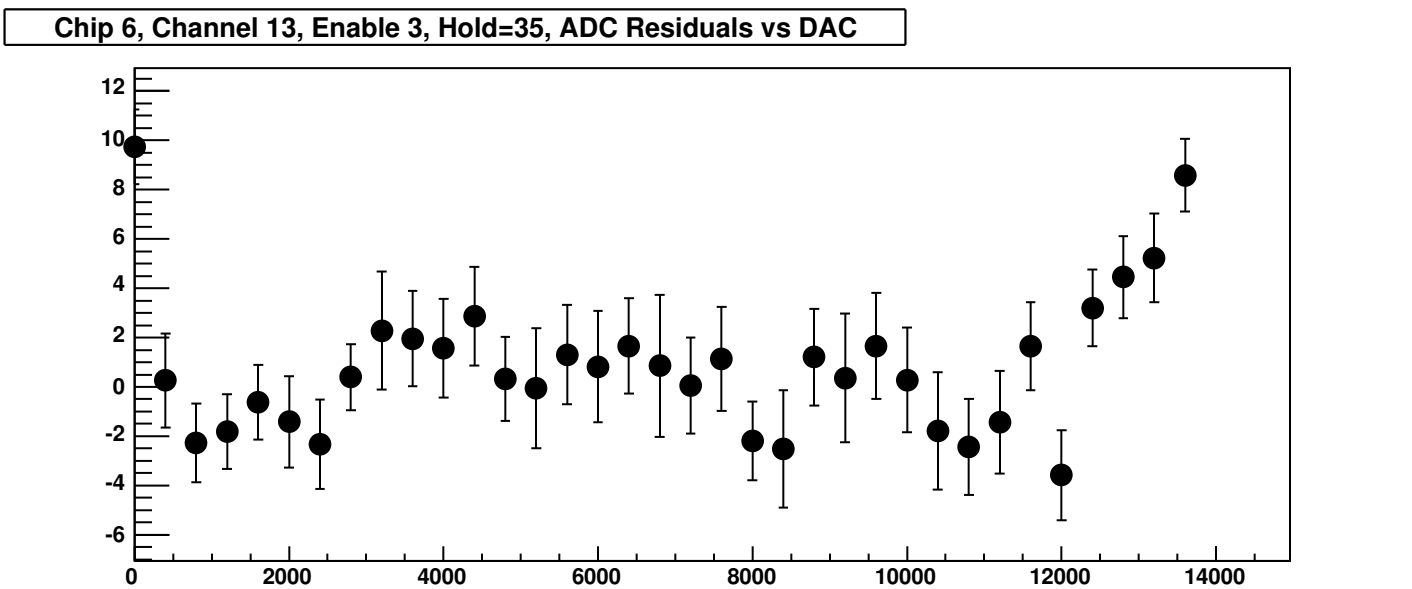

Chip 6, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 43.54 / 23
p0 -209.5 ± 0.7628
p1 0.02071 ± 0.0001169
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

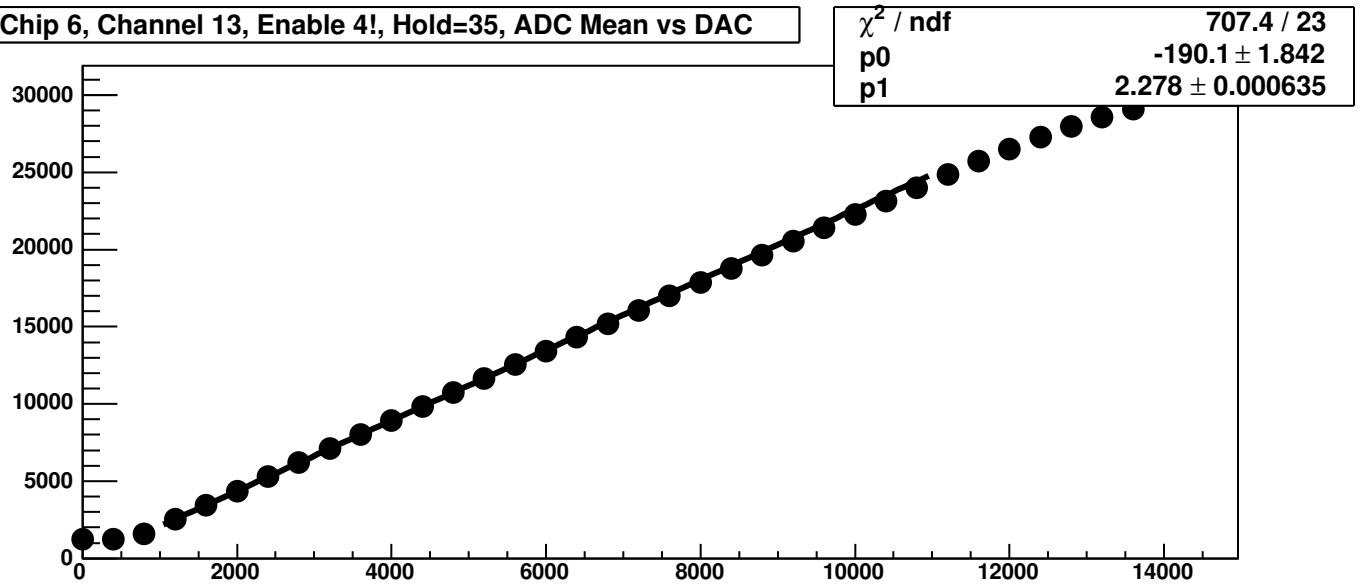

Chip 6, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
-60
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 16.37 / 23
p0 -211.6 ± 0.8066
p1 0.02687 ± 0.0001302
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

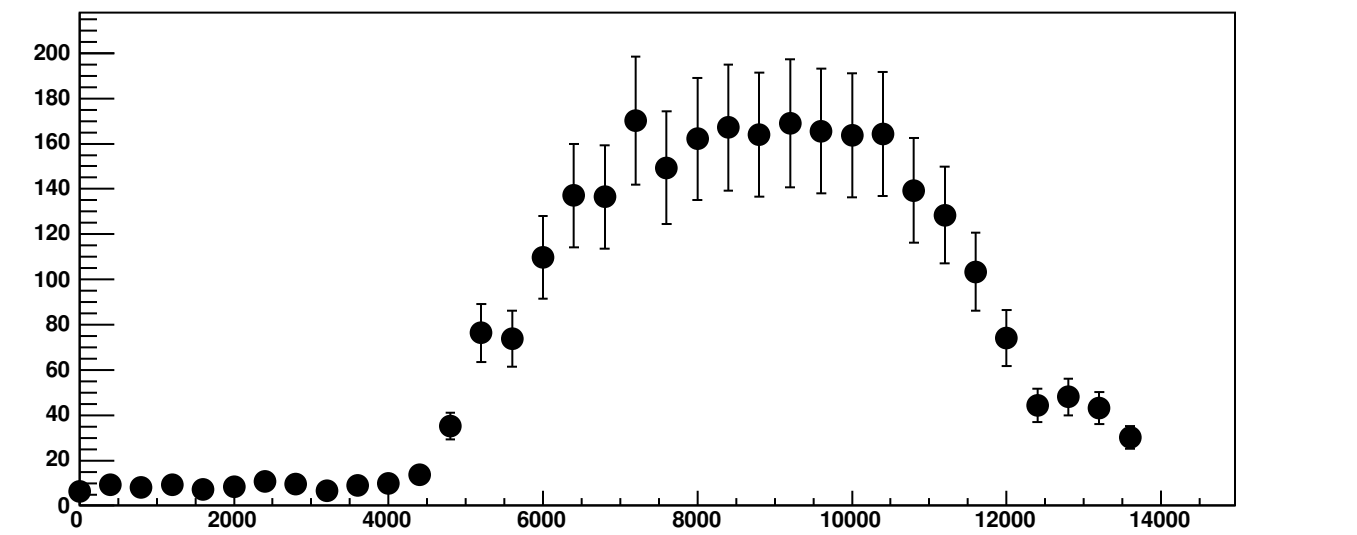

Chip 6, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000

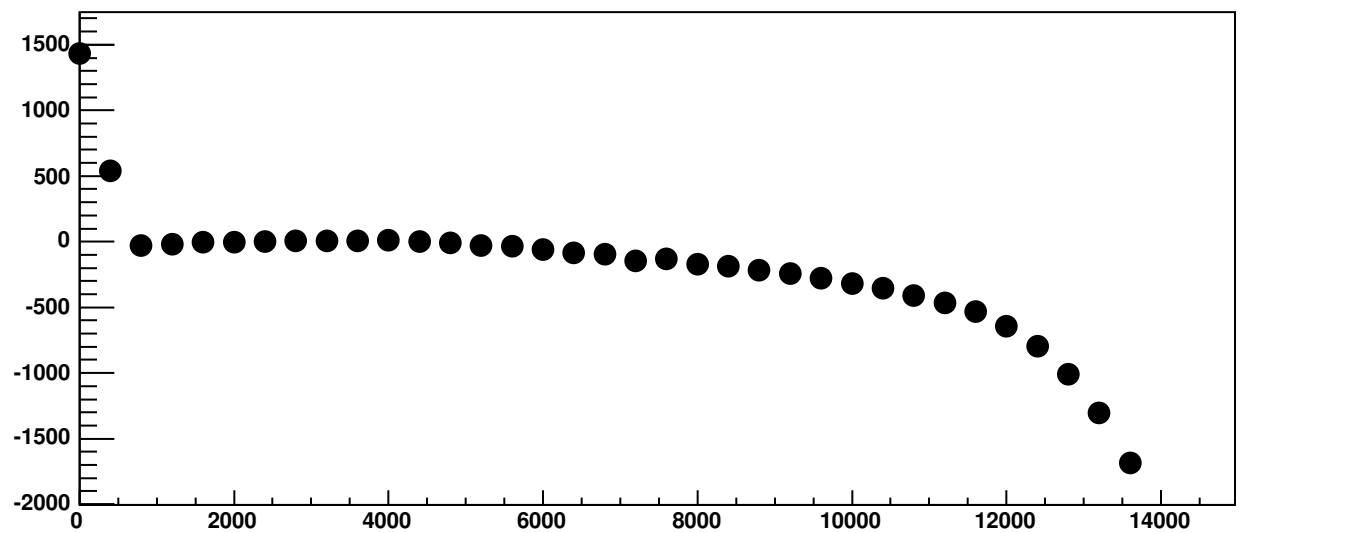
Chip 6, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC

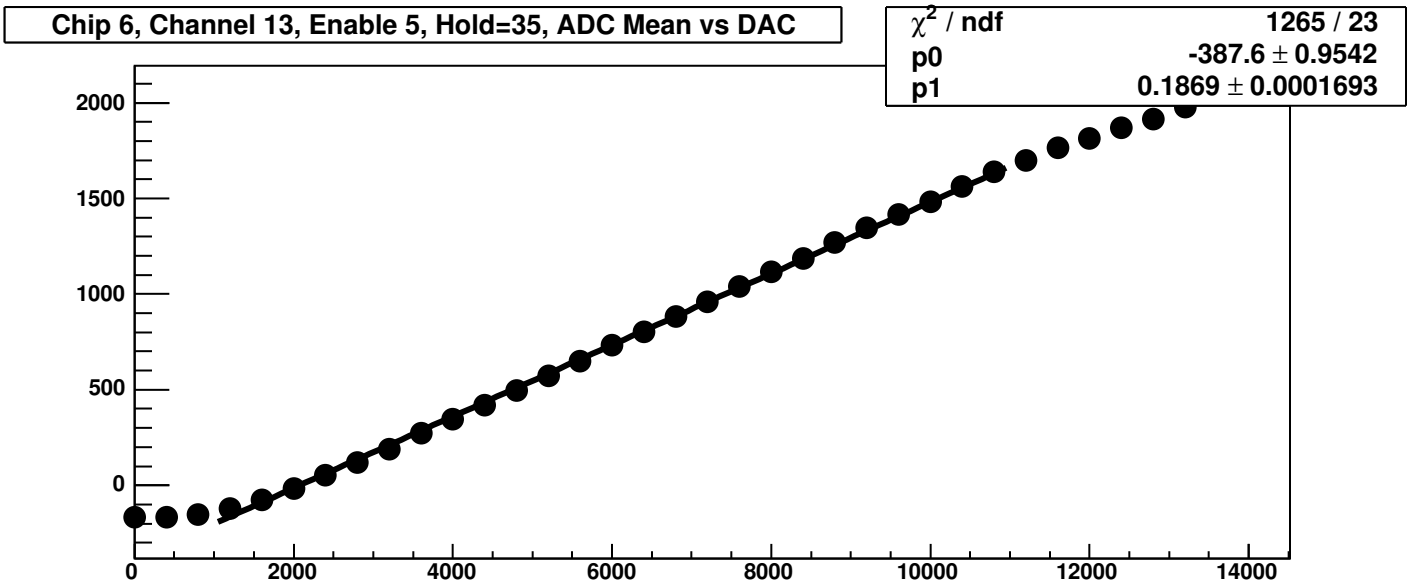


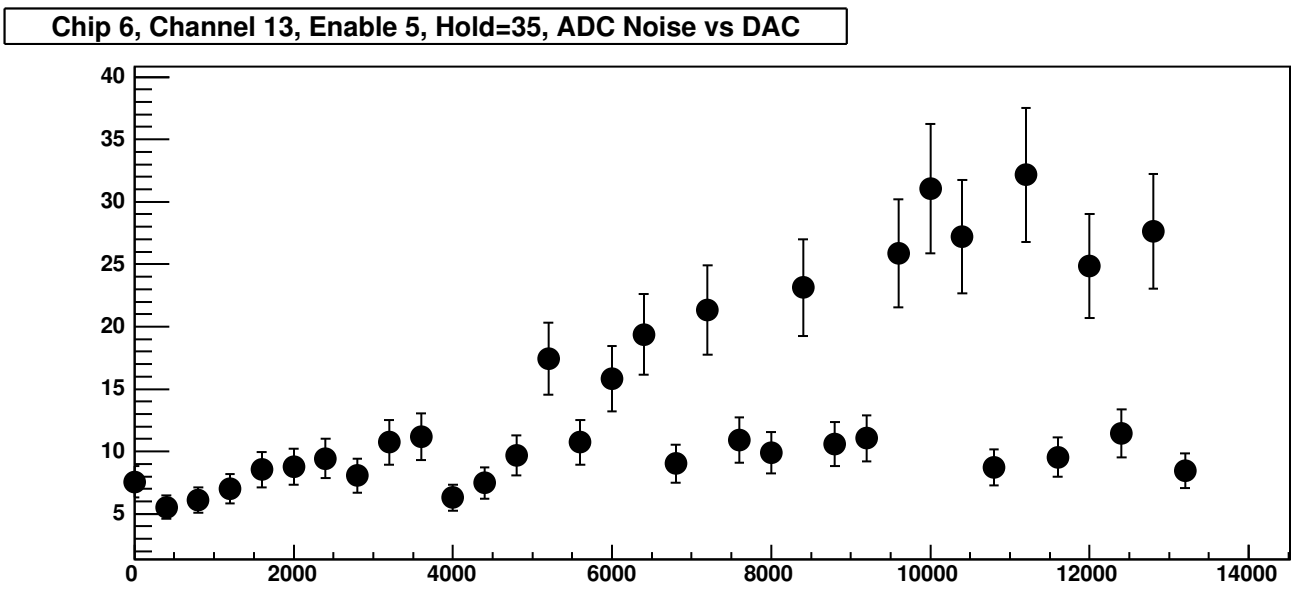
Chip 6, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC

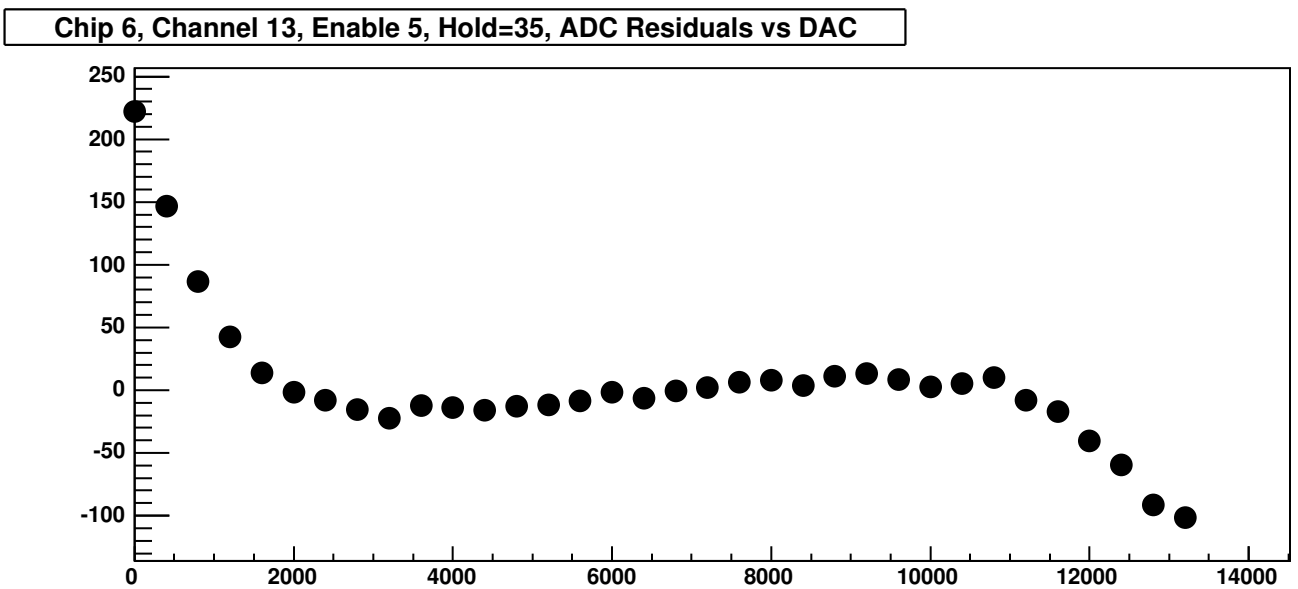


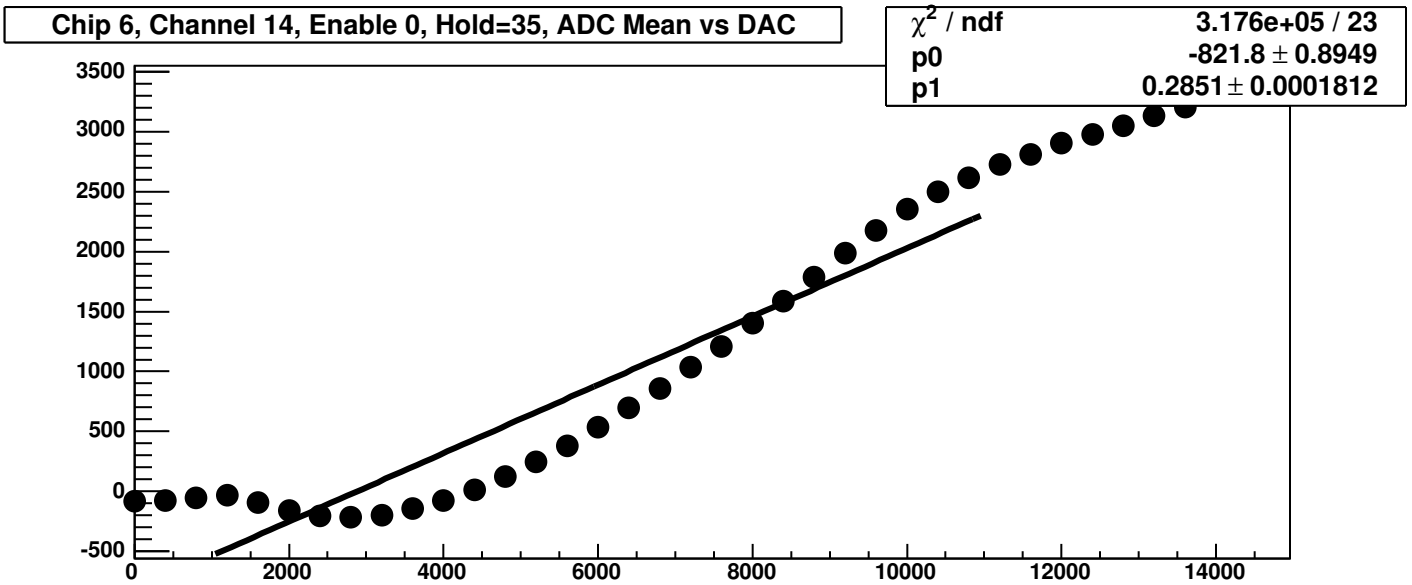
Chip 6, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC

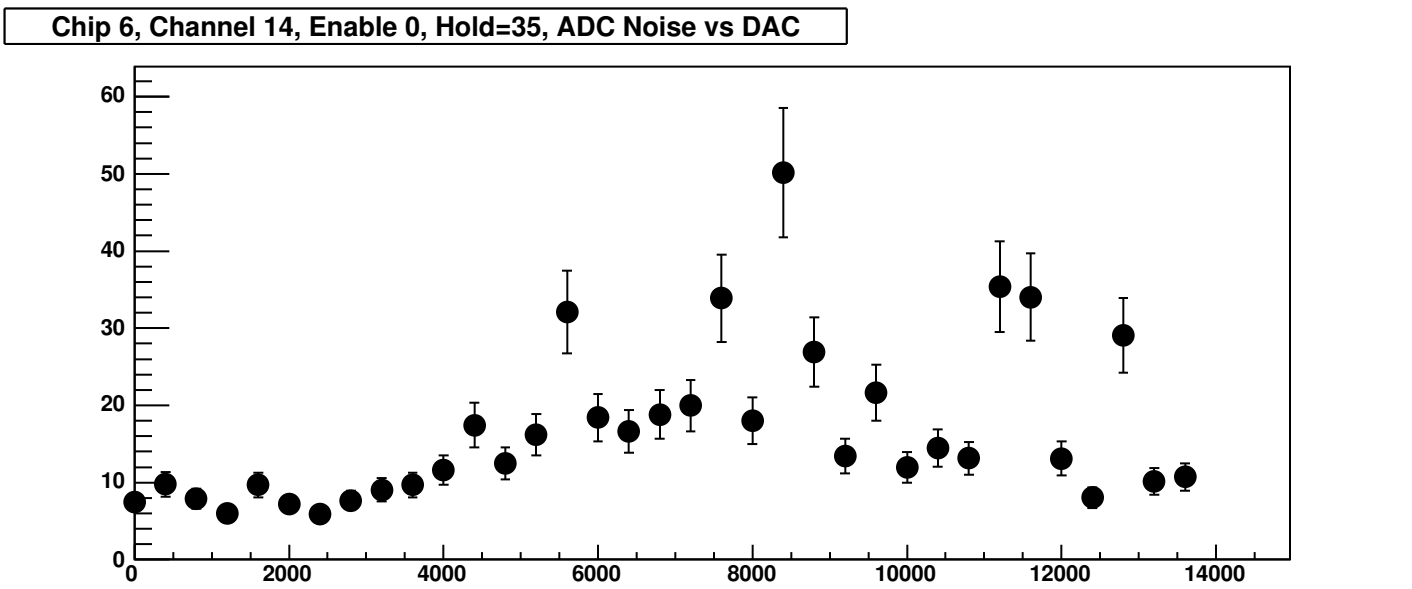


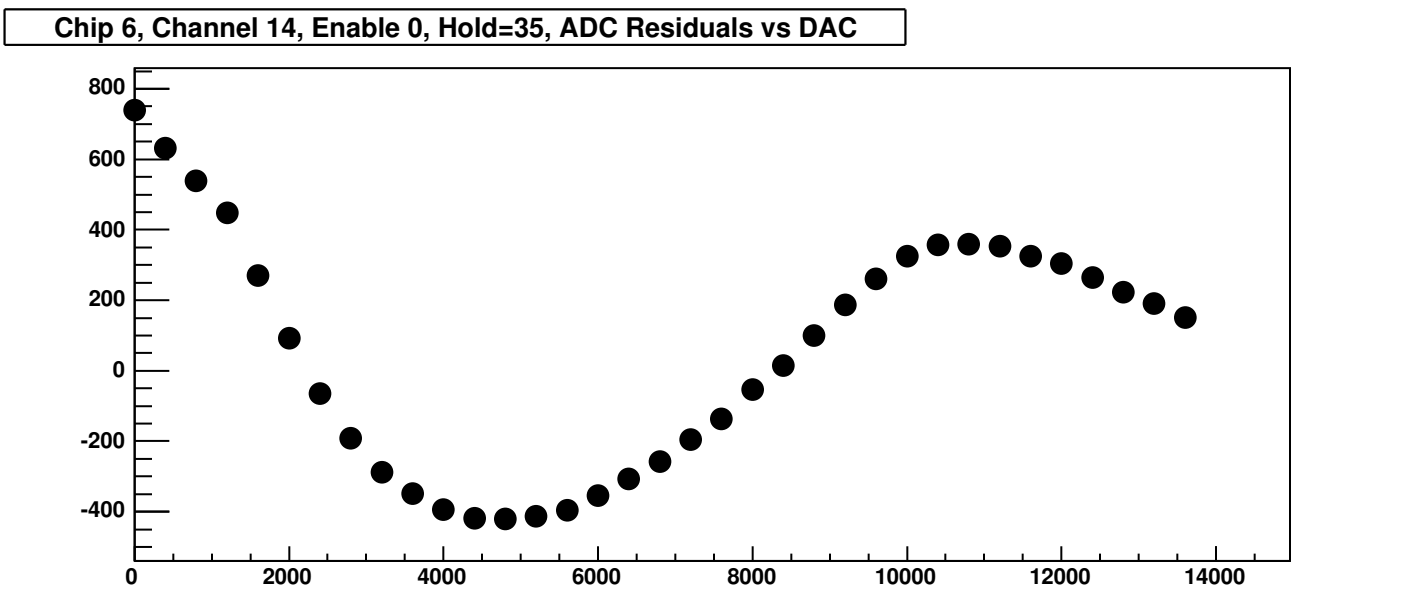

Chip 6, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1265 / 23
p0 -387.6 ± 0.9542
p1 0.1869 ± 0.0001693
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

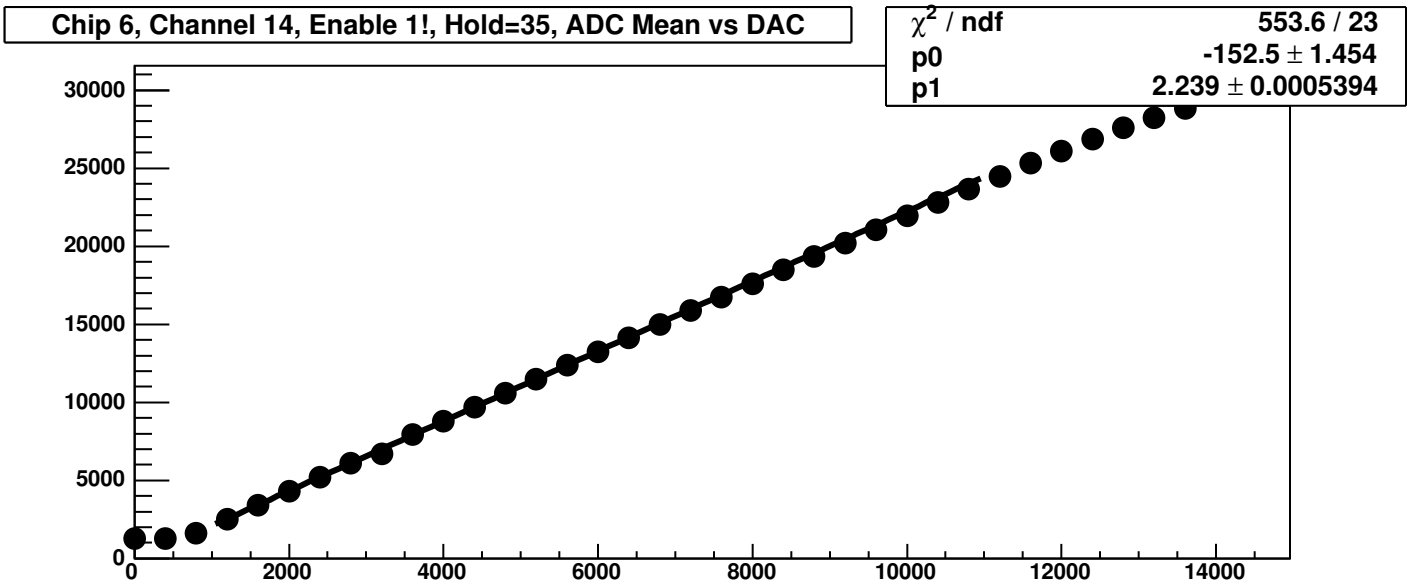

Chip 6, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

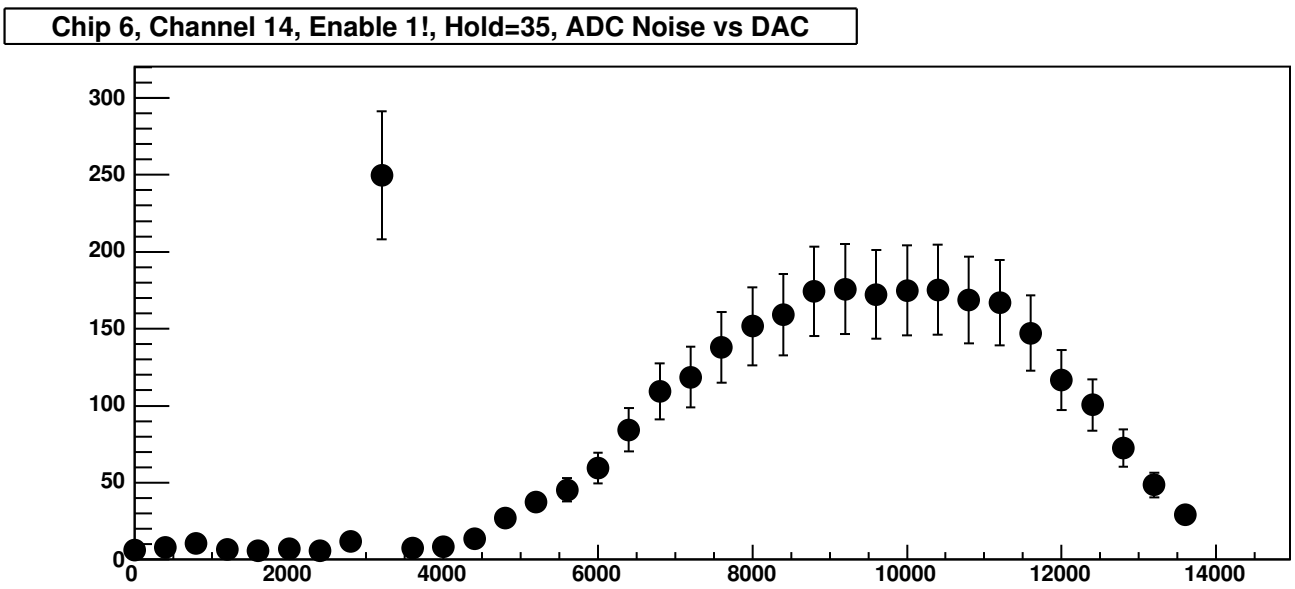

Chip 6, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

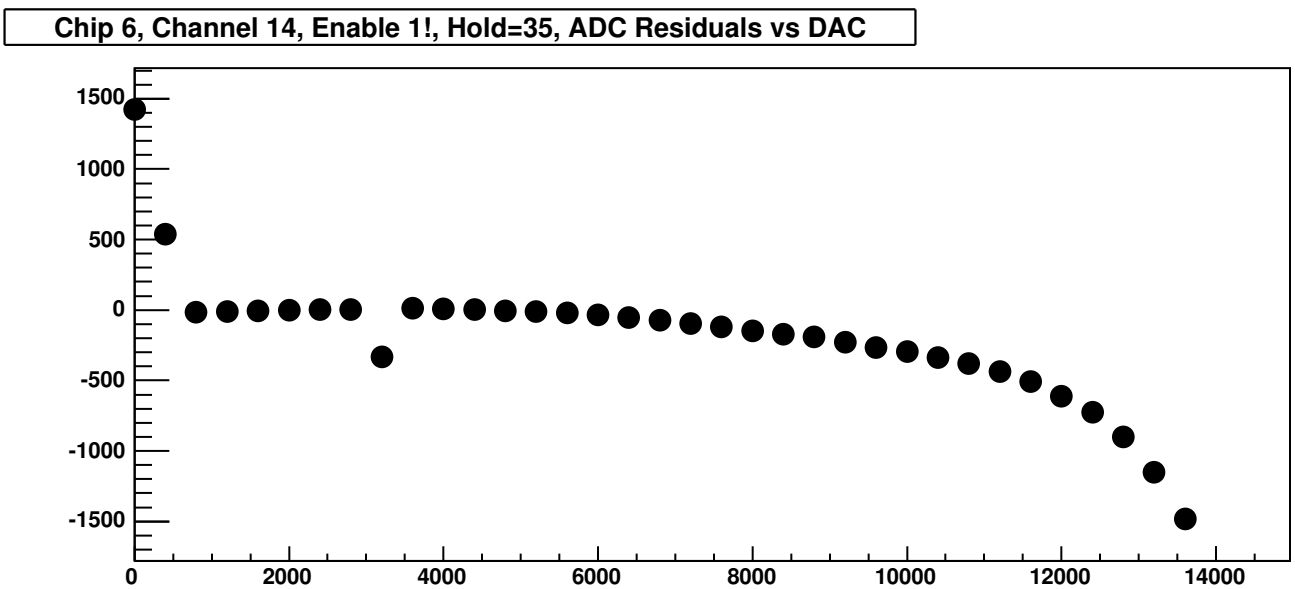

Chip 6, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.176e+05 / 23
p0 -821.8 ± 0.8949
p1 0.2851 ± 0.0001812
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

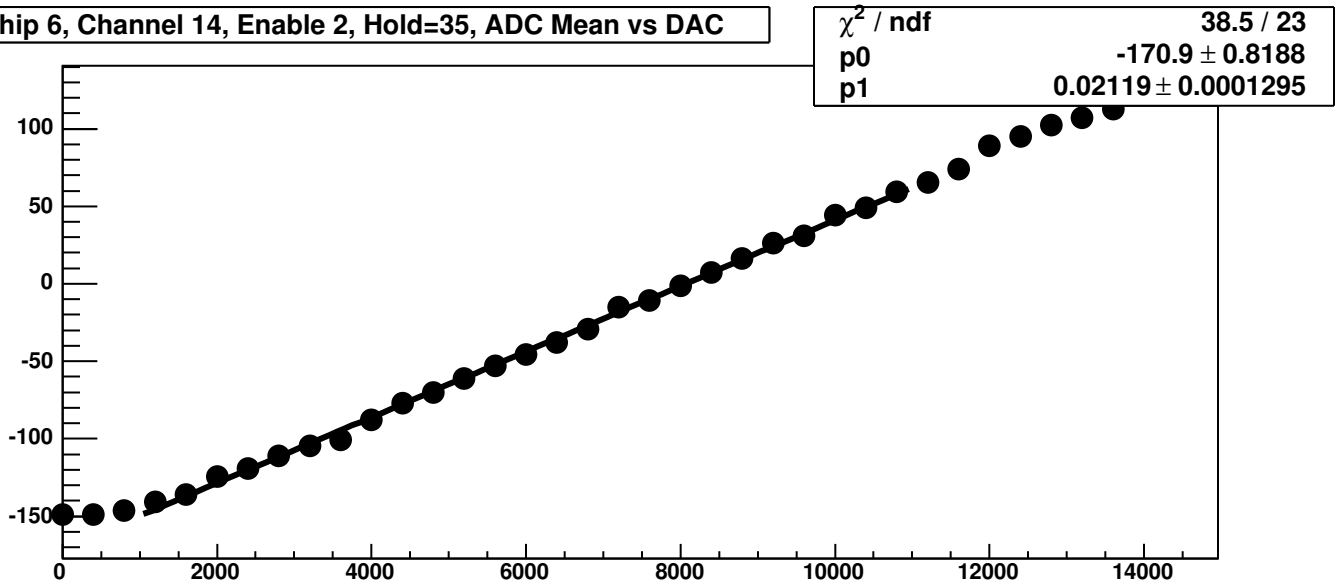

Chip 6, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 553.6 / 23
p0 -152.5 ± 1.454
p1 2.239 ± 0.0005394
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

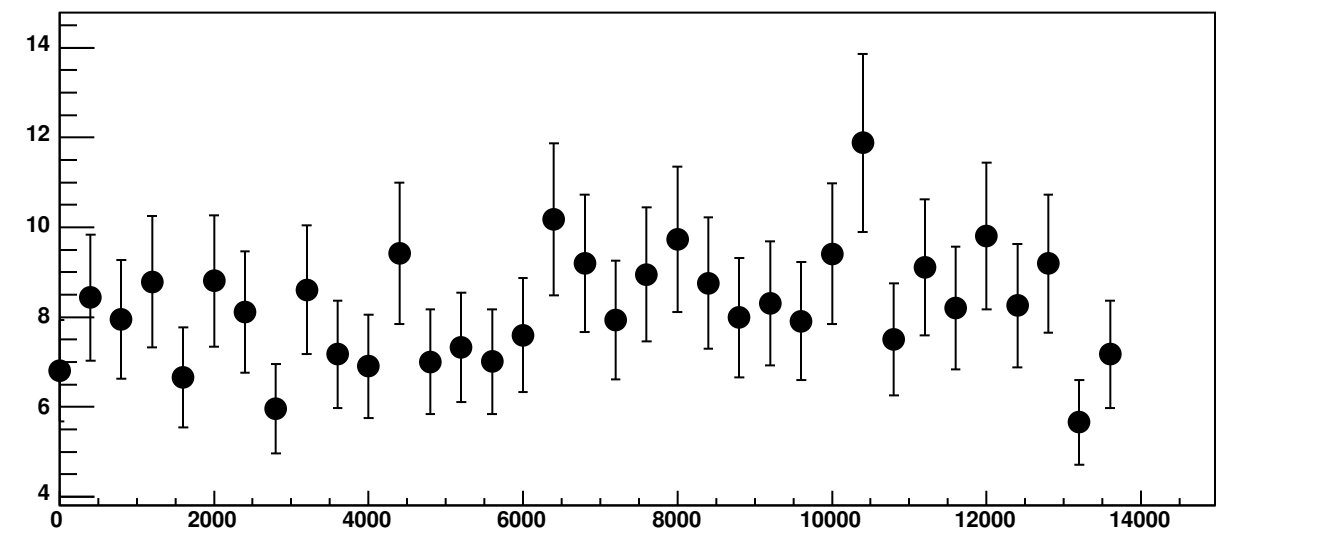

Chip 6, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

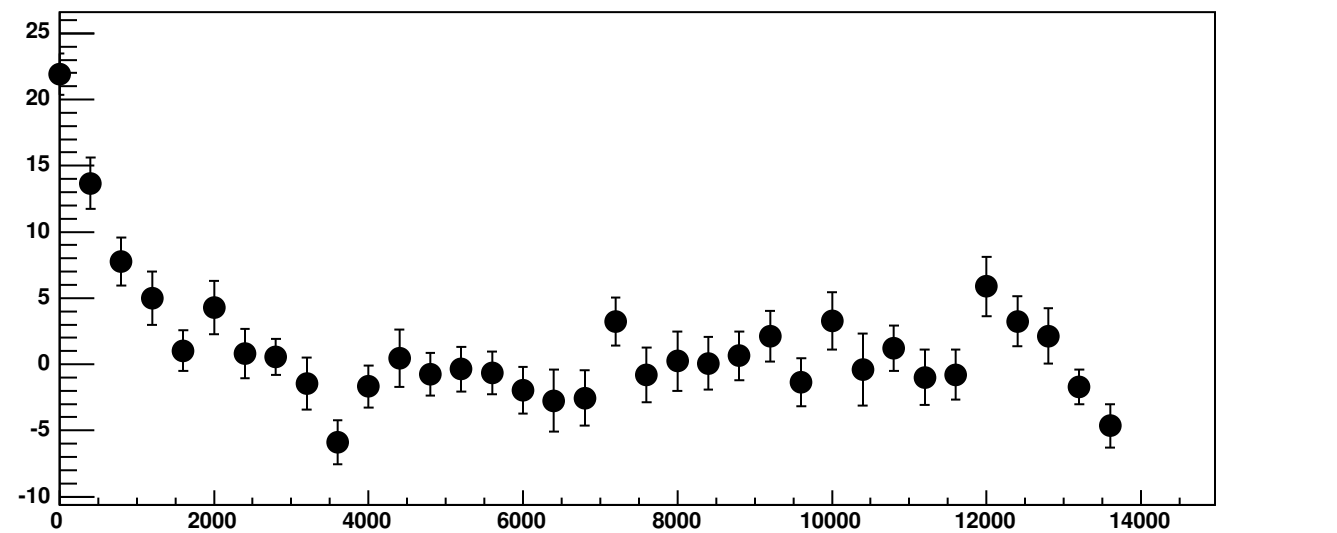
Chip 6, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

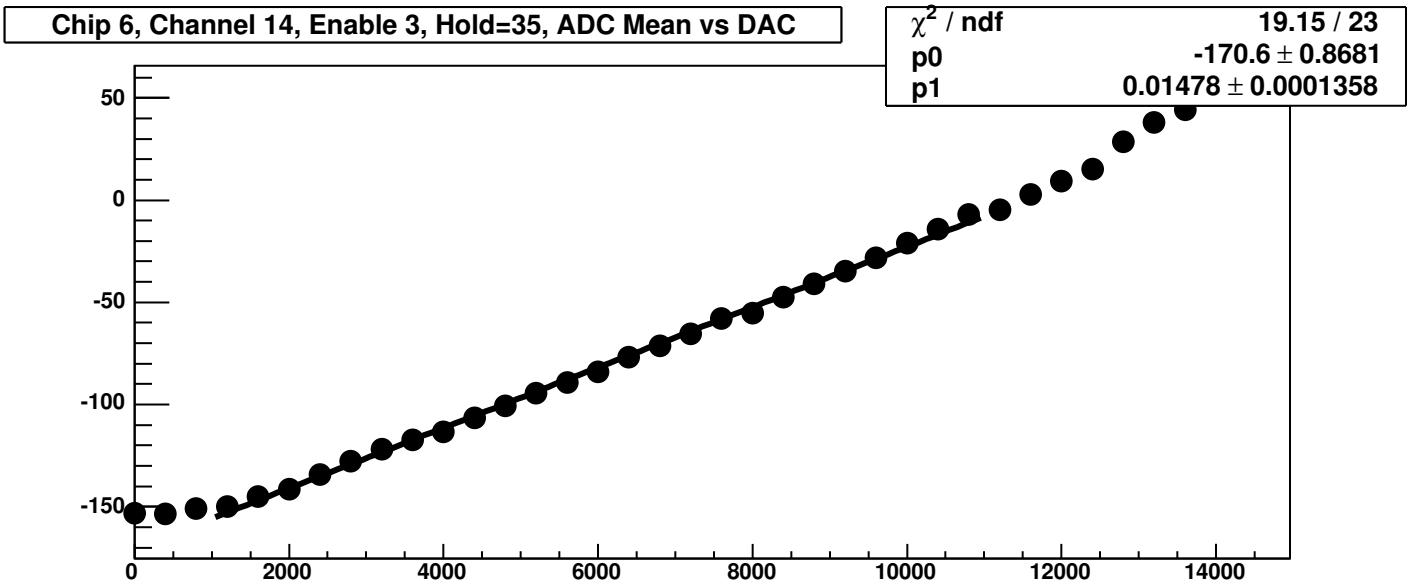


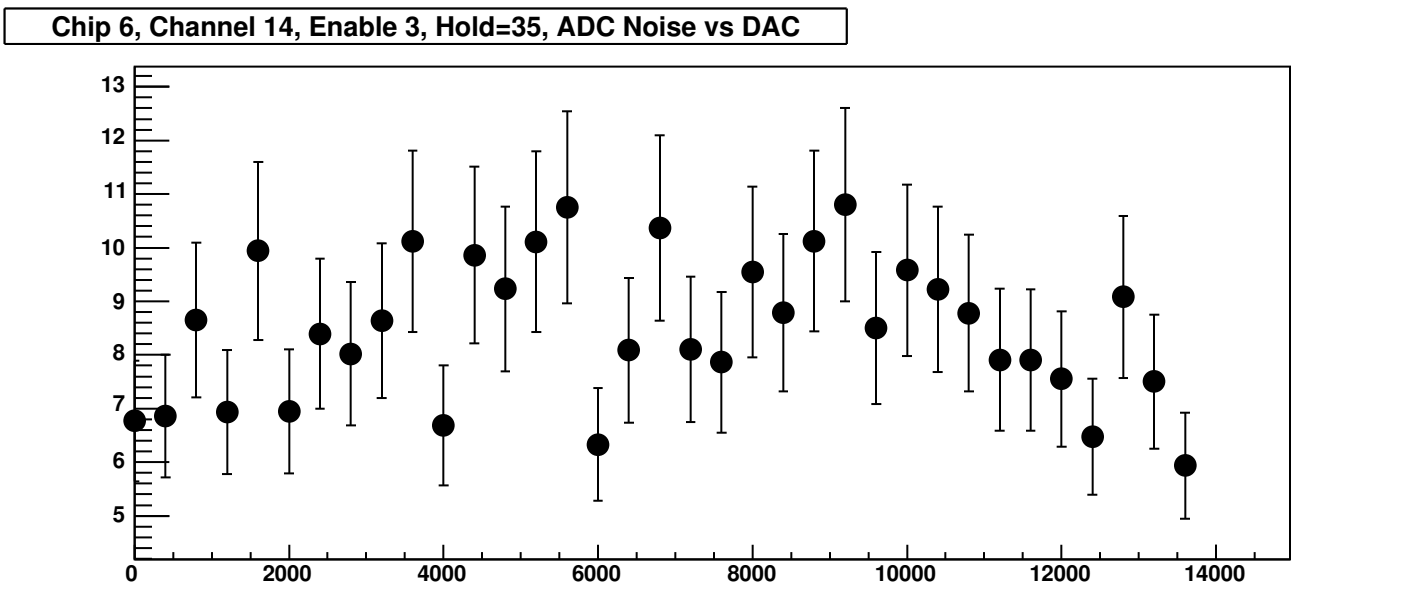
Chip 6, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC

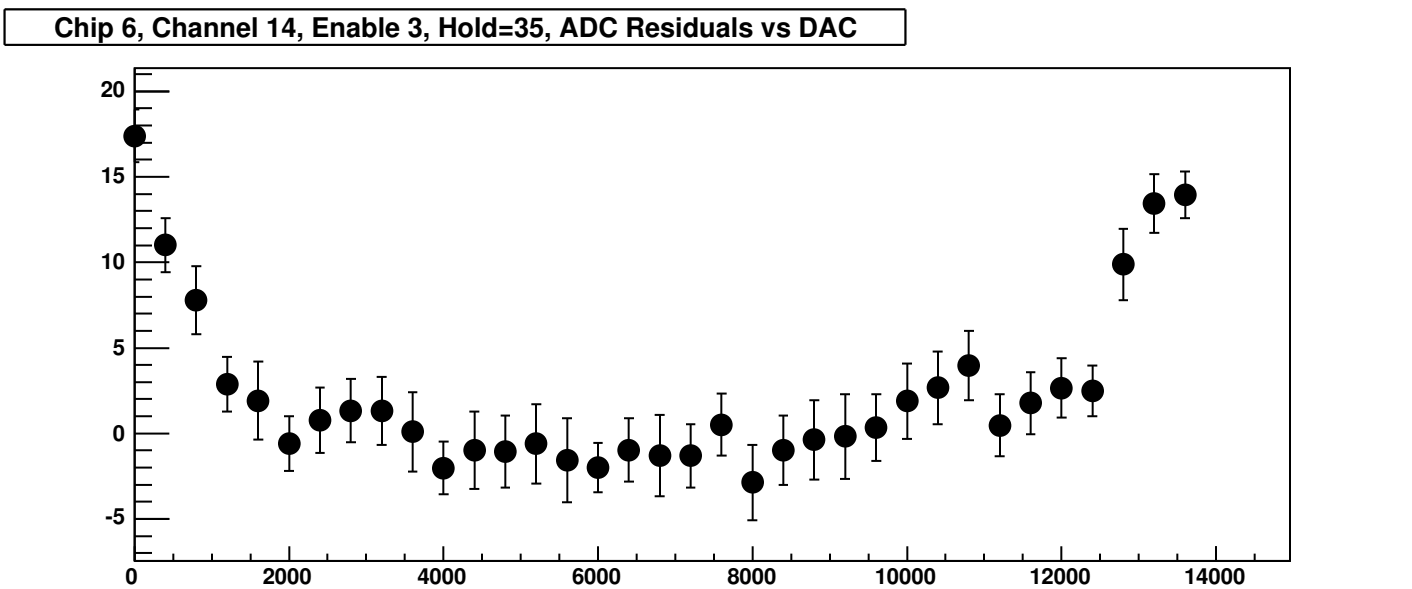


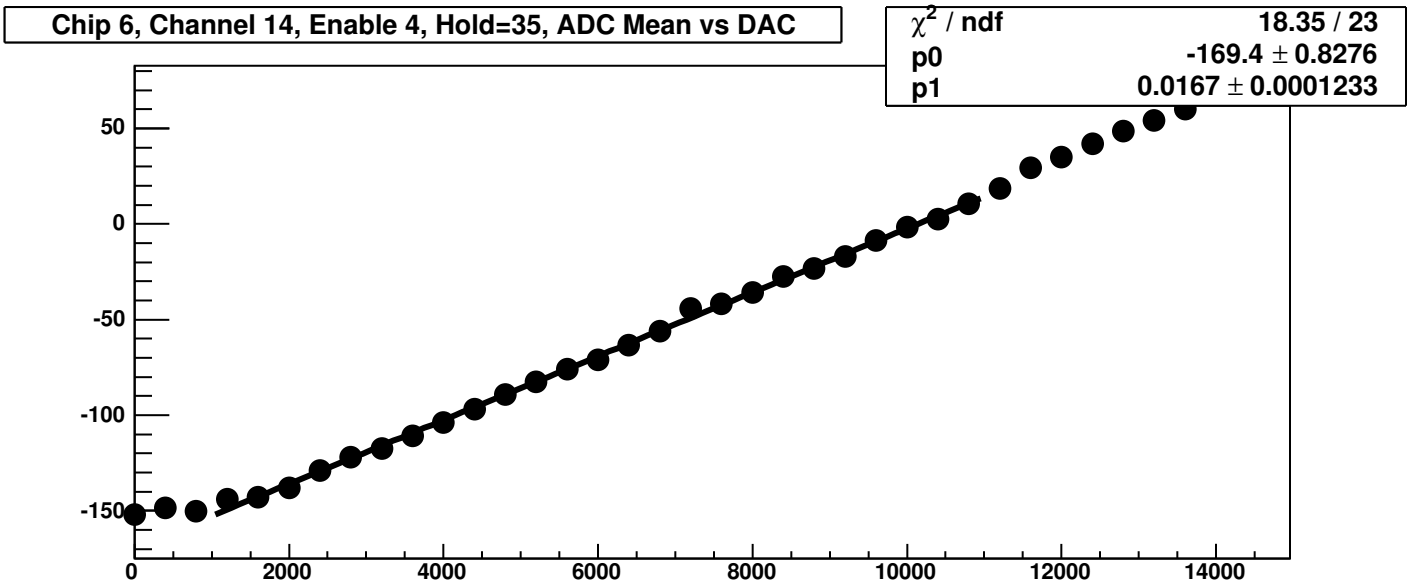
Chip 6, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

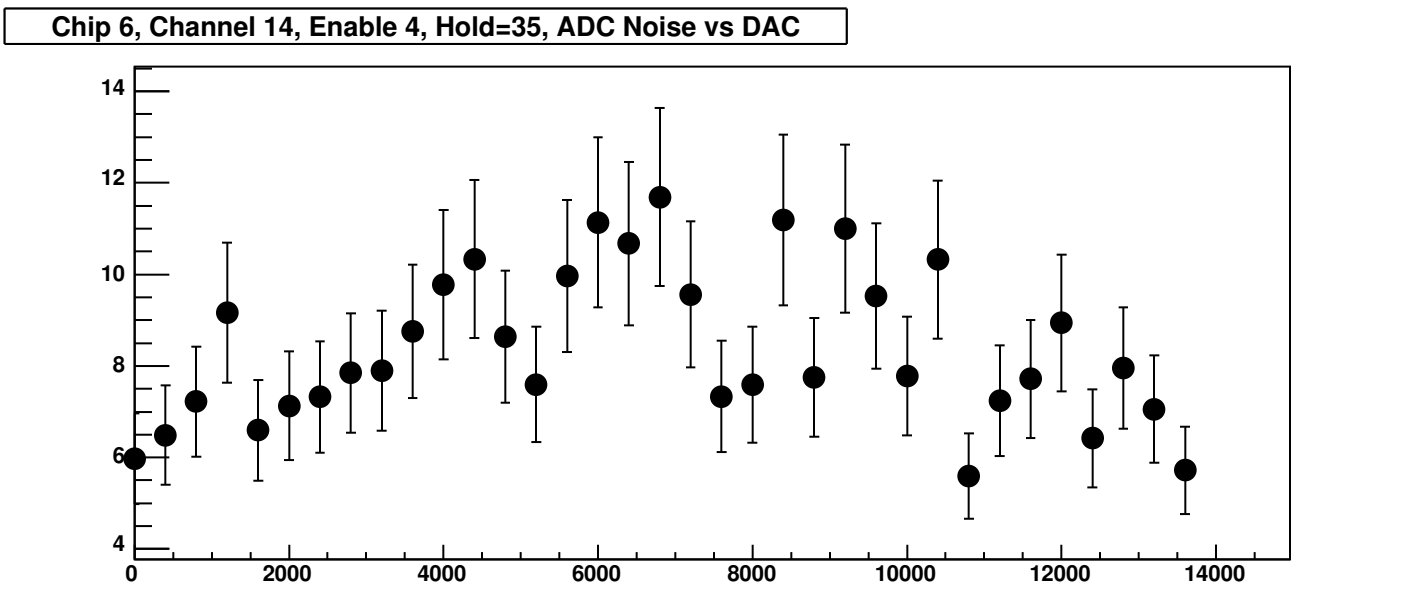


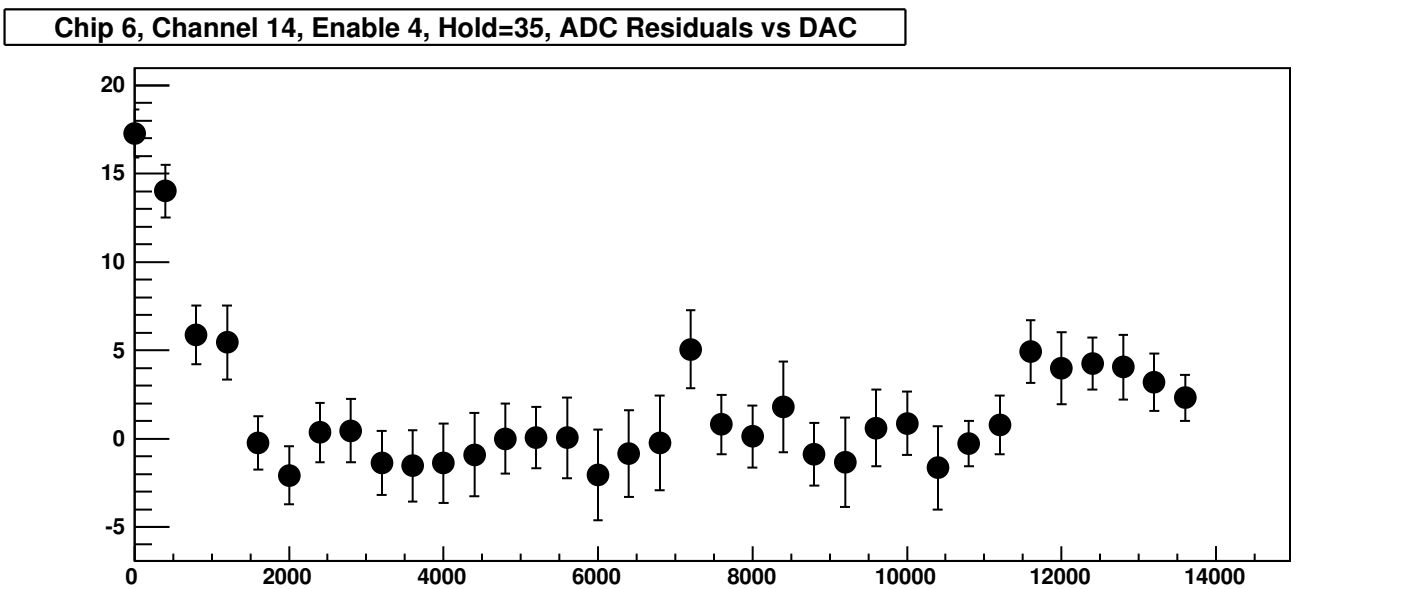

Chip 6, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 19.15 / 23
p0 -170.6 ± 0.8681
p1 0.01478 ± 0.0001358
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

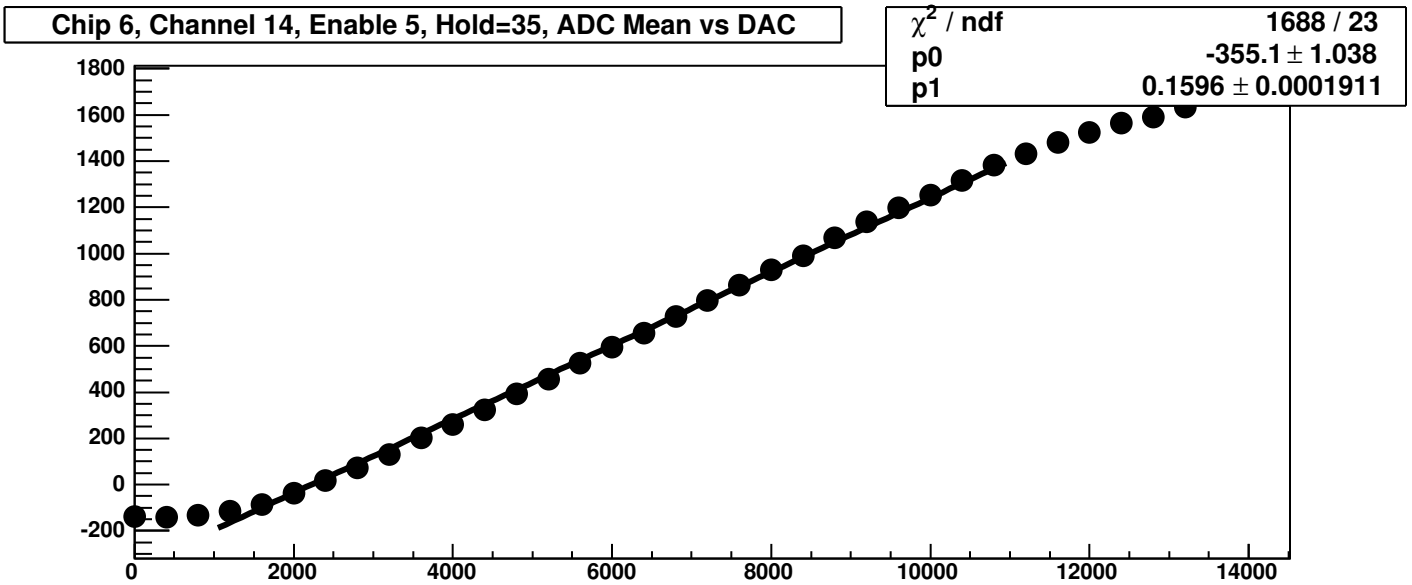

Chip 6, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

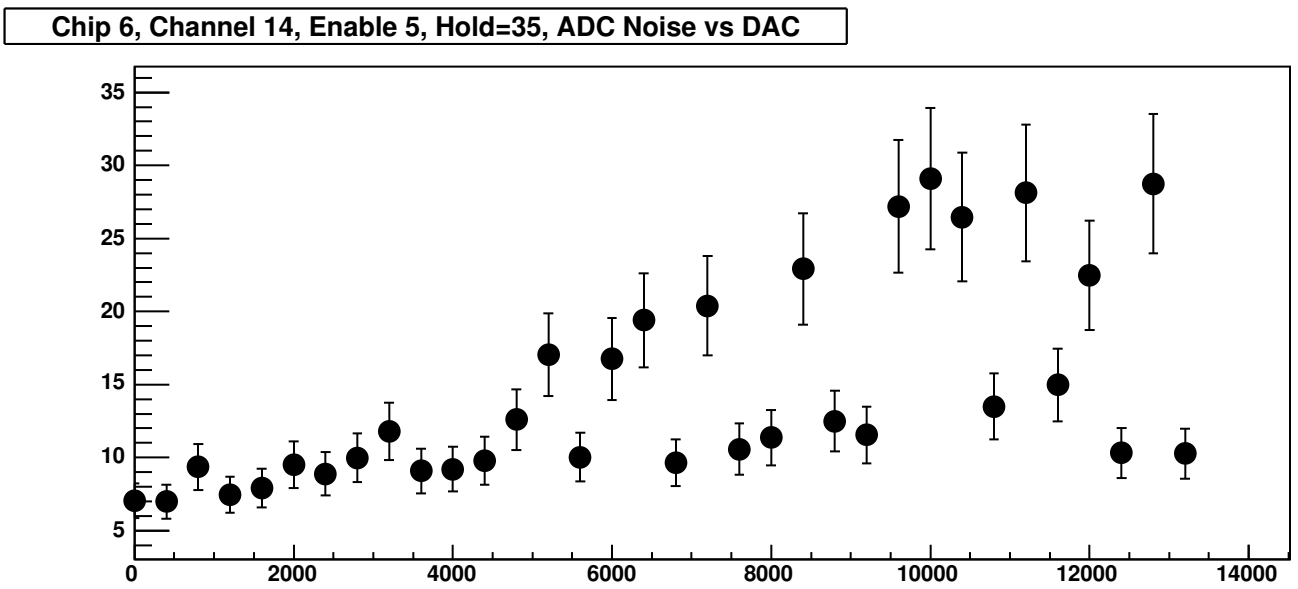

Chip 6, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

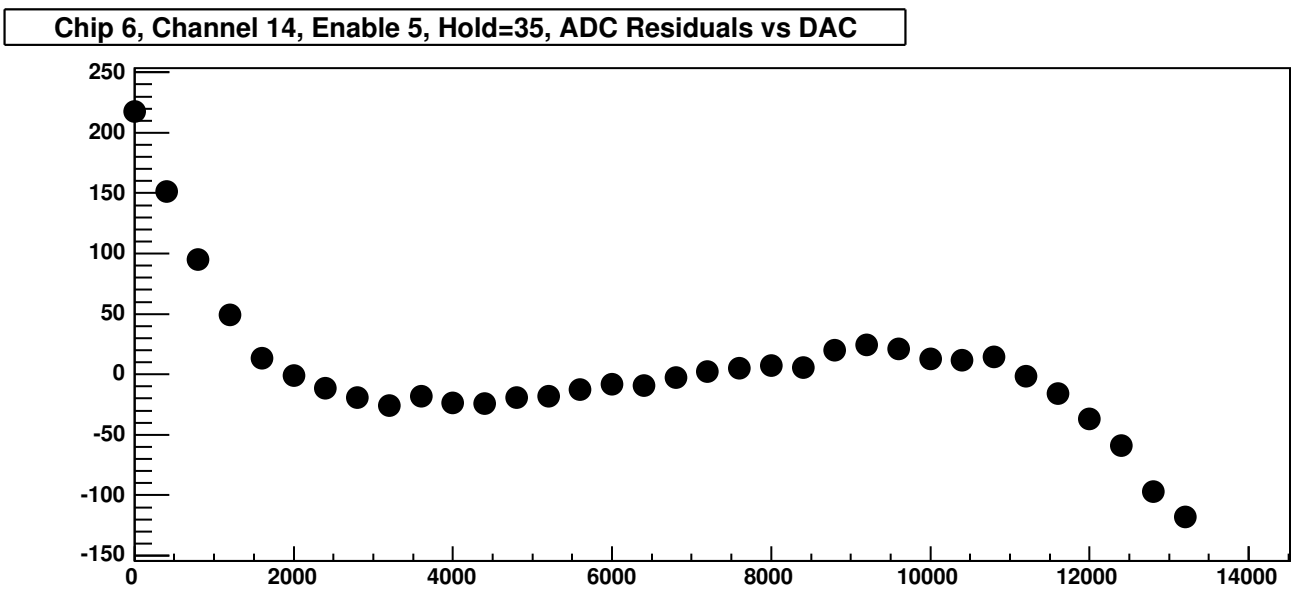

Chip 6, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 18.35 / 23
p0 -169.4 ± 0.8276
p1 0.0167 ± 0.0001233
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

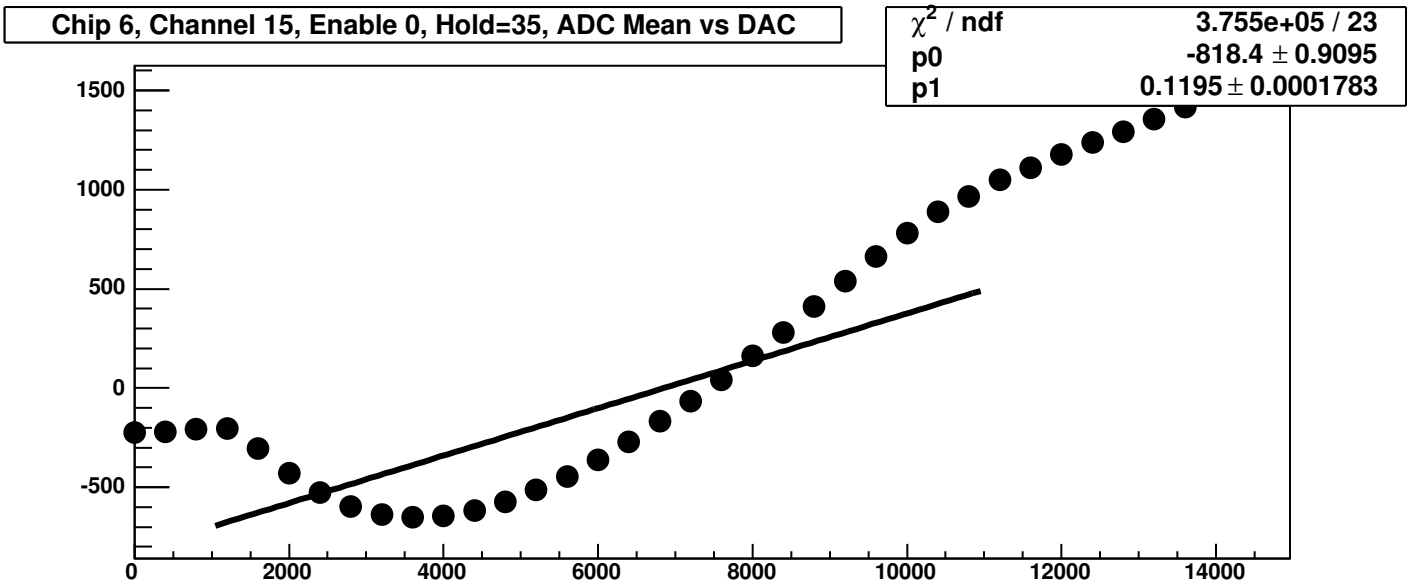

Chip 6, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

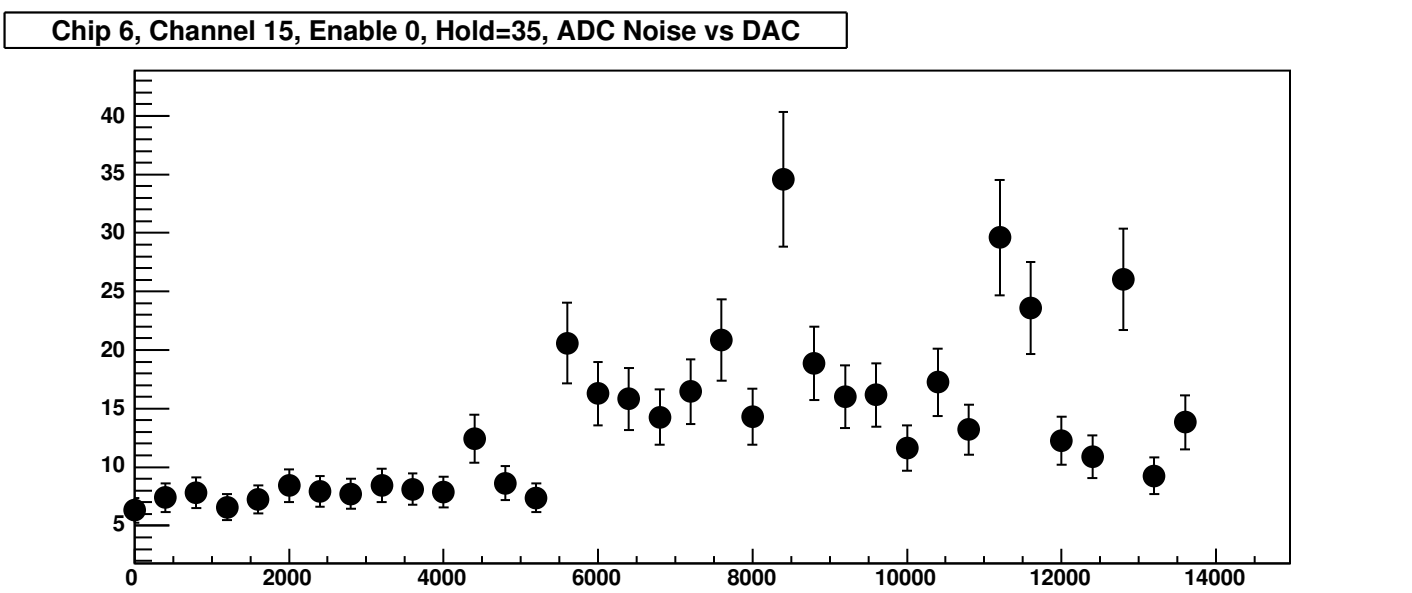

Chip 6, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

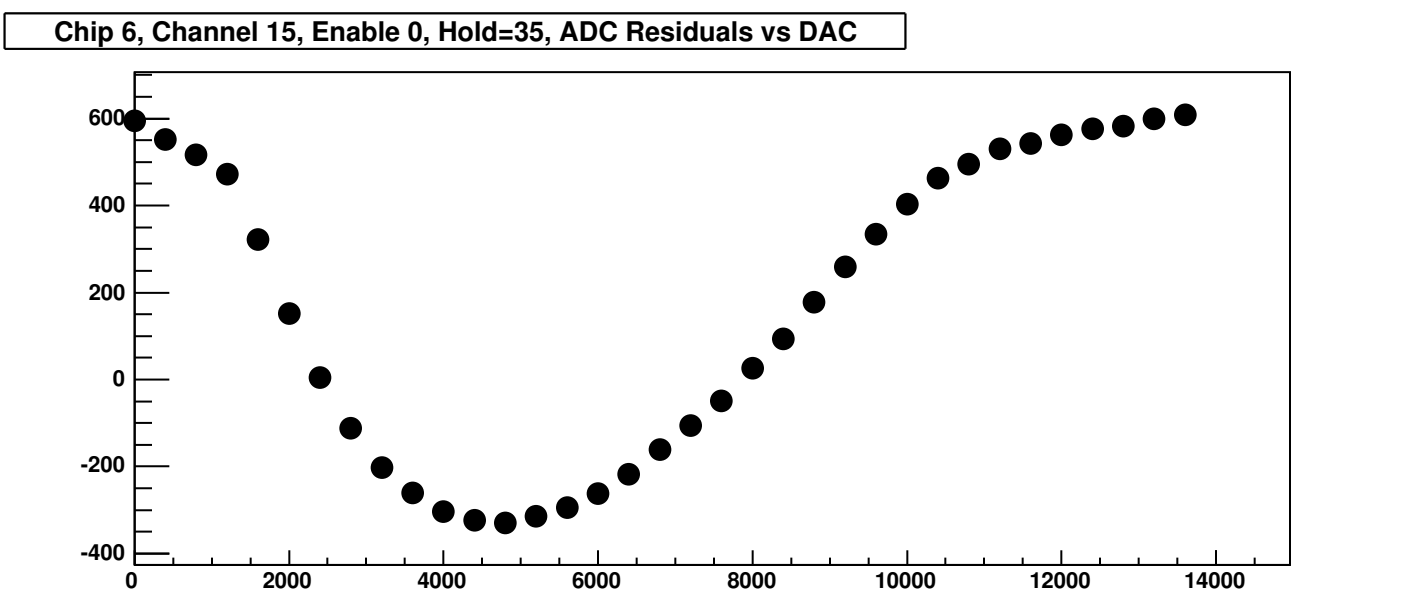

Chip 6, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1688 / 23
p0 -355.1 ± 1.038
p1 0.1596 ± 0.0001911
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

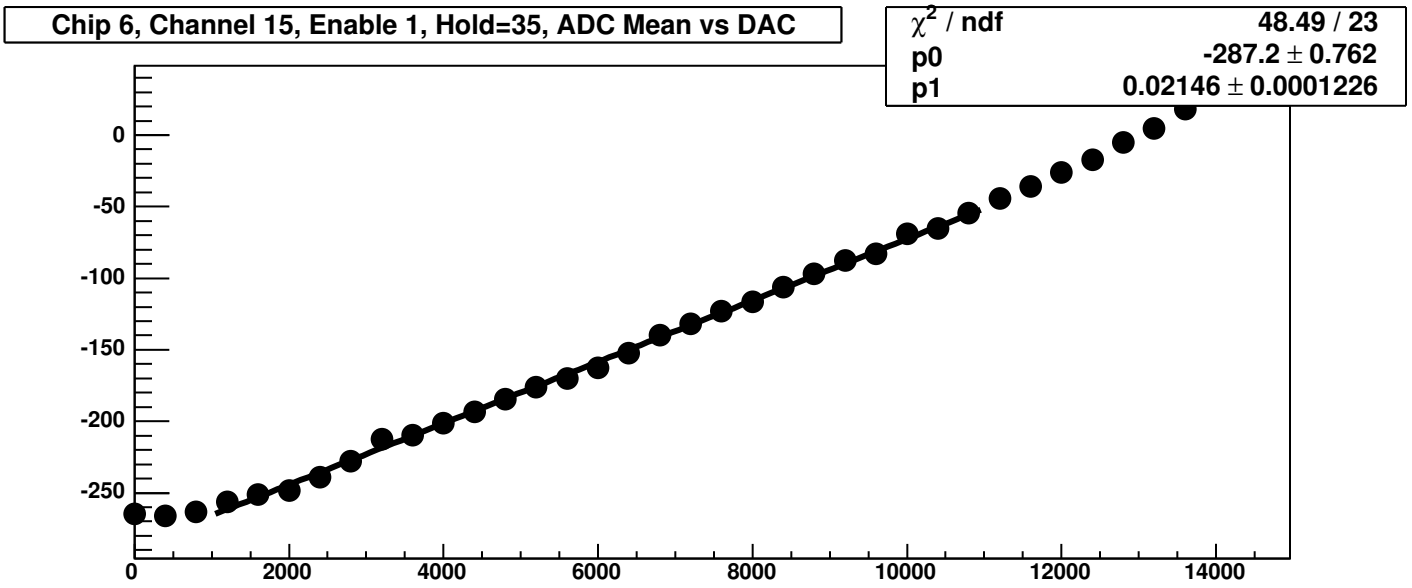

Chip 6, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

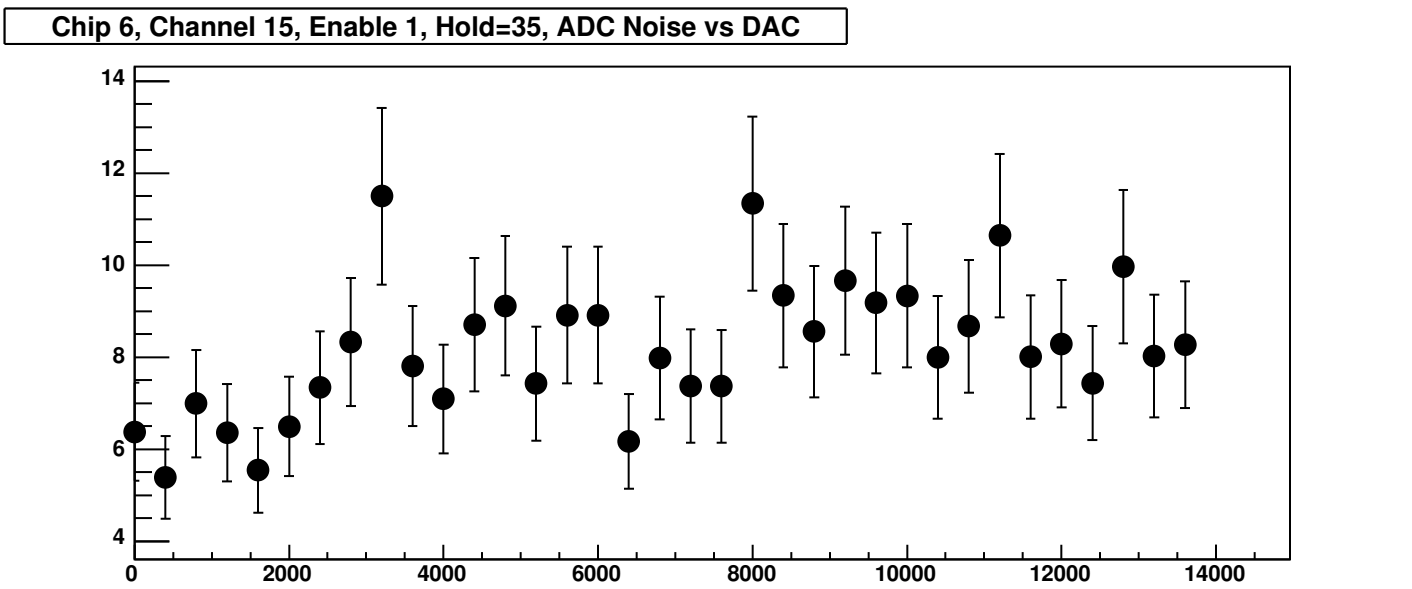

Chip 6, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

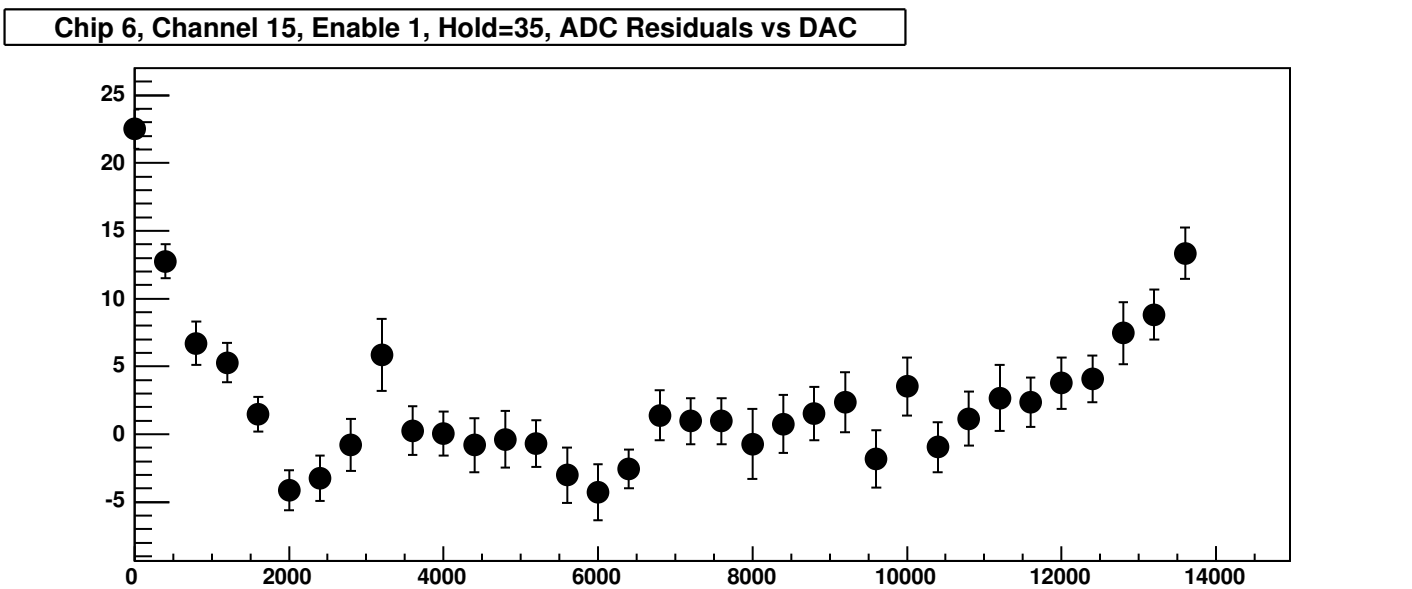

Chip 6, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.755e+05 / 23
p0 -818.4 ± 0.9095
p1 0.1195 ± 0.0001783
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

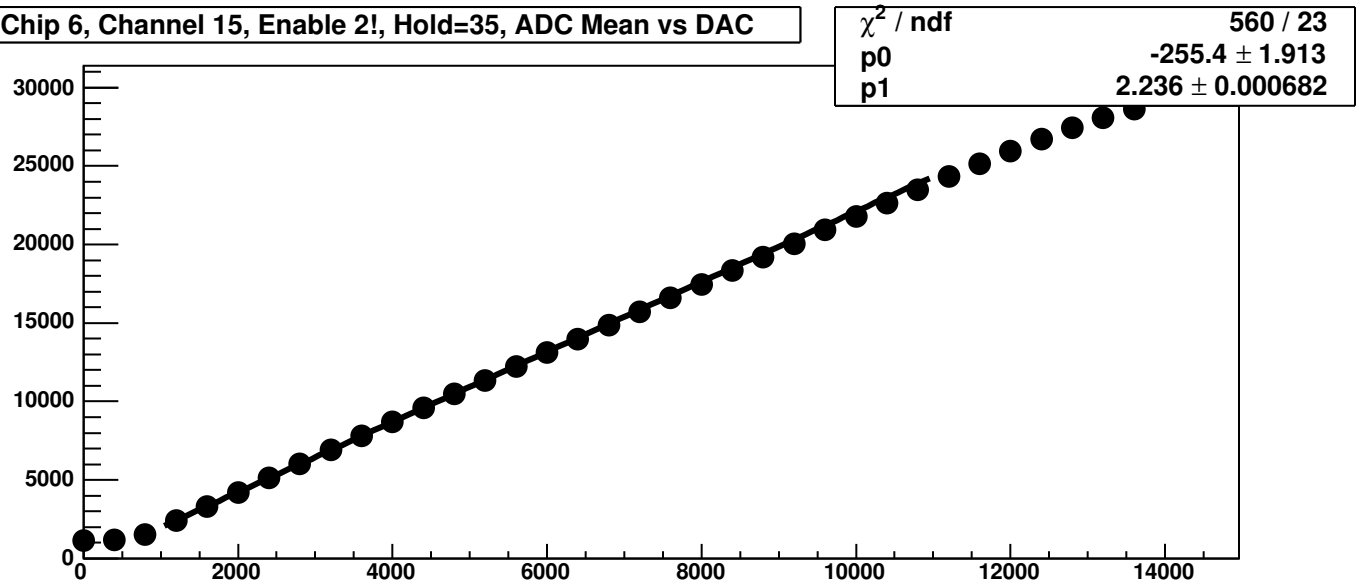

Chip 6, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 48.49 / 23
p0 -287.2 ± 0.762
p1 0.02146 ± 0.0001226
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

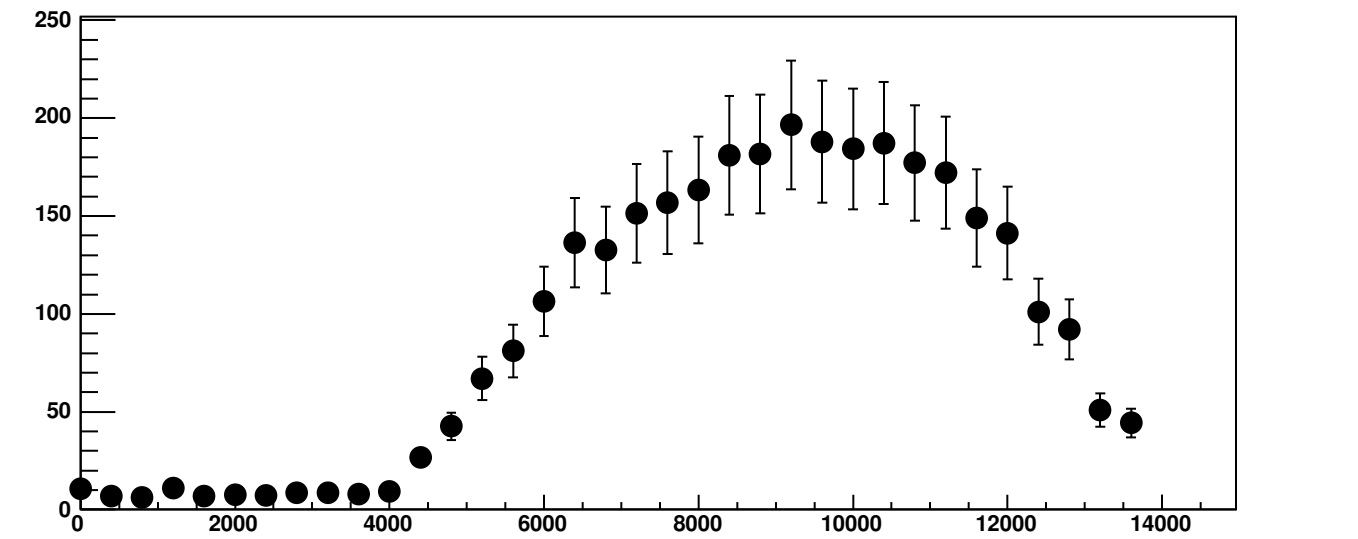

Chip 6, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

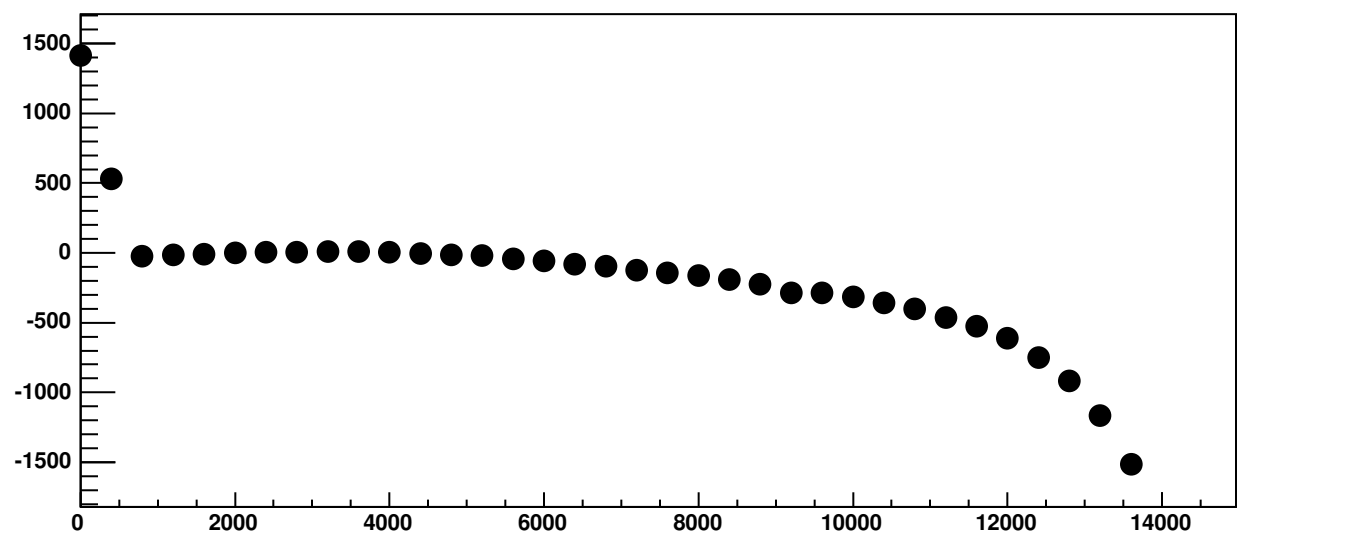
Chip 6, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC

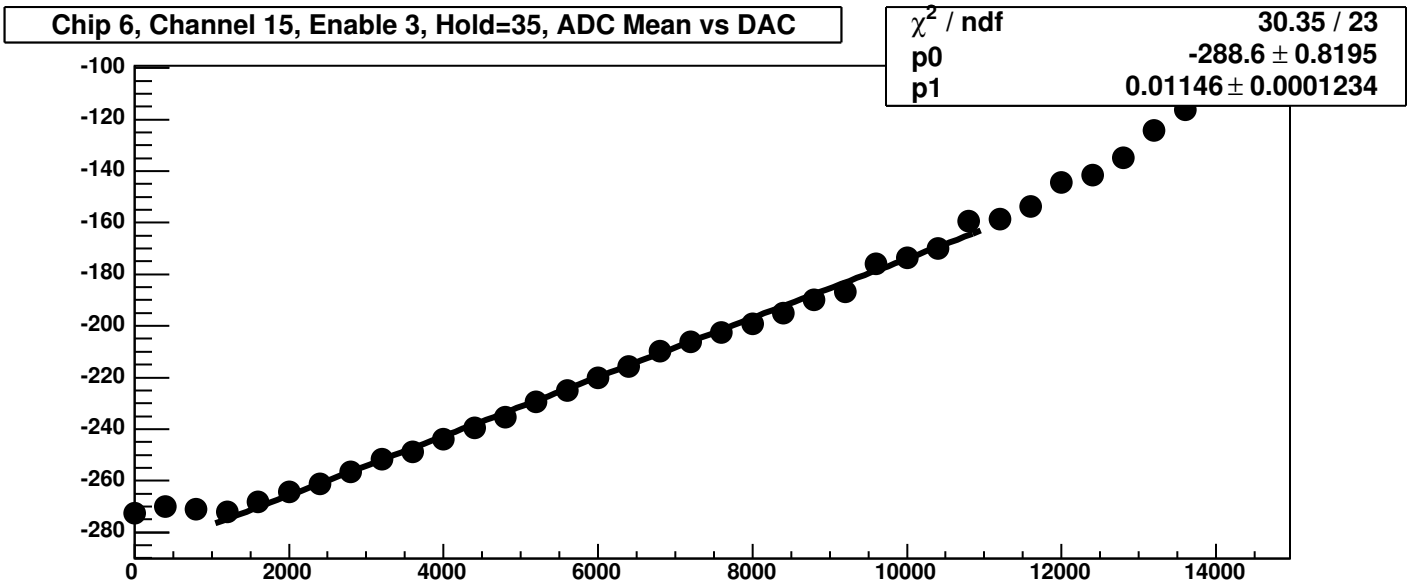


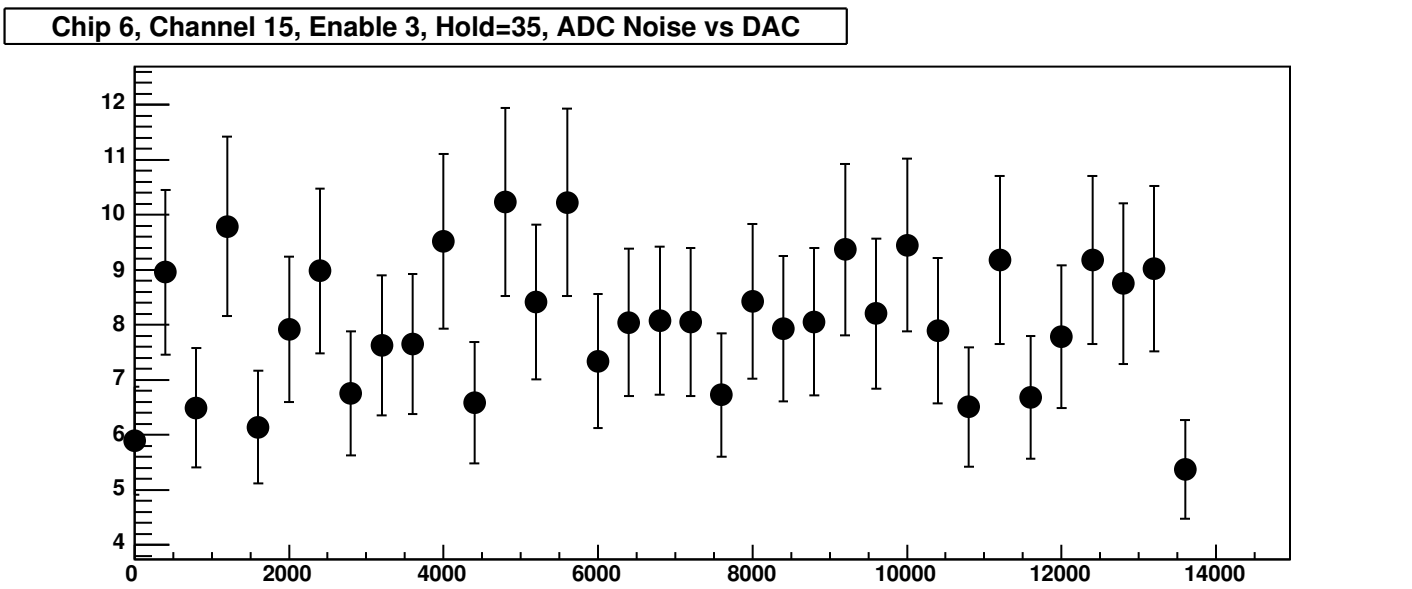
Chip 6, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC

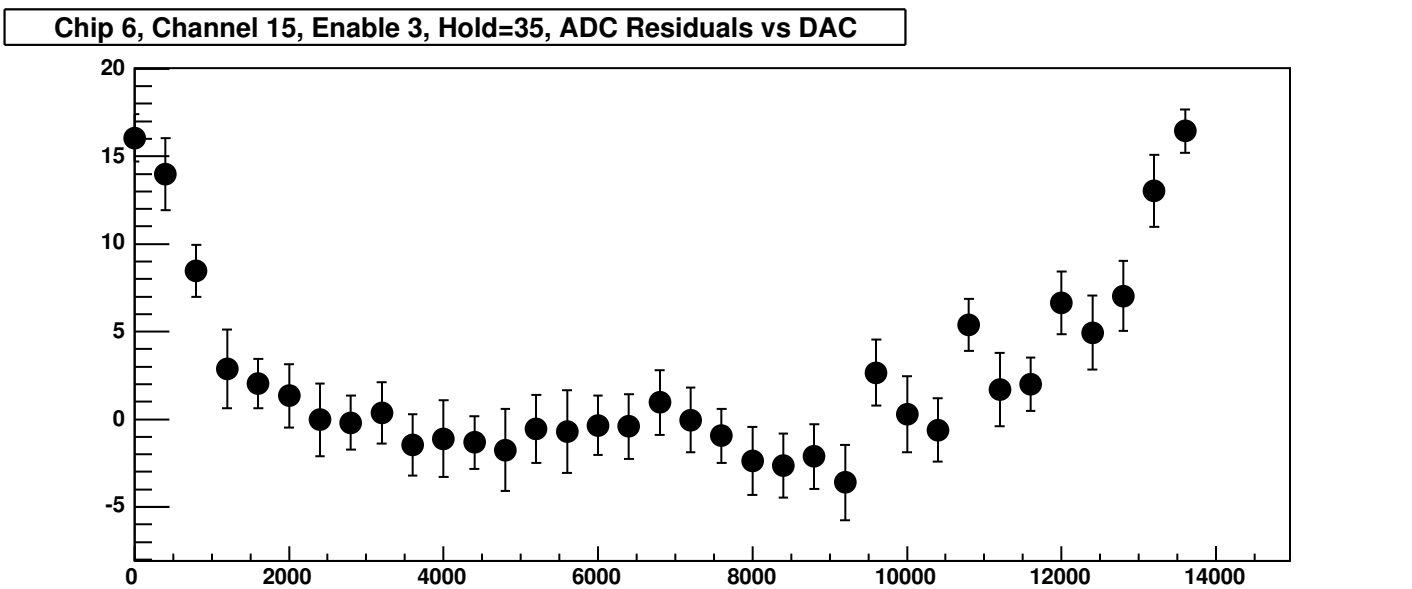


Chip 6, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC

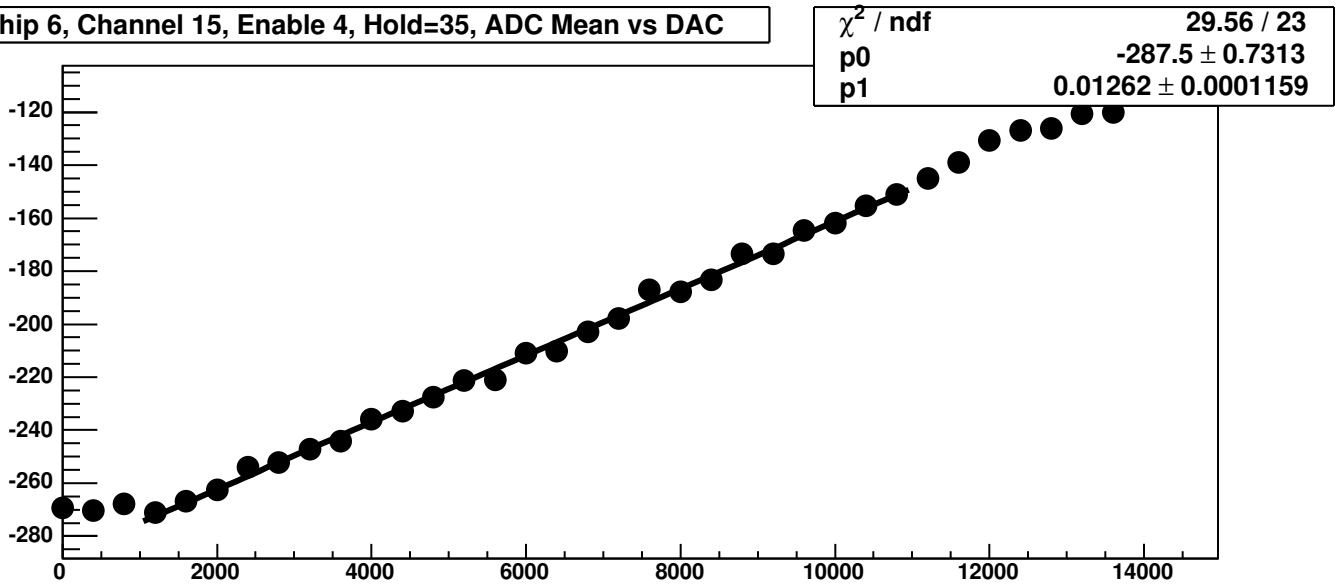



Chip 6, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 30.35 / 23
p0 -288.6 ± 0.8195
p1 0.01146 ± 0.0001234
-100
-120
-140
-160
-180
-200
-220
-240
-260
-280
0
2000
4000
6000
8000
10000
12000
14000

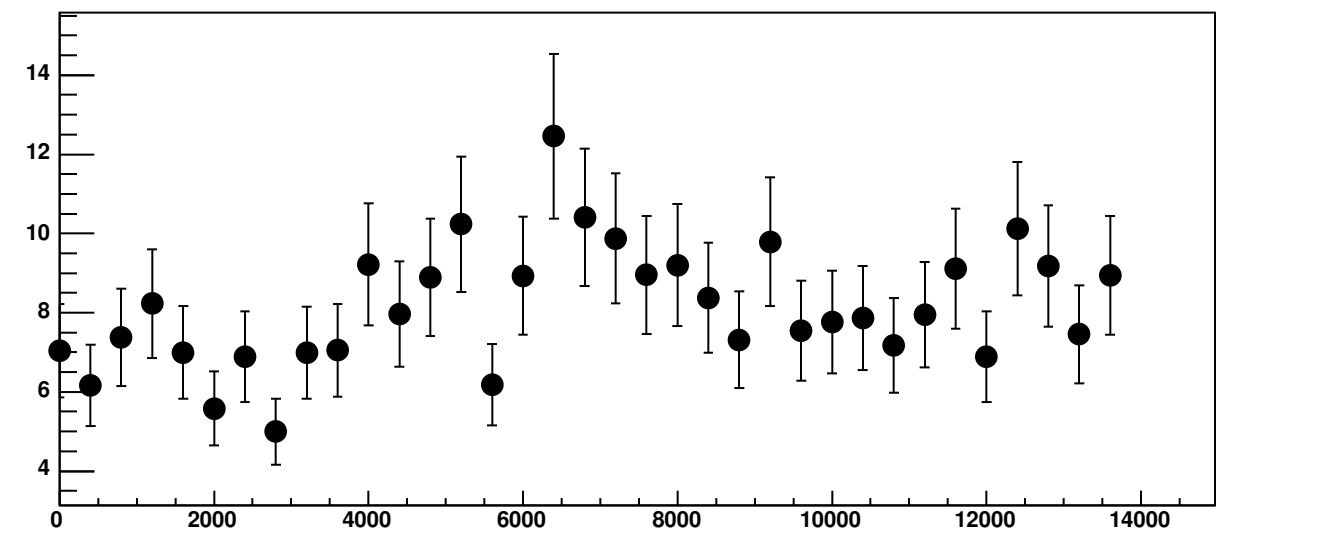

Chip 6, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

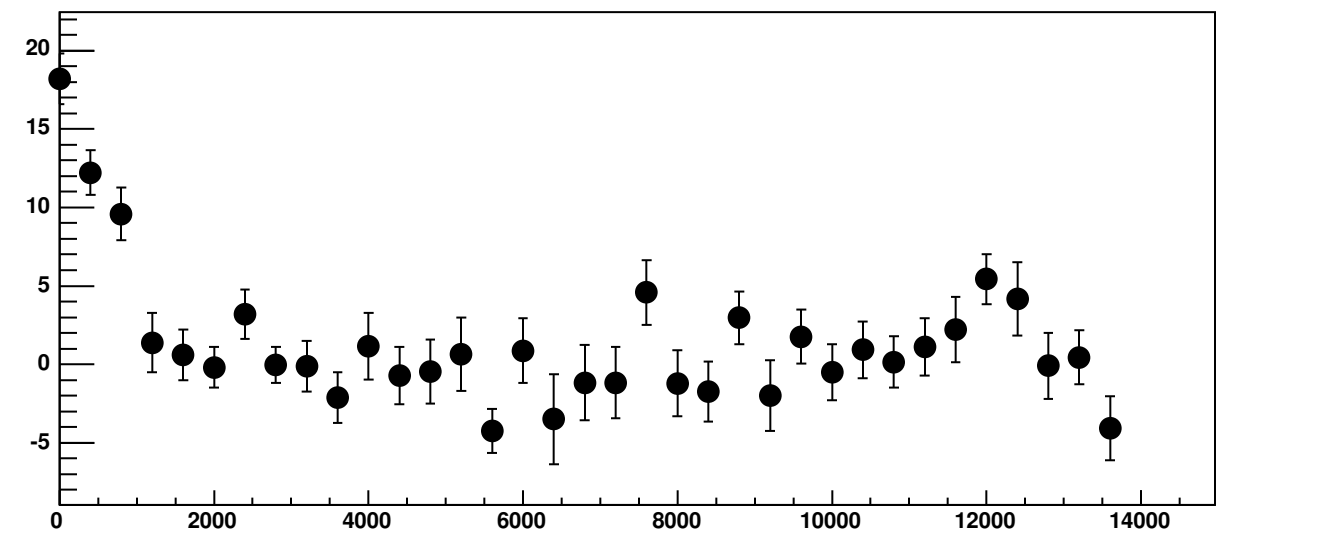
Chip 6, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

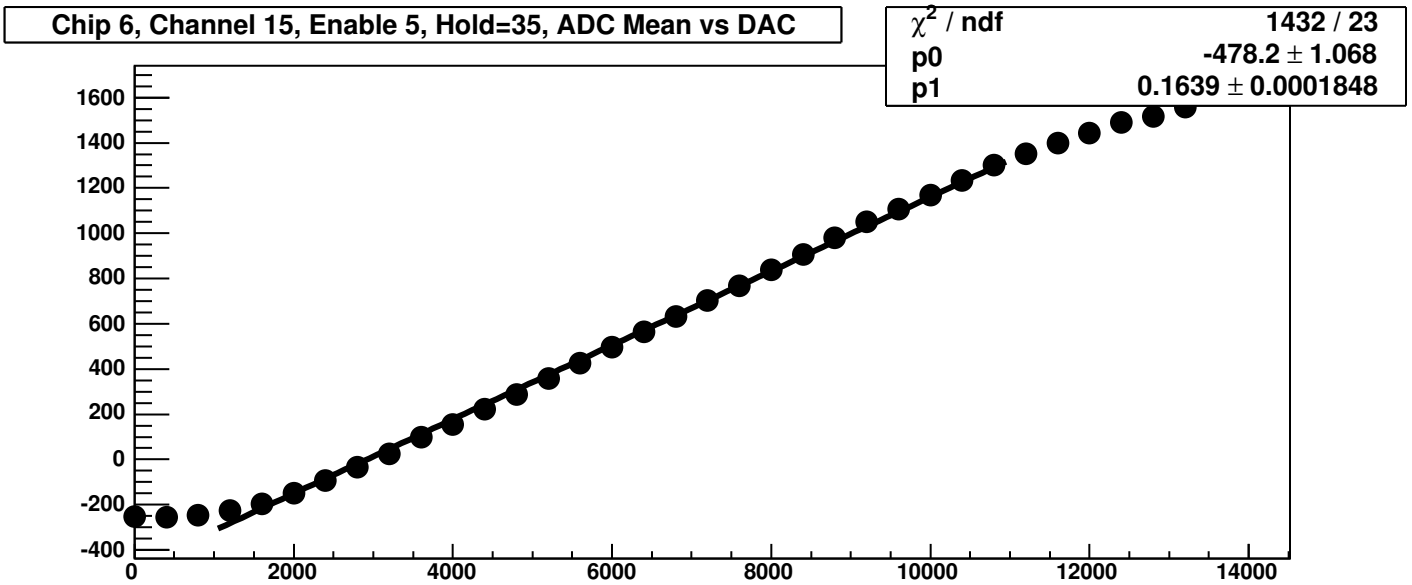


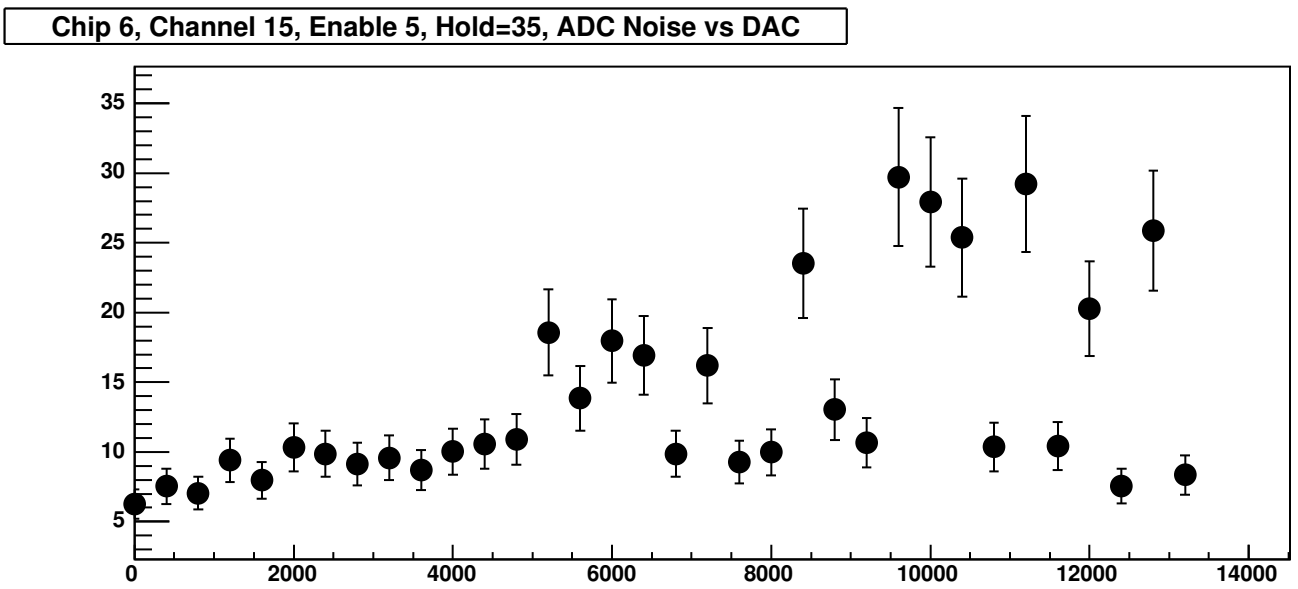
Chip 6, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC

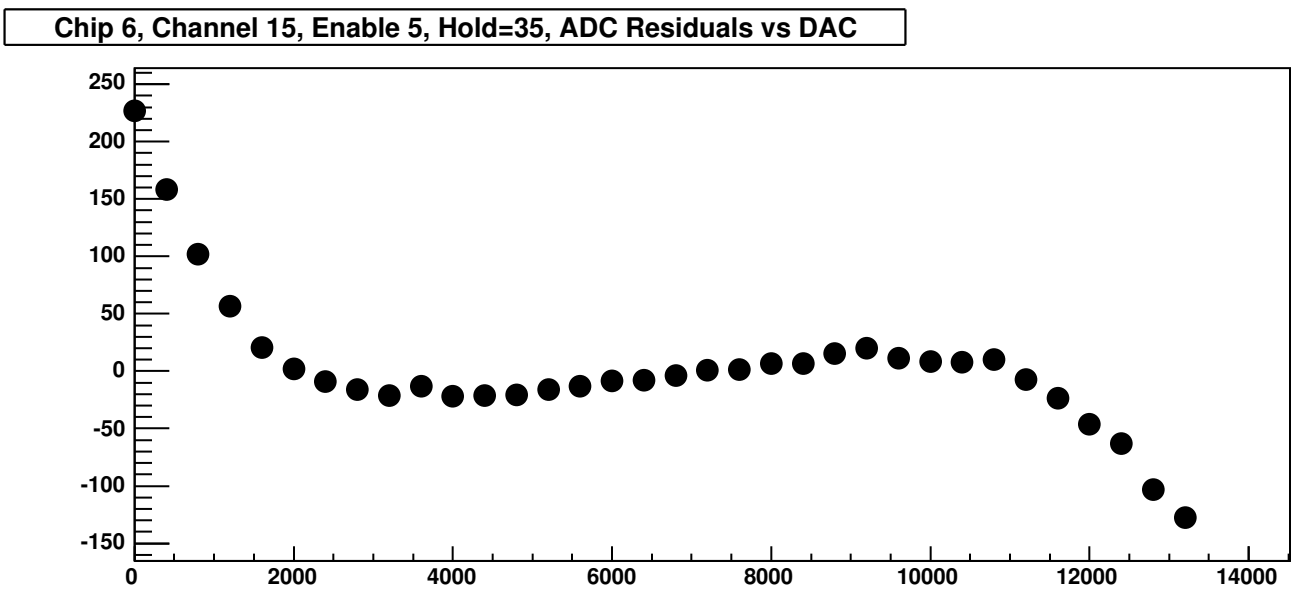


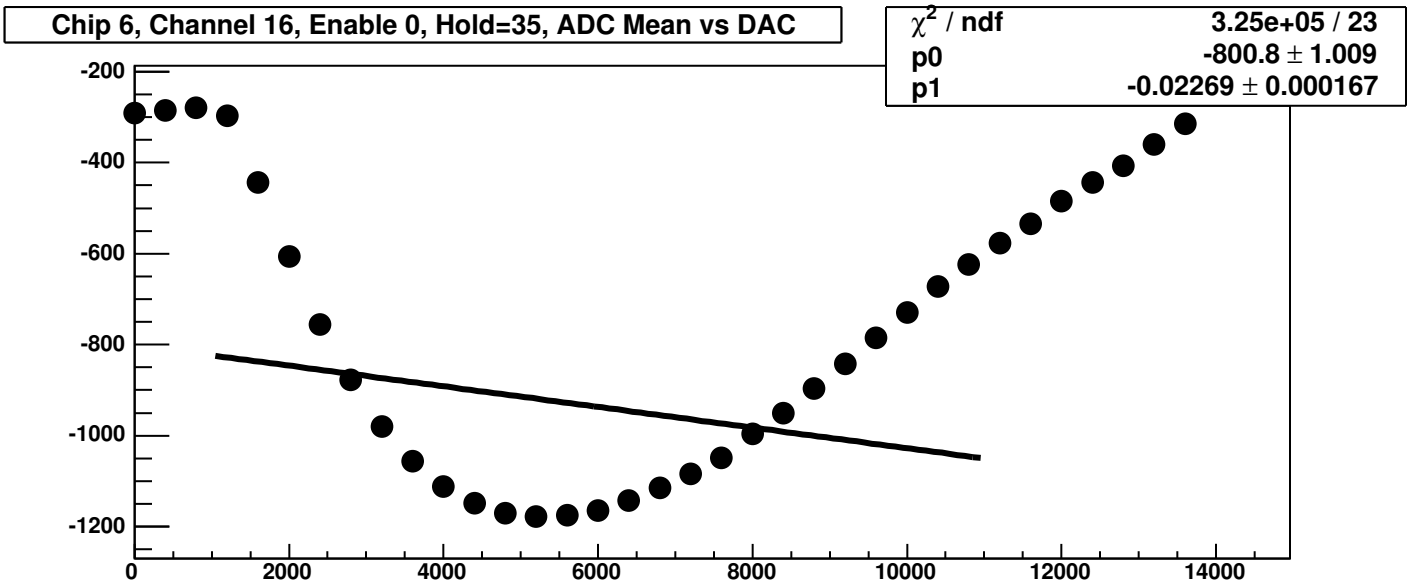
Chip 6, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

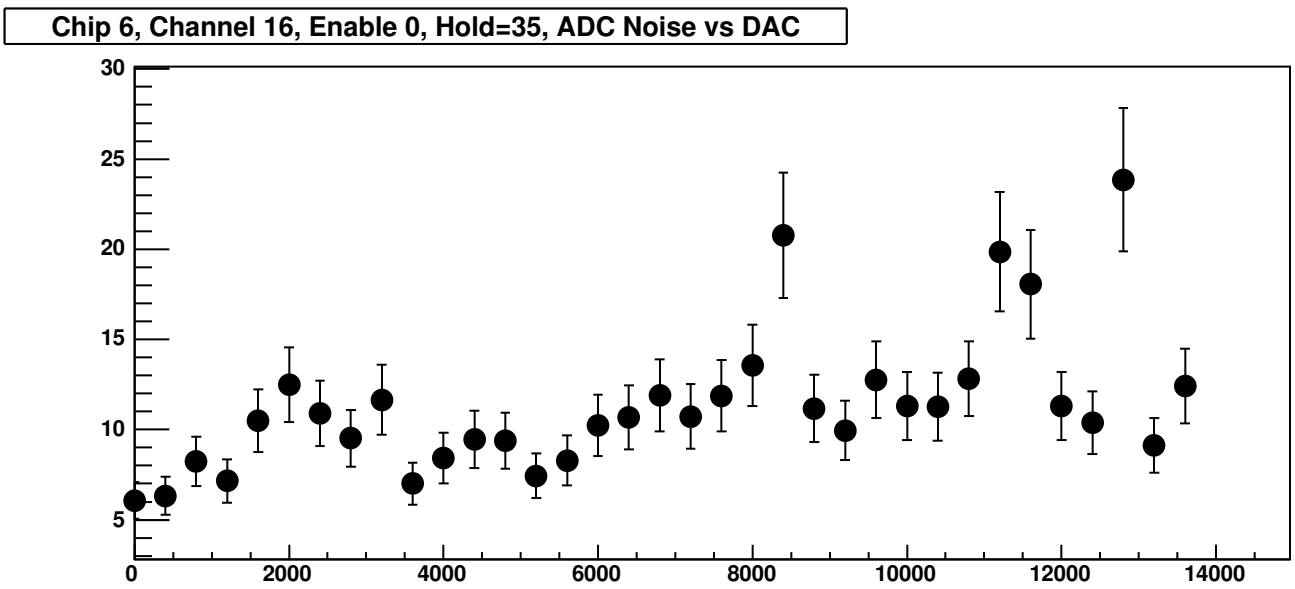


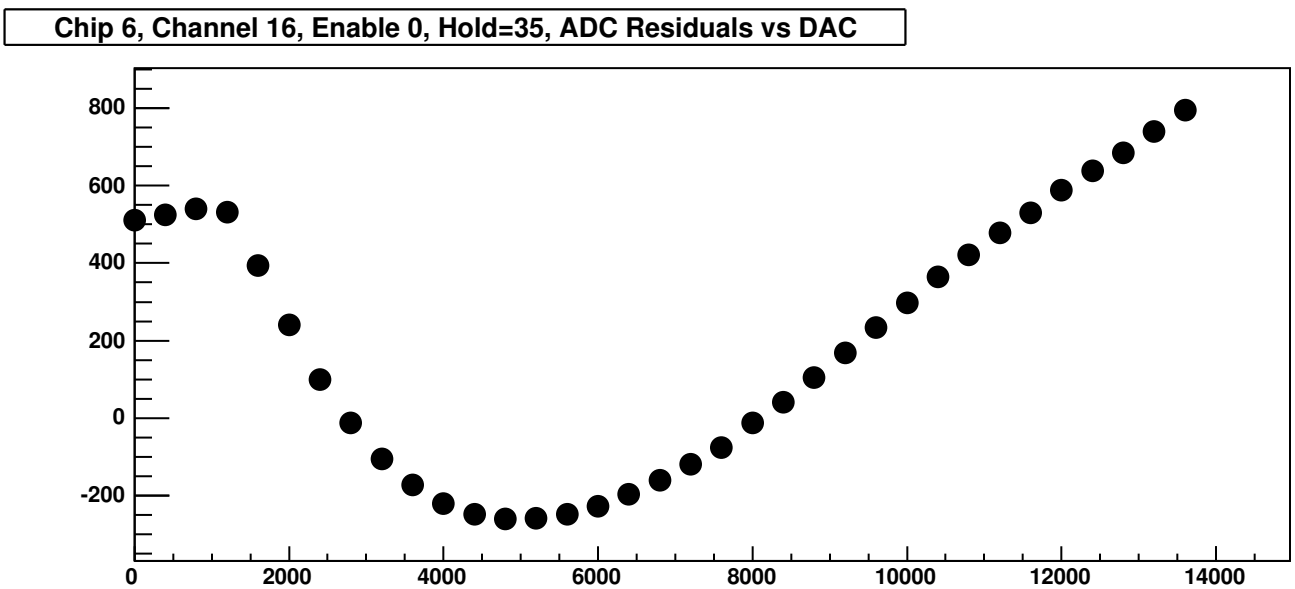

Chip 6, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1432 / 23
p0 -478.2 ± 1.068
p1 0.1639 ± 0.0001848
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

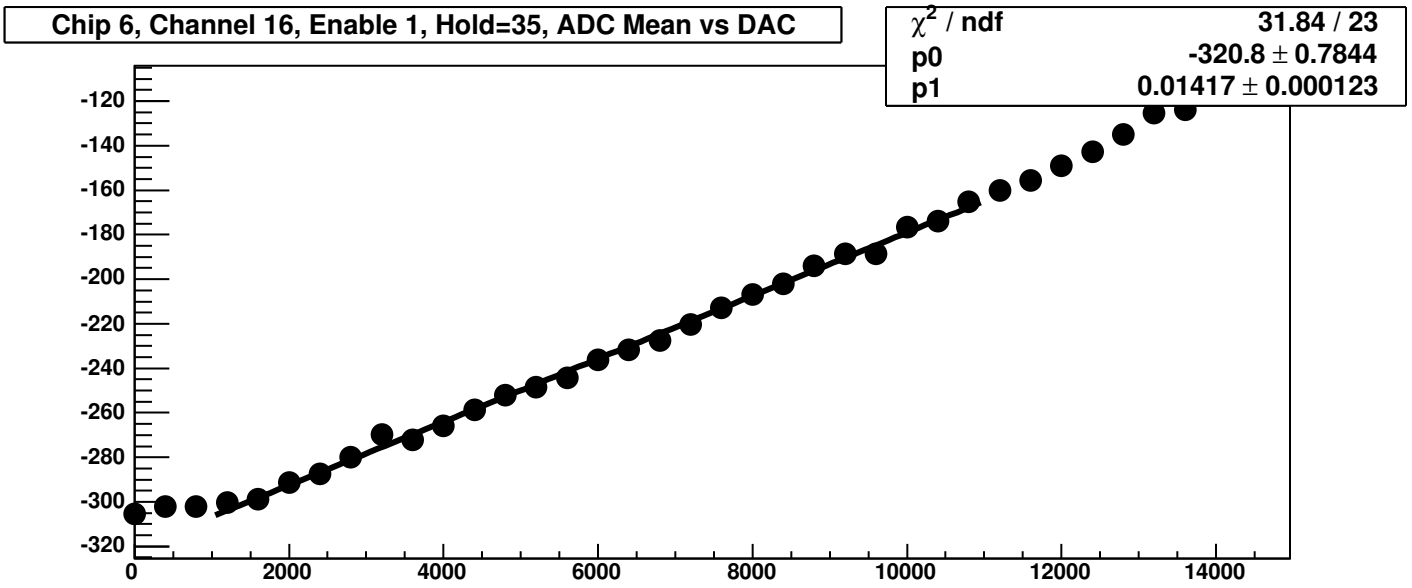

Chip 6, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

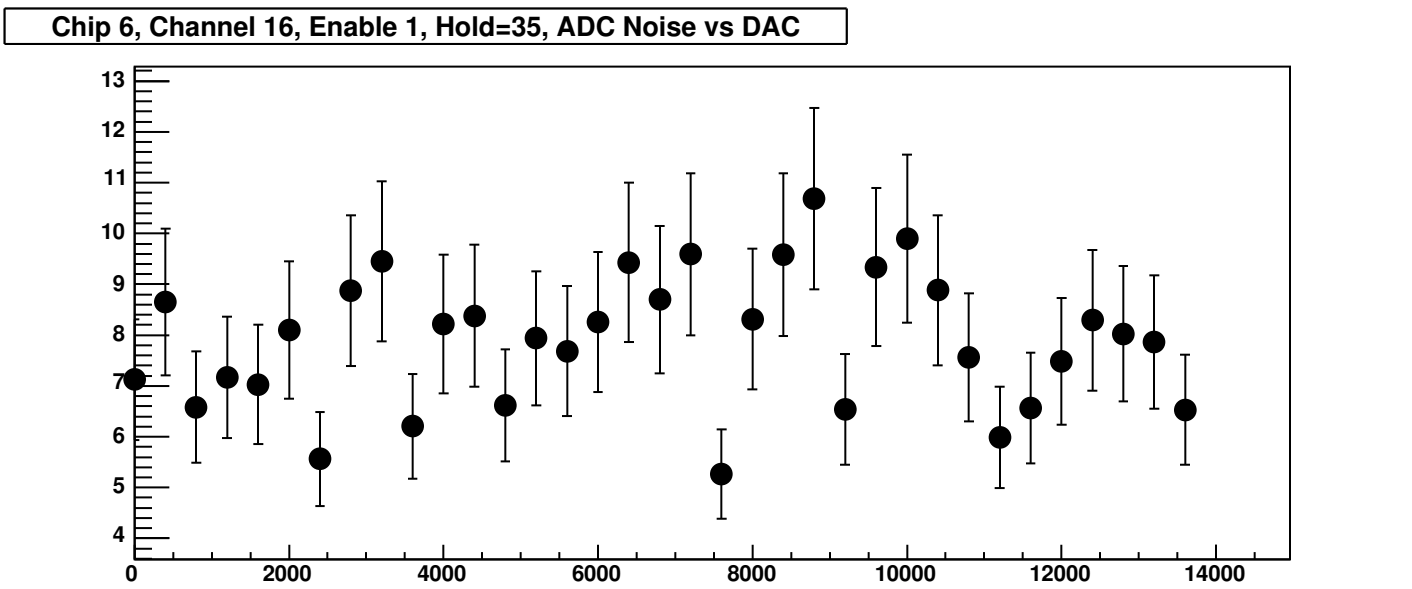

Chip 6, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

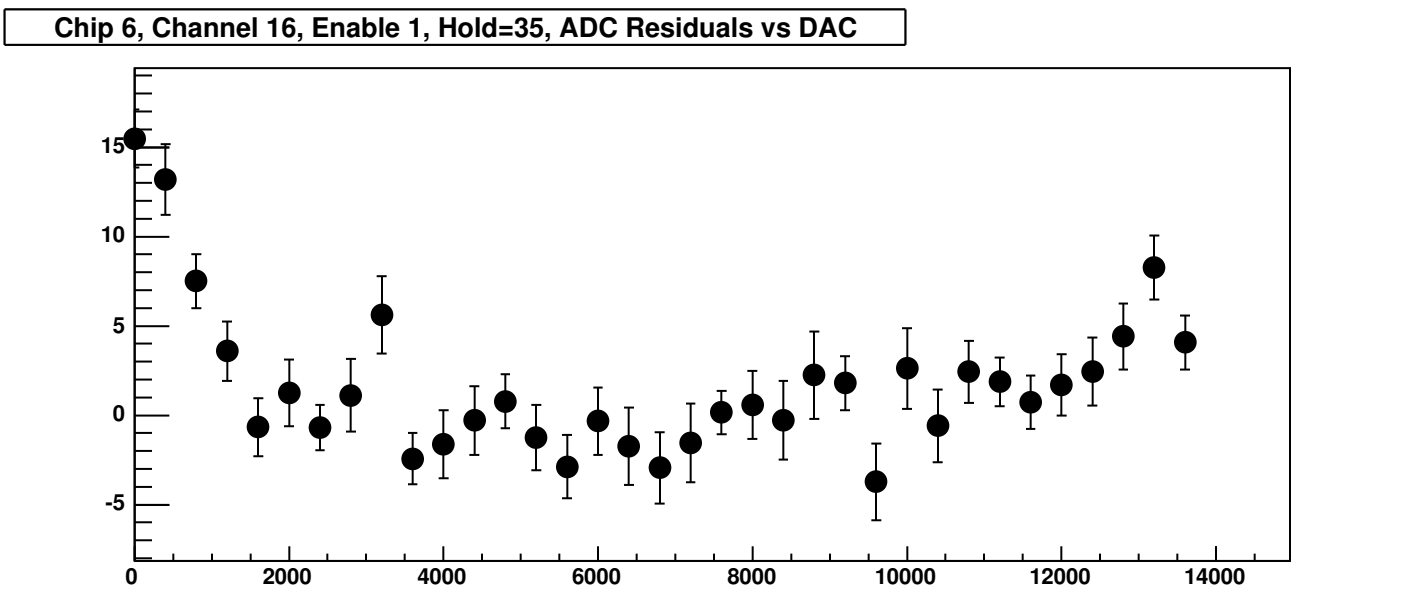

Chip 6, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.25e+05 / 23
p0 -800.8 ± 1.009
p1 -0.02269 ± 0.000167
-200
-400
-600
-800
-1000
-1200
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

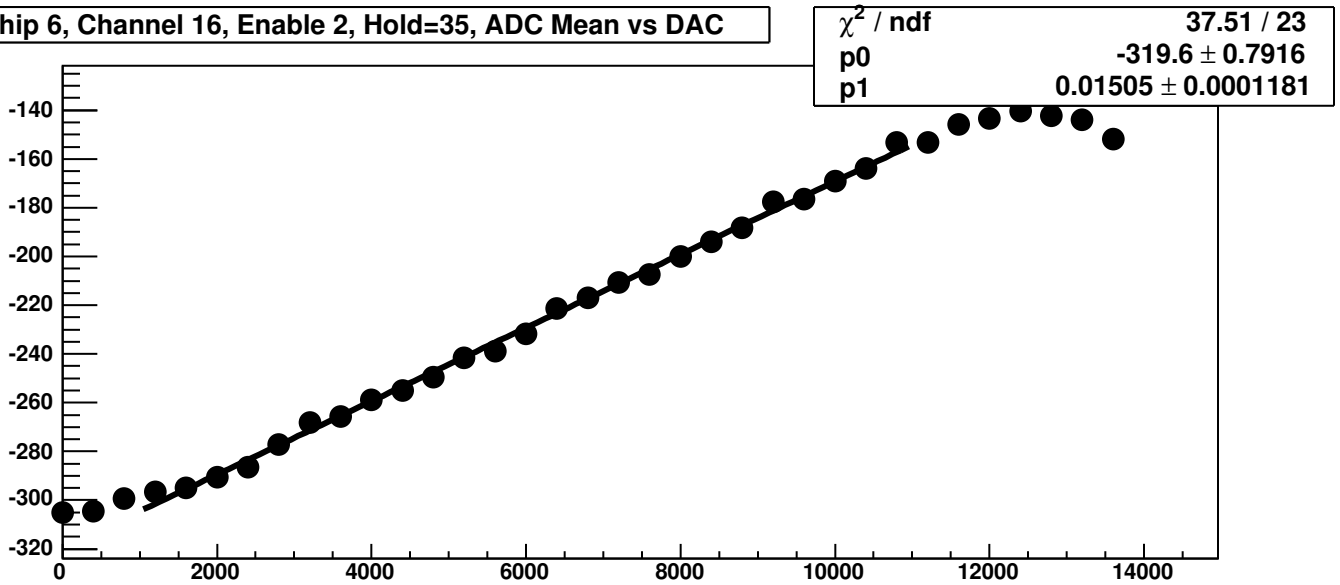

Chip 6, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.84 / 23
p0 -320.8 ± 0.7844
p1 0.01417 ± 0.000123
-120
-140
-160
-180
-200
-220
-240
-260
-280
-300
-320
0
2000
4000
6000
8000
10000
12000
14000

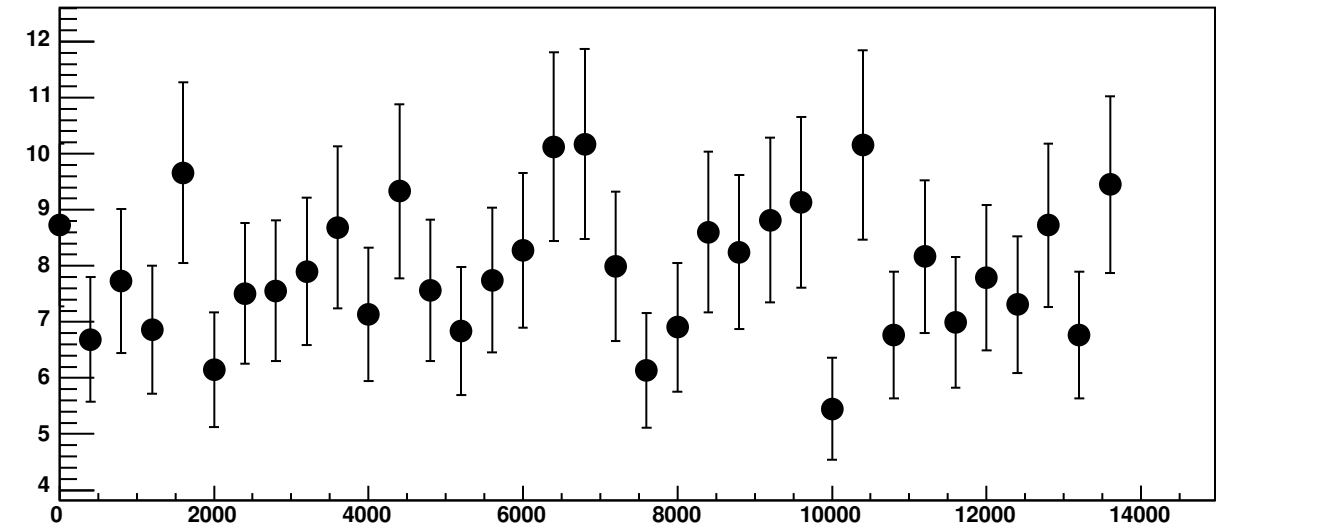

Chip 6, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

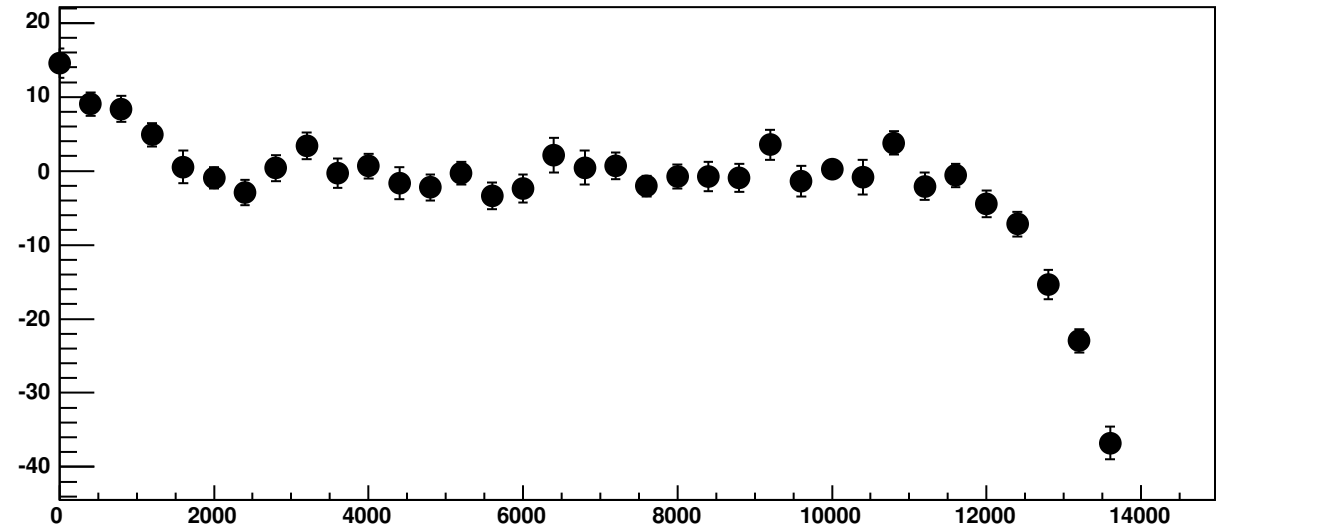
Chip 6, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

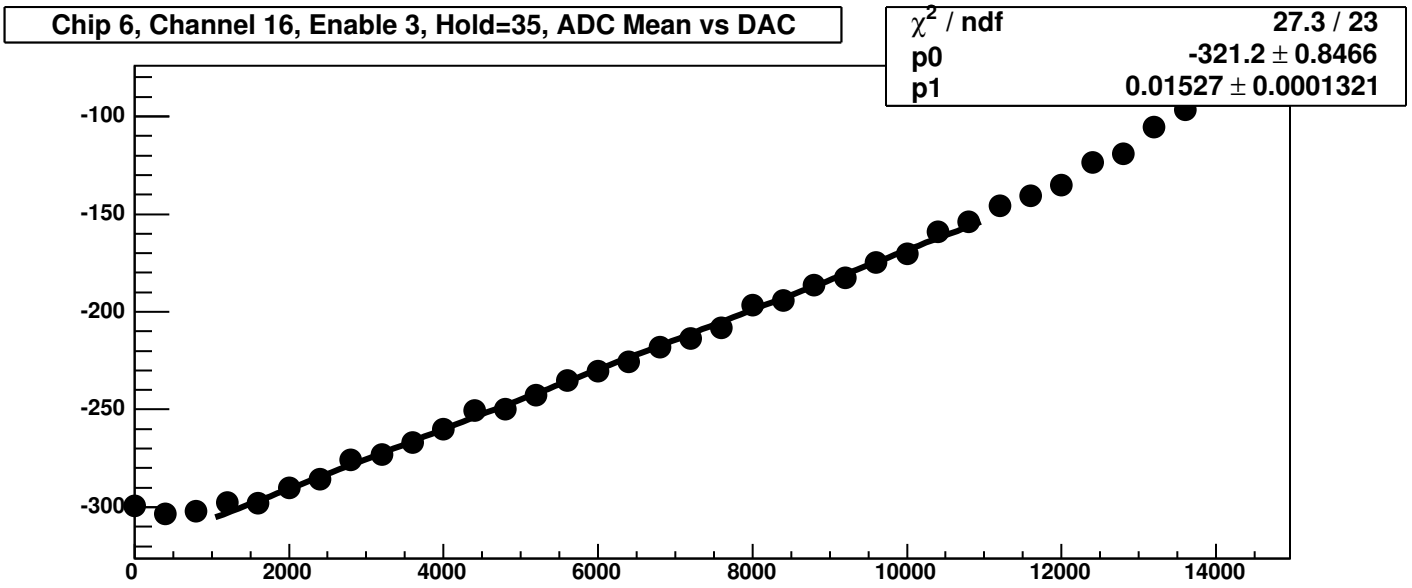


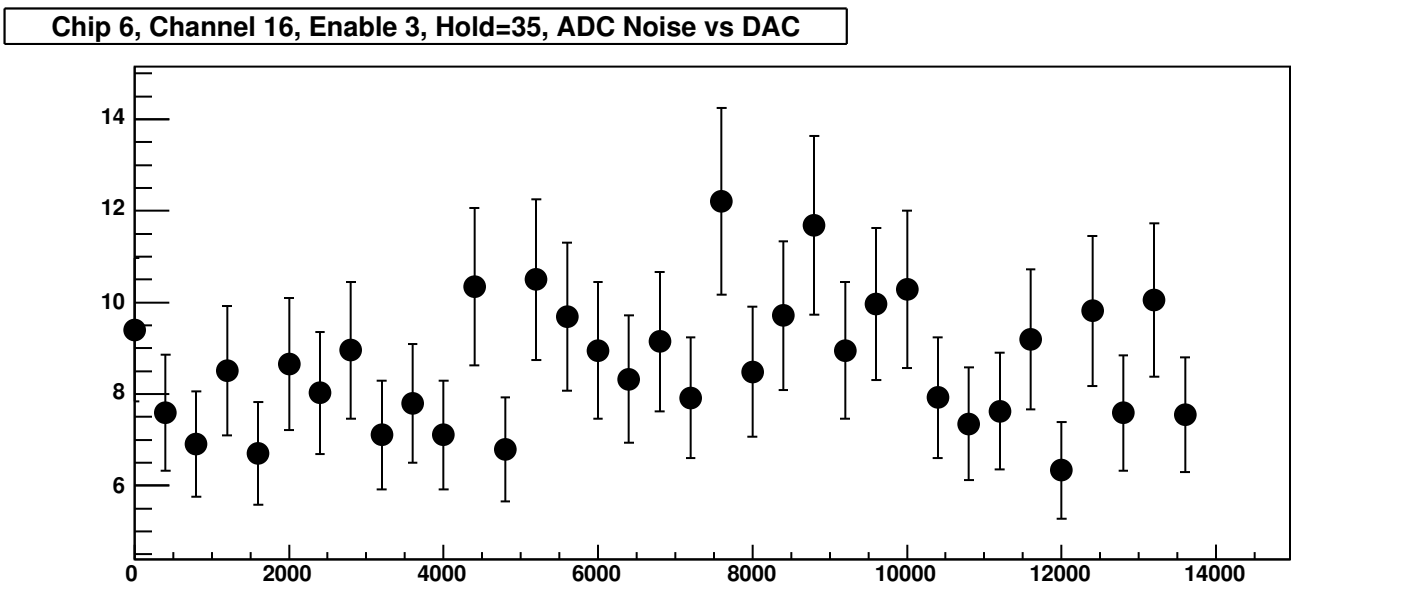
Chip 6, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC

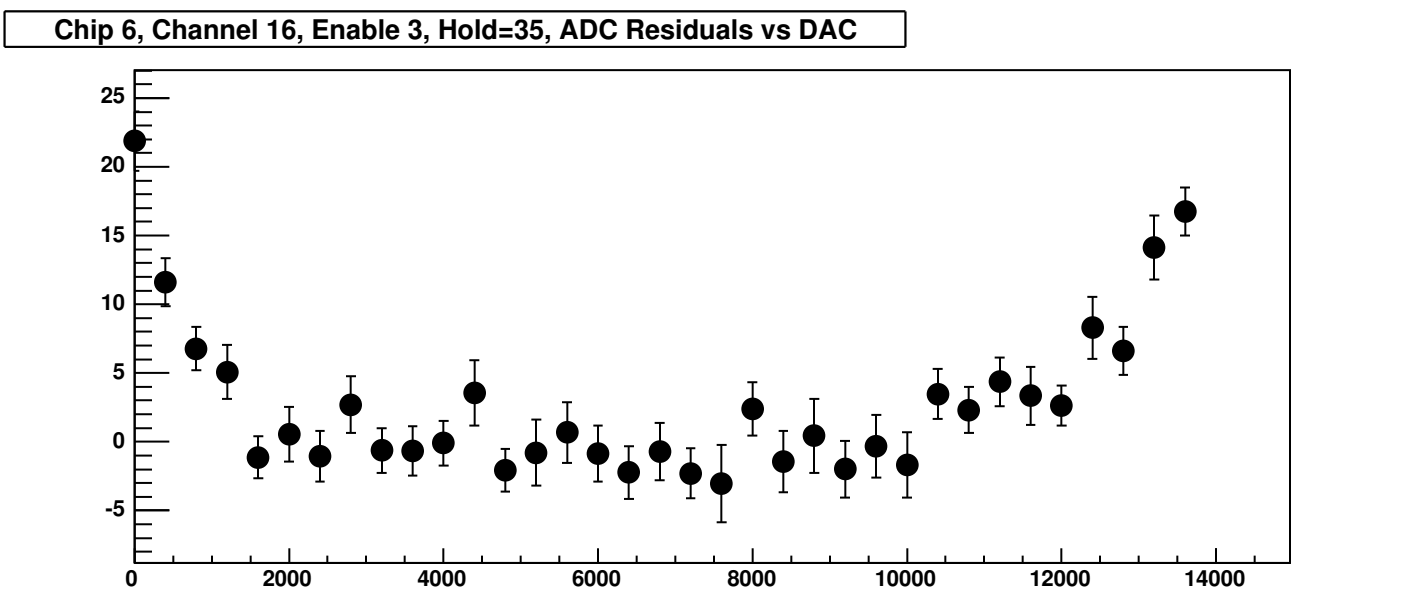


Chip 6, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

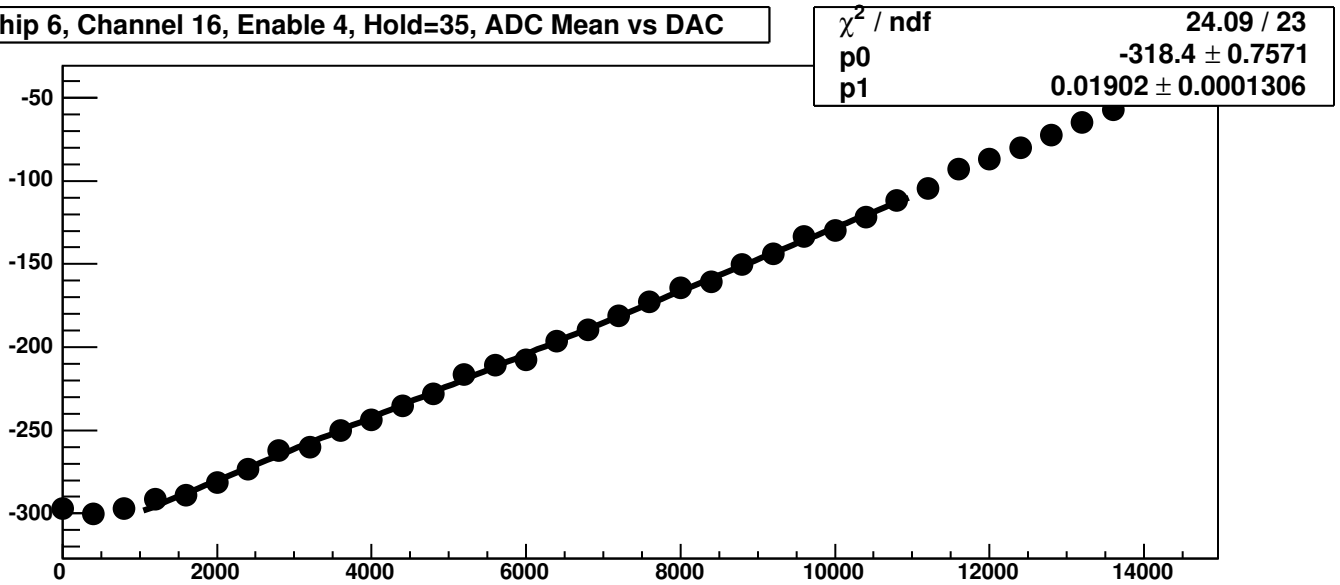



Chip 6, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 27.3 / 23
p0 -321.2 ± 0.8466
p1 0.01527 ± 0.0001321
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

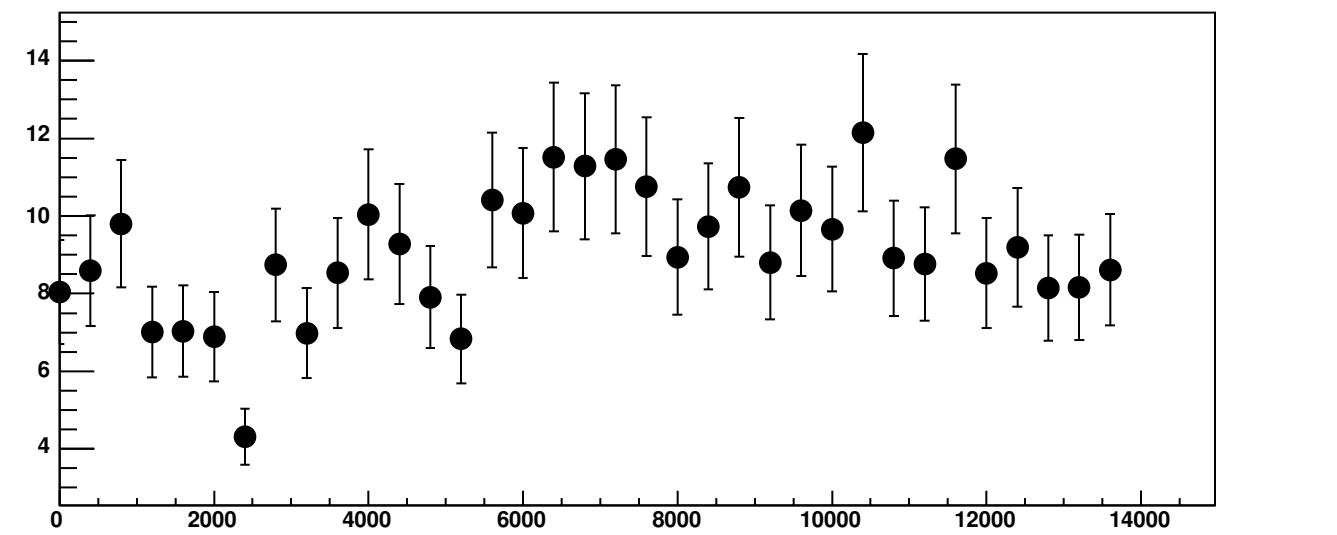

Chip 6, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

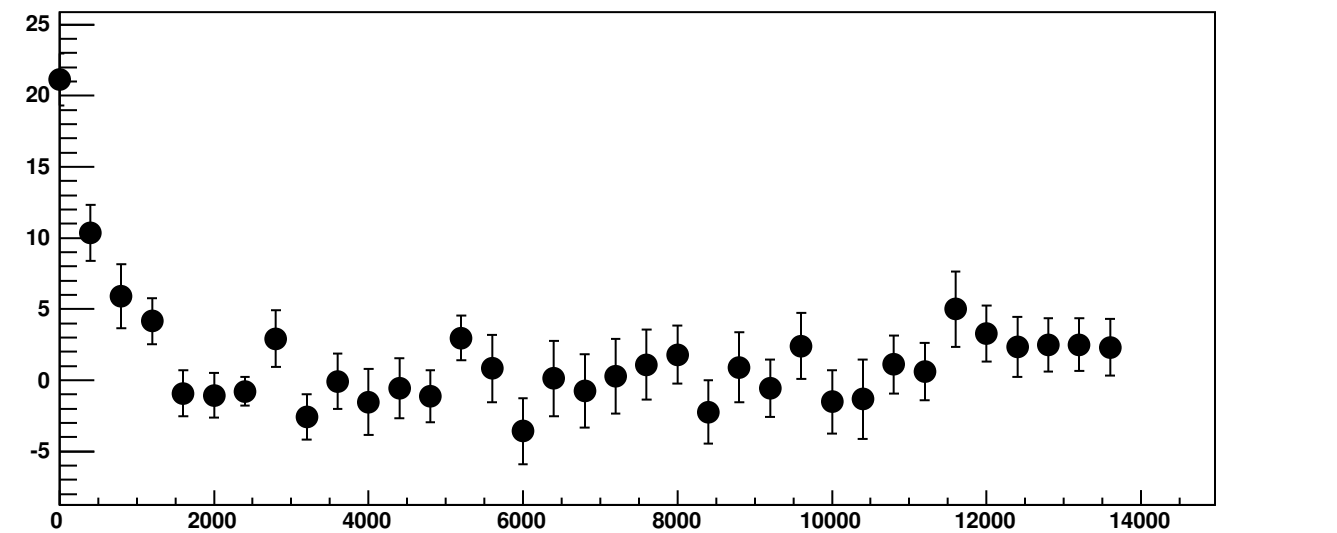
Chip 6, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

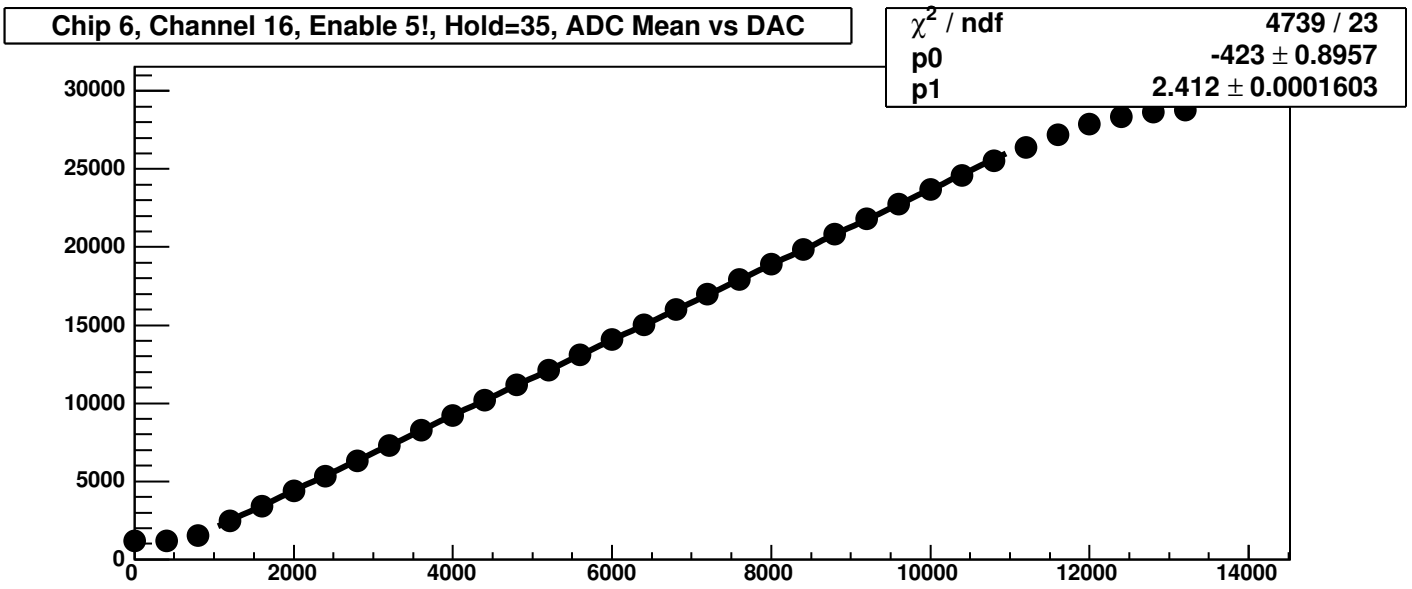


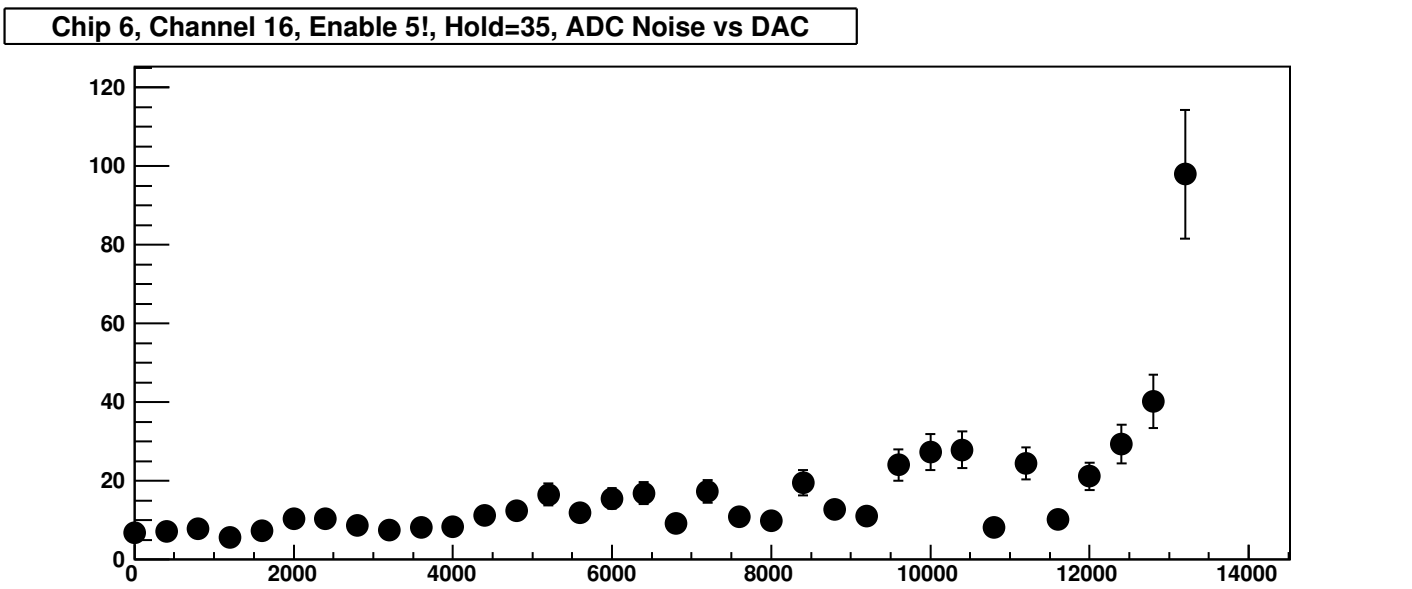
Chip 6, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC

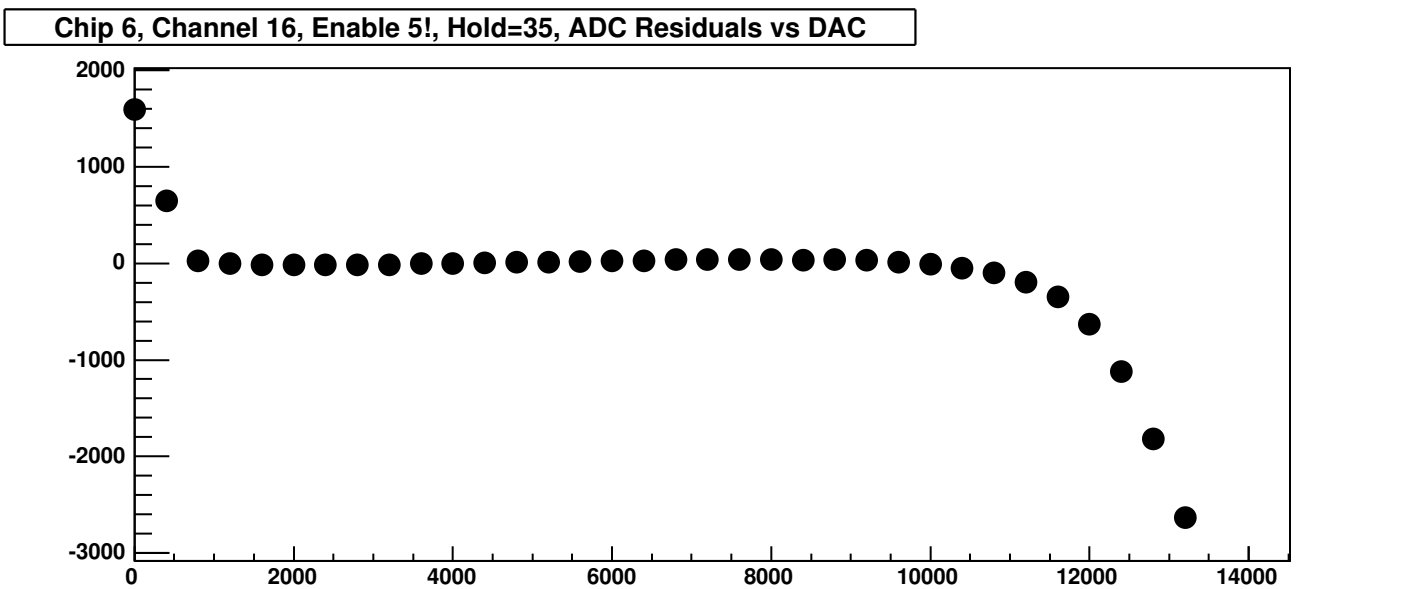


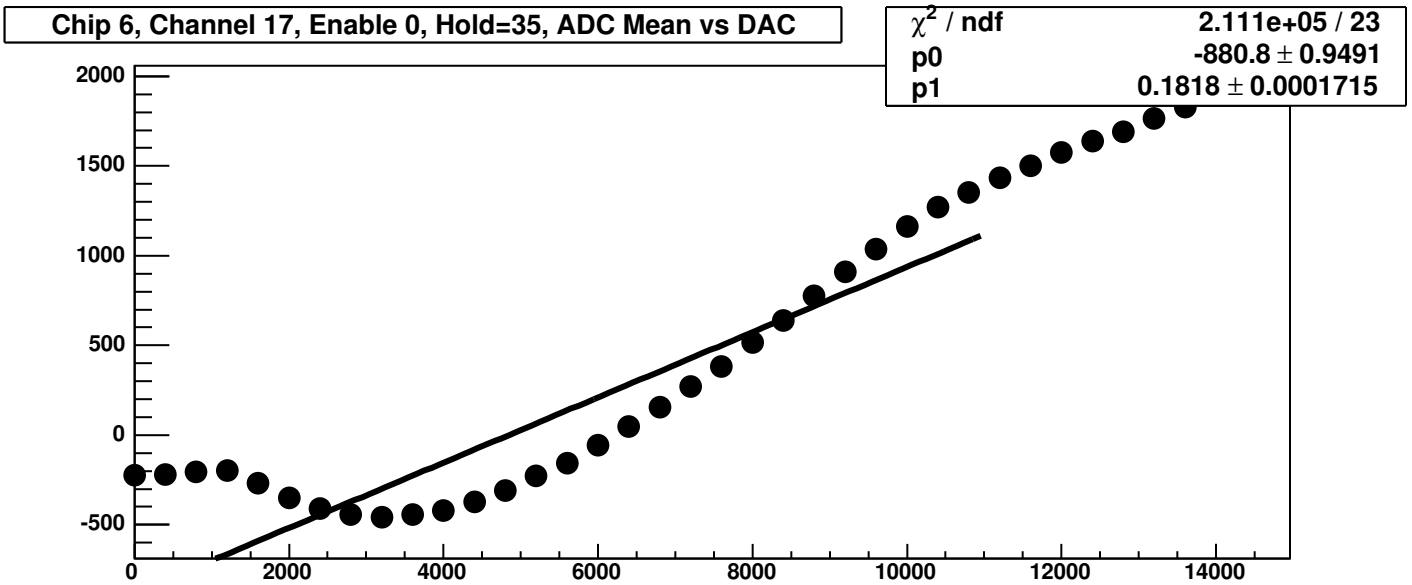
Chip 6, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

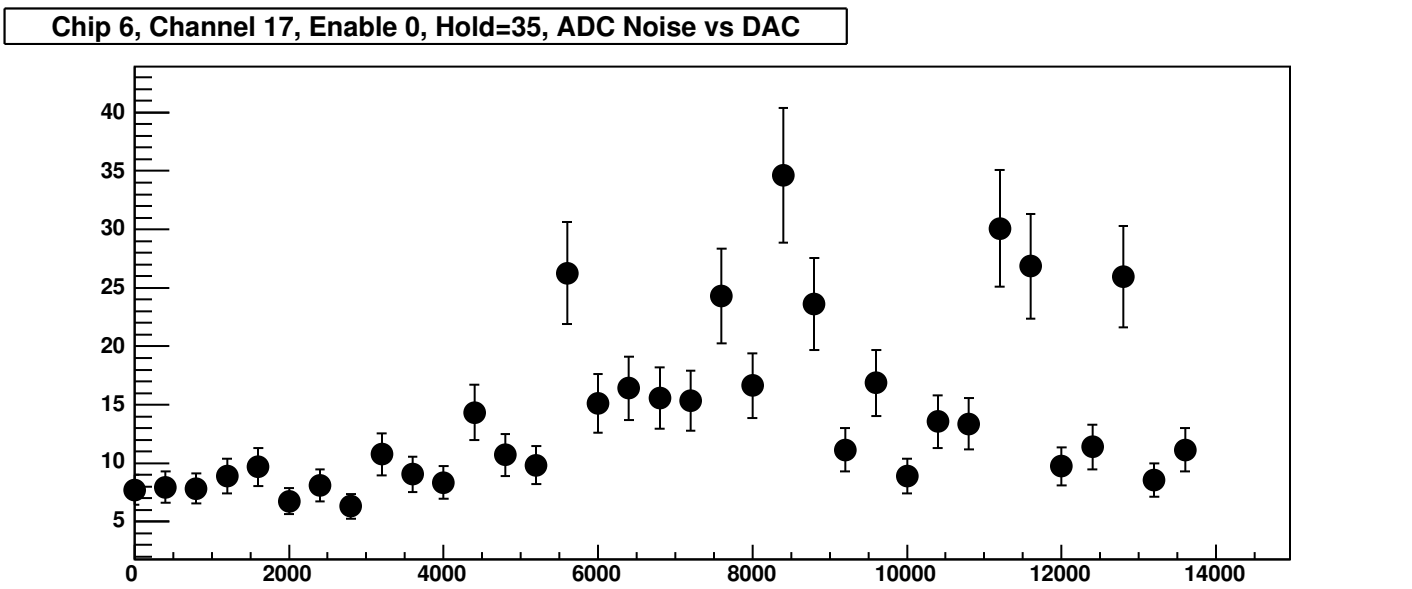


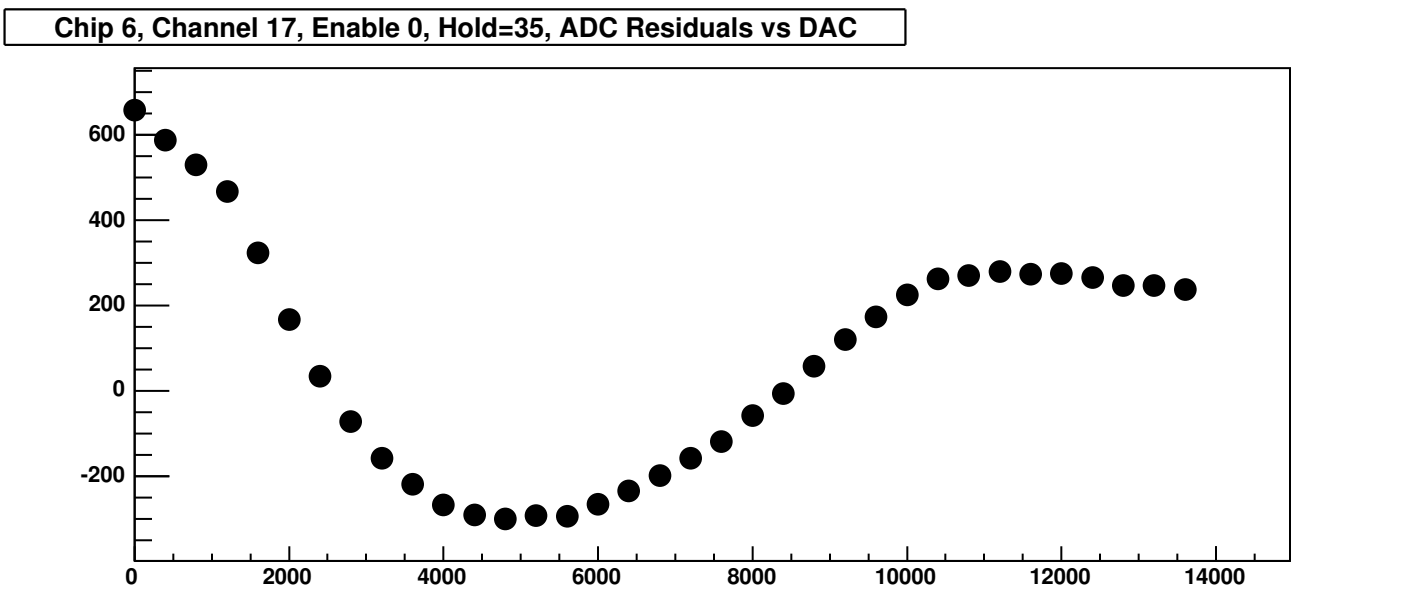

Chip 6, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4739 / 23
p0 -423 ± 0.8957
p1 2.412 ± 0.0001603
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

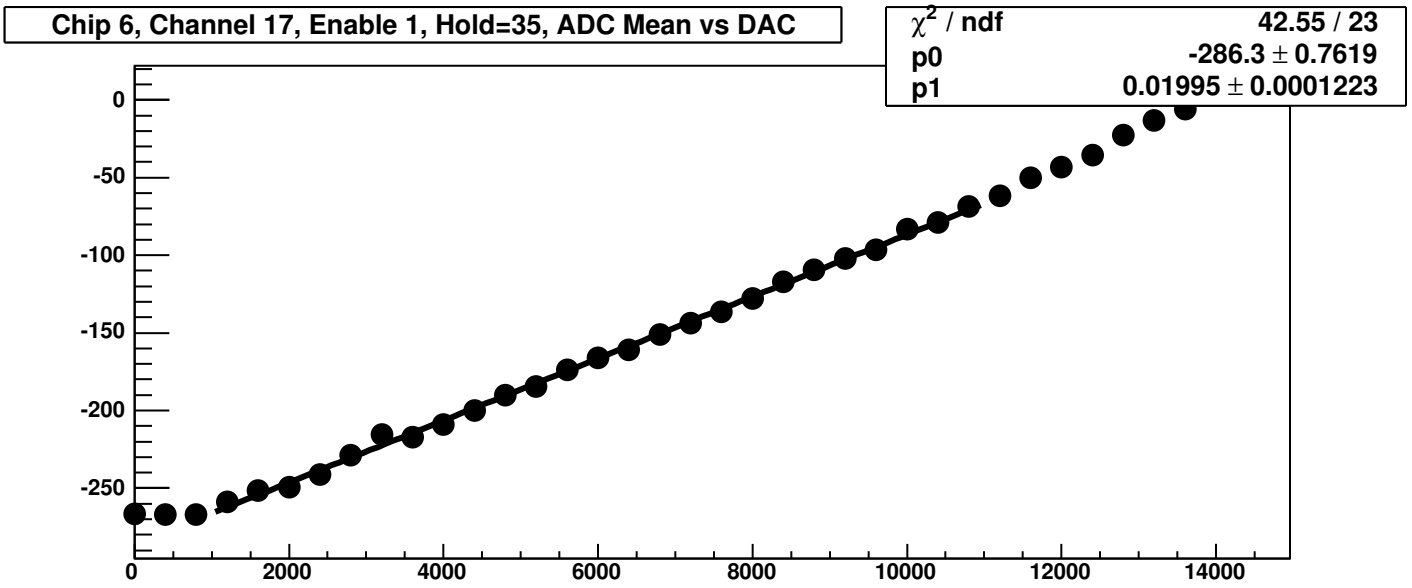

Chip 6, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

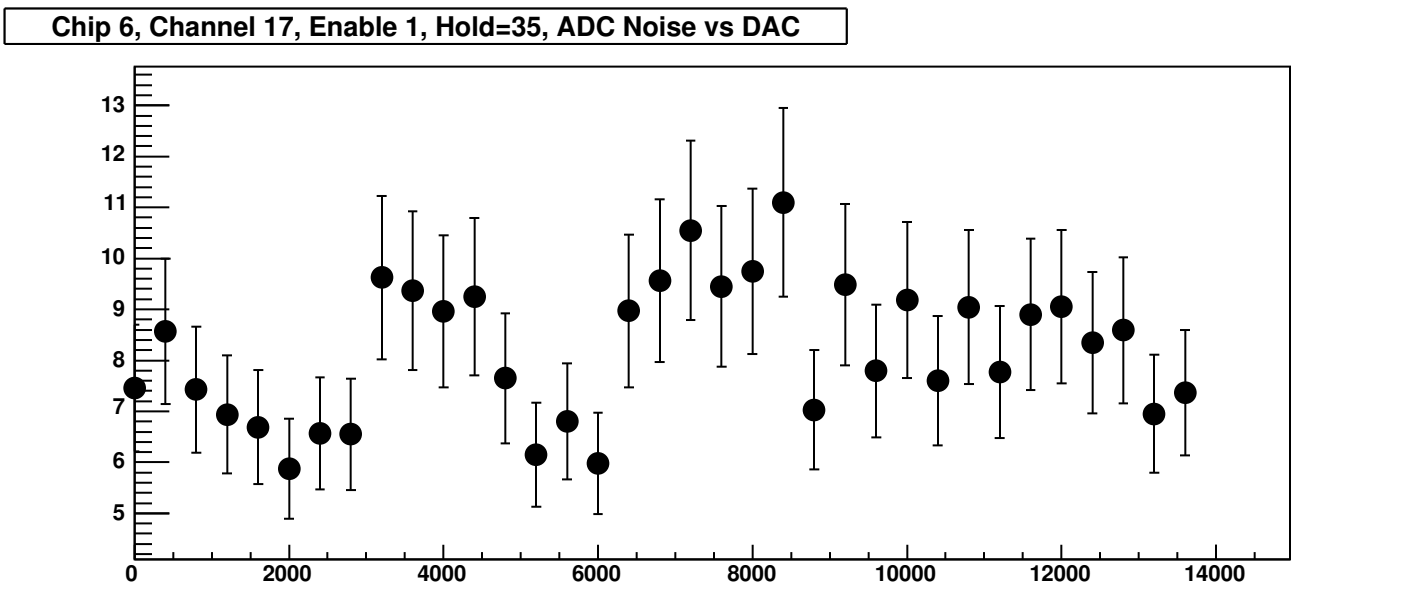

Chip 6, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

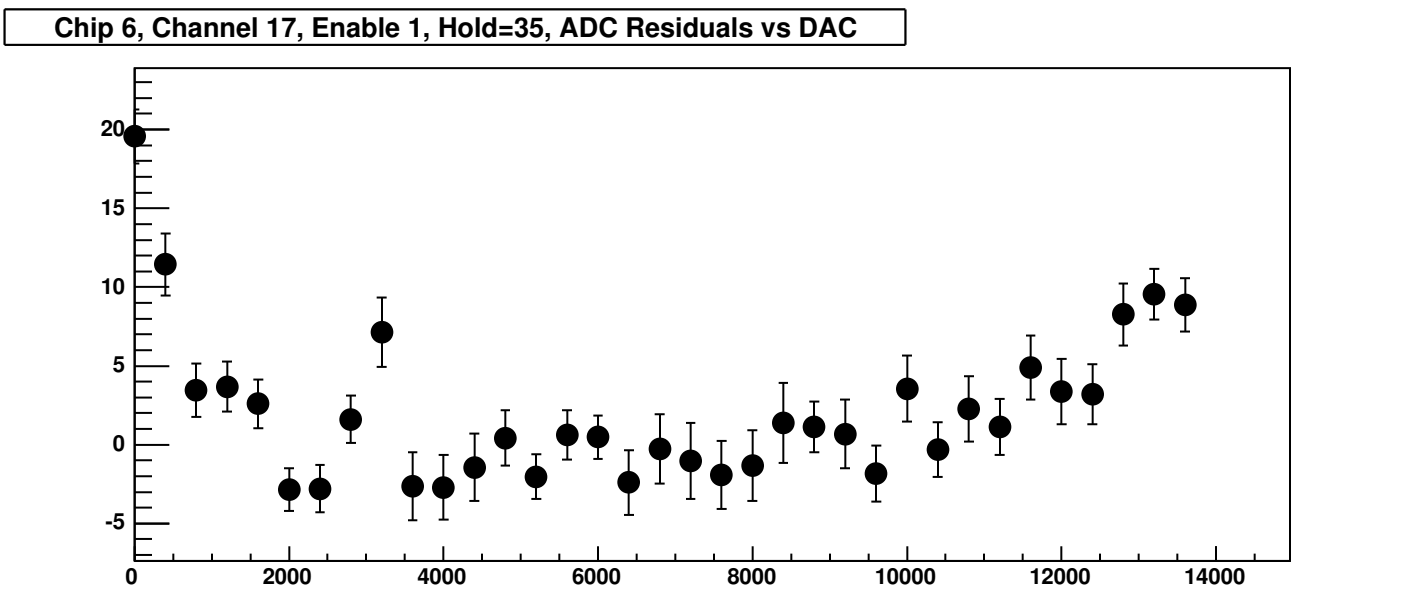

Chip 6, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.111e+05 / 23
p0 -880.8 ± 0.9491
p1 0.1818 ± 0.0001715
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

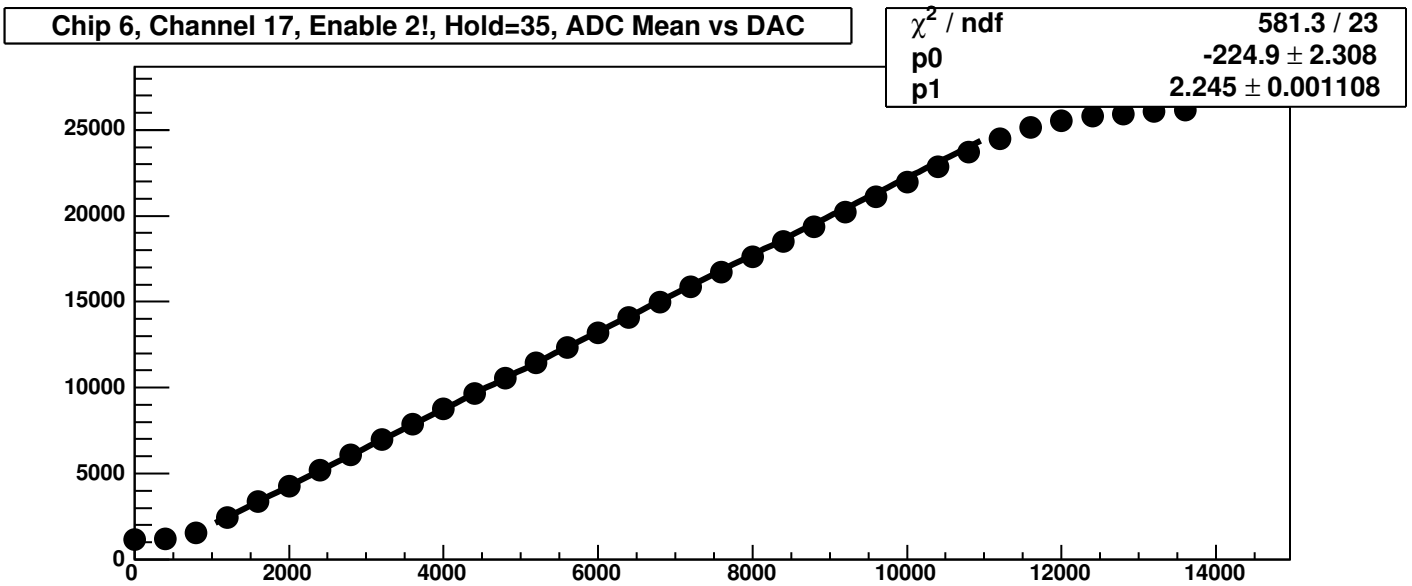

Chip 6, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

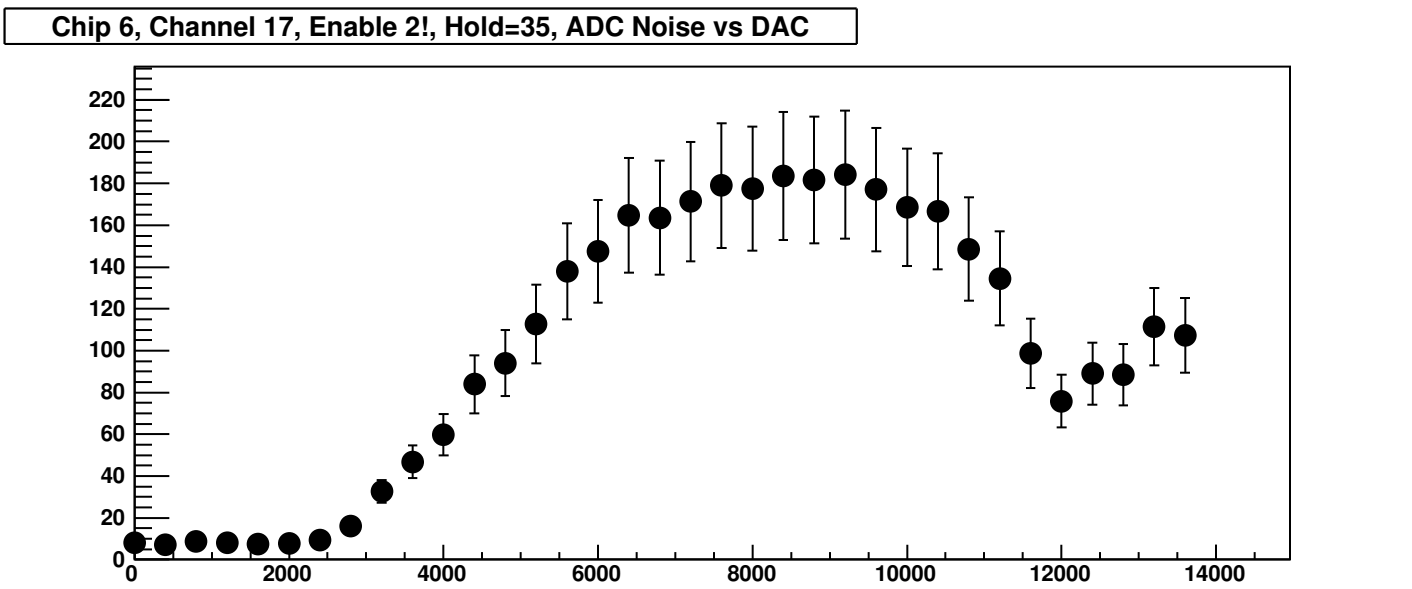

Chip 6, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

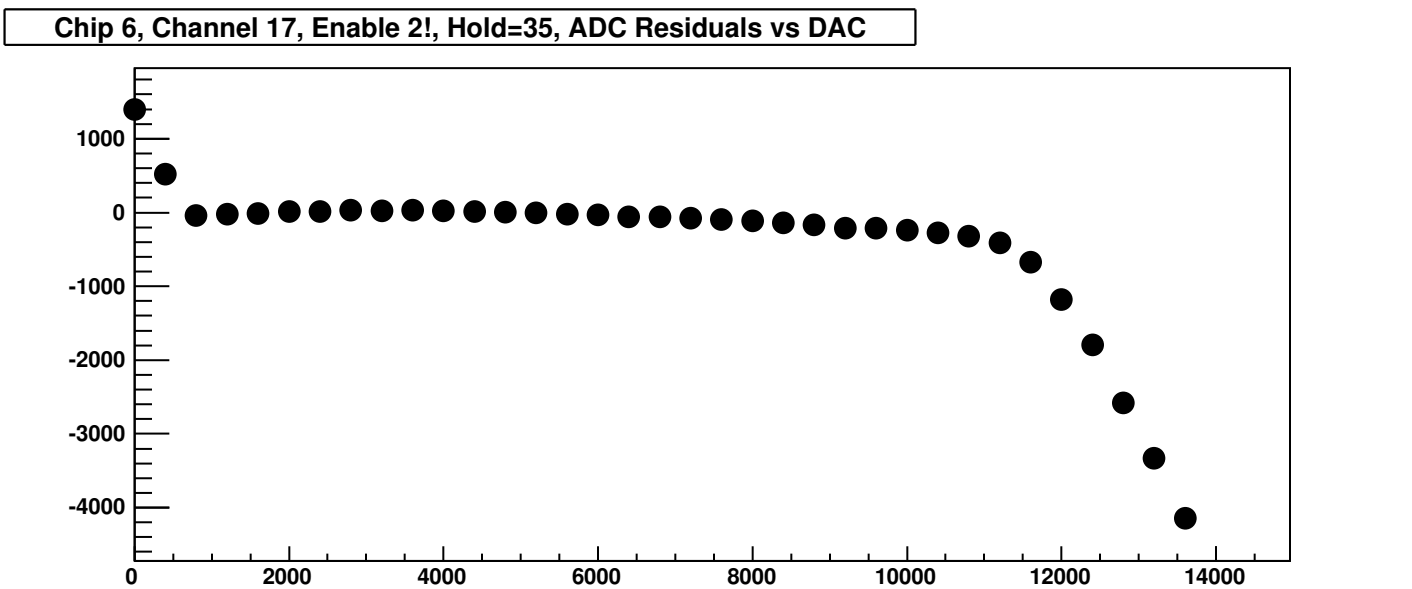

Chip 6, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 42.55 / 23
p0 -286.3 ± 0.7619
p1 0.01995 ± 0.0001223
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

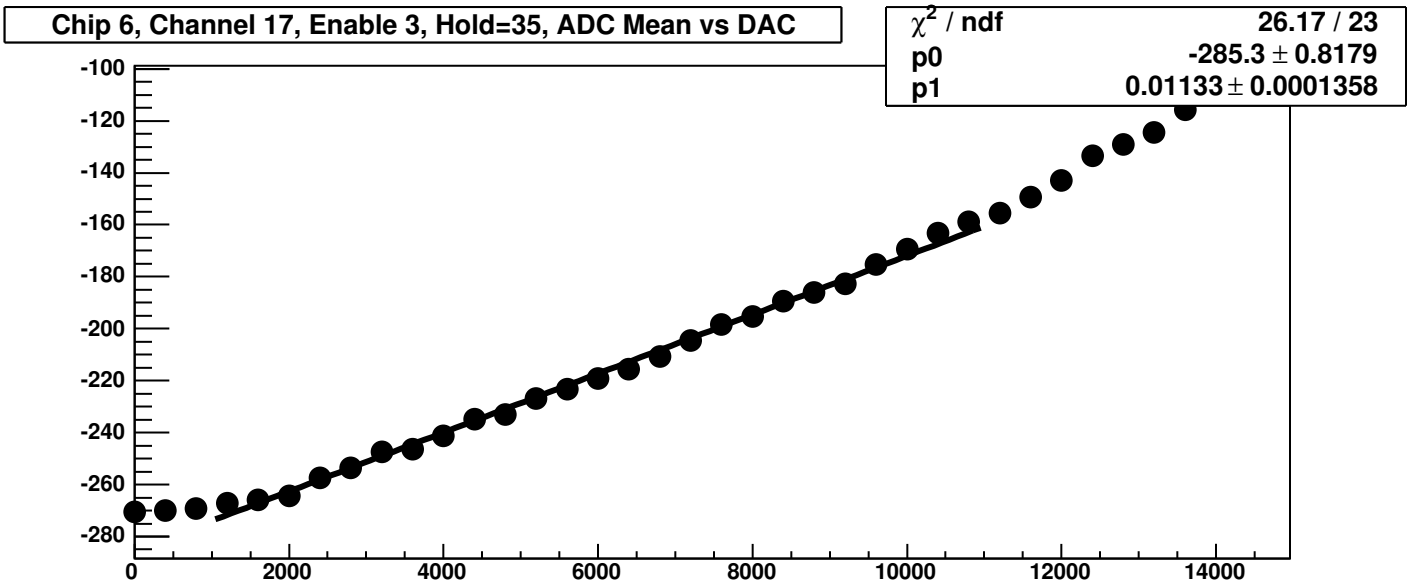

Chip 6, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

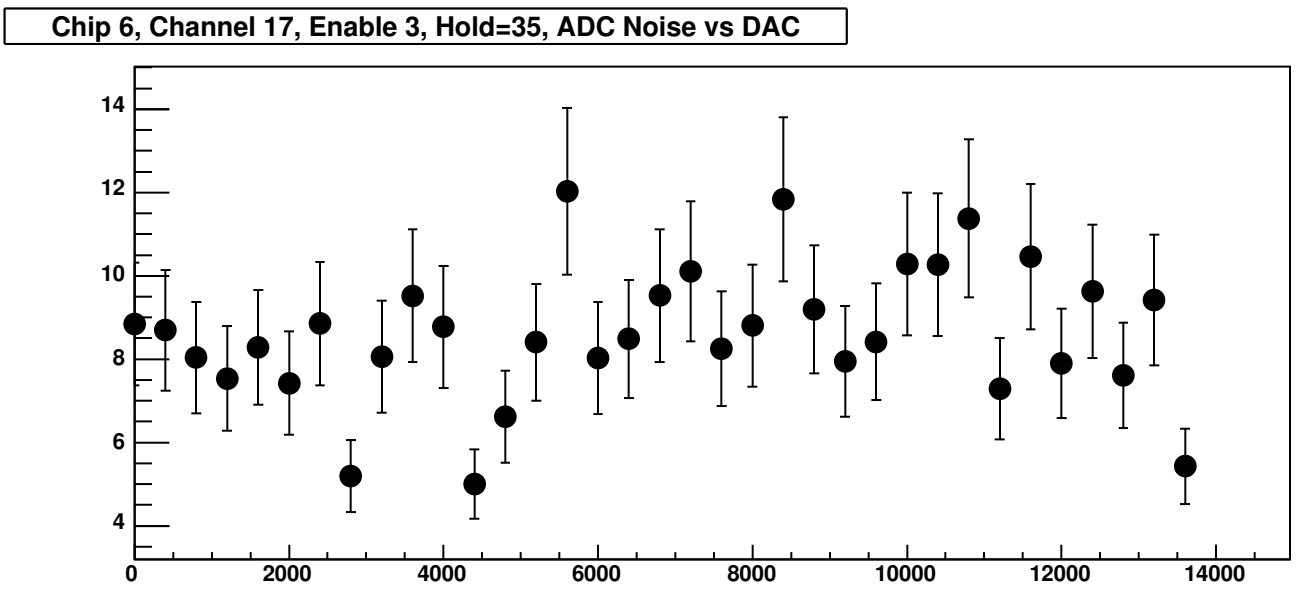

Chip 6, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

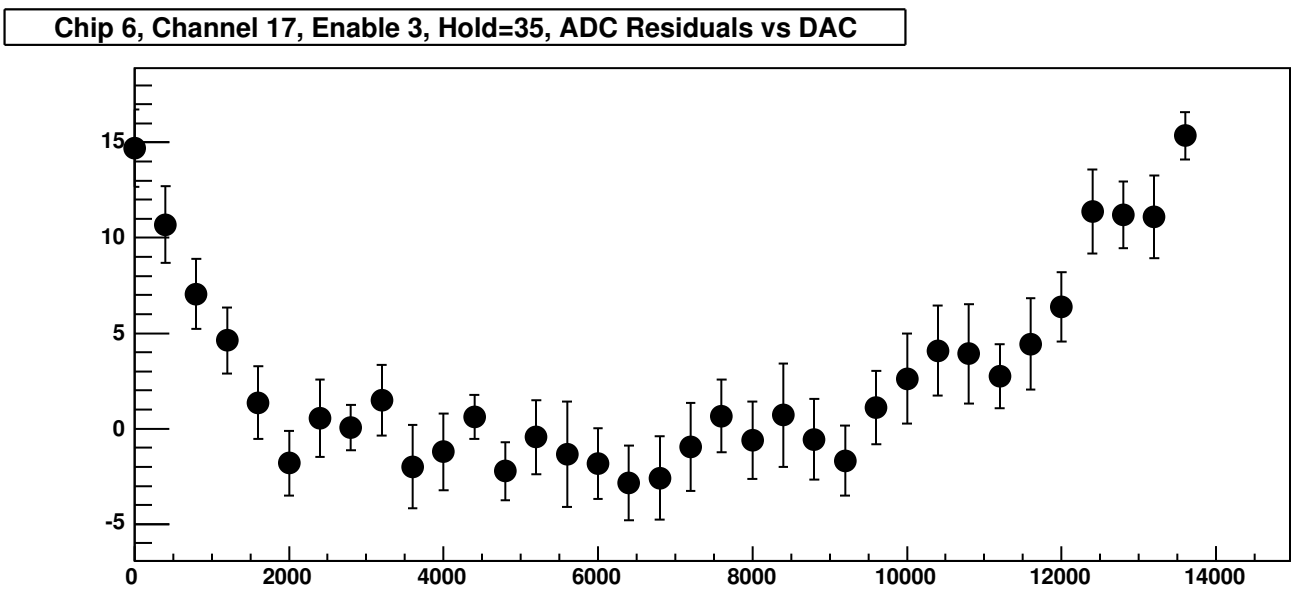

Chip 6, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 581.3 / 23
p0 -224.9 ± 2.308
p1 2.245 ± 0.001108
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

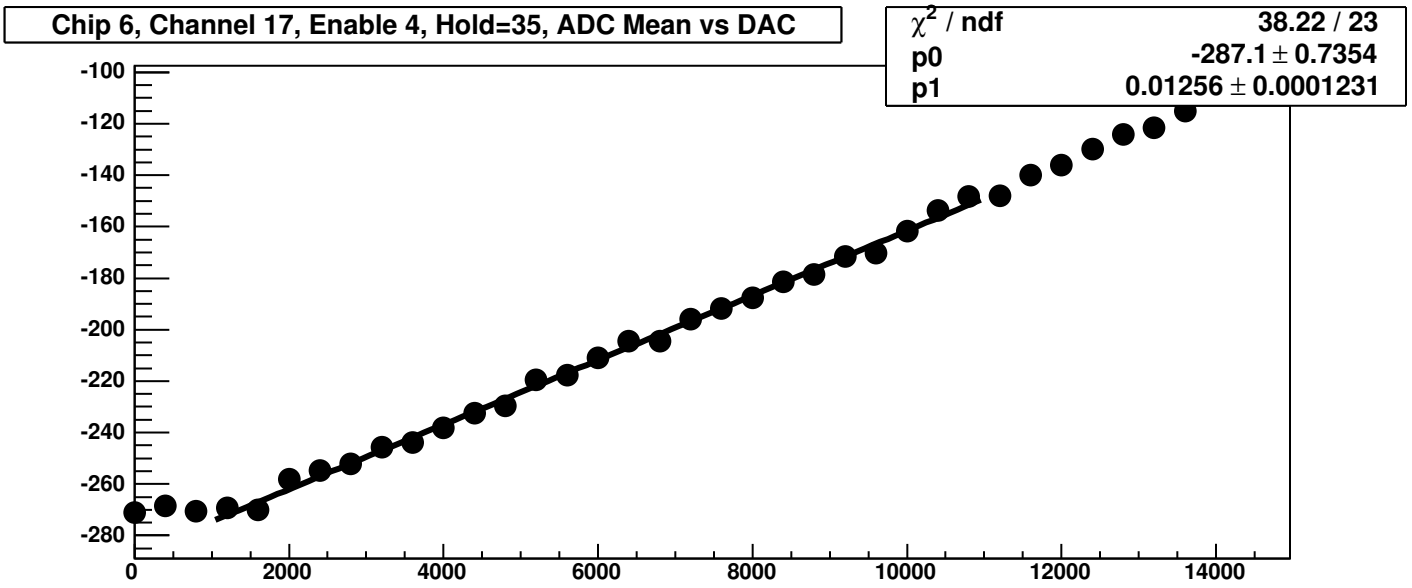

Chip 6, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

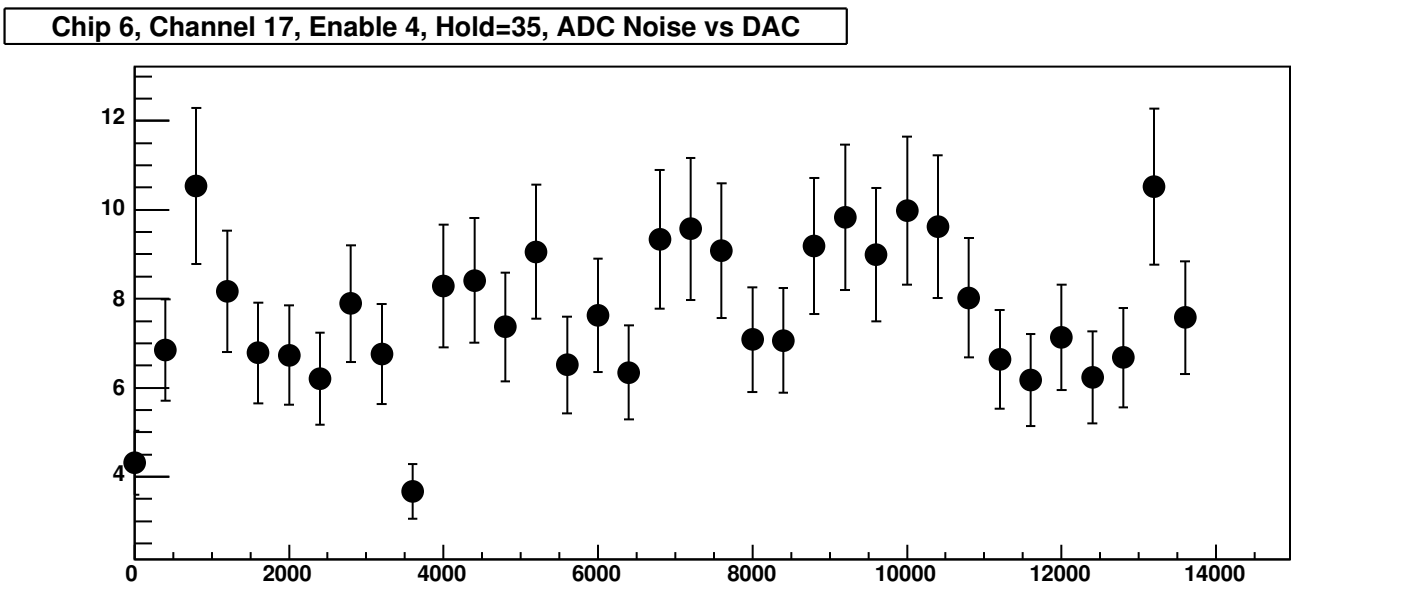

Chip 6, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

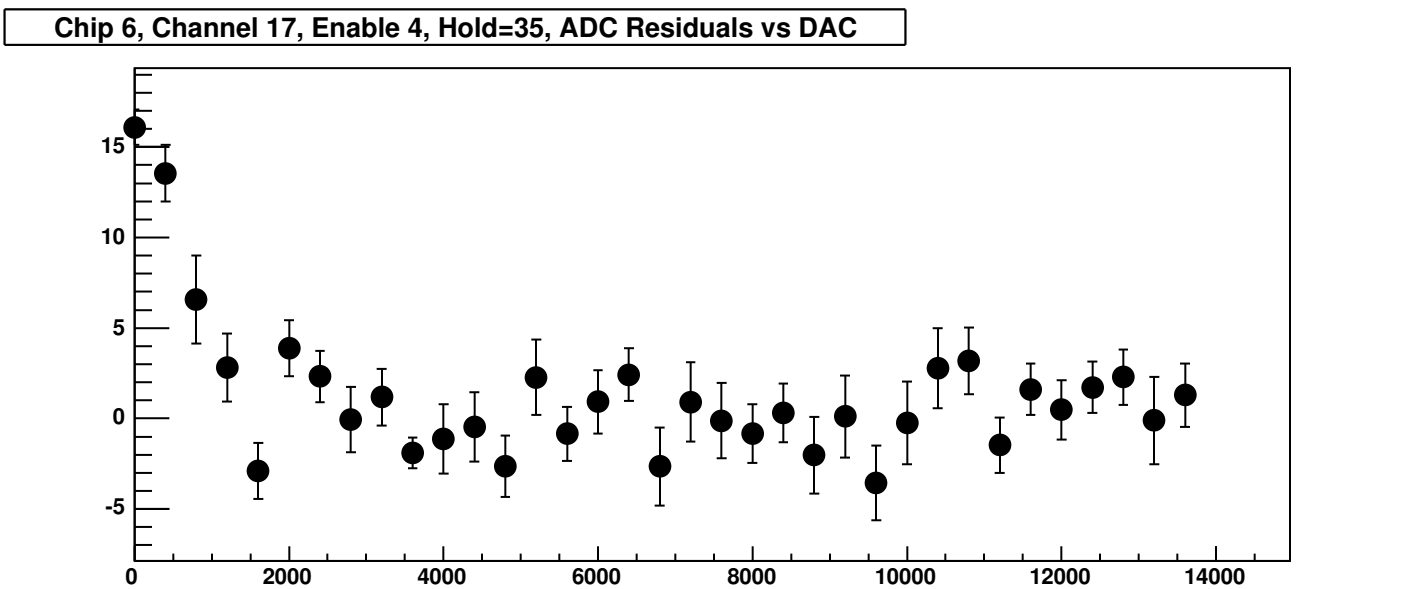

Chip 6, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.17 / 23
p0 -285.3 ± 0.8179
p1 0.01133 ± 0.0001358
-100
-120
-140
-160
-180
-200
-220
-240
-260
-280
0
2000
4000
6000
8000
10000
12000
14000

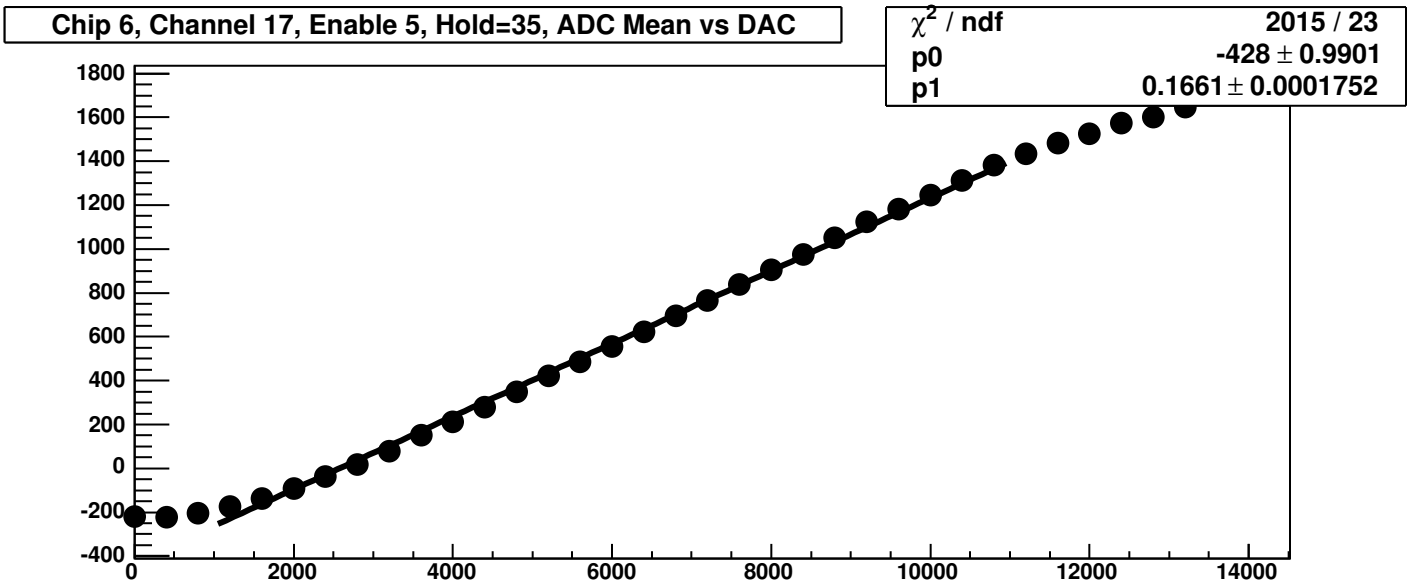

Chip 6, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

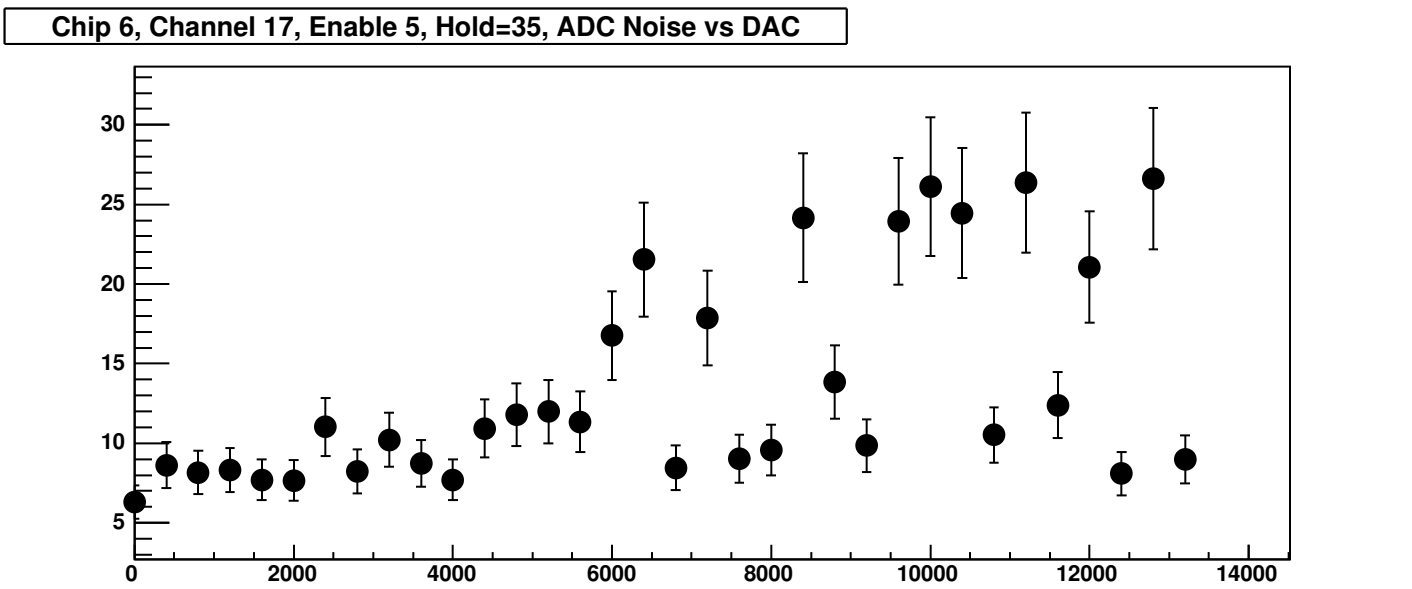

Chip 6, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

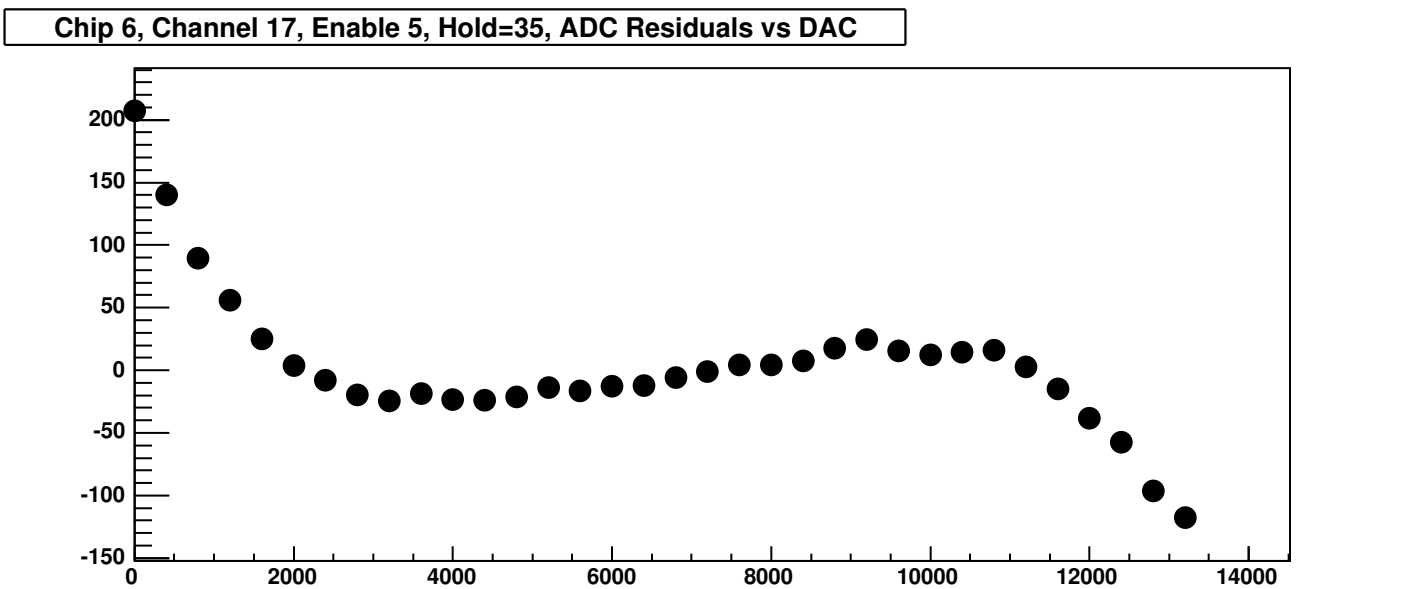

Chip 6, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 38.22 / 23
p0 -287.1 ± 0.7354
p1 0.01256 ± 0.0001231
-100
-120
-140
-160
-180
-200
-220
-240
-260
-280
0
2000
4000
6000
8000
10000
12000
14000

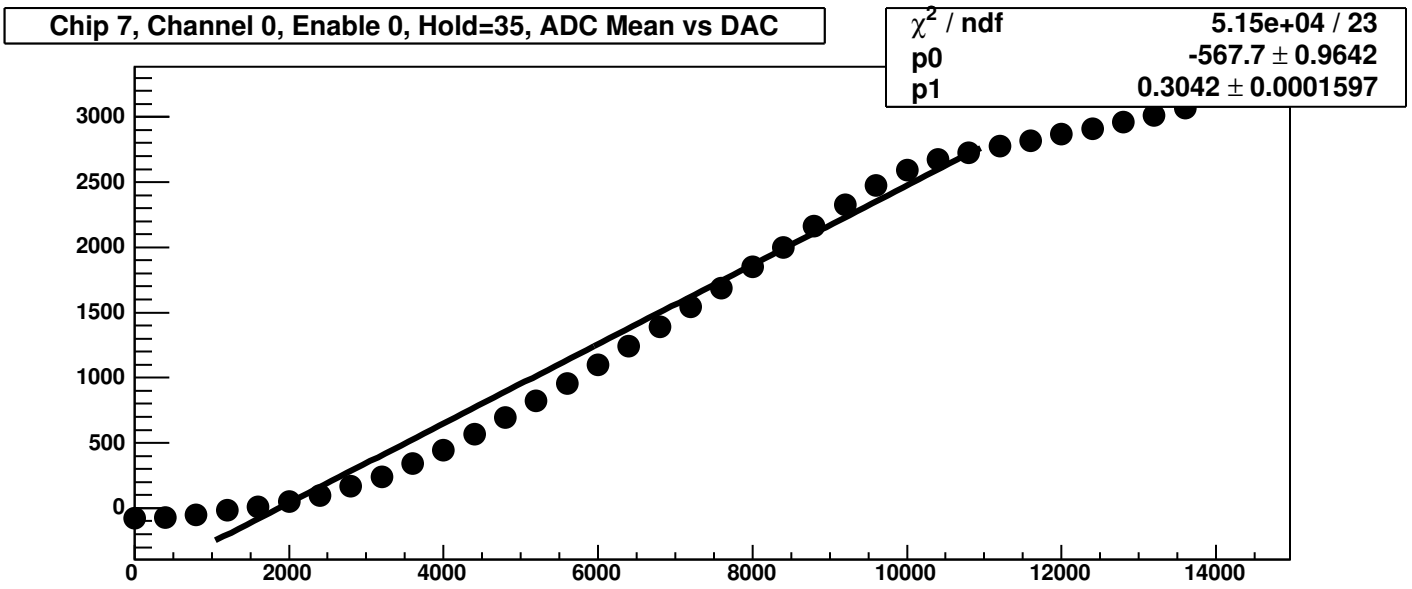

Chip 6, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

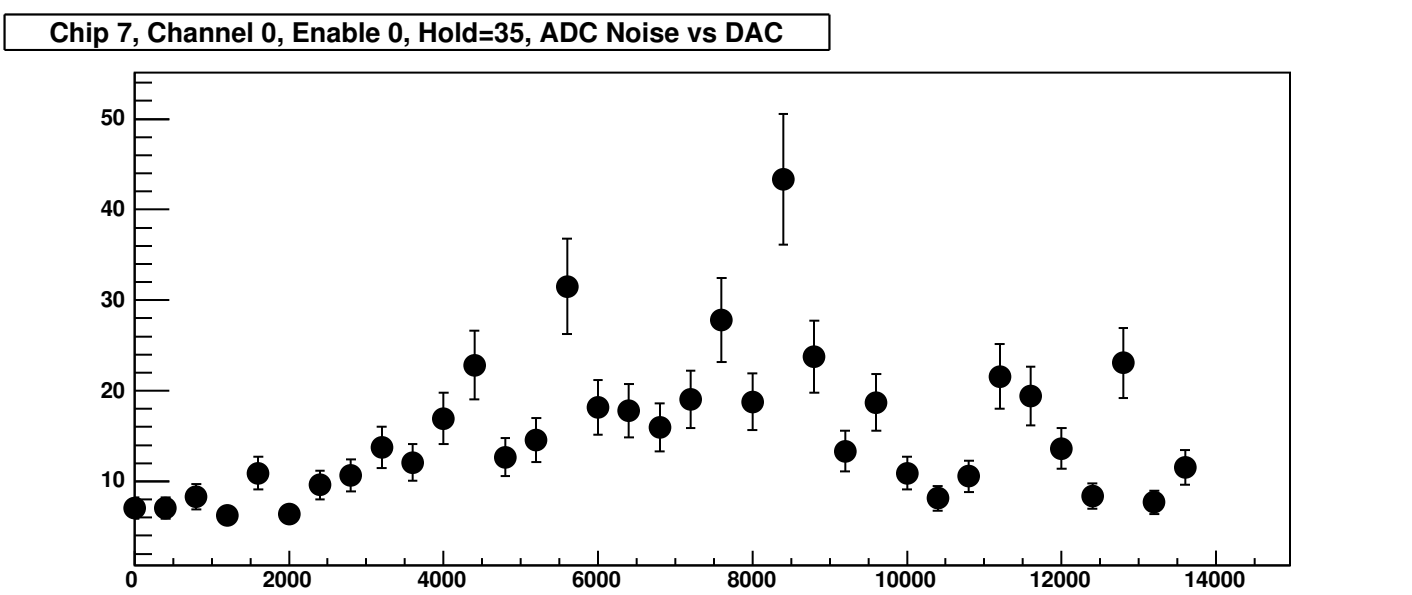

Chip 6, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

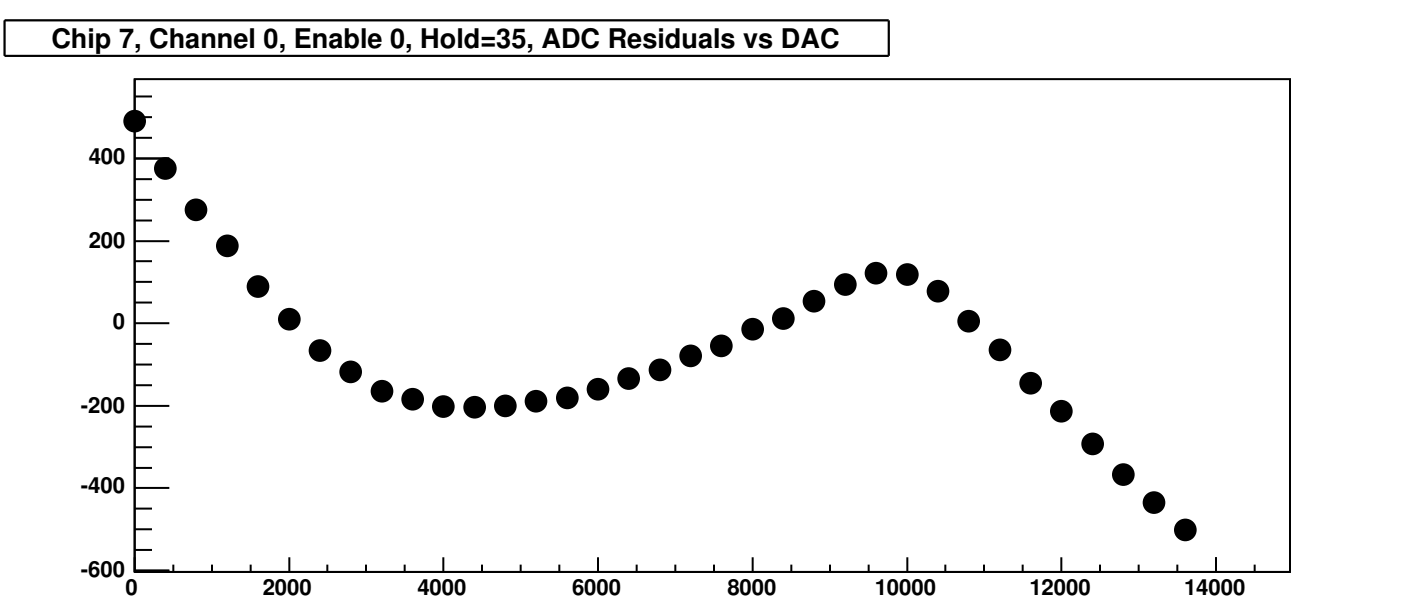

Chip 6, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2015 / 23
p0 -428 ± 0.9901
p1 0.1661 ± 0.0001752
1800
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

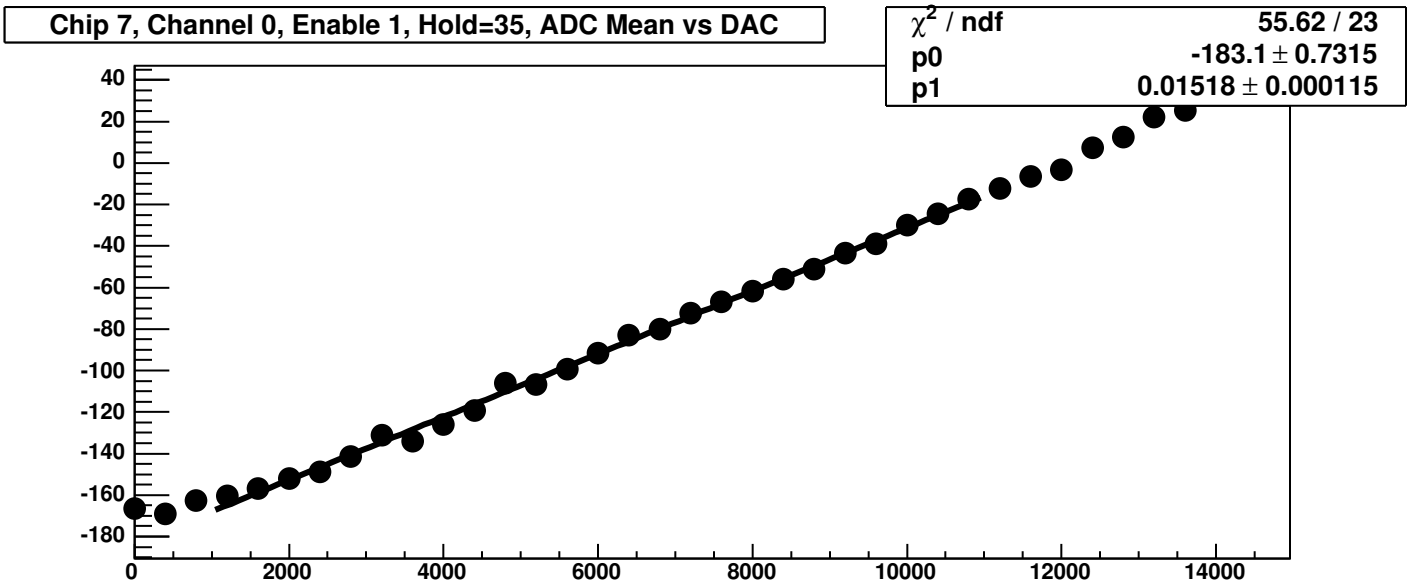

Chip 6, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

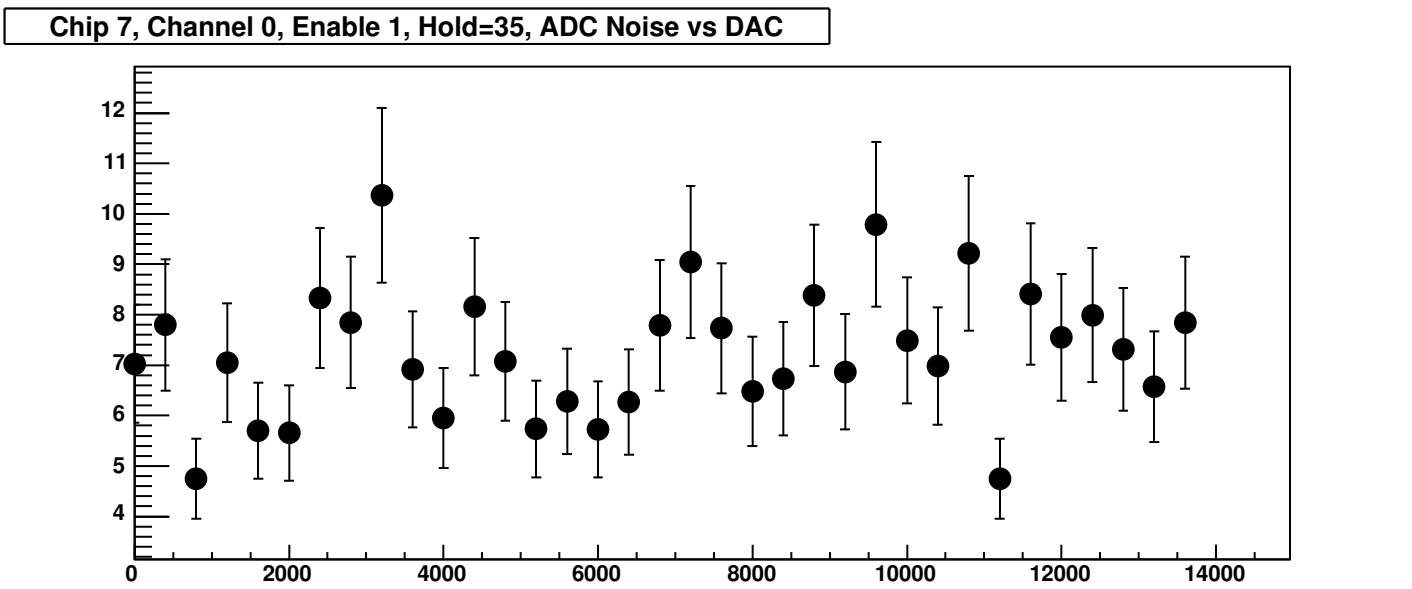

Chip 6, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

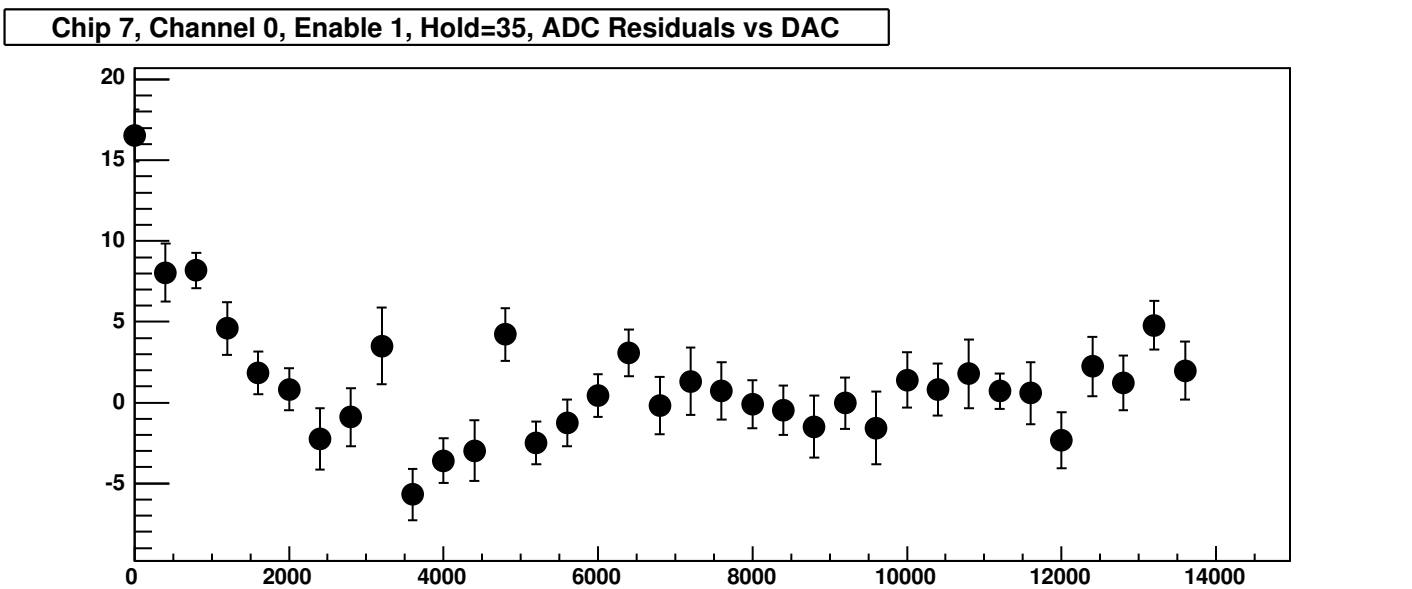

Chip 7, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.15e+04 / 23
p0 -567.7 ± 0.9642
p1 0.3042 ± 0.0001597
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

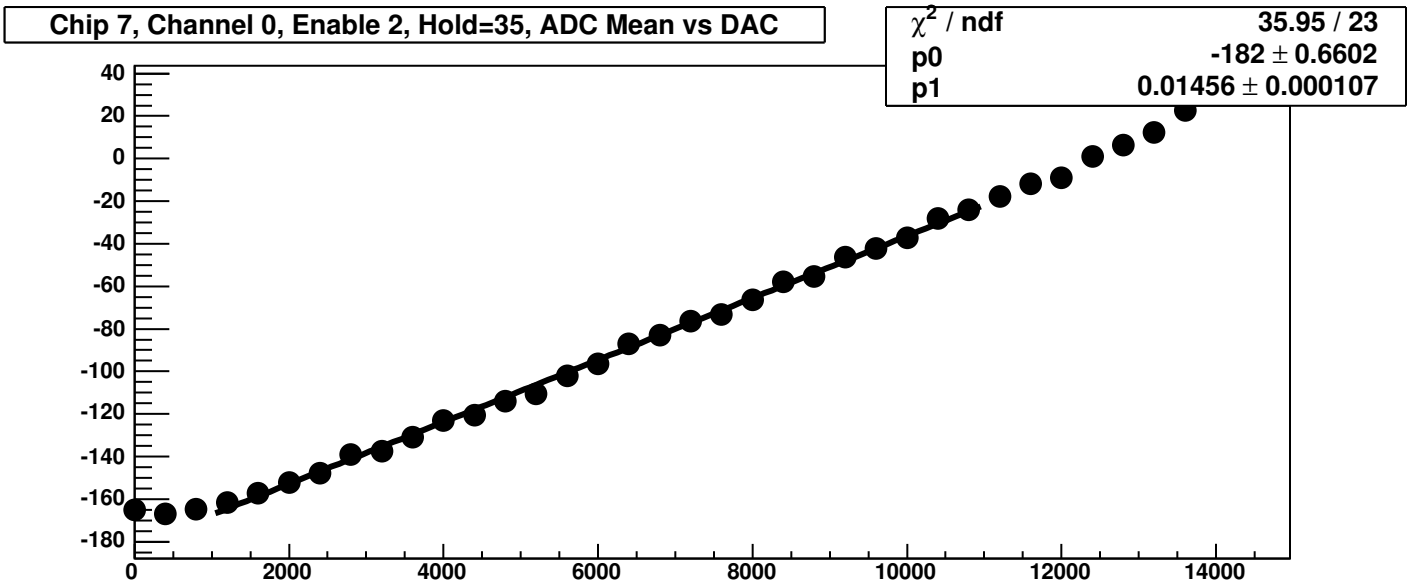

Chip 7, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

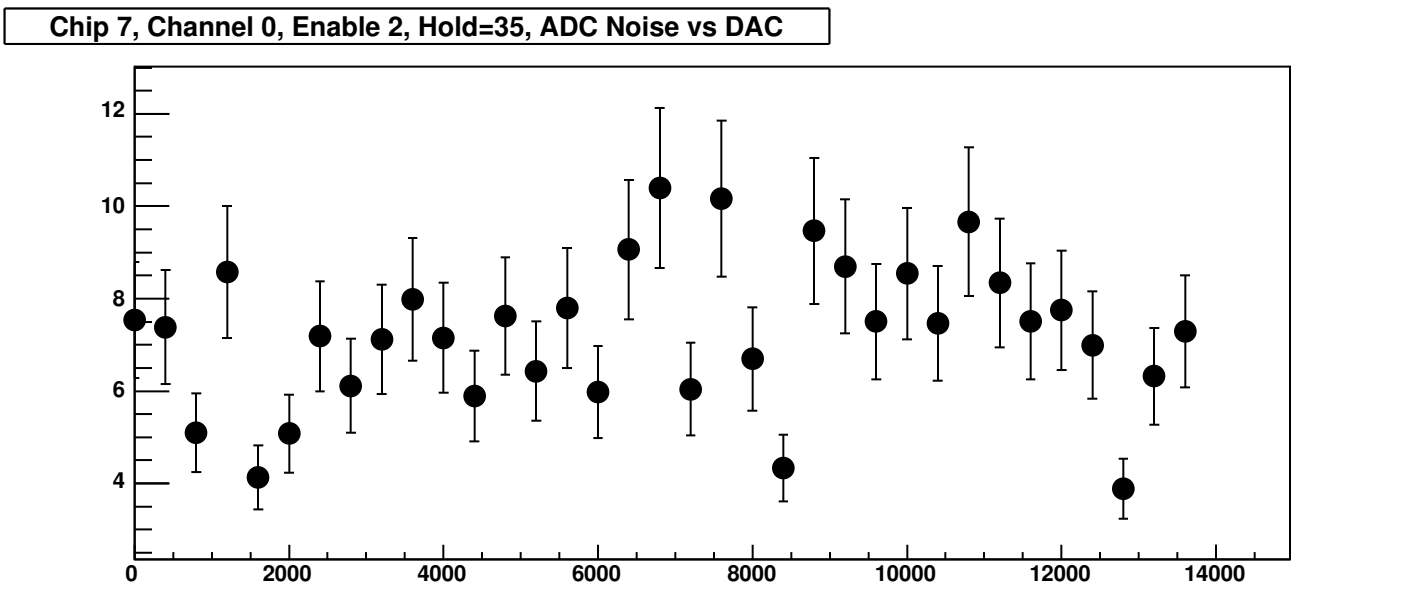

Chip 7, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

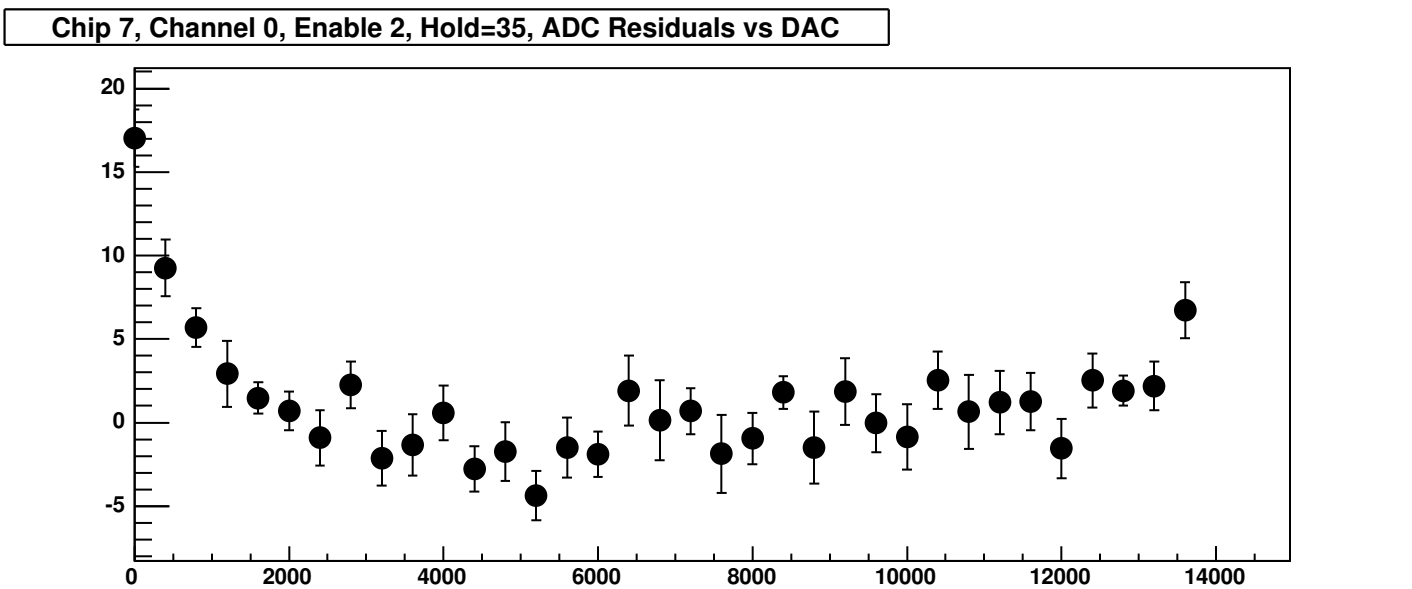

Chip 7, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 55.62 / 23
p0 -183.1 ± 0.7315
p1 0.01518 ± 0.000115
40
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000

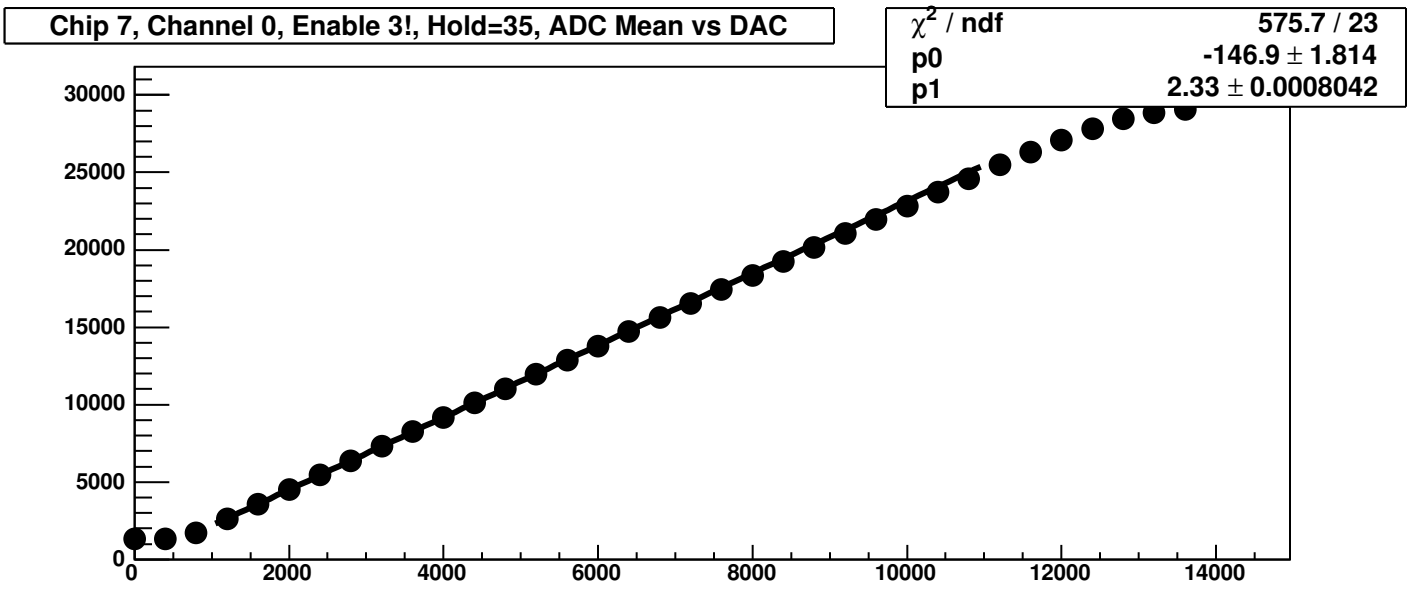

Chip 7, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

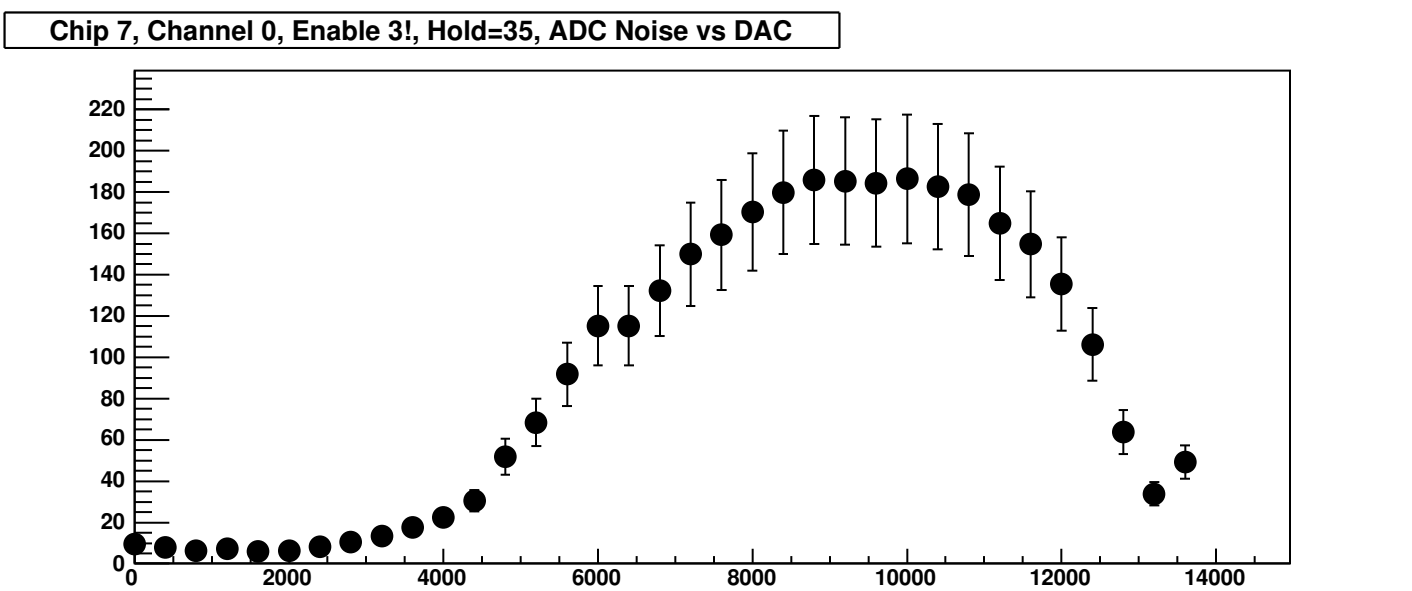

Chip 7, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

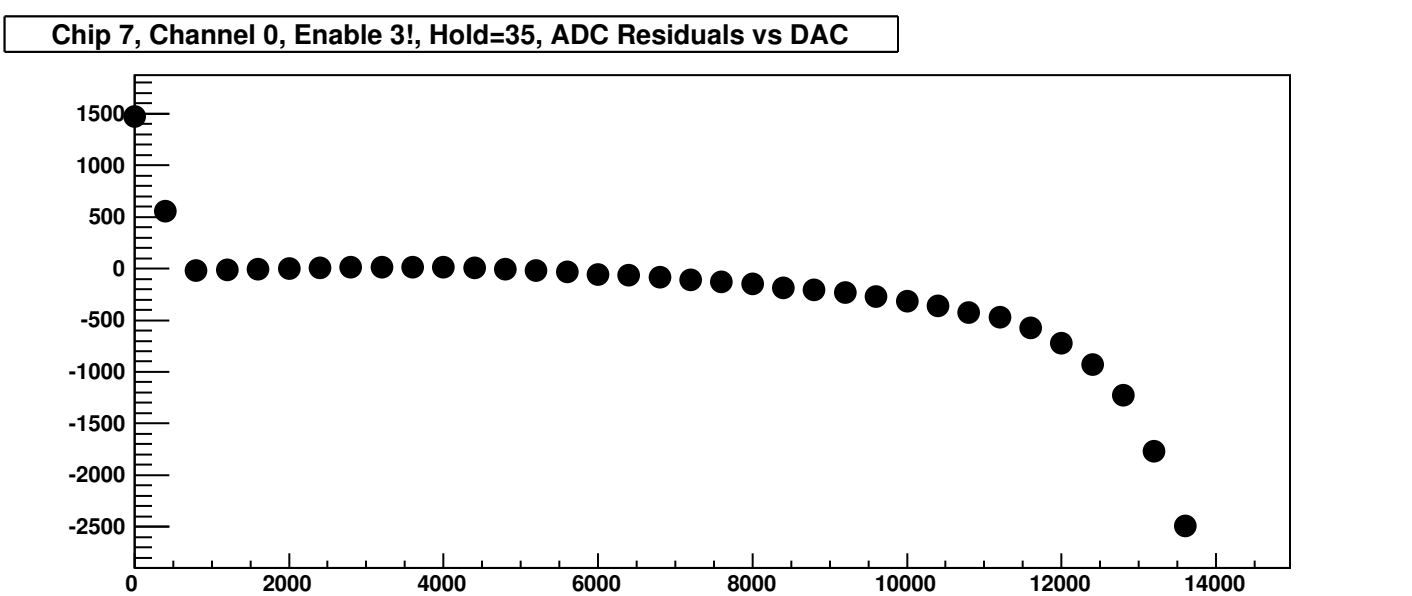

Chip 7, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 35.95 / 23
p0 -182 ± 0.6602
p1 0.01456 ± 0.000107
40
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

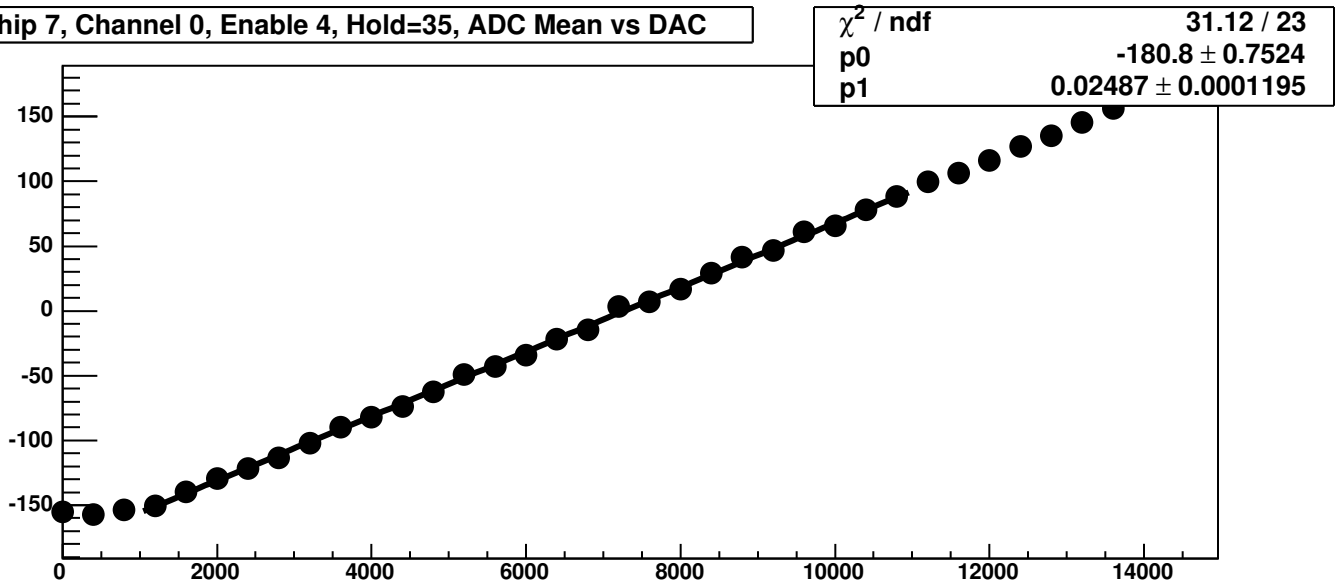

Chip 7, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 575.7 / 23
p0 -146.9 ± 1.814
p1 2.33 ± 0.0008042
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

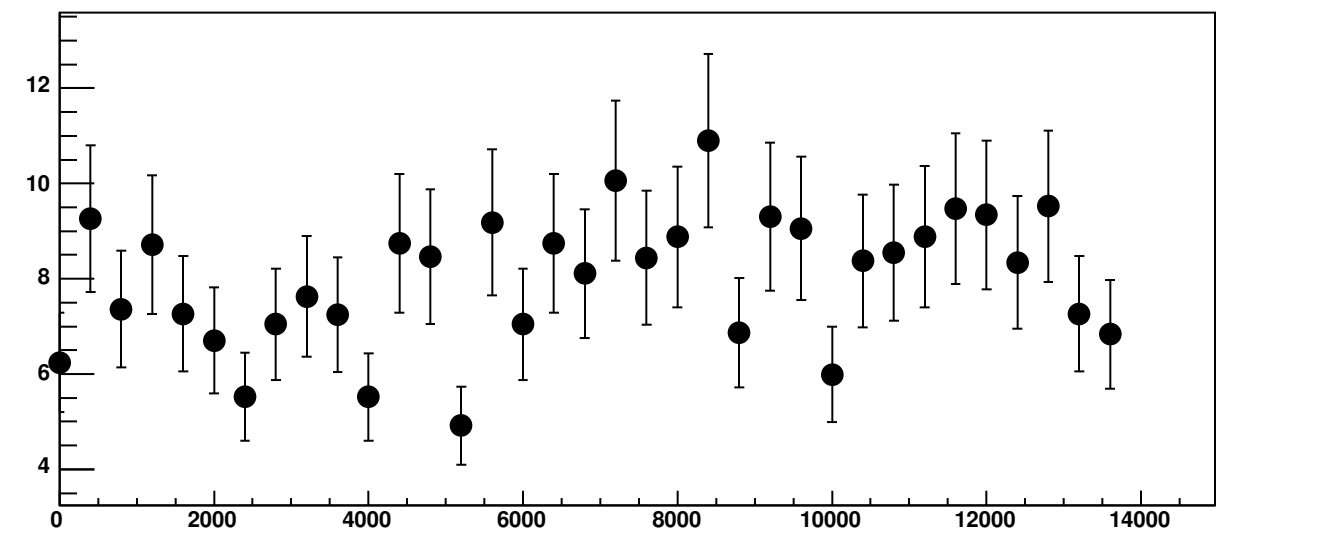

Chip 7, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

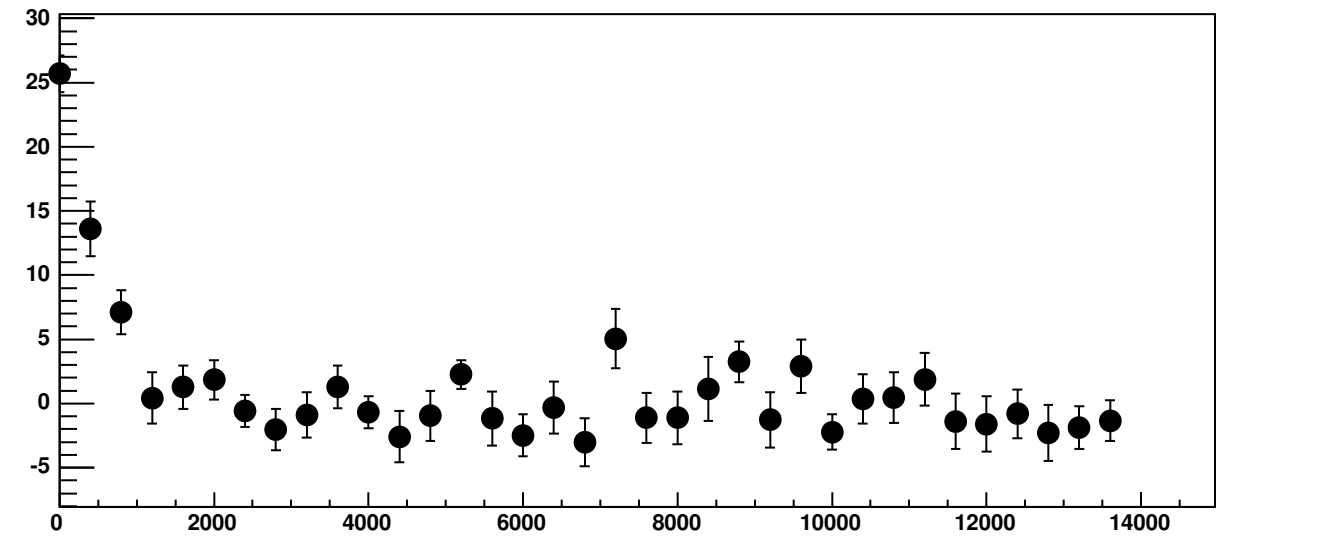
Chip 7, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

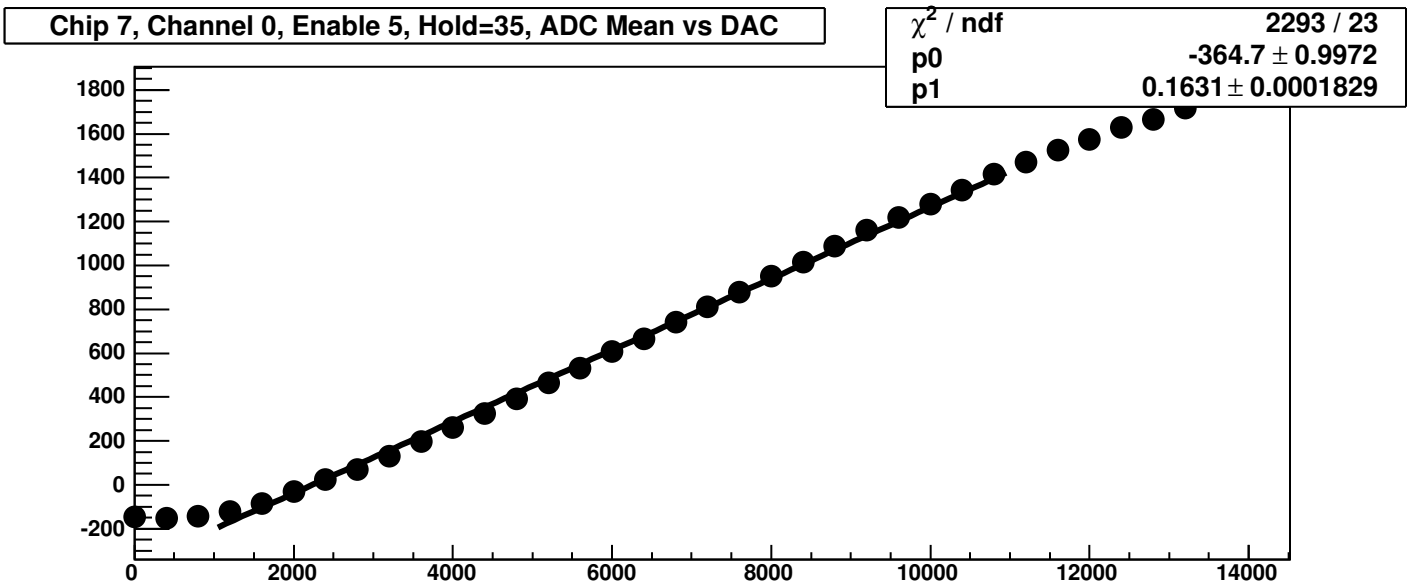


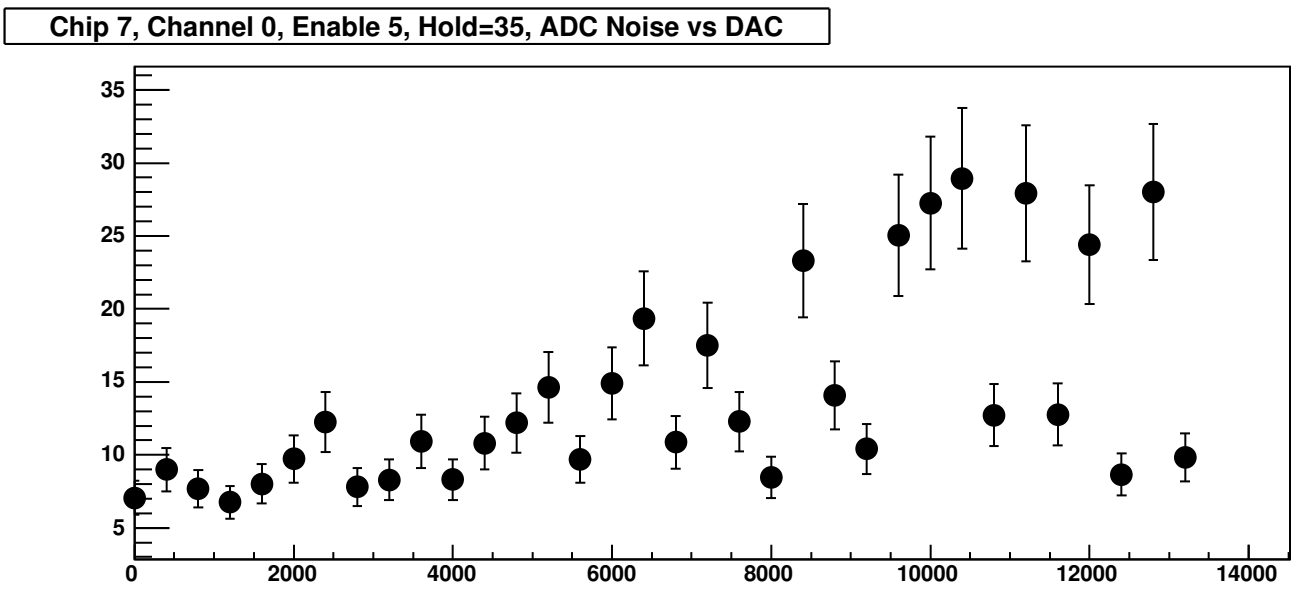
Chip 7, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

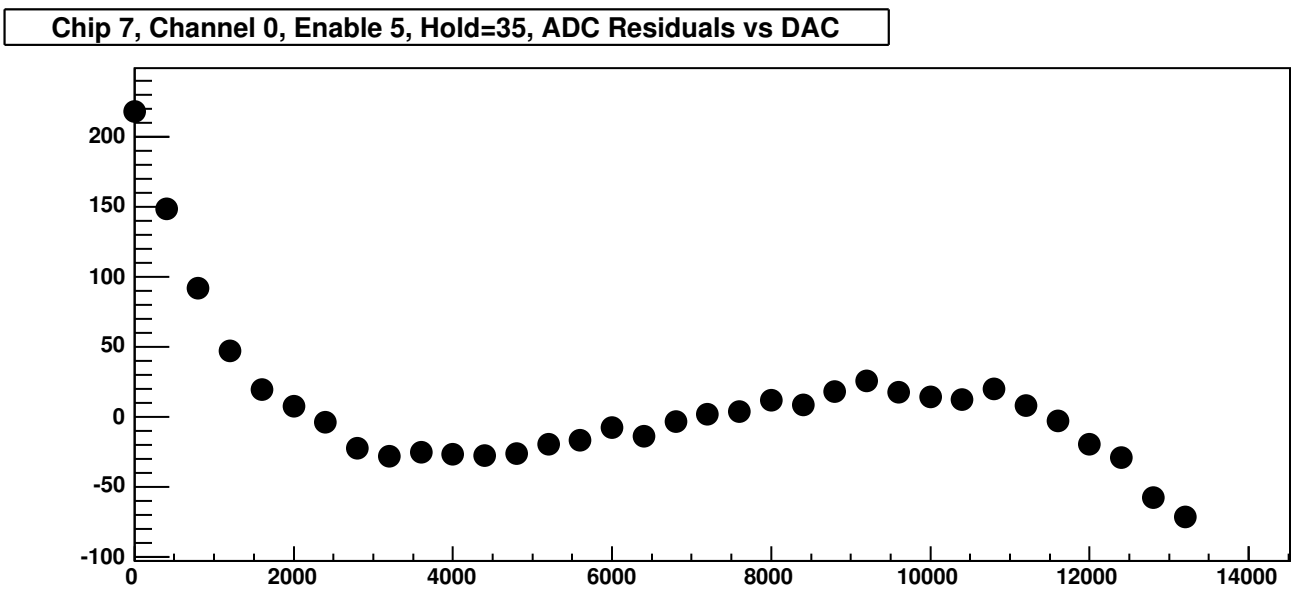


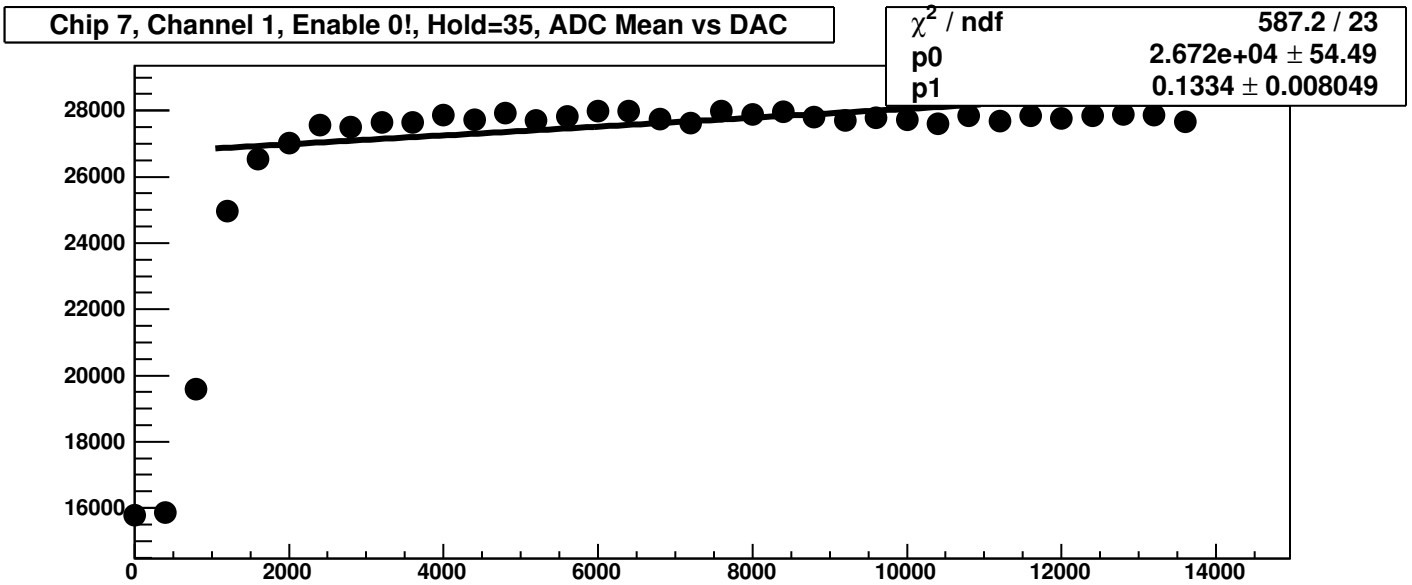
Chip 7, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

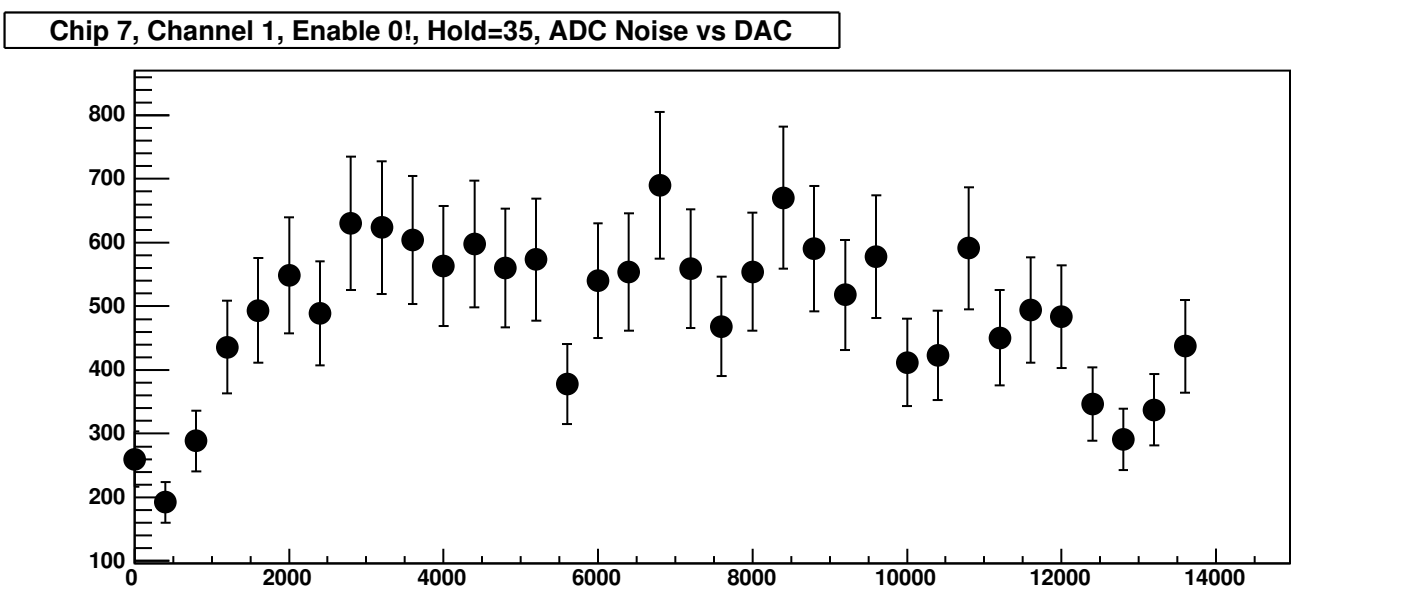


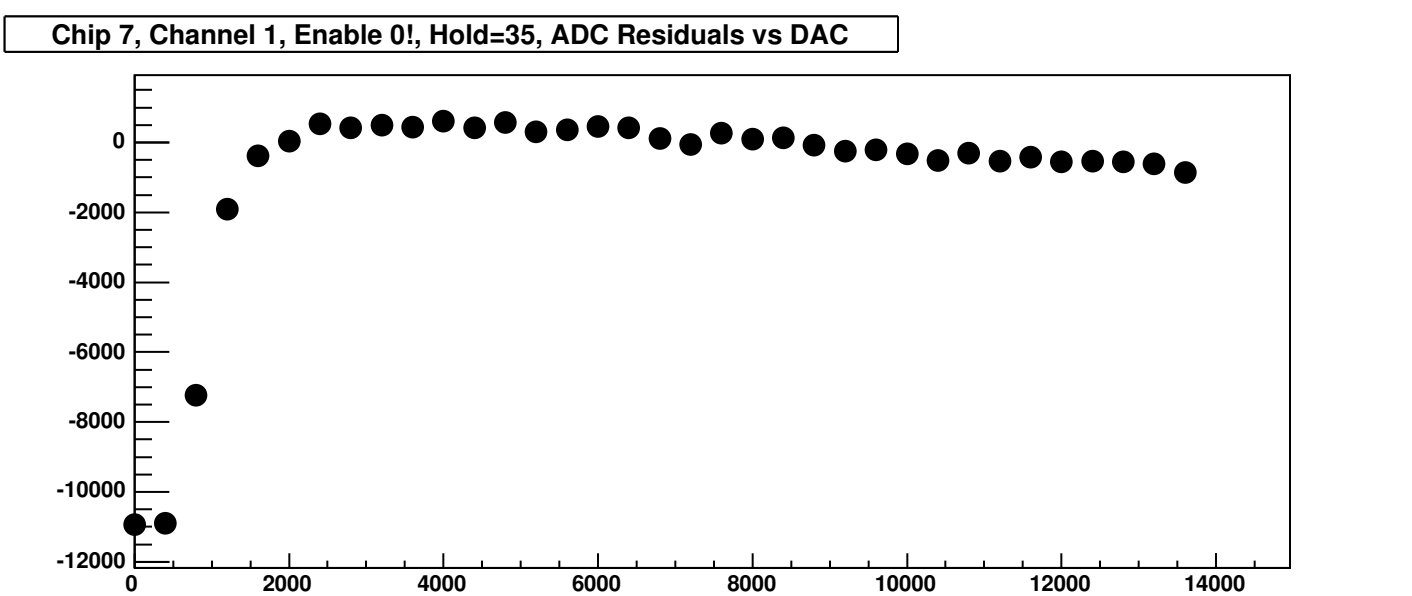

Chip 7, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2293 / 23
p0 -364.7 ± 0.9972
p1 0.1631 ± 0.0001829
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

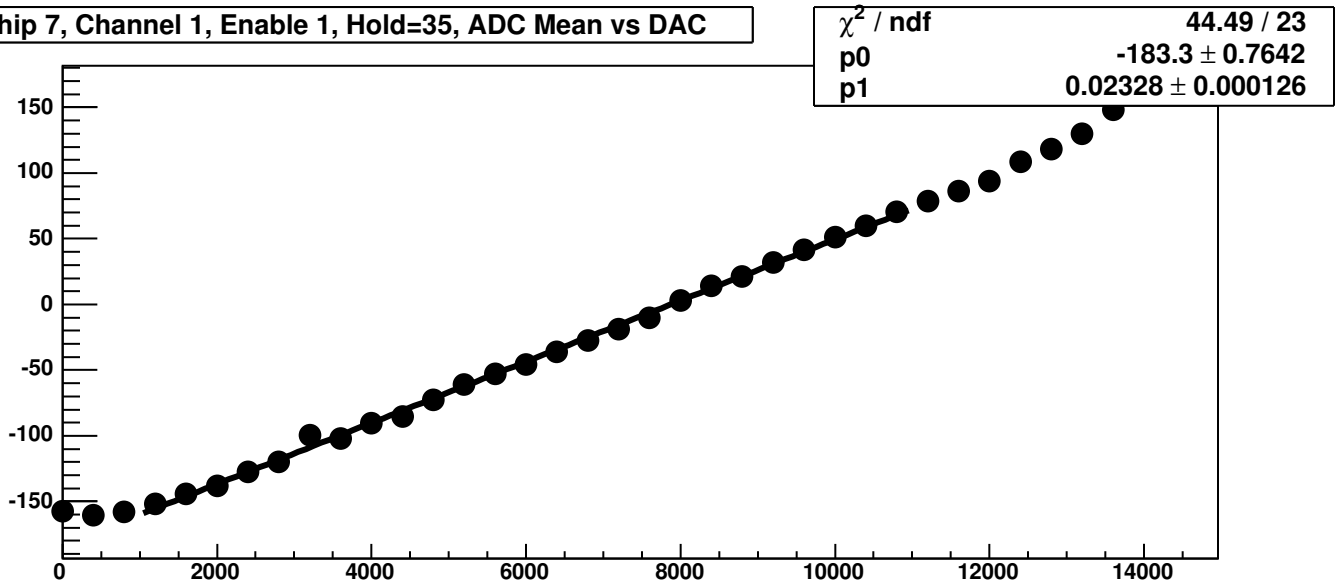

Chip 7, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 587.2 / 23
p0 2.672e+04 ± 54.49
p1 0.1334 ± 0.008049
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

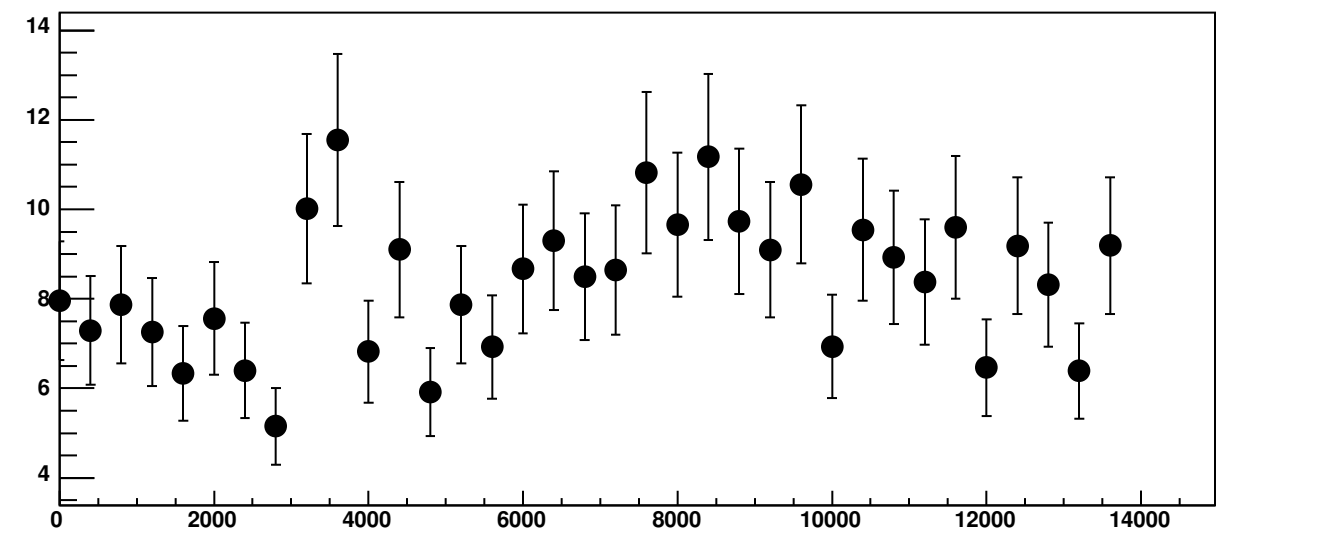

Chip 7, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
800
700
600
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
2000
4000
6000
8000
10000
12000
14000

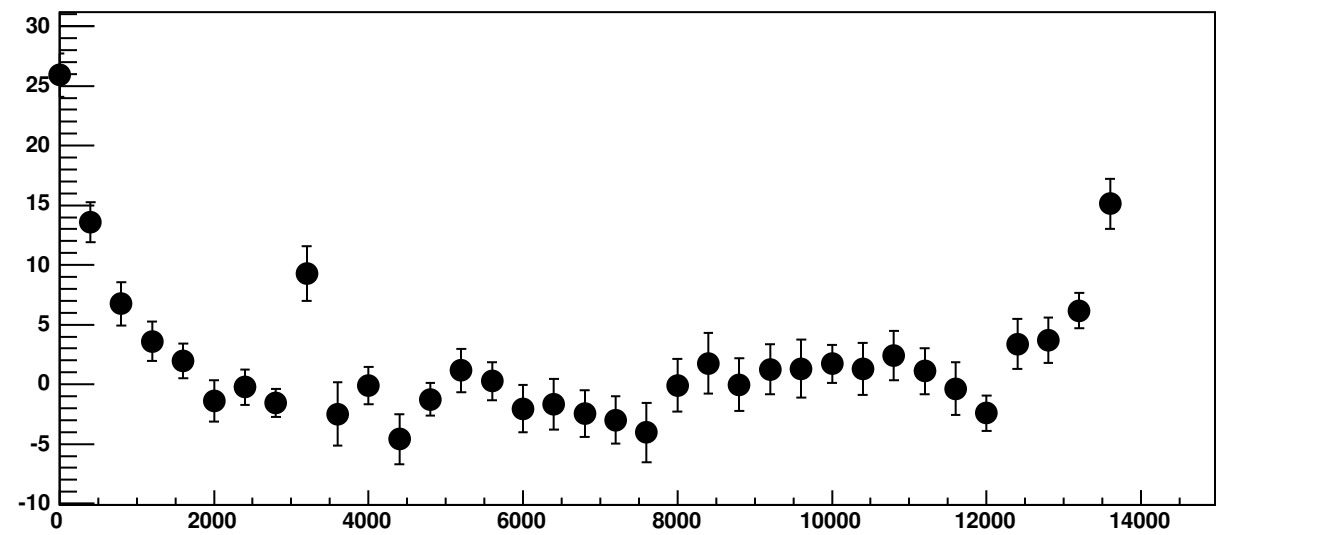
Chip 7, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC



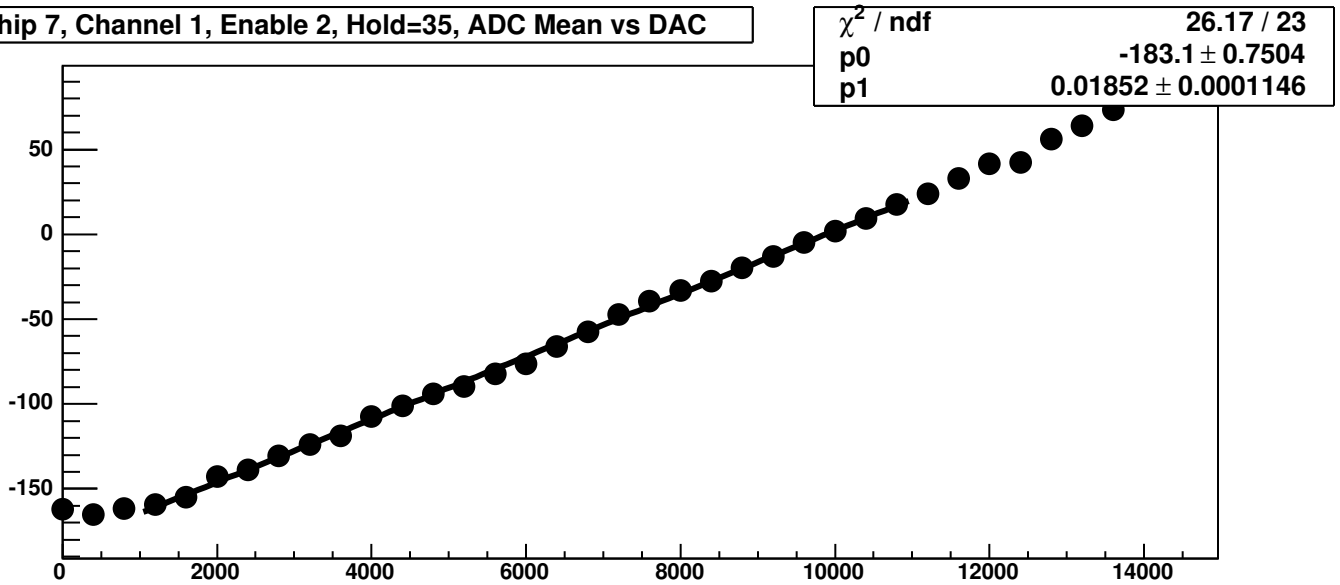
Chip 7, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC



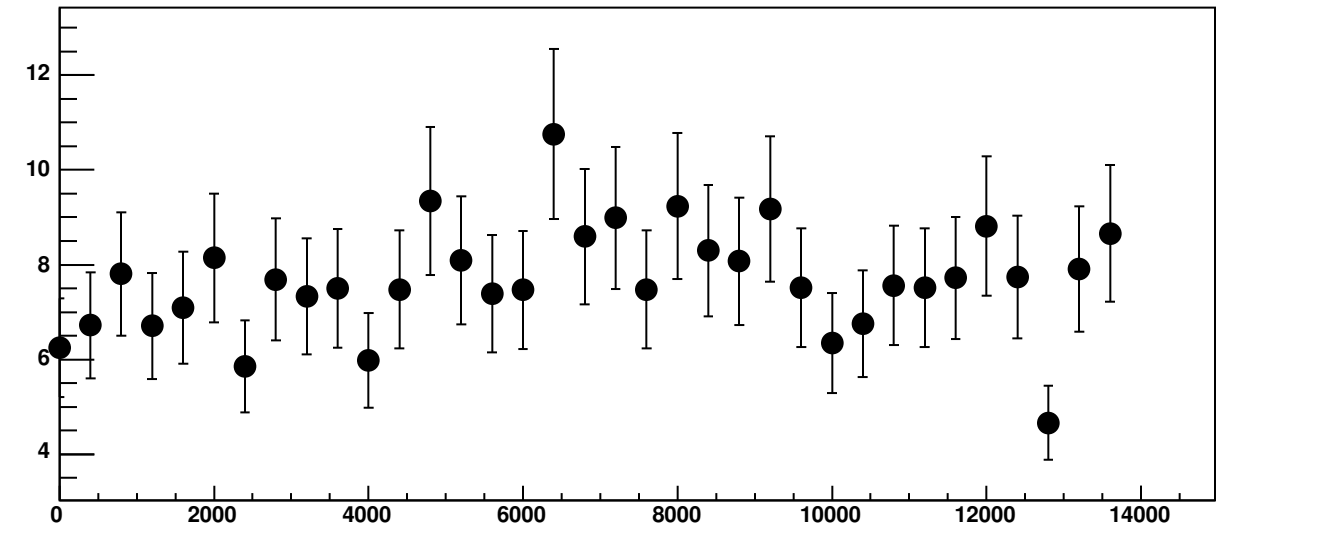
Chip 7, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC



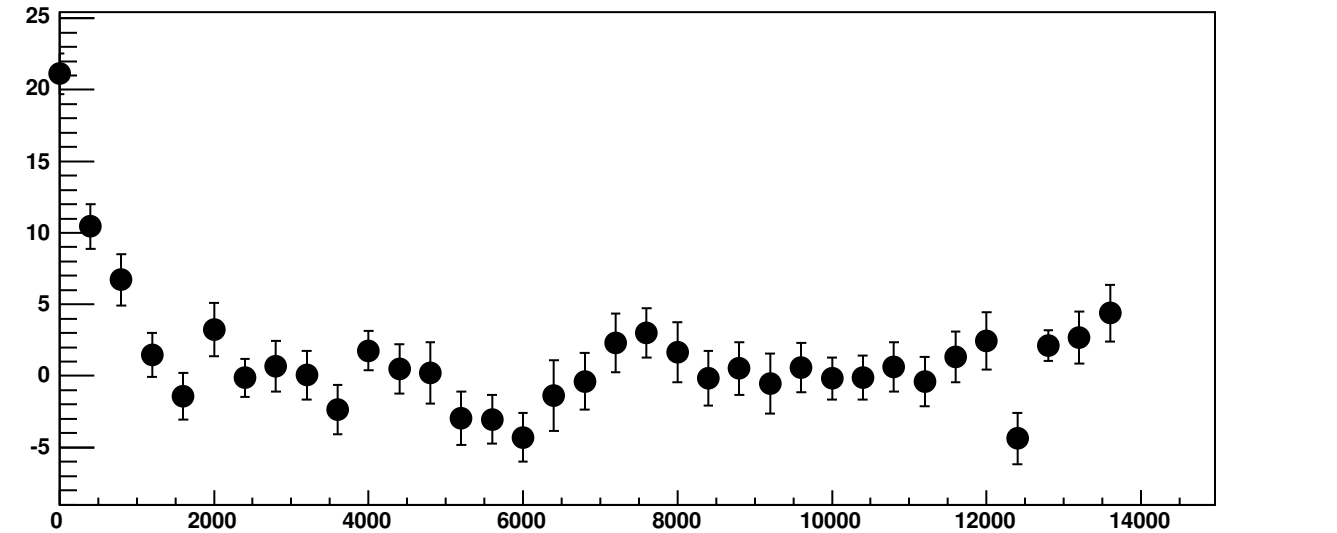
Chip 7, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

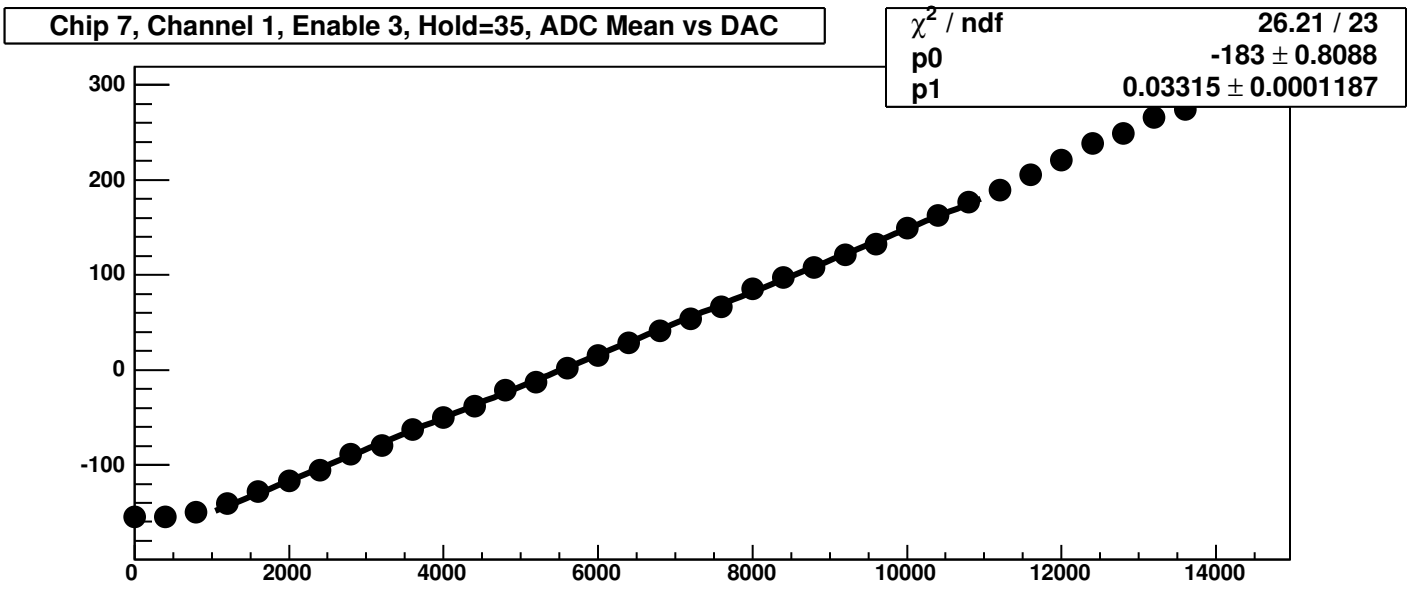


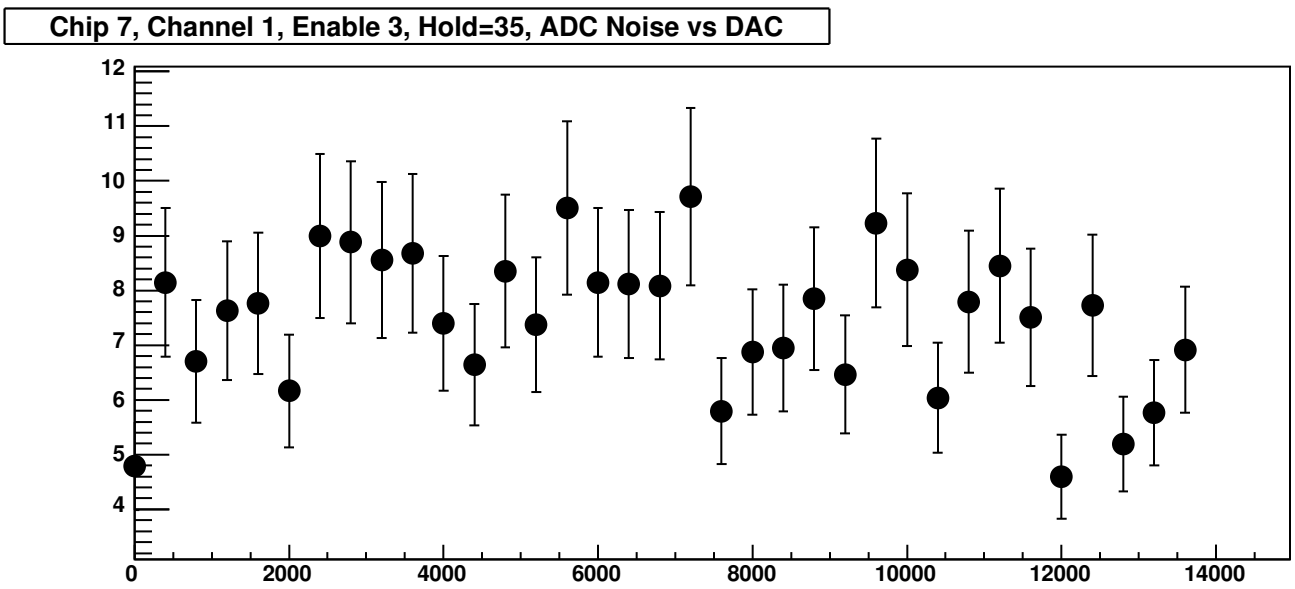
Chip 7, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC

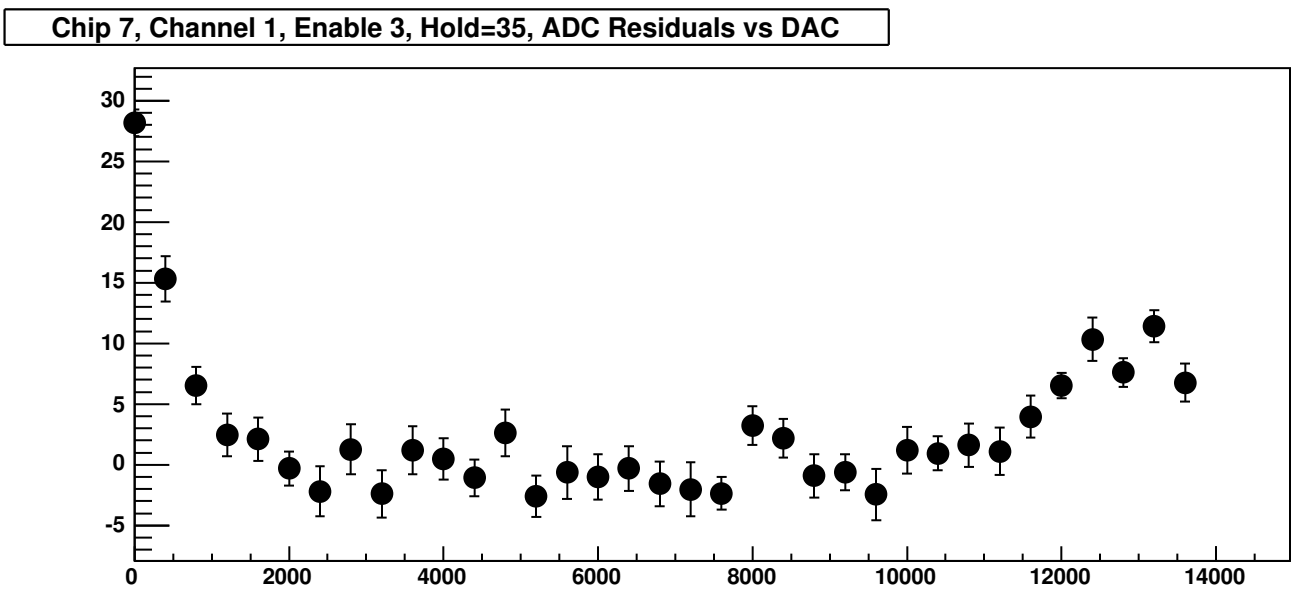


Chip 7, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

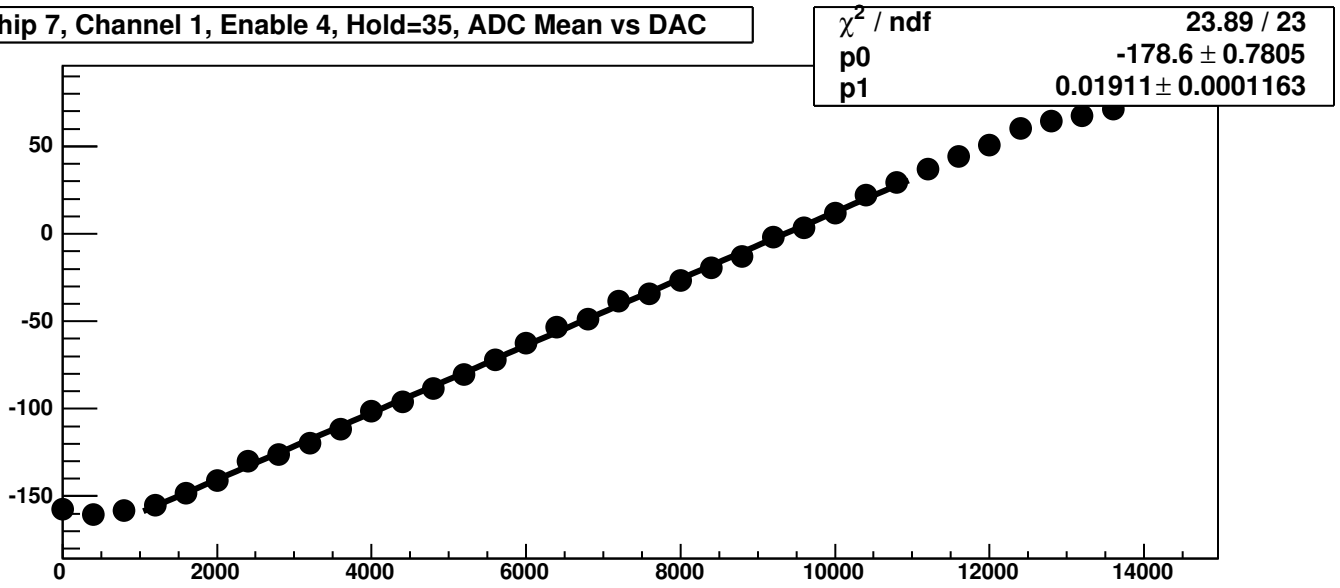



Chip 7, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.21 / 23
p0 -183 ± 0.8088
p1 0.03315 ± 0.0001187
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

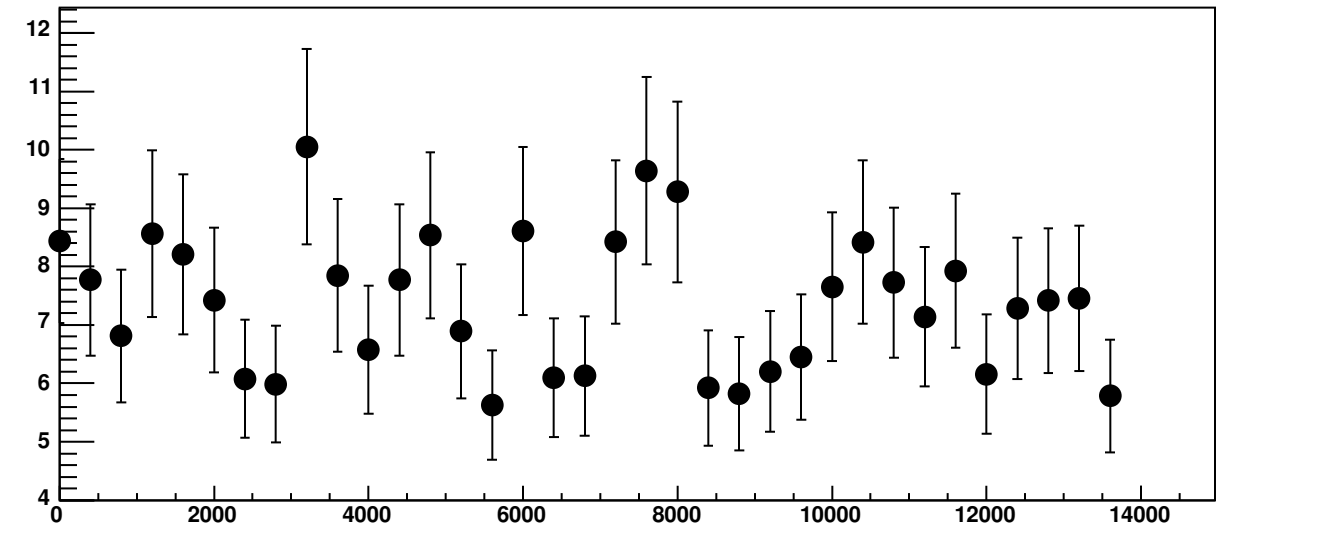

Chip 7, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

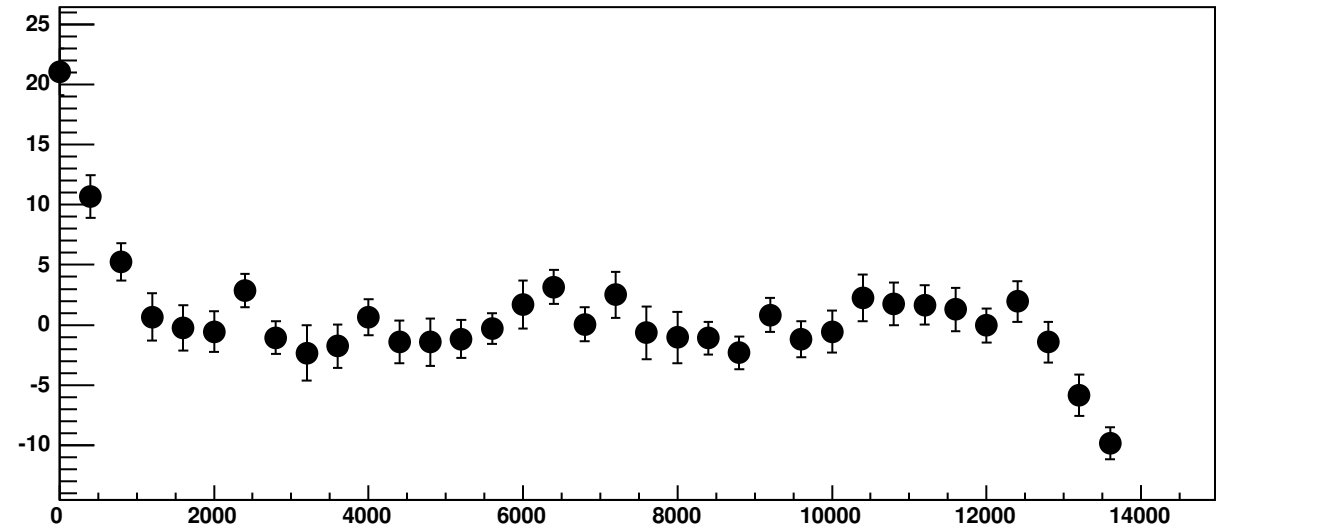
Chip 7, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



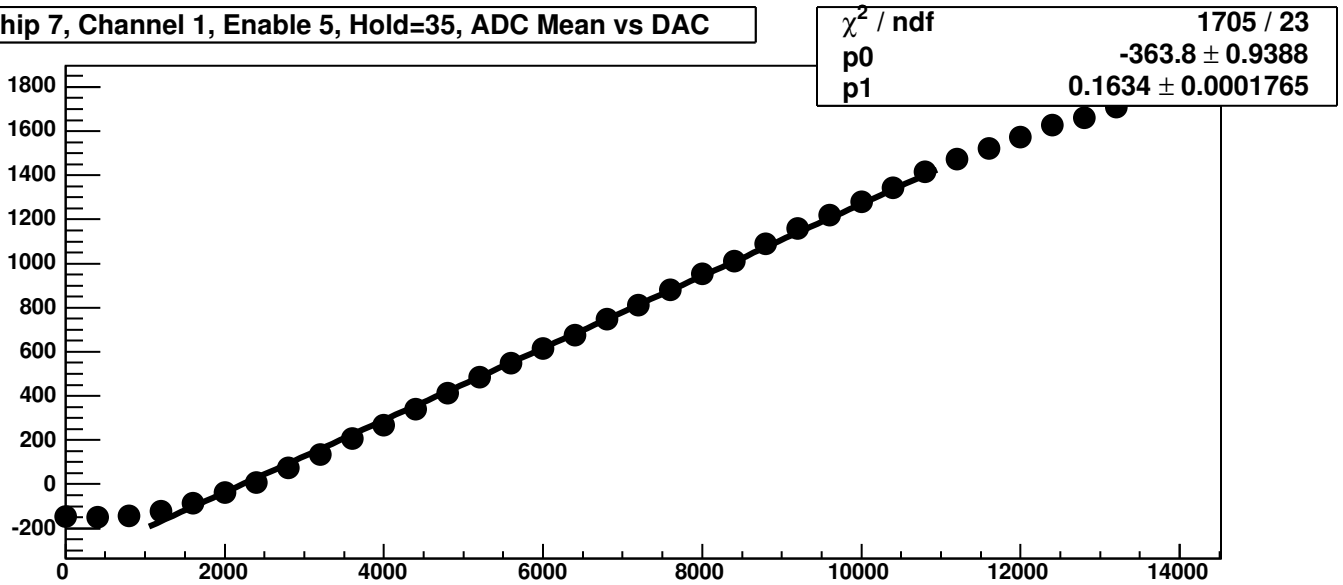
Chip 7, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



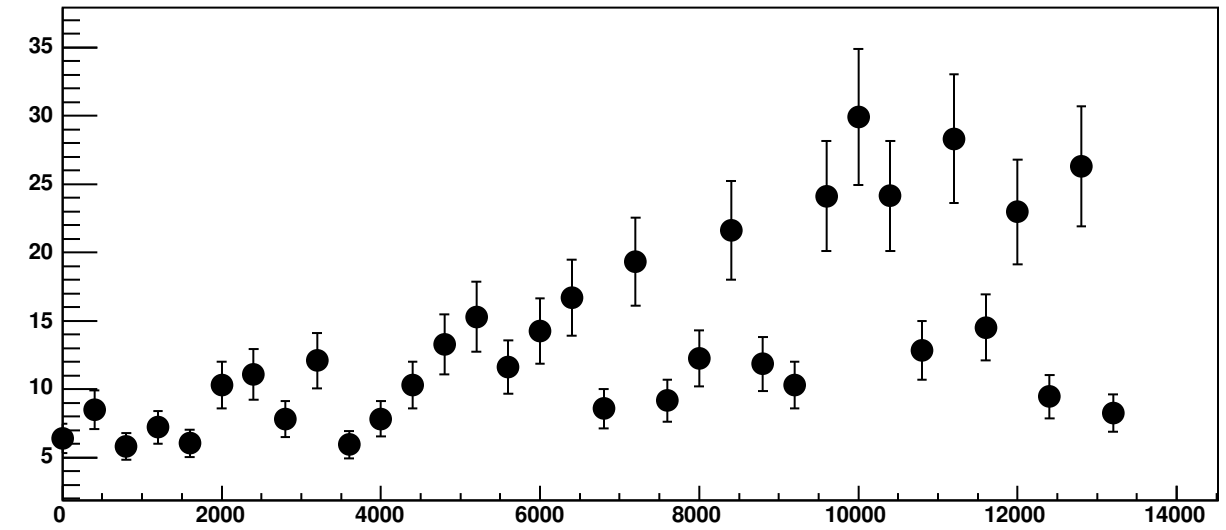
Chip 7, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



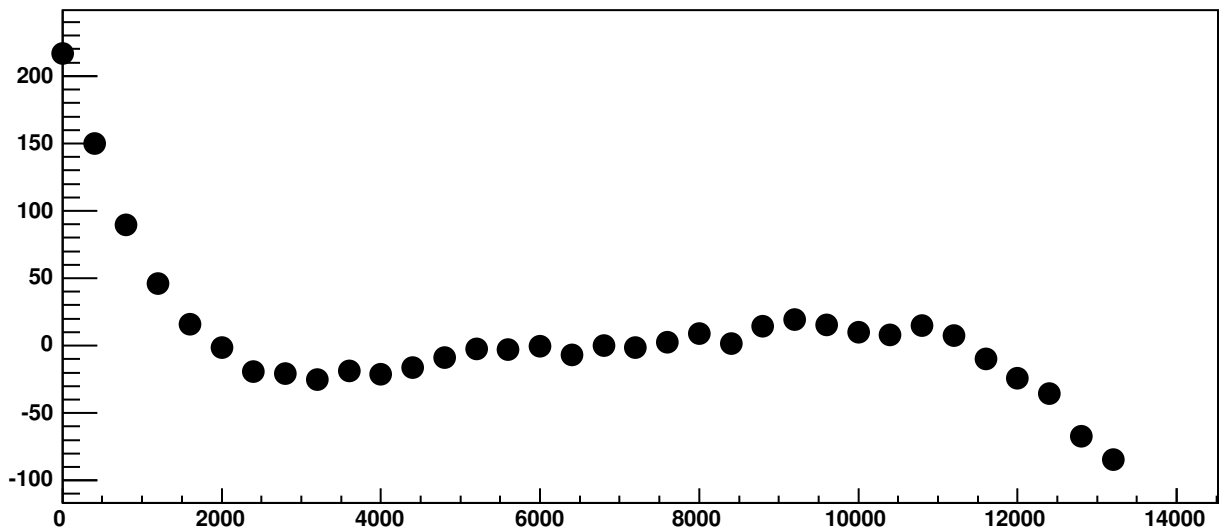
Chip 7, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

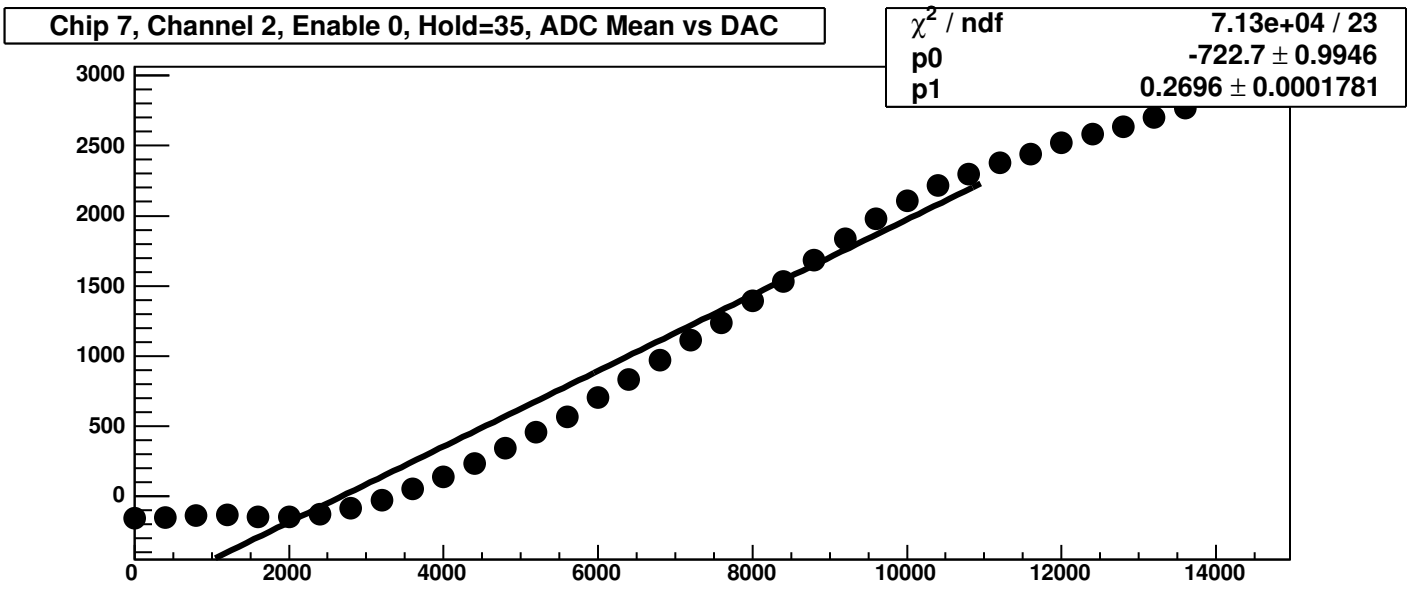


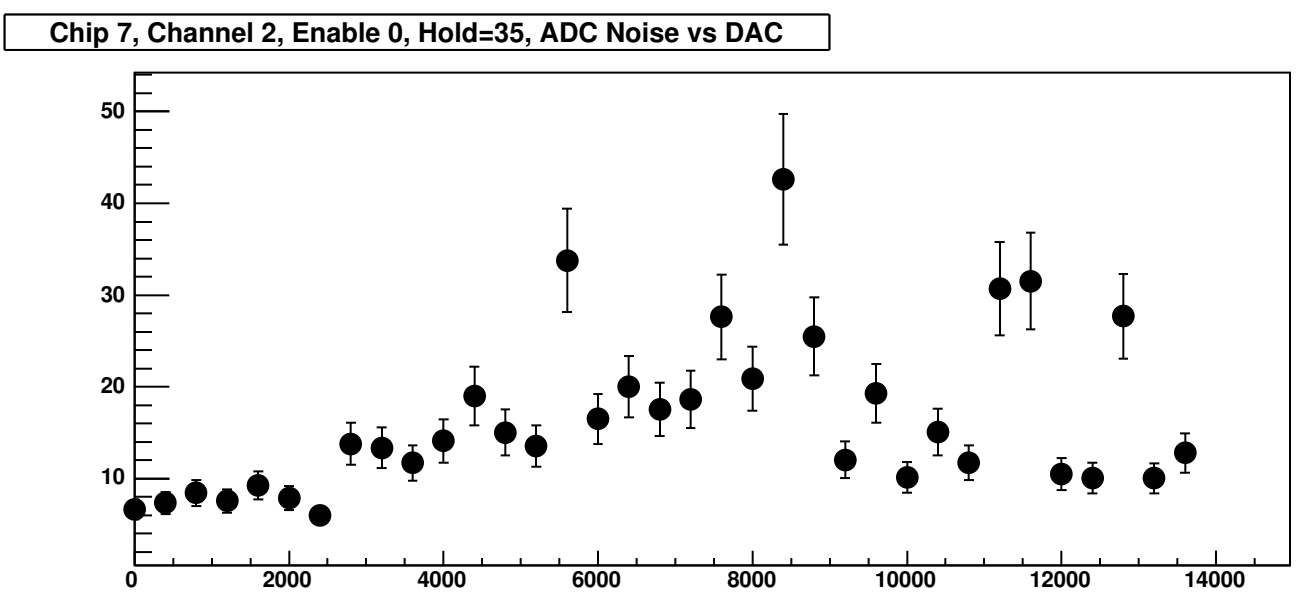
Chip 7, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC

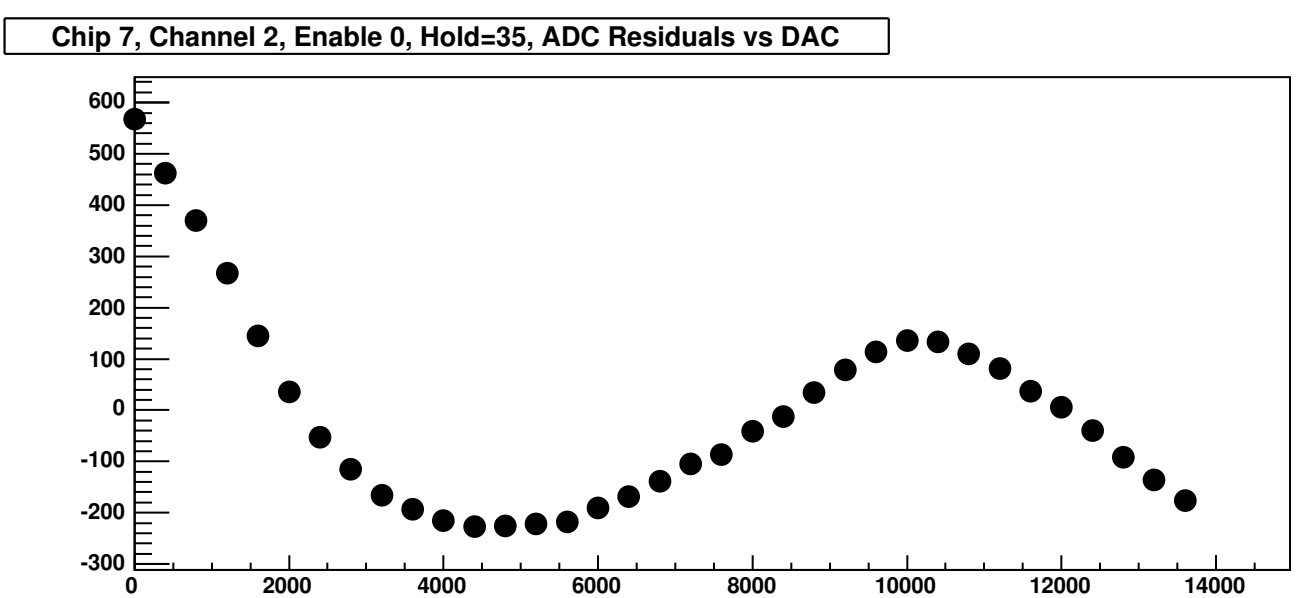


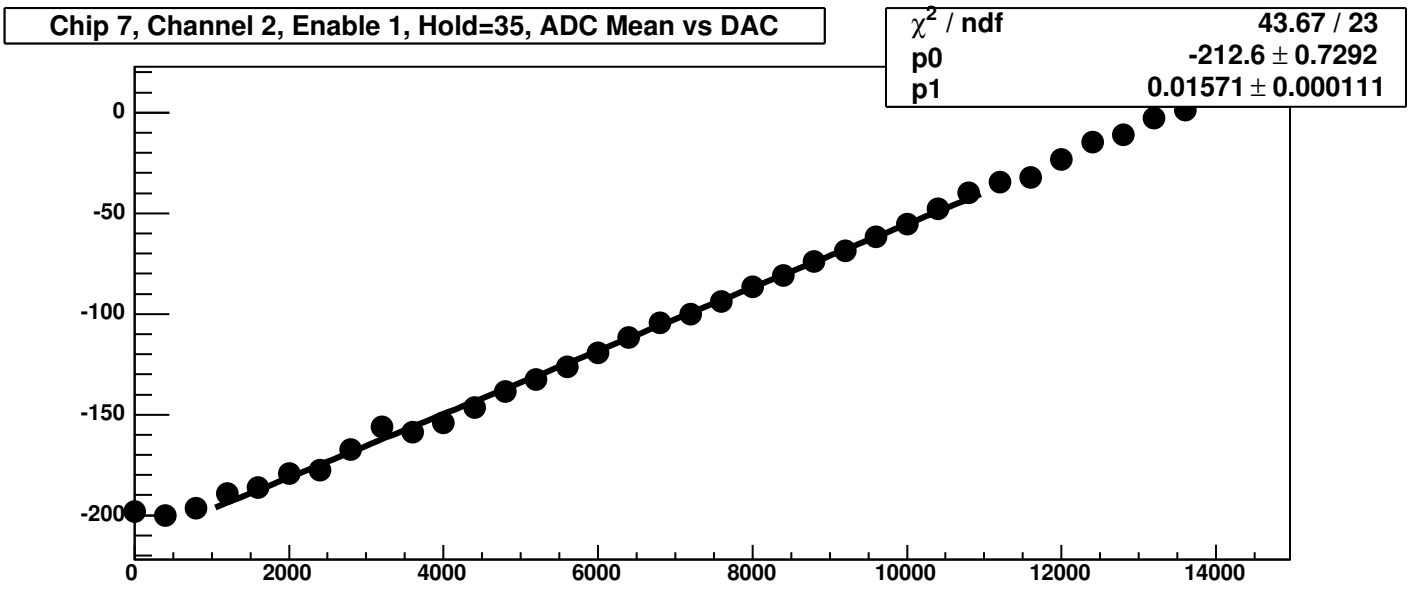
Chip 7, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

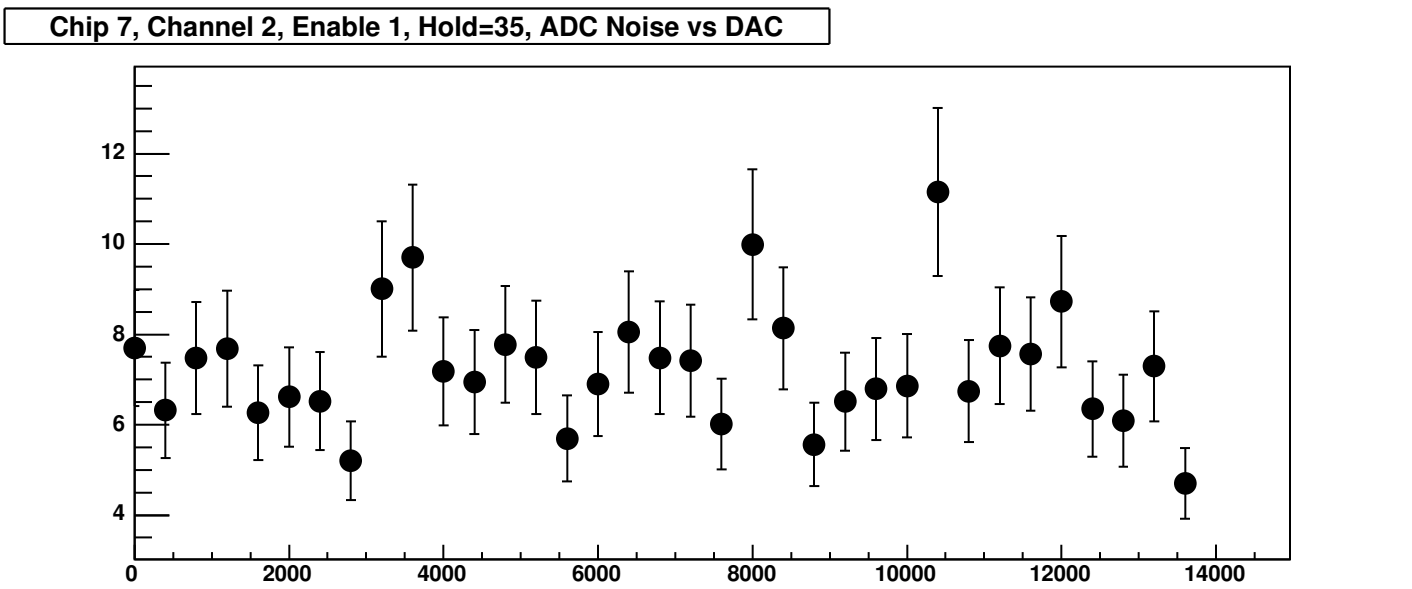


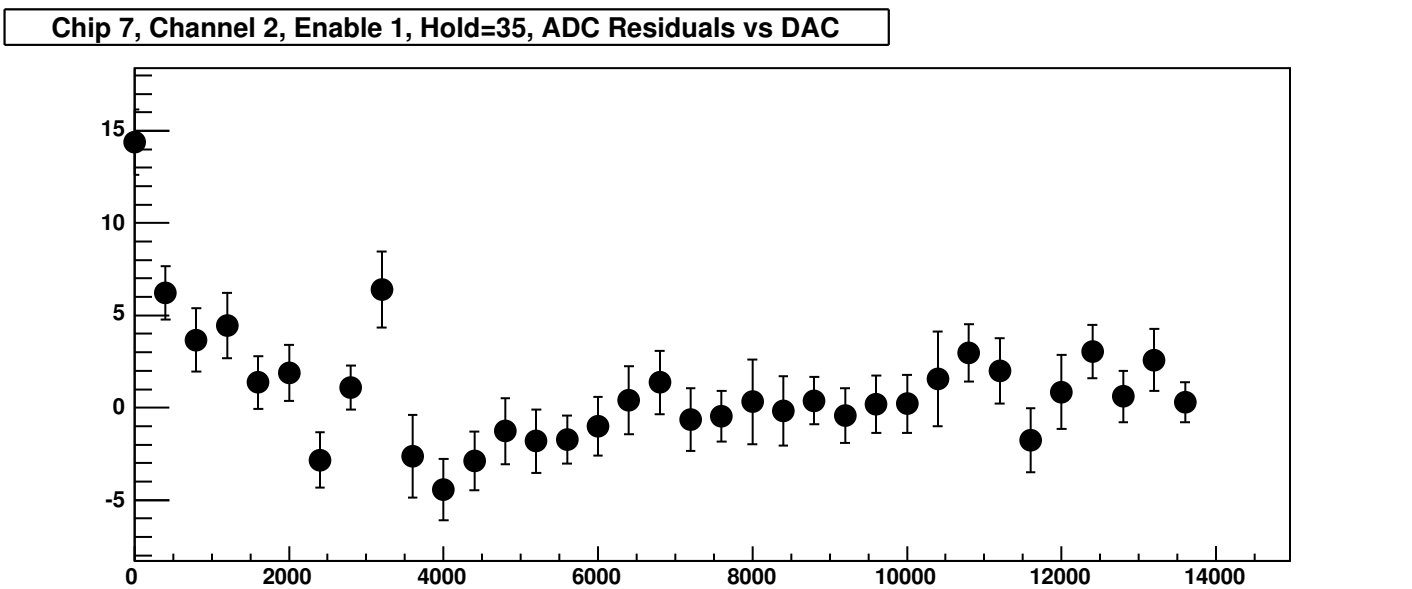

Chip 7, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 7.13e+04 / 23
p0 -722.7 ± 0.9946
p1 0.2696 ± 0.0001781
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

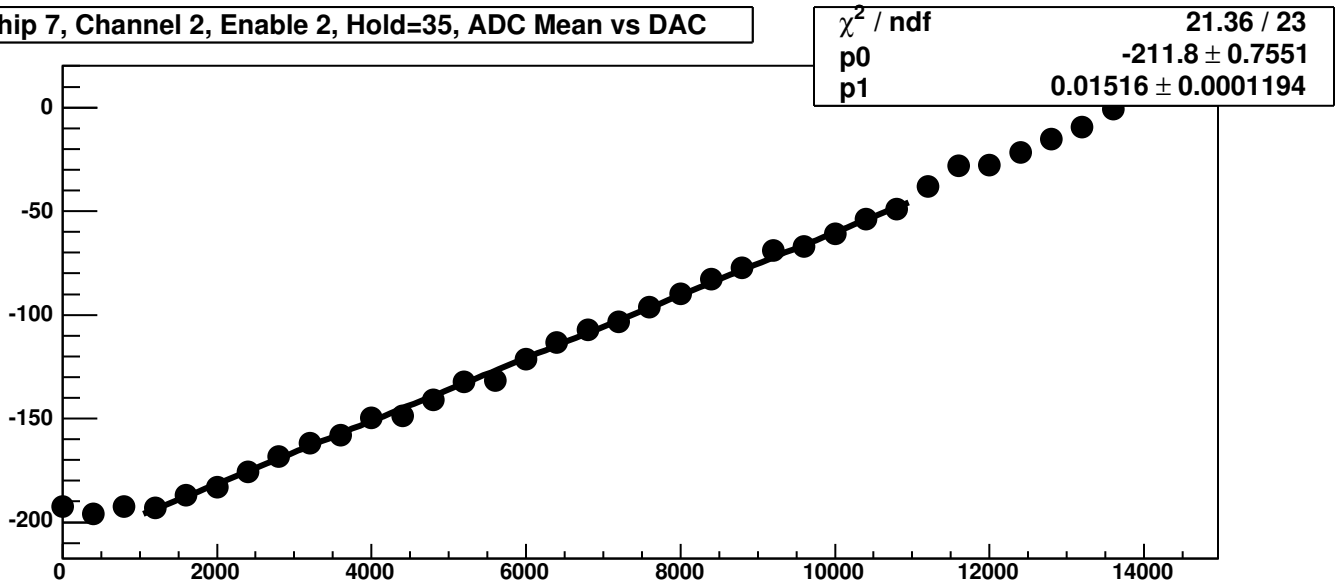

Chip 7, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 43.67 / 23
p0 -212.6 ± 0.7292
p1 0.01571 ± 0.000111
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

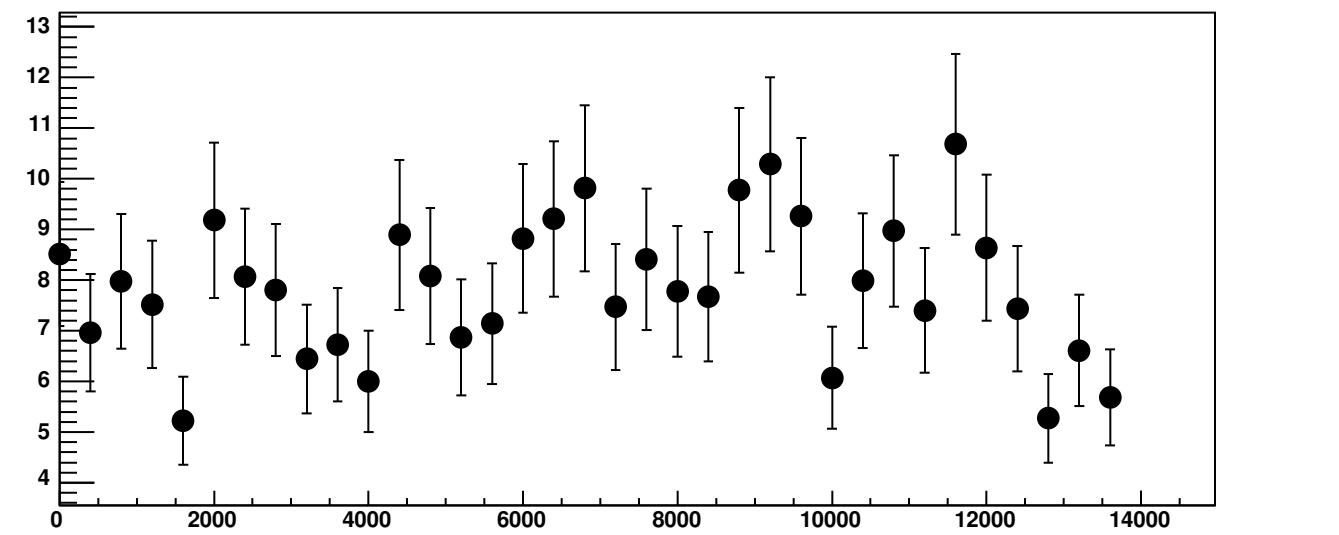

Chip 7, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

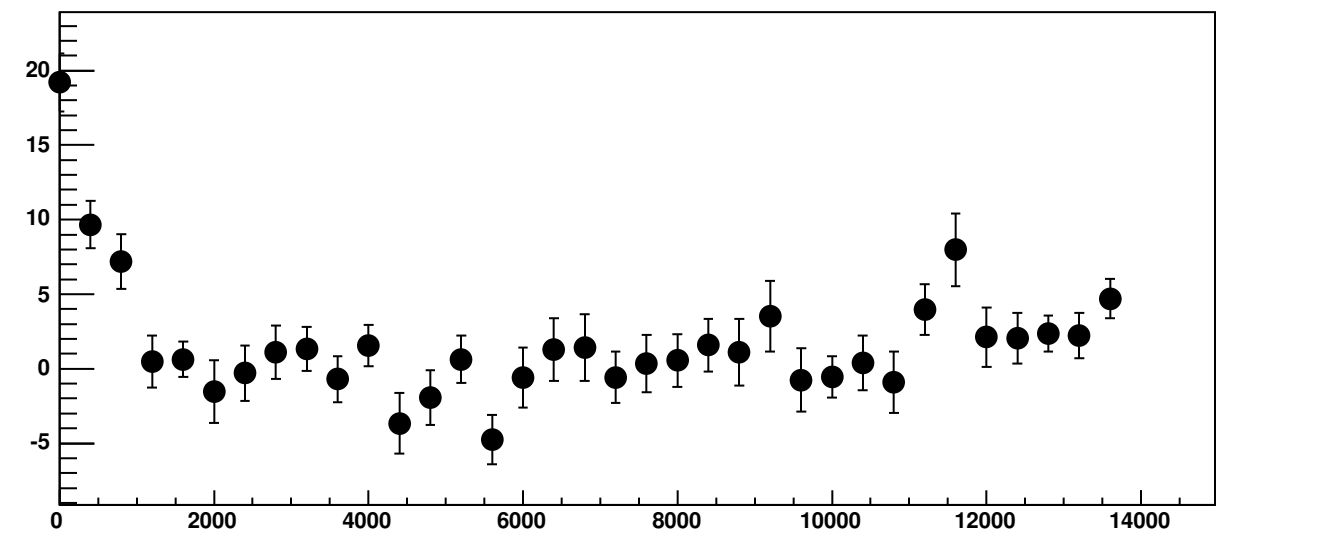
Chip 7, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

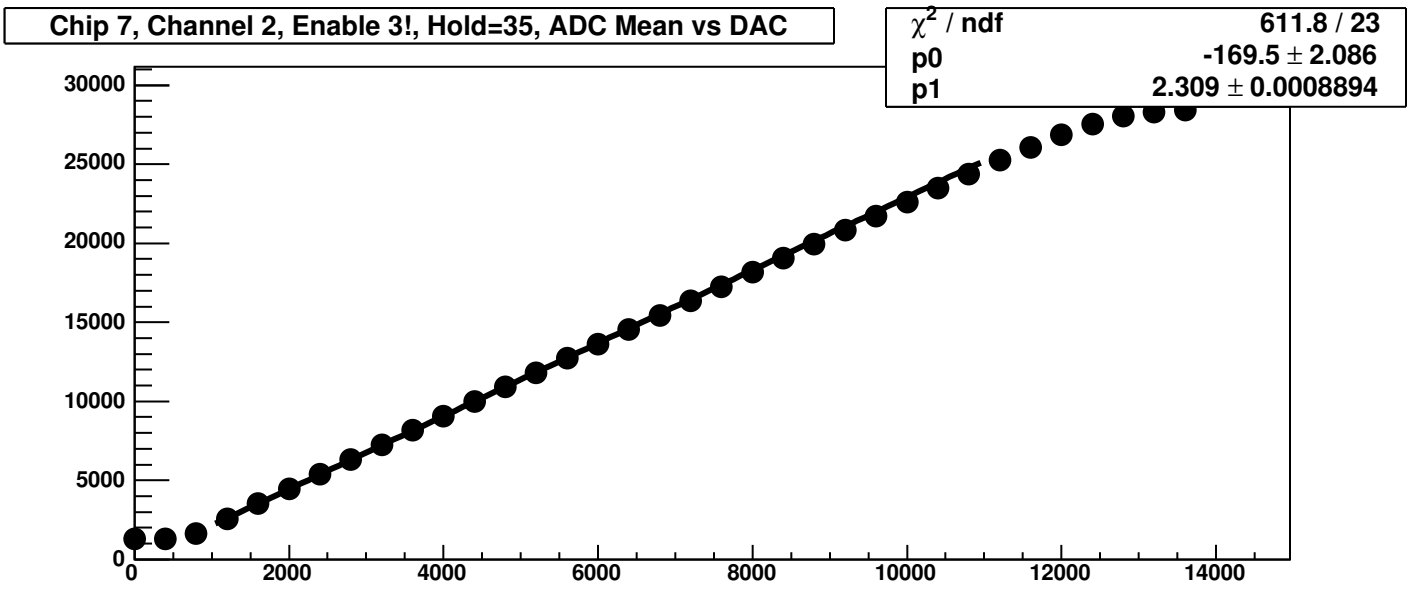


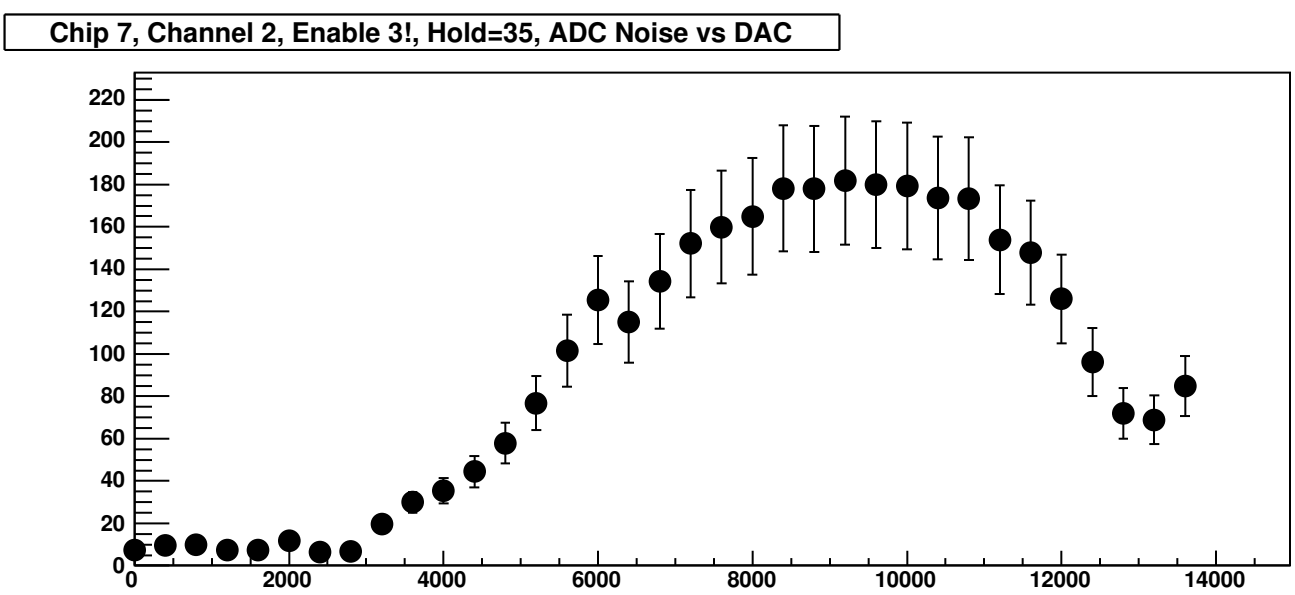
Chip 7, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

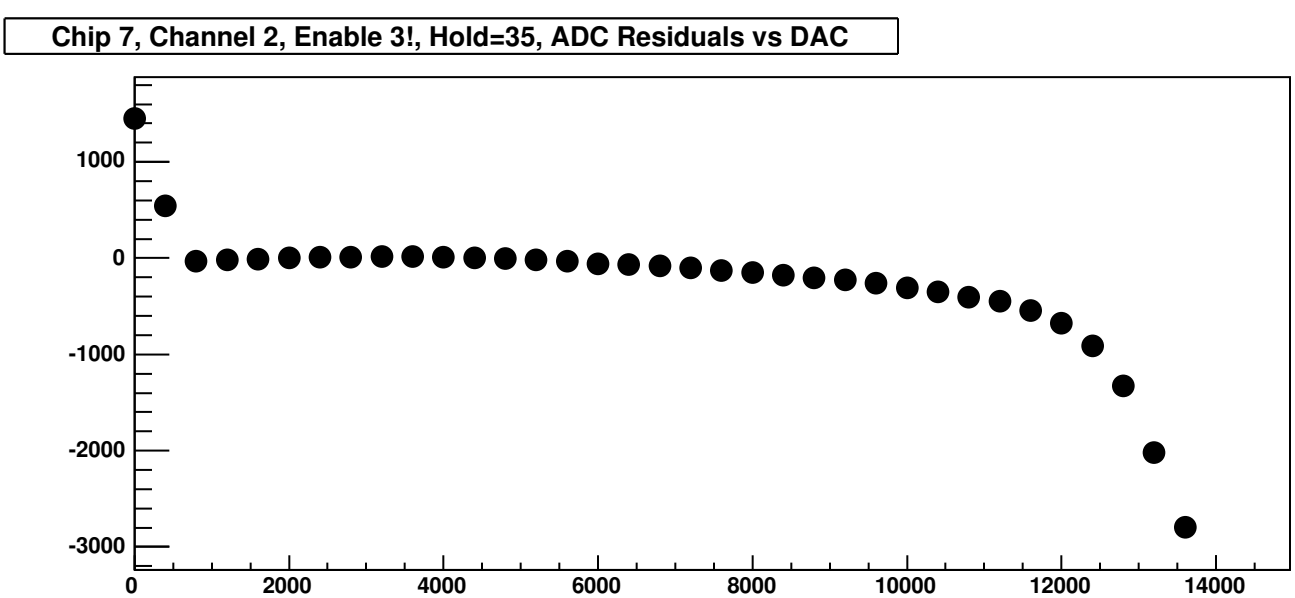


Chip 7, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

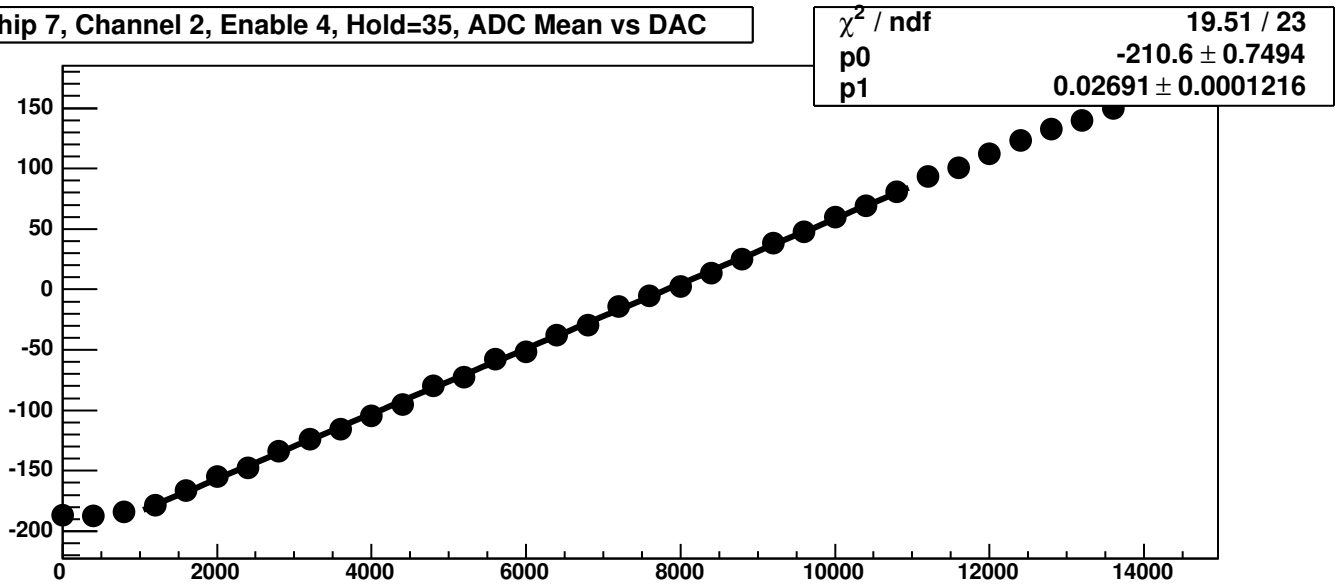



Chip 7, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 611.8 / 23
p0 -169.5 ± 2.086
p1 2.309 ± 0.0008894
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

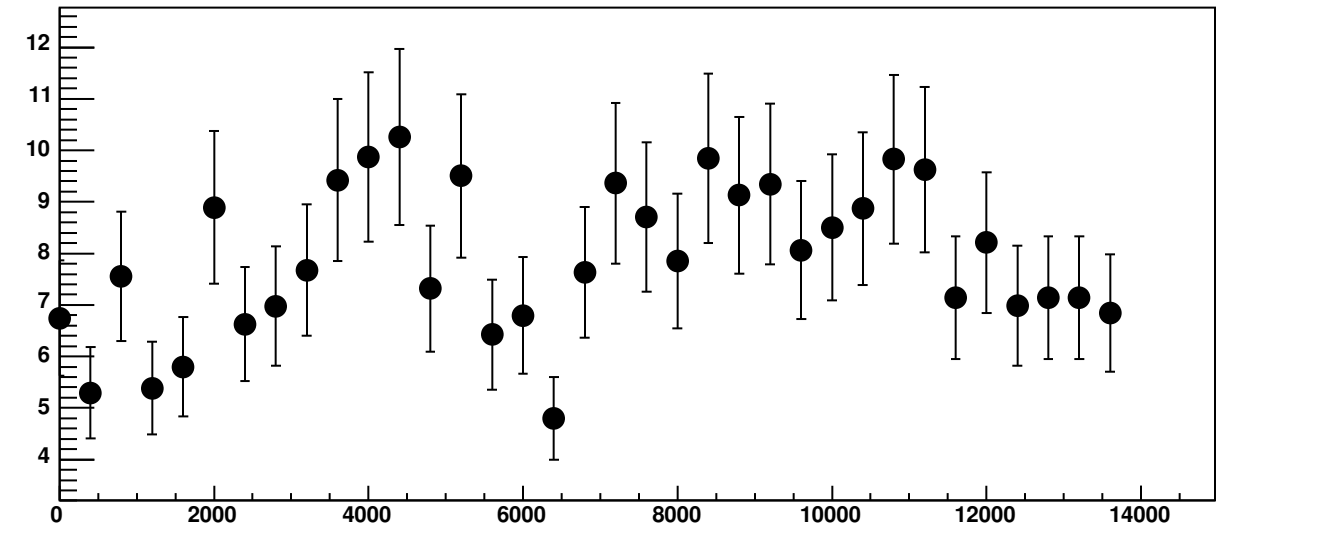

Chip 7, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

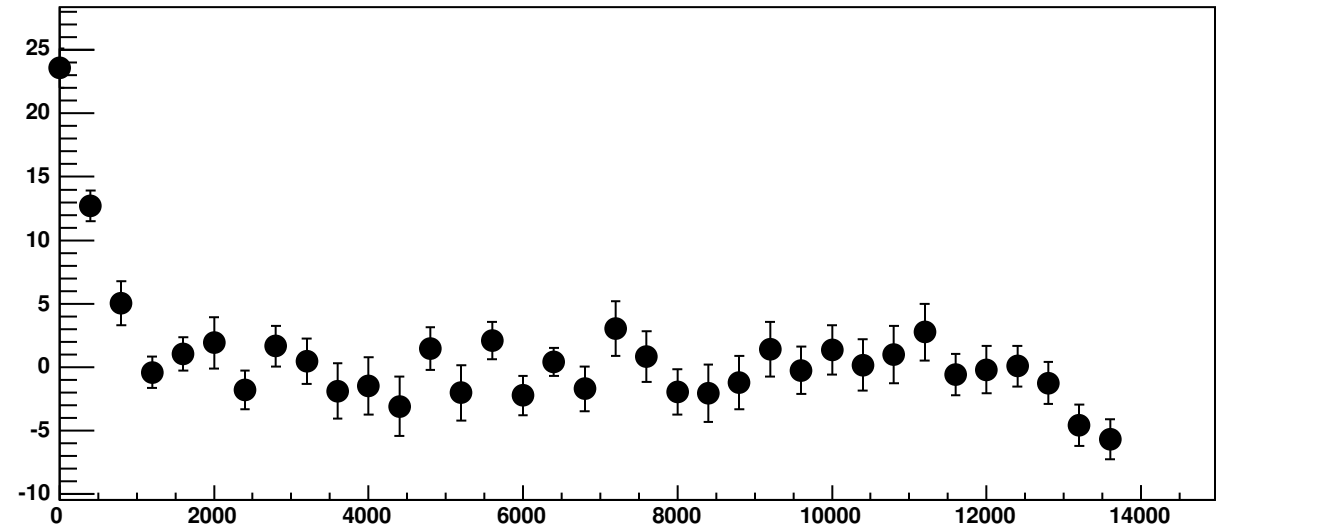
Chip 7, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

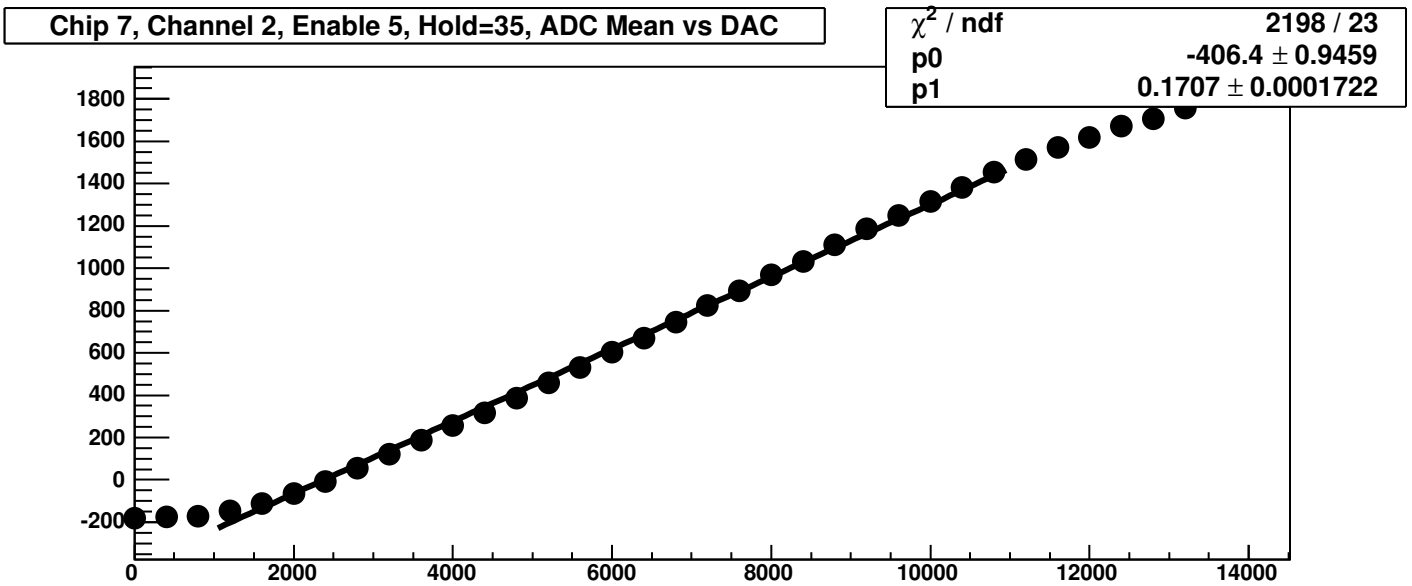


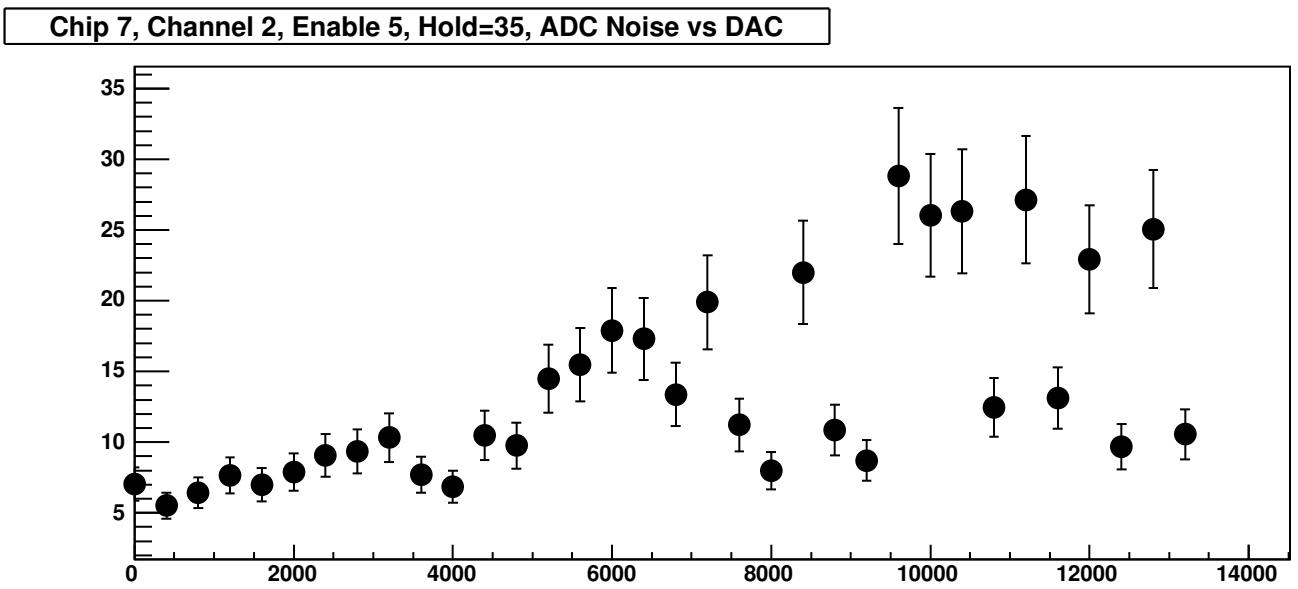
Chip 7, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

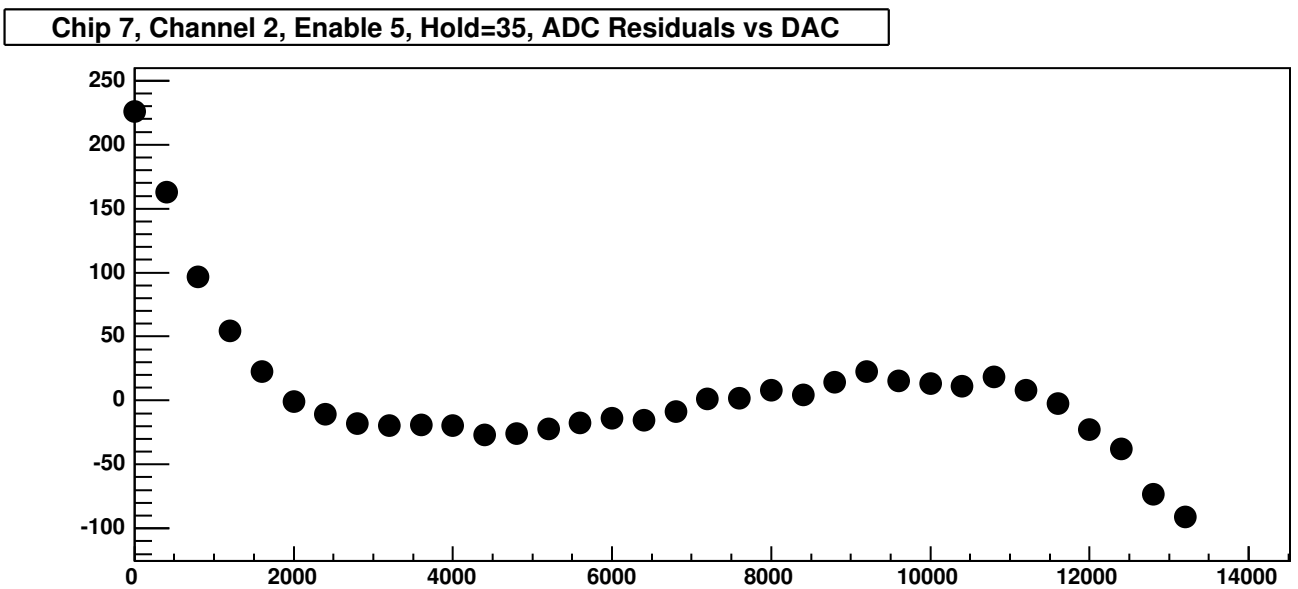


Chip 7, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

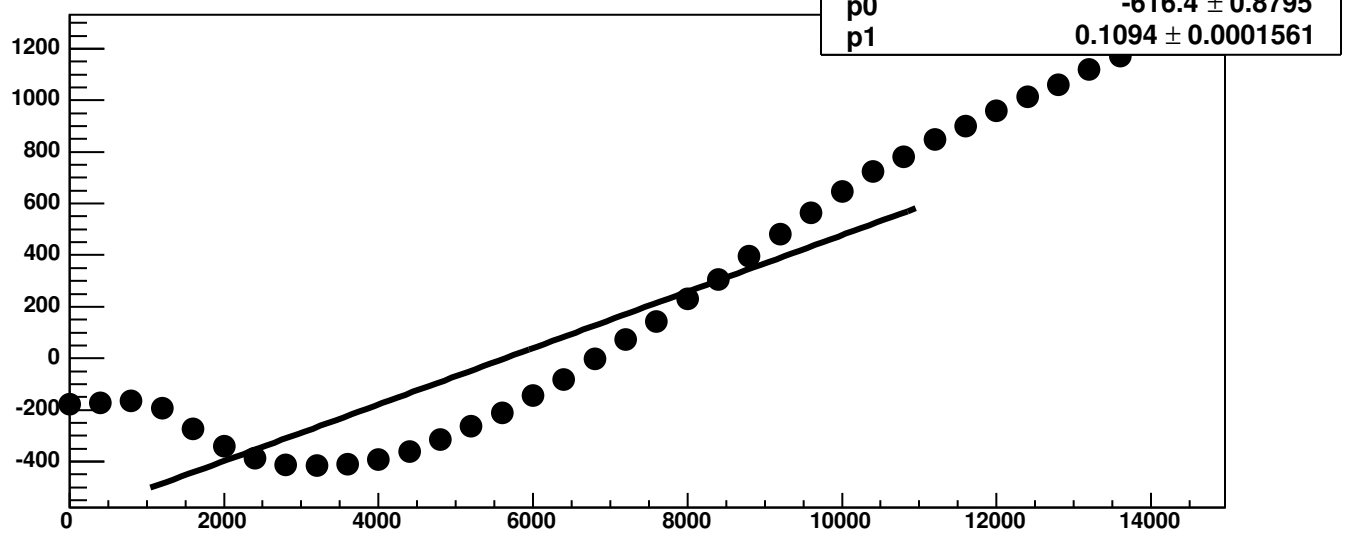



Chip 7, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2198 / 23
p0 -406.4 ± 0.9459
p1 0.1707 ± 0.0001722
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

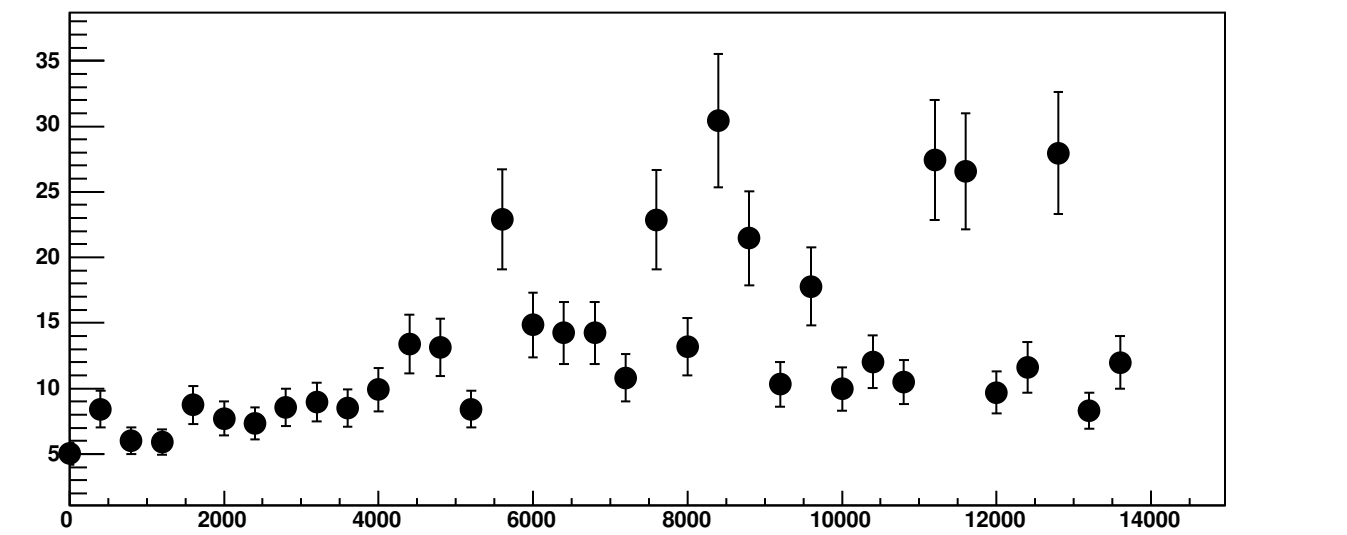

Chip 7, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

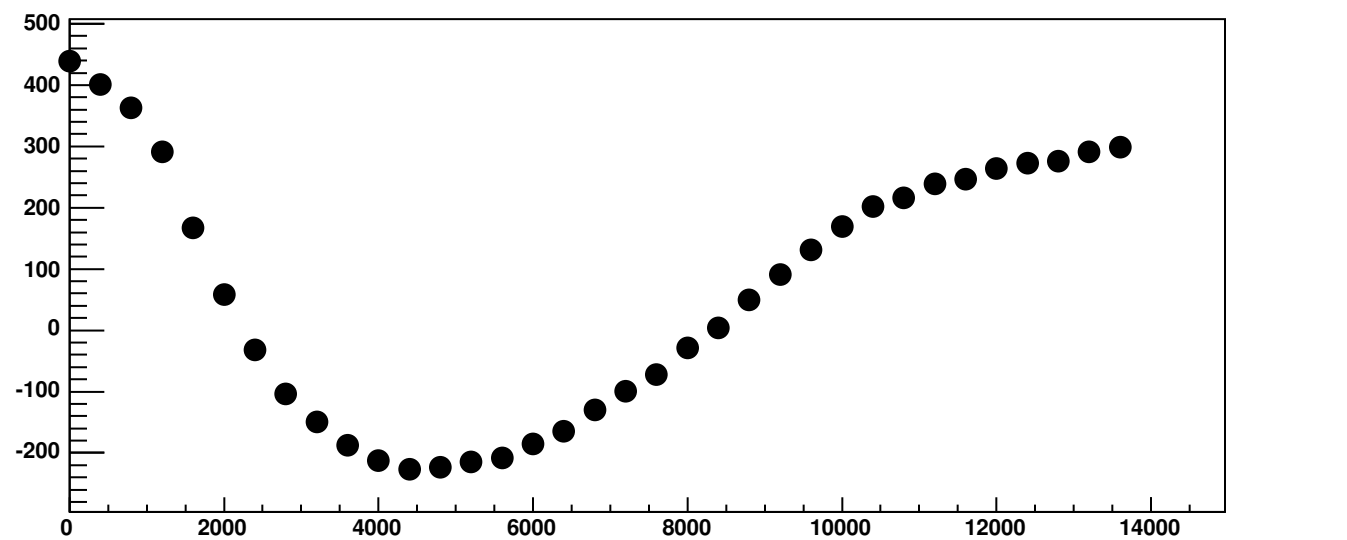
Chip 7, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

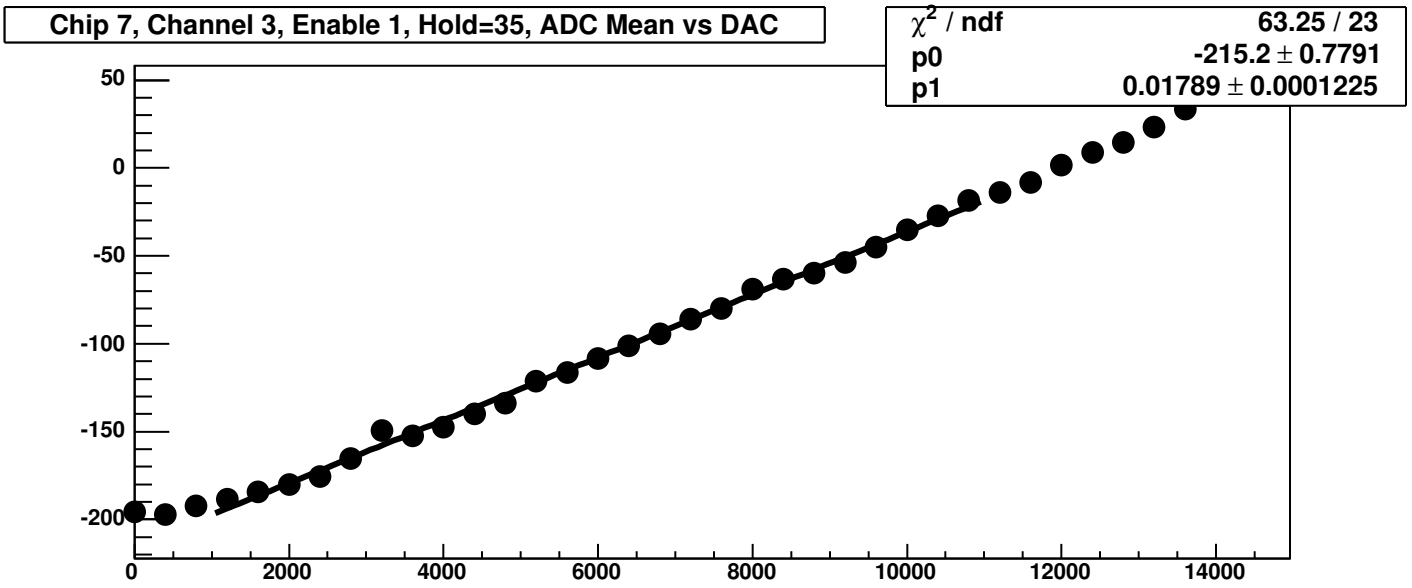


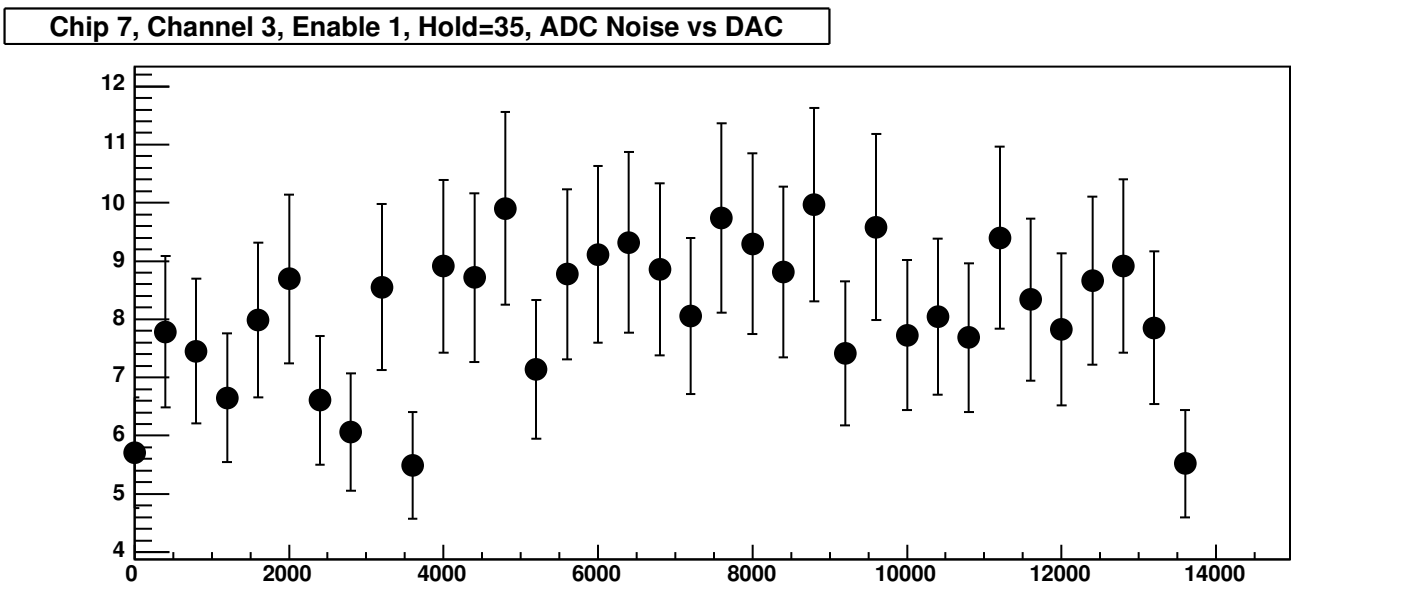
Chip 7, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

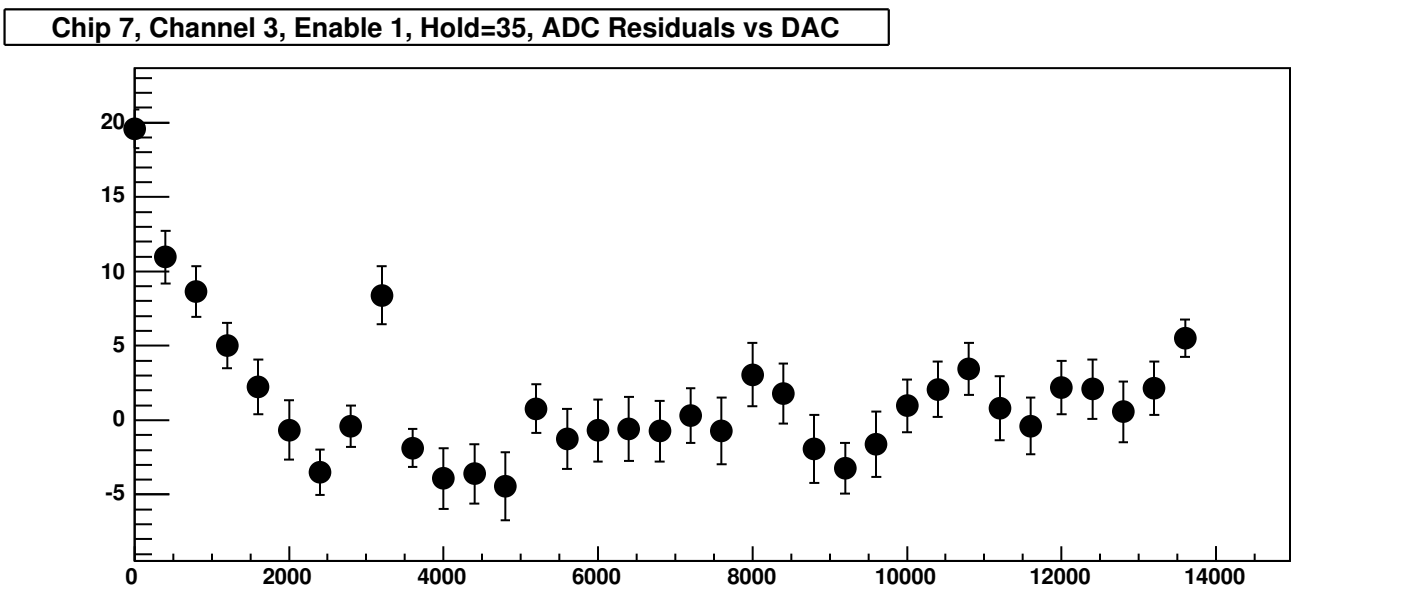


Chip 7, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

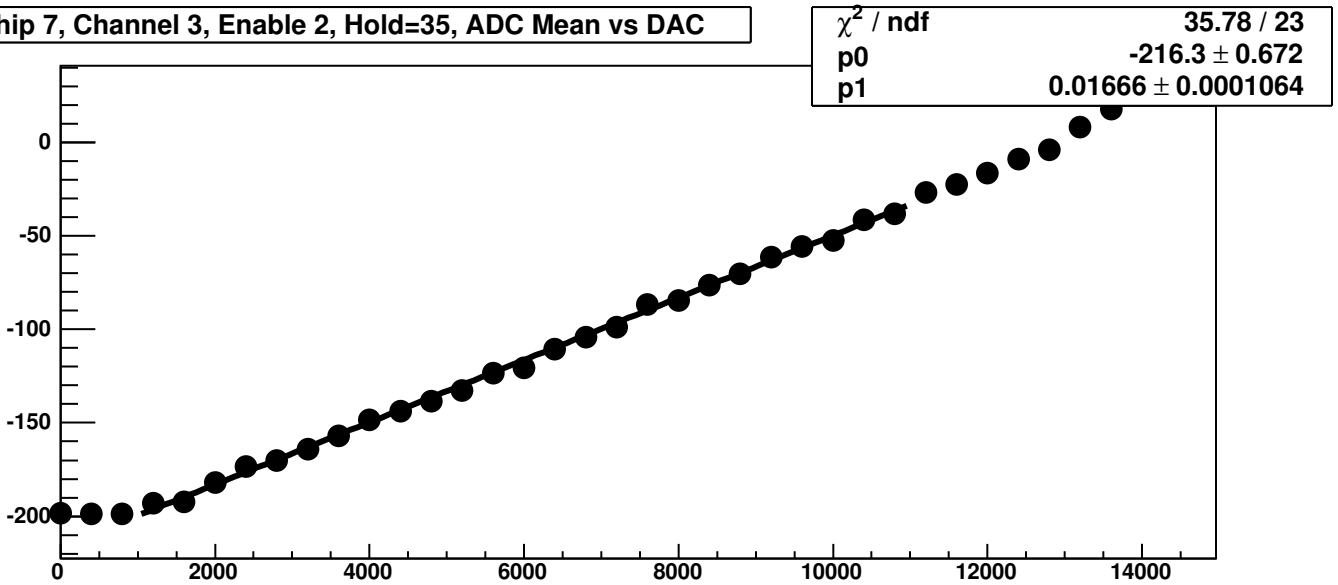



Chip 7, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 63.25 / 23
p0 -215.2 ± 0.7791
p1 0.01789 ± 0.0001225
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

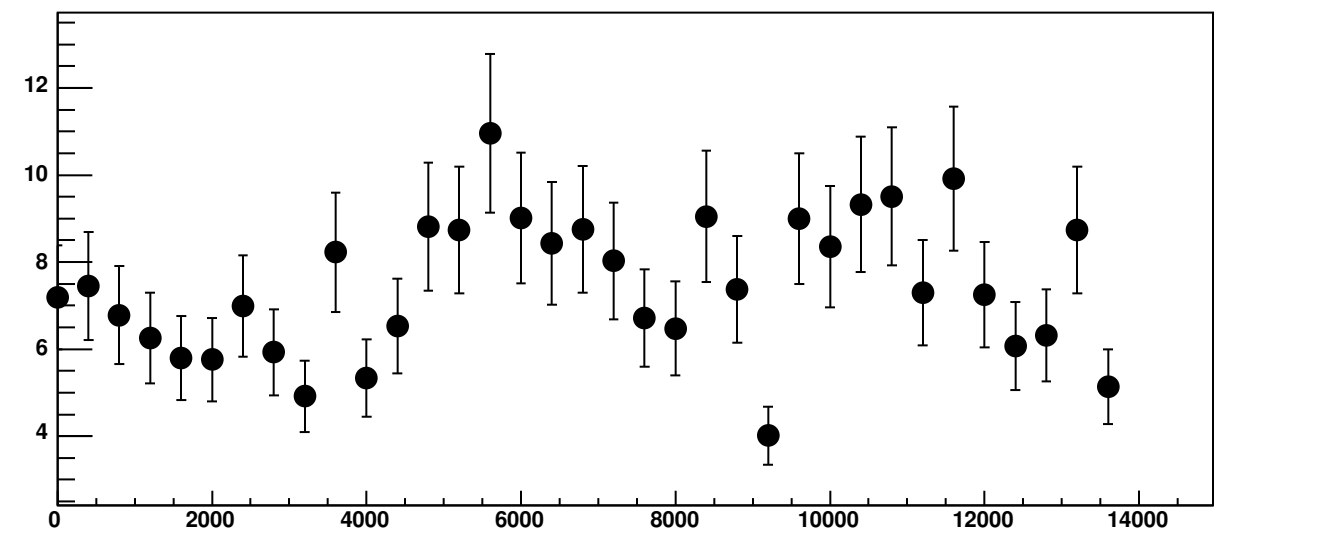

Chip 7, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

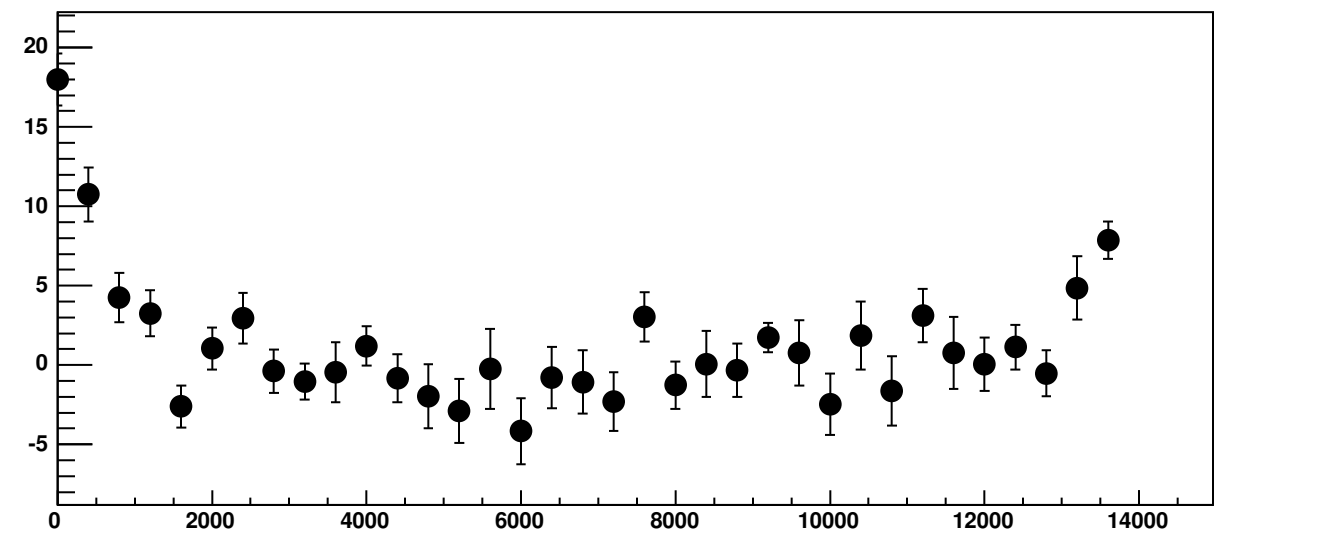
Chip 7, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

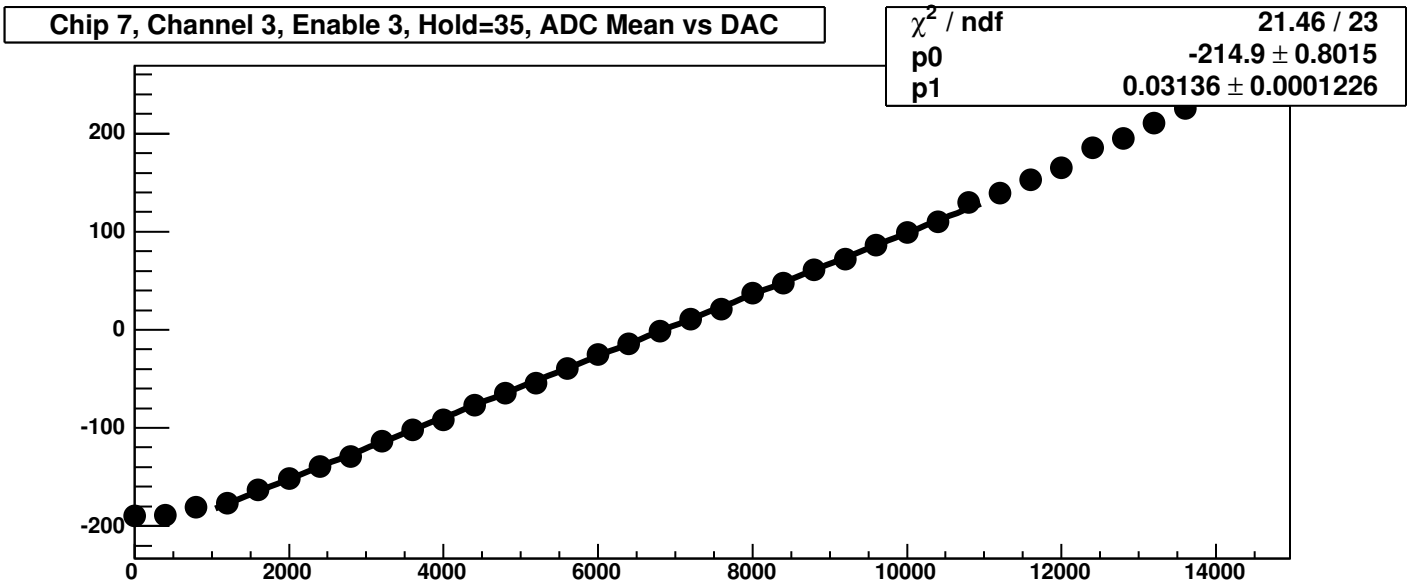


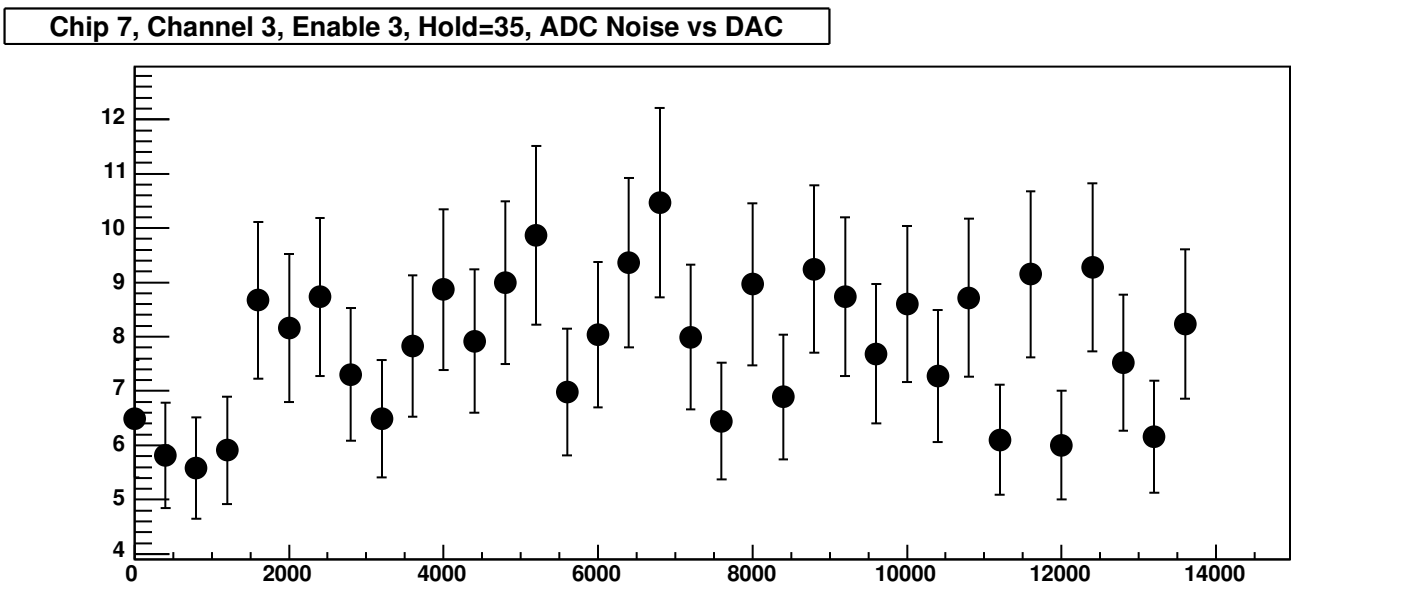
Chip 7, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC

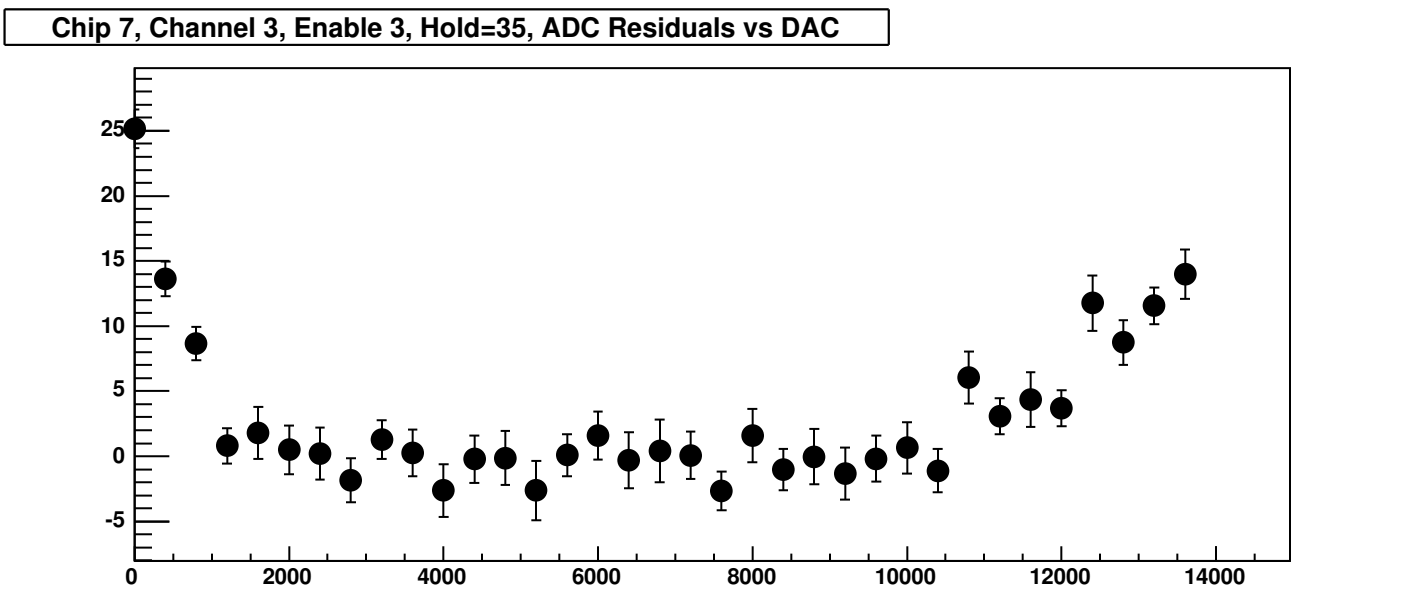


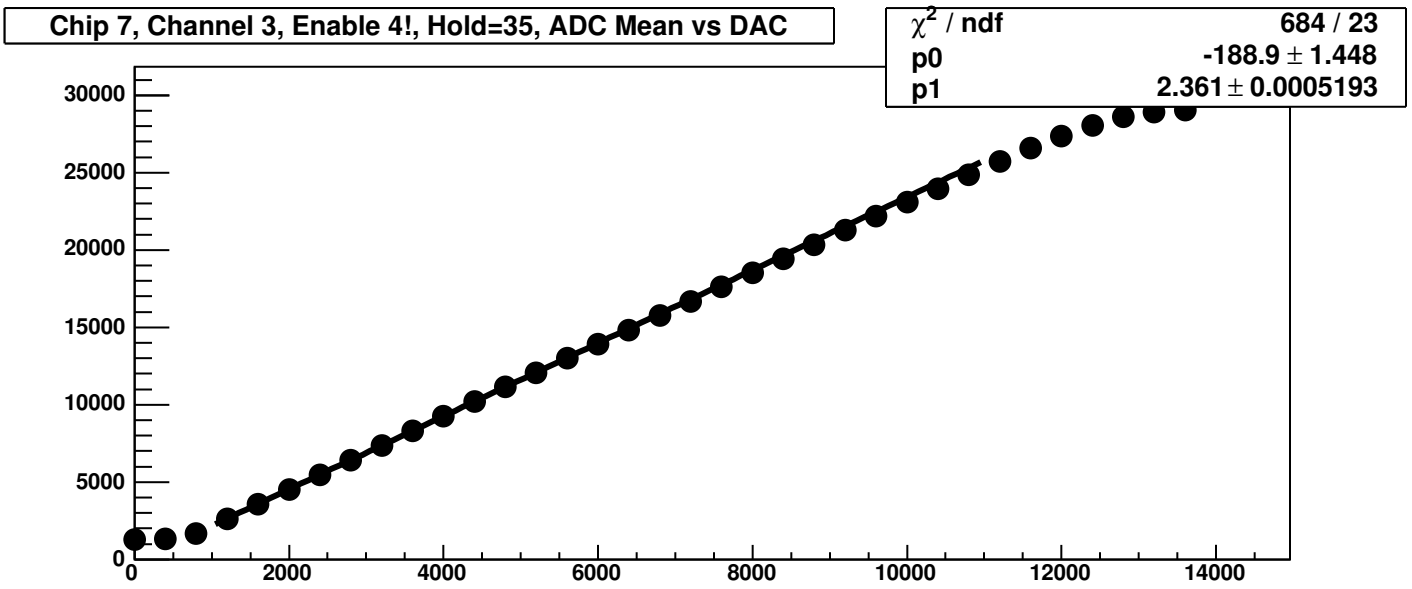
Chip 7, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

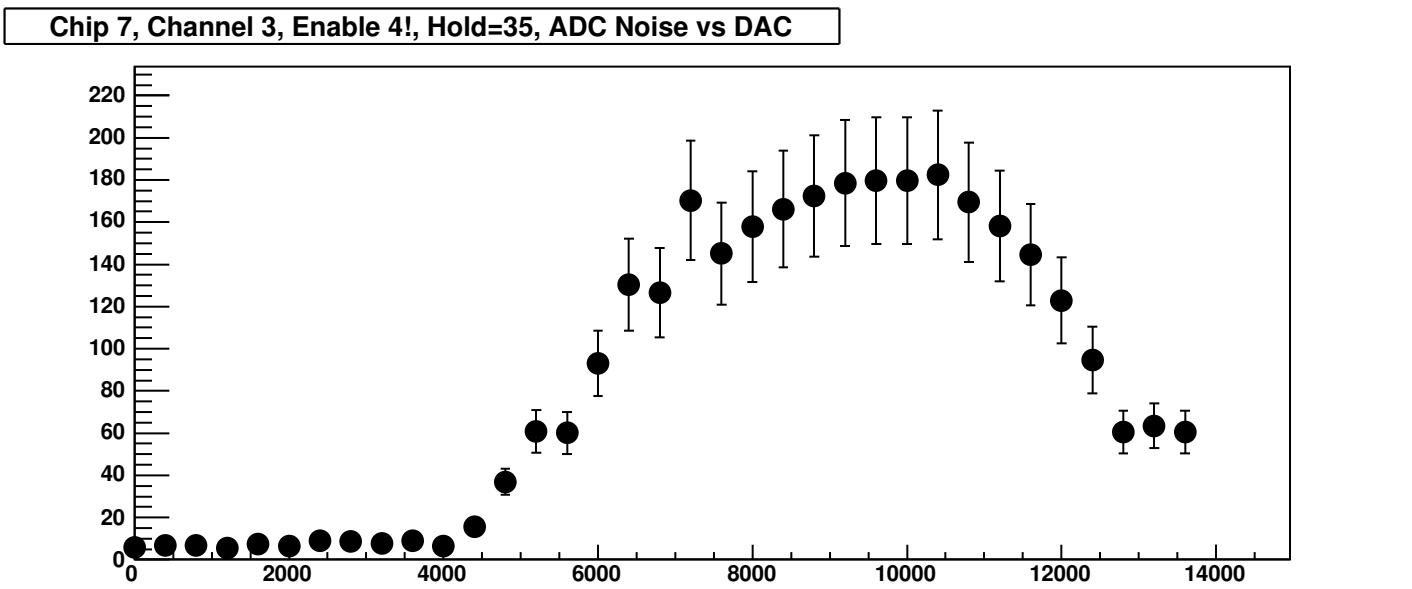


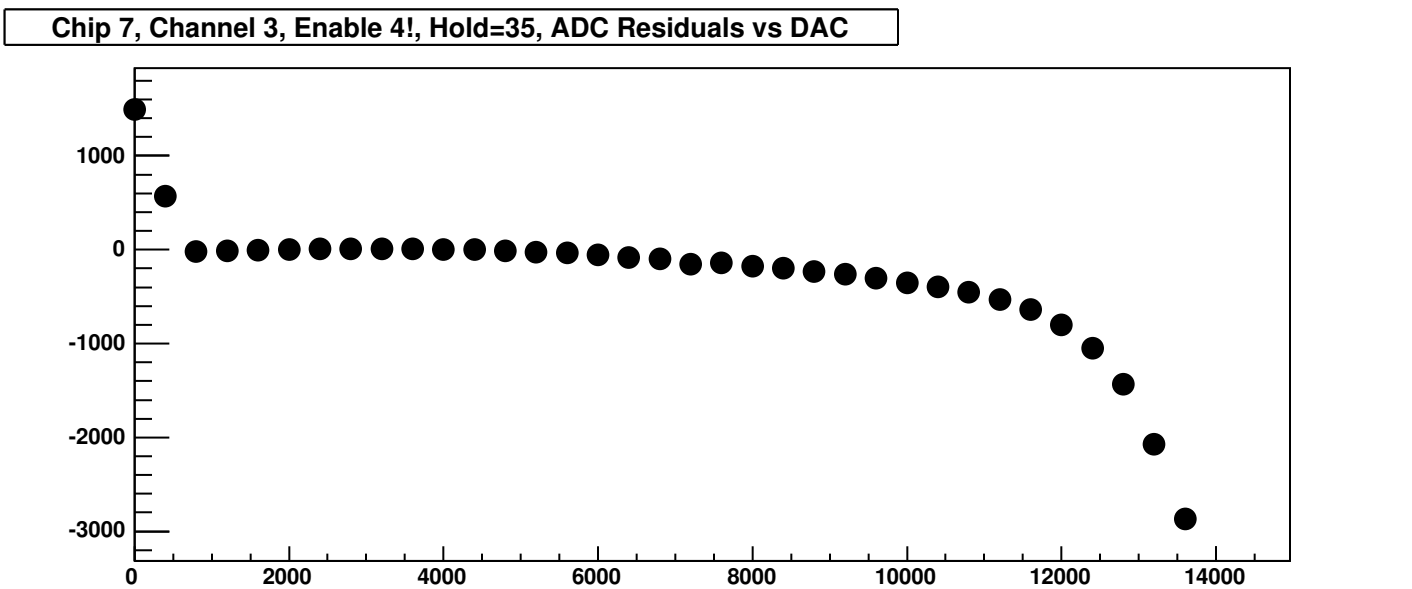

Chip 7, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.46 / 23
p0 -214.9 ± 0.8015
p1 0.03136 ± 0.0001226
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

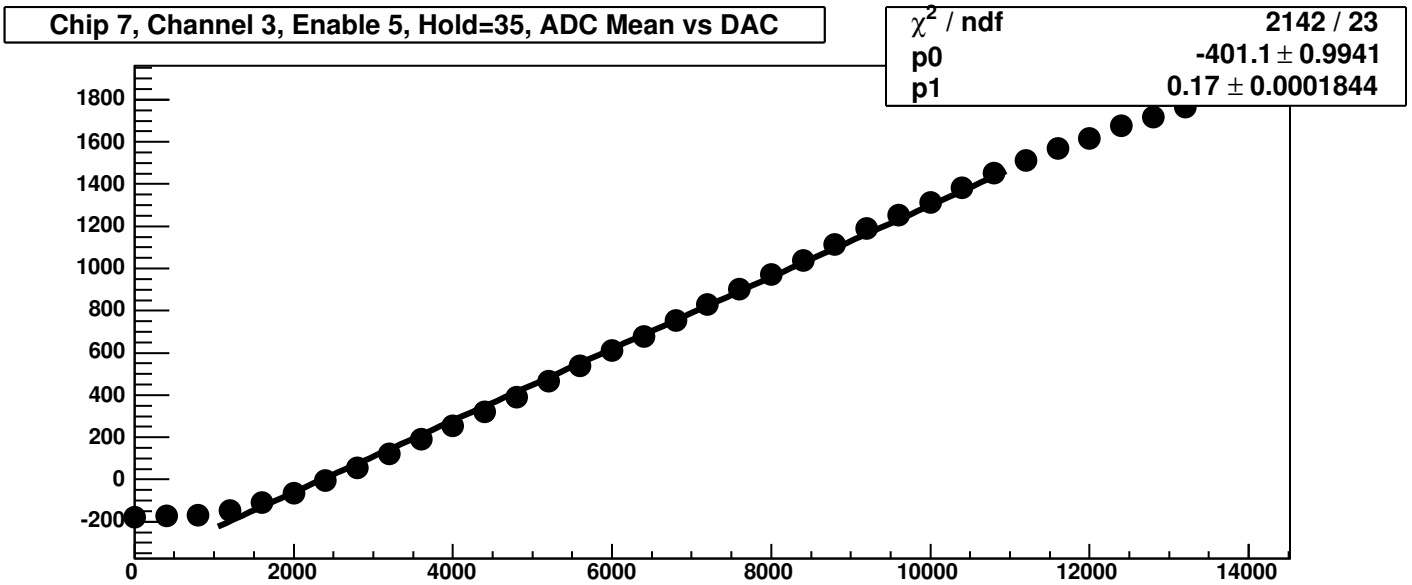

Chip 7, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

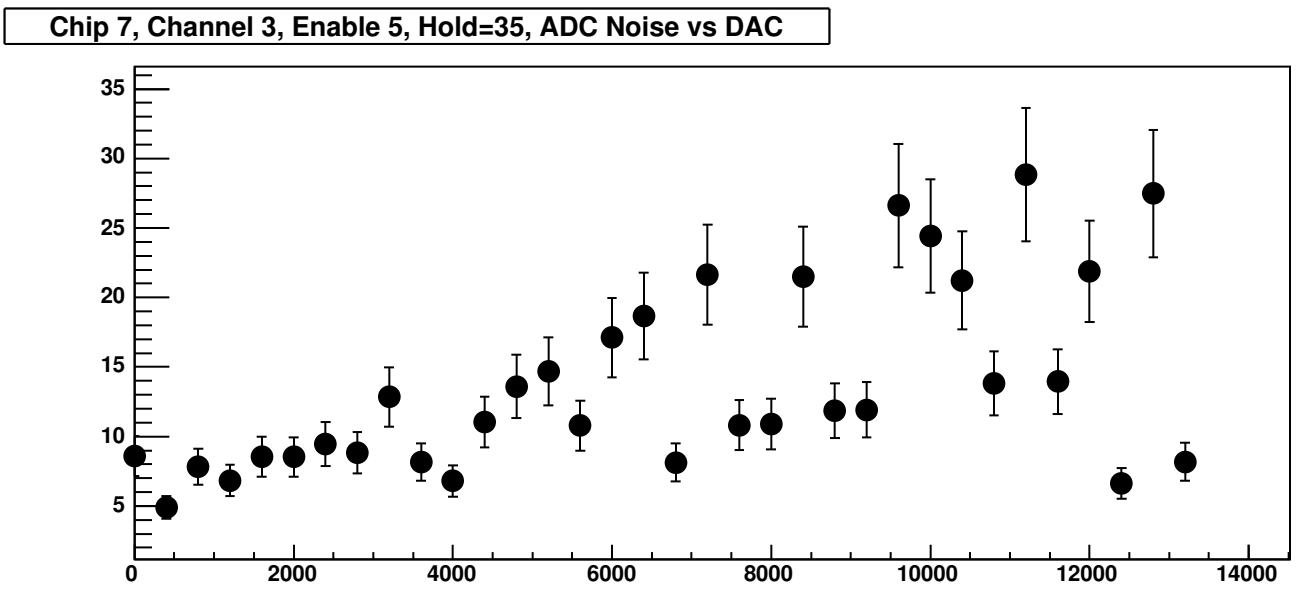

Chip 7, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

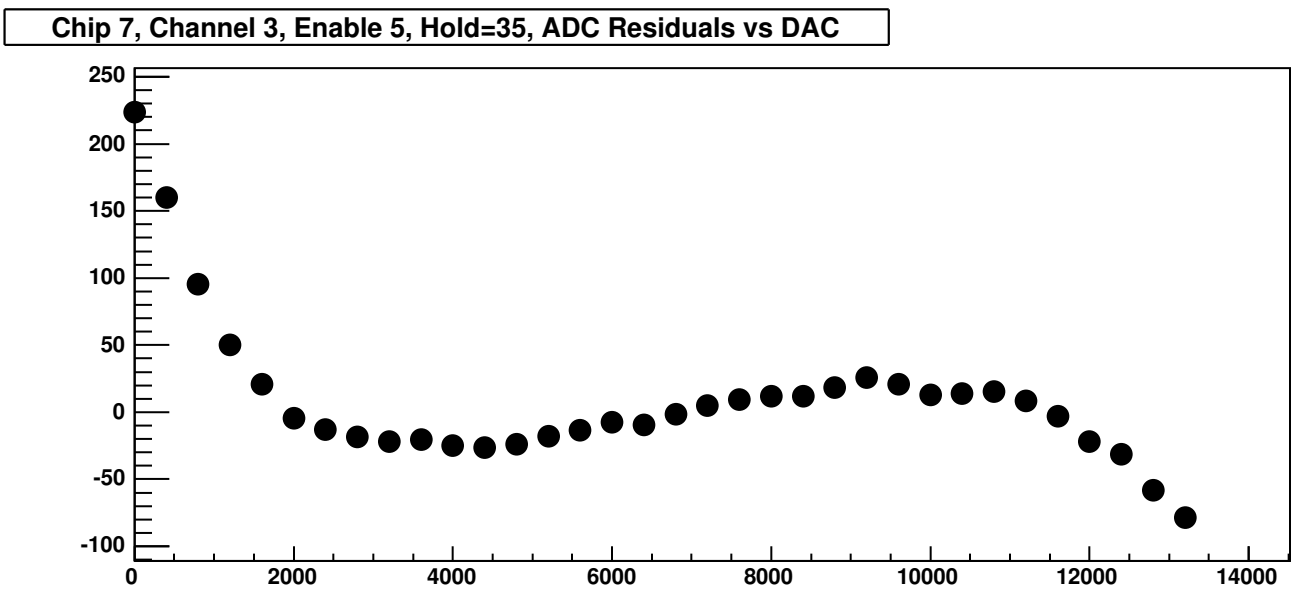

Chip 7, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 684 / 23
p0 -188.9 ± 1.448
p1 2.361 ± 0.0005193
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

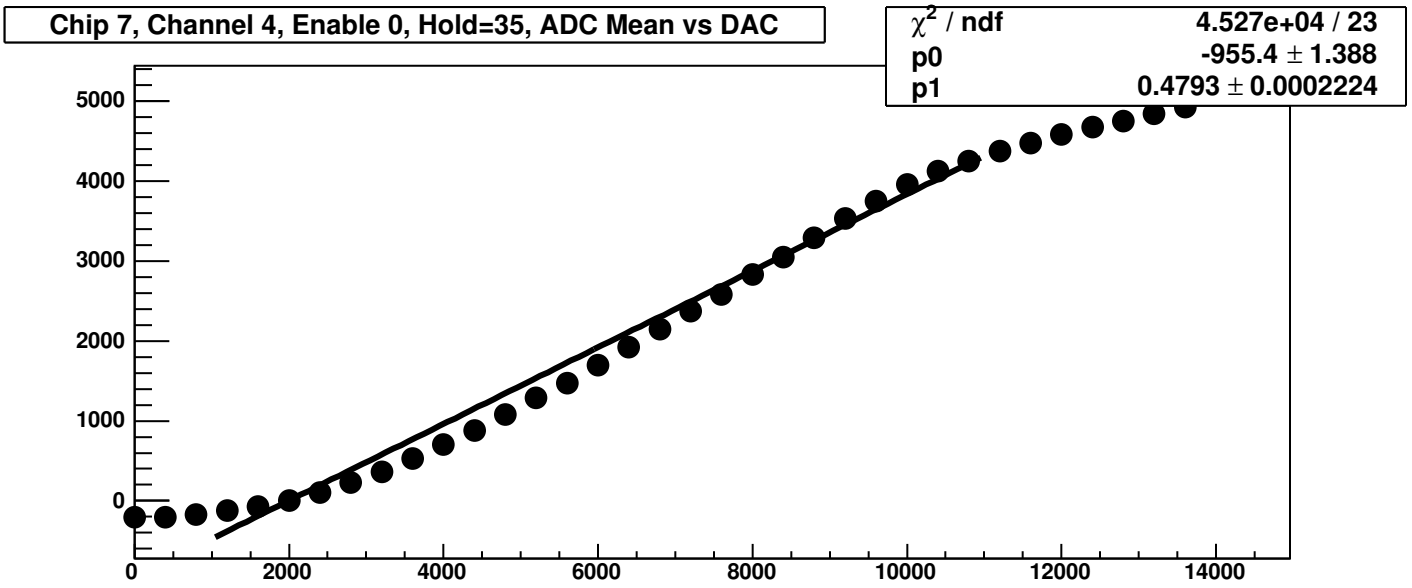

Chip 7, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

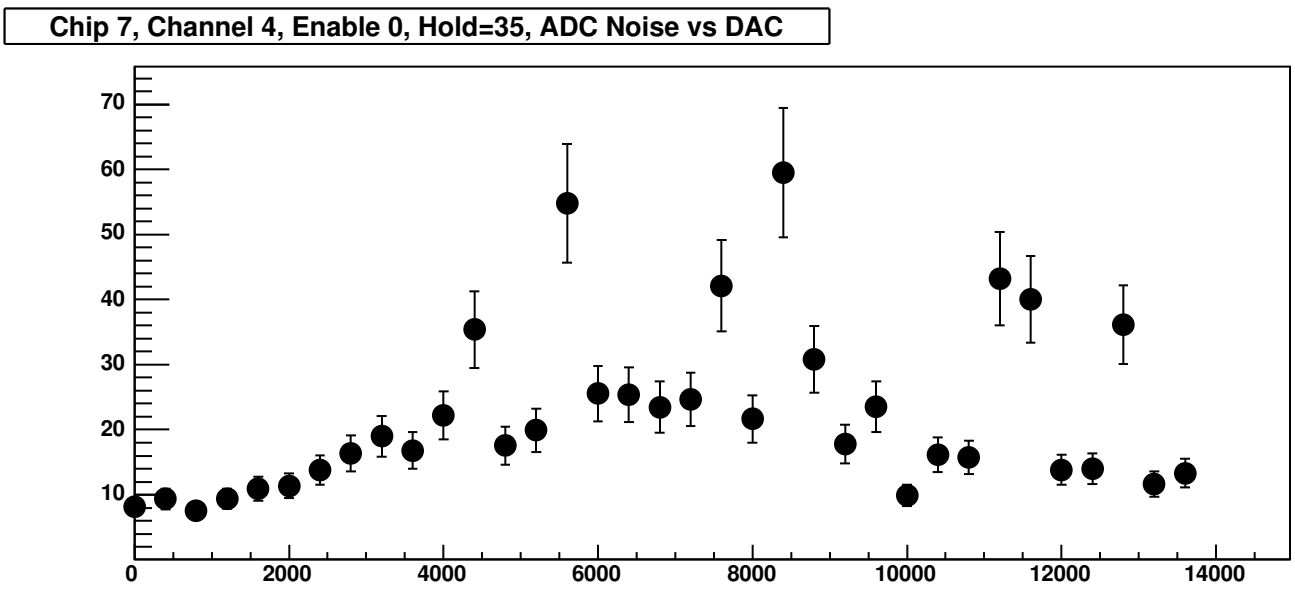

Chip 7, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

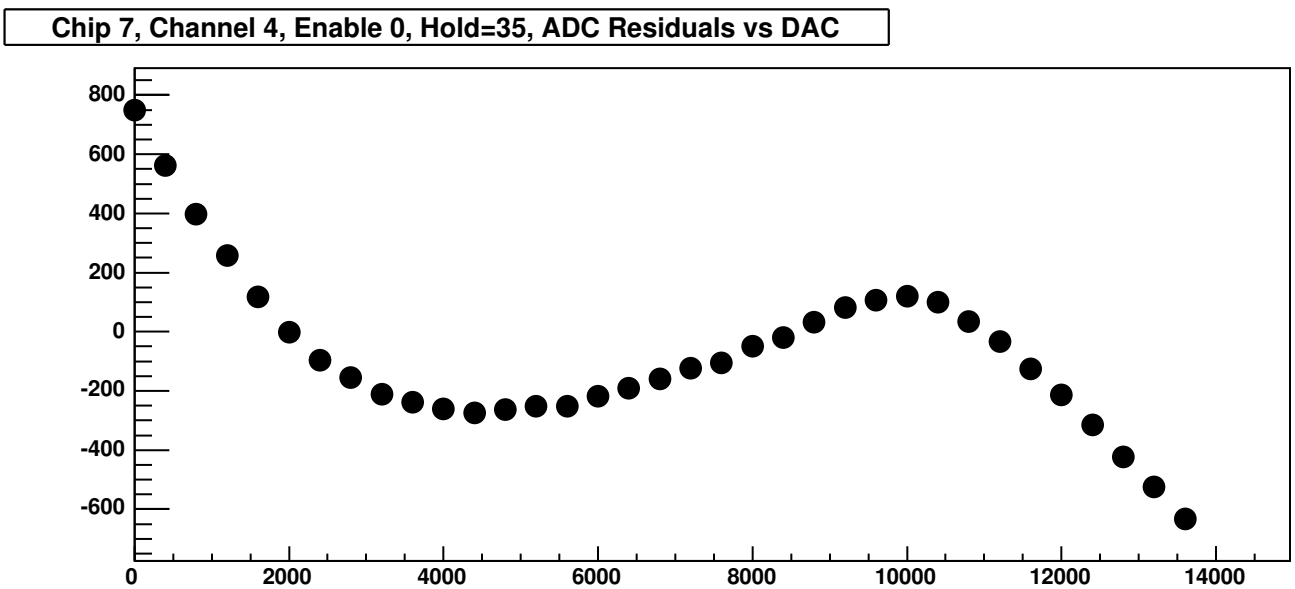

Chip 7, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2142 / 23
p0 -401.1 ± 0.9941
p1 0.17 ± 0.0001844
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

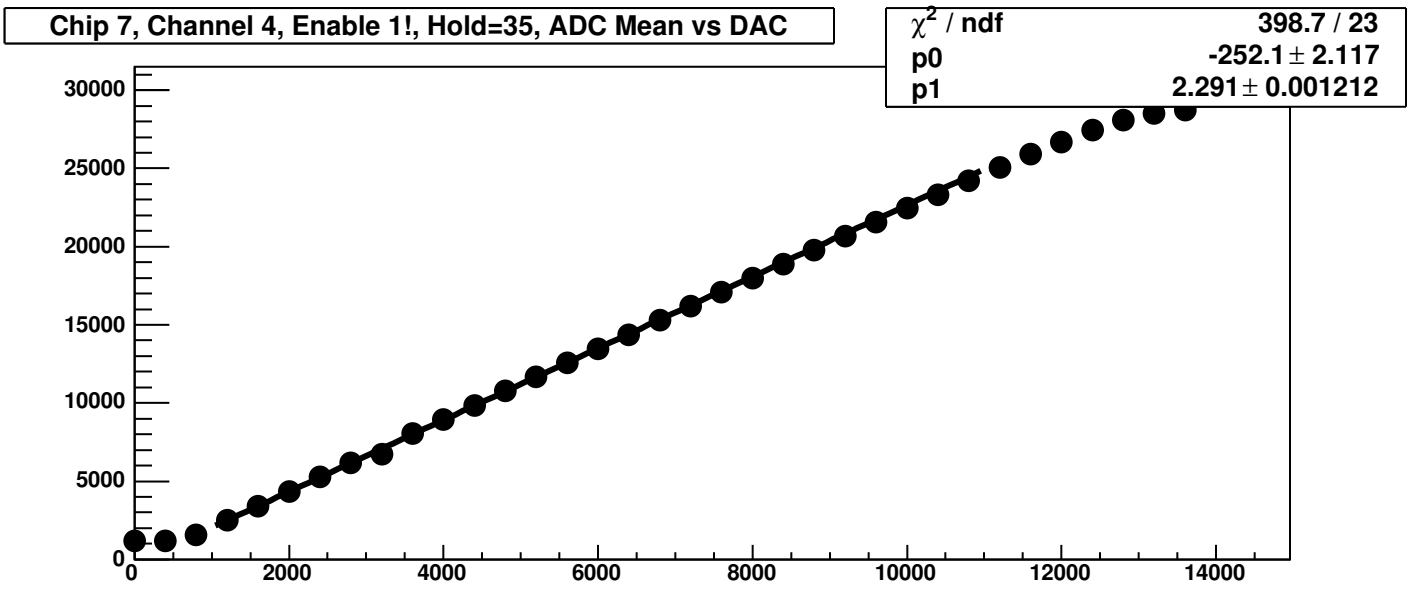

Chip 7, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

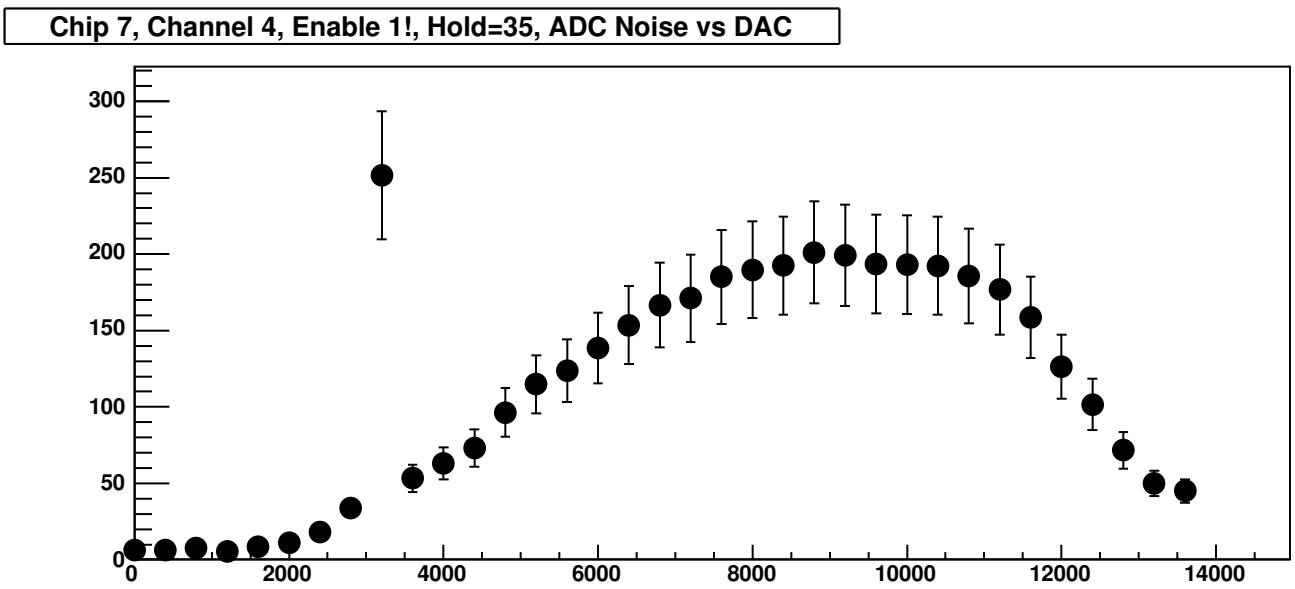

Chip 7, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

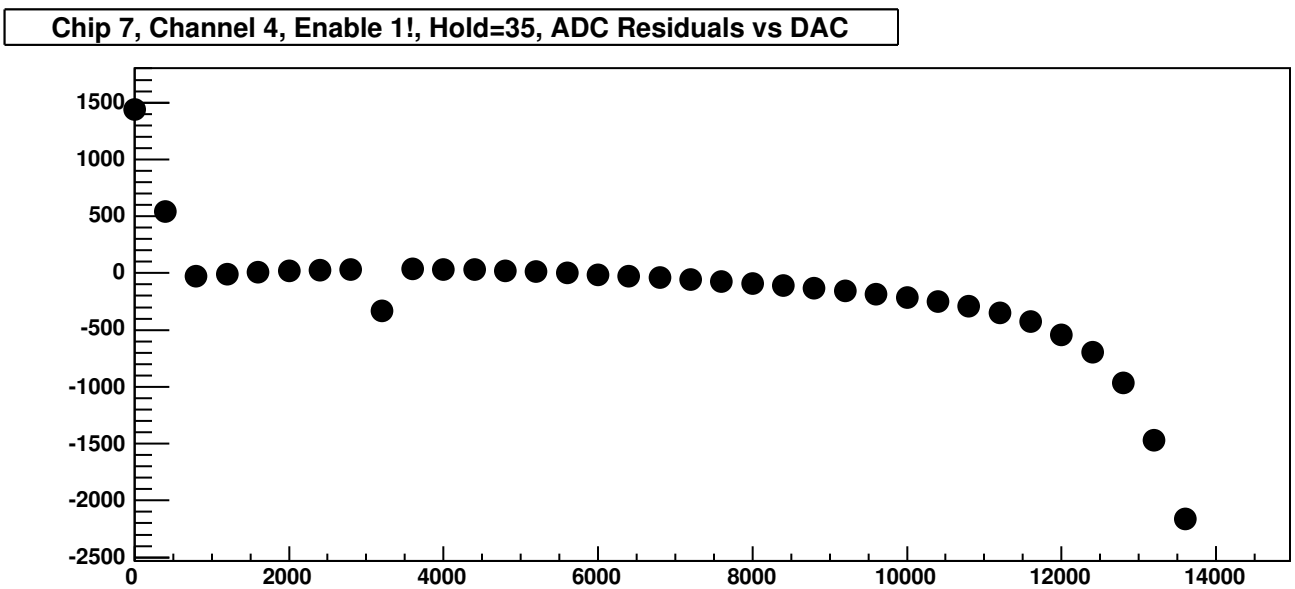

Chip 7, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.527e+04 / 23
p0 -955.4 ± 1.388
p1 0.4793 ± 0.0002224
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

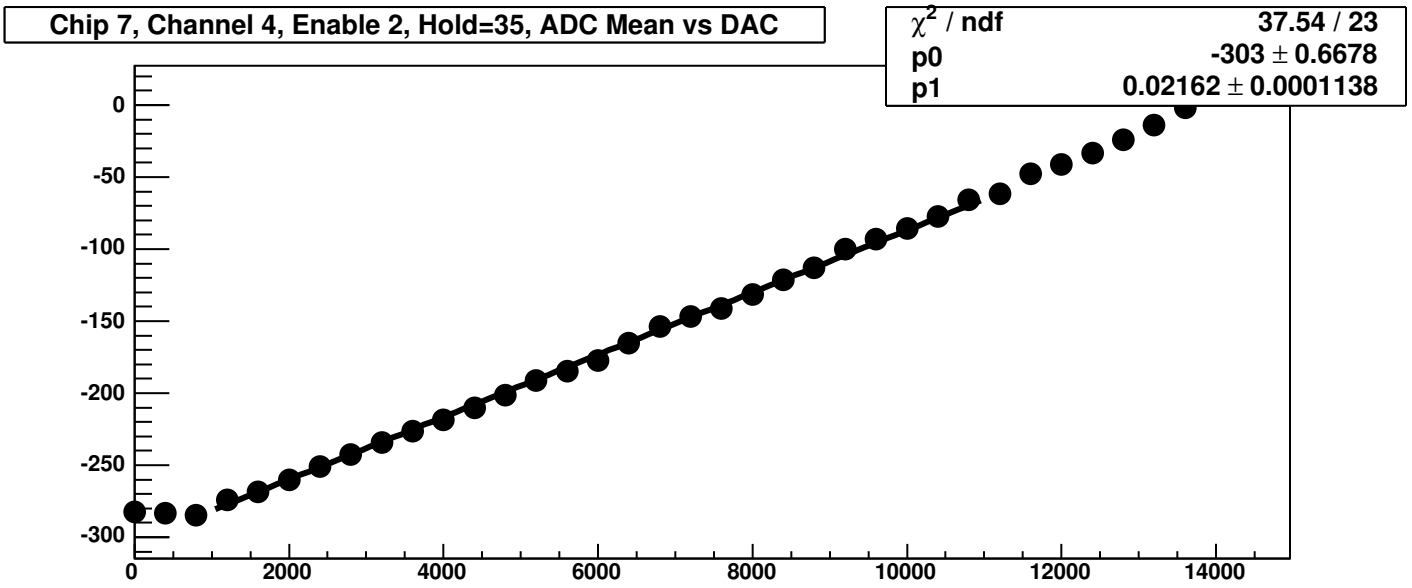

Chip 7, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

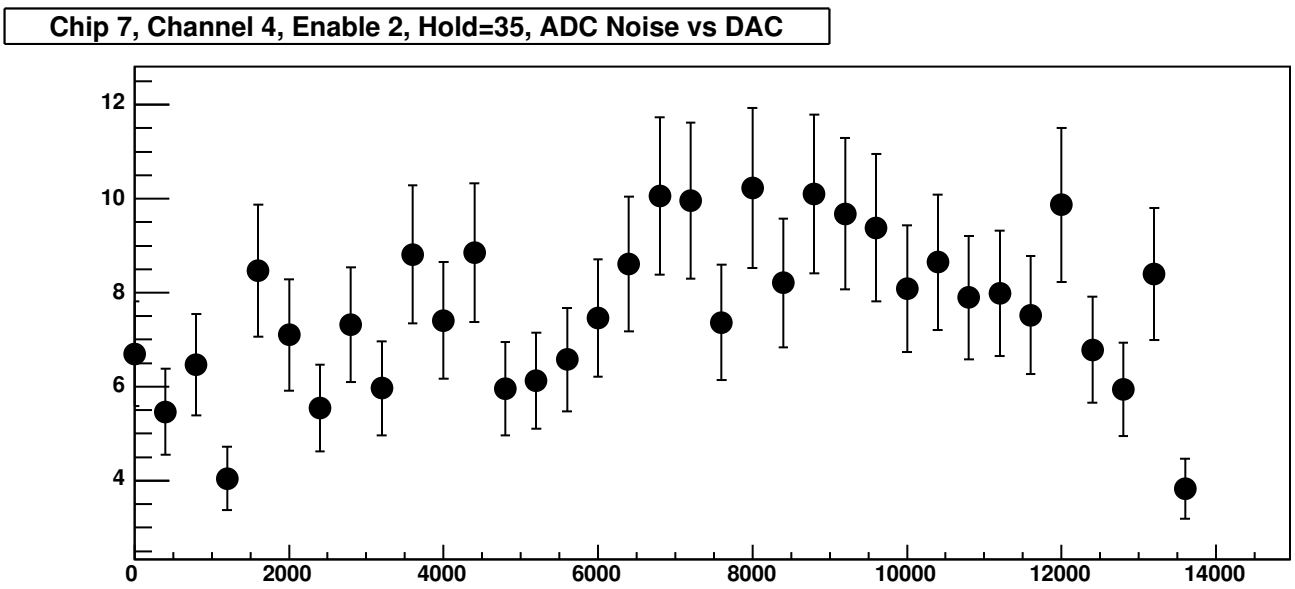

Chip 7, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

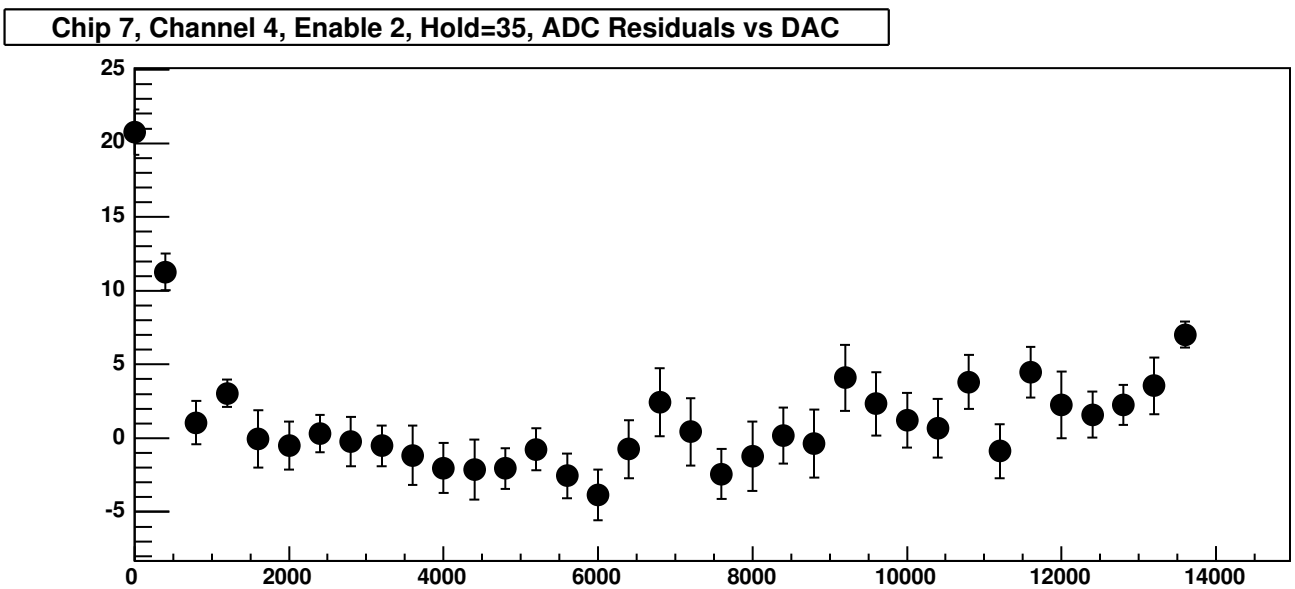

Chip 7, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 398.7 / 23
p0 -252.1 ± 2.117
p1 2.291 ± 0.001212
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

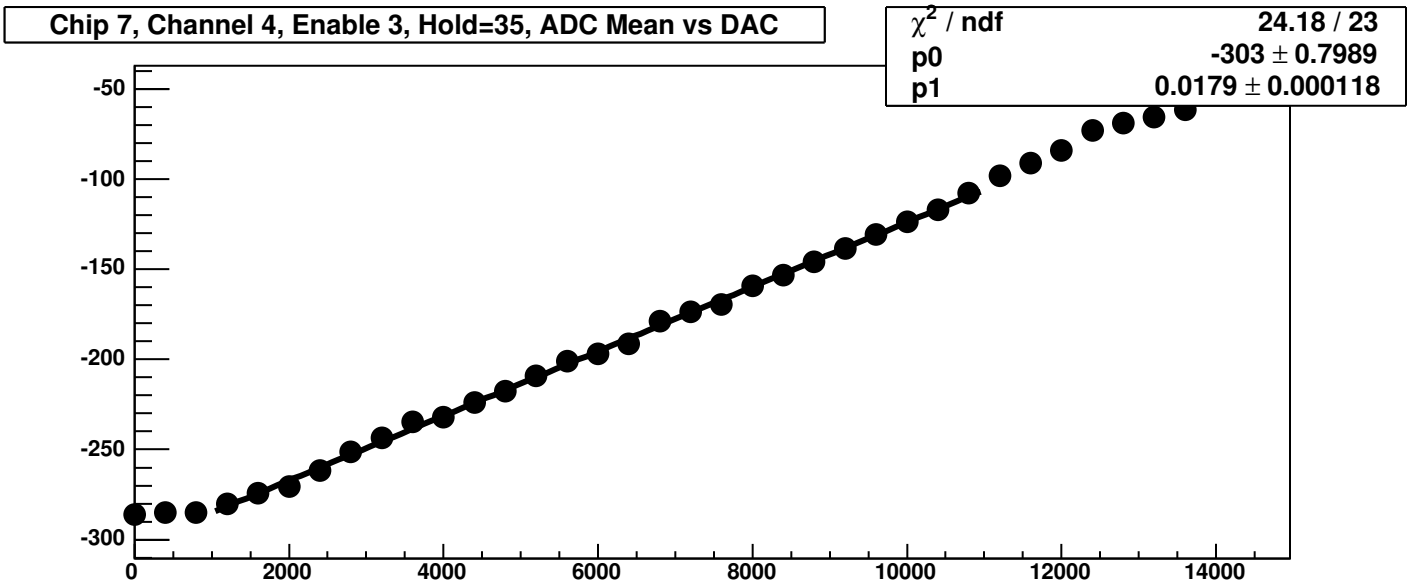

Chip 7, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

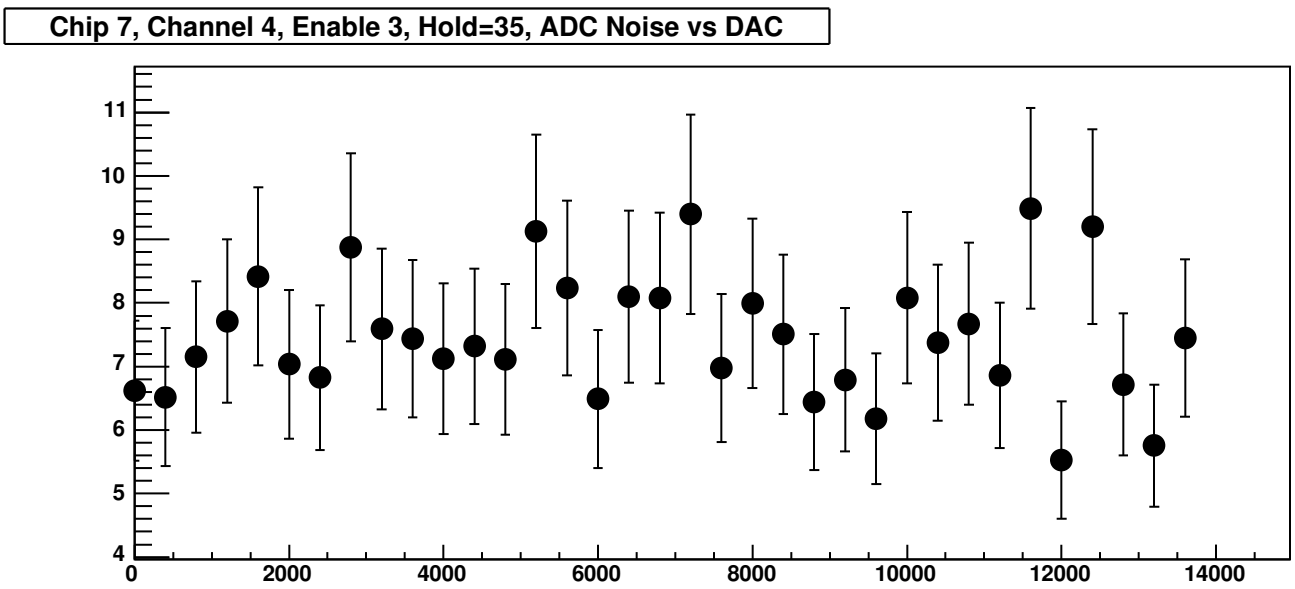

Chip 7, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

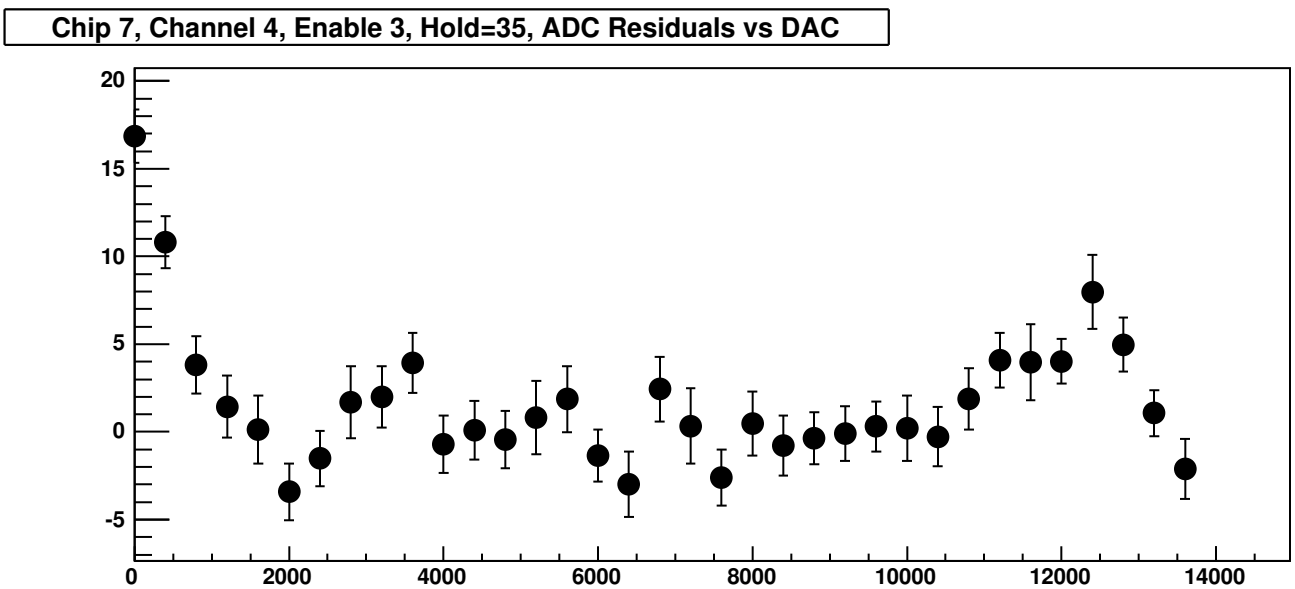

Chip 7, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 37.54 / 23
p0 -303 ± 0.6678
p1 0.02162 ± 0.0001138
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

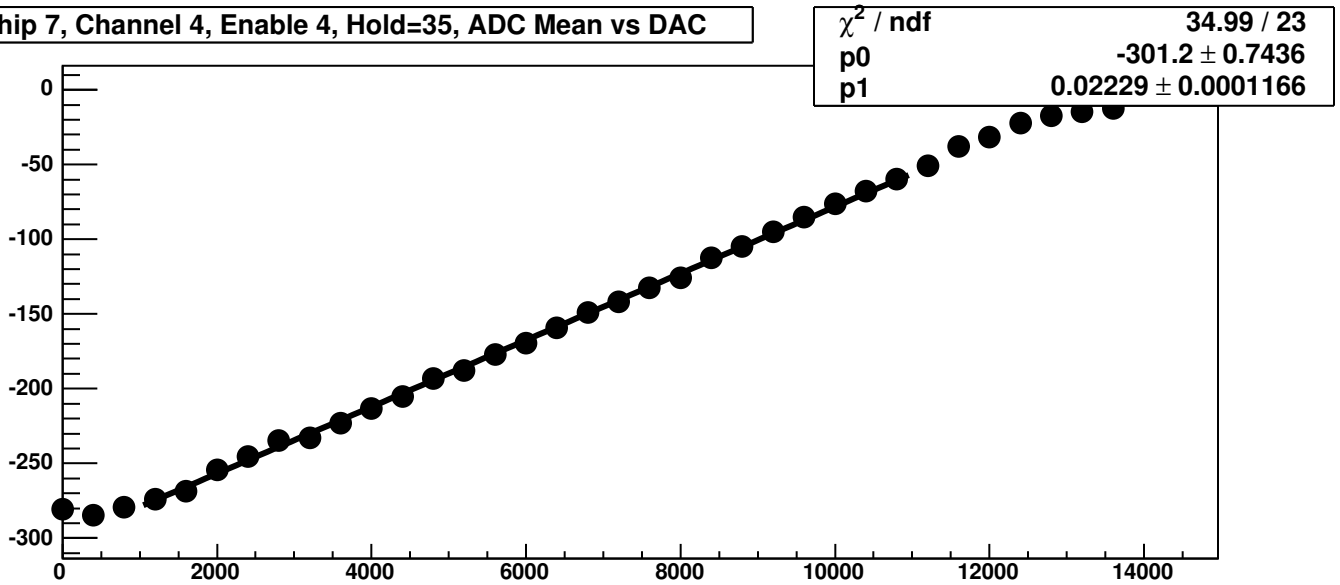

Chip 7, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.18 / 23
p0 -303 ± 0.7989
p1 0.0179 ± 0.000118
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

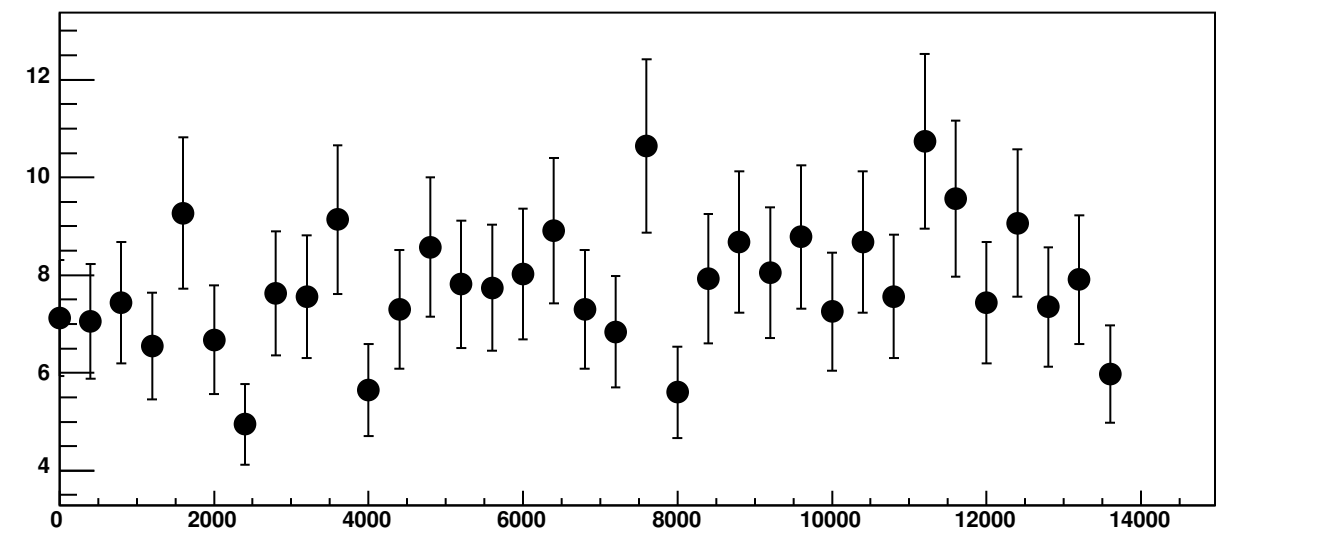

Chip 7, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

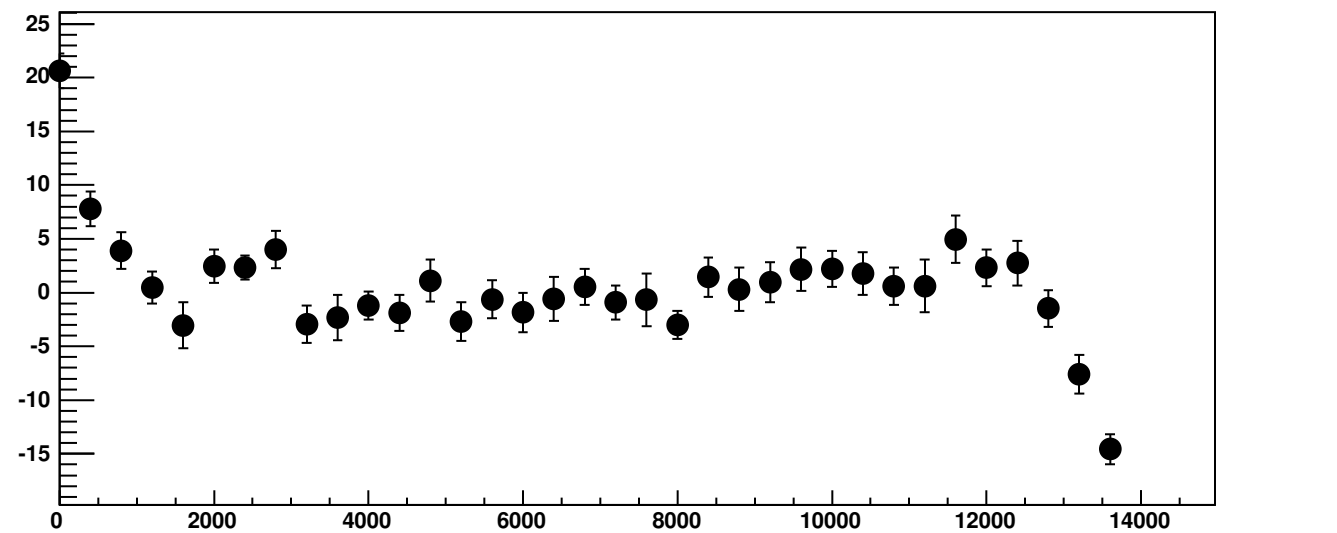
Chip 7, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

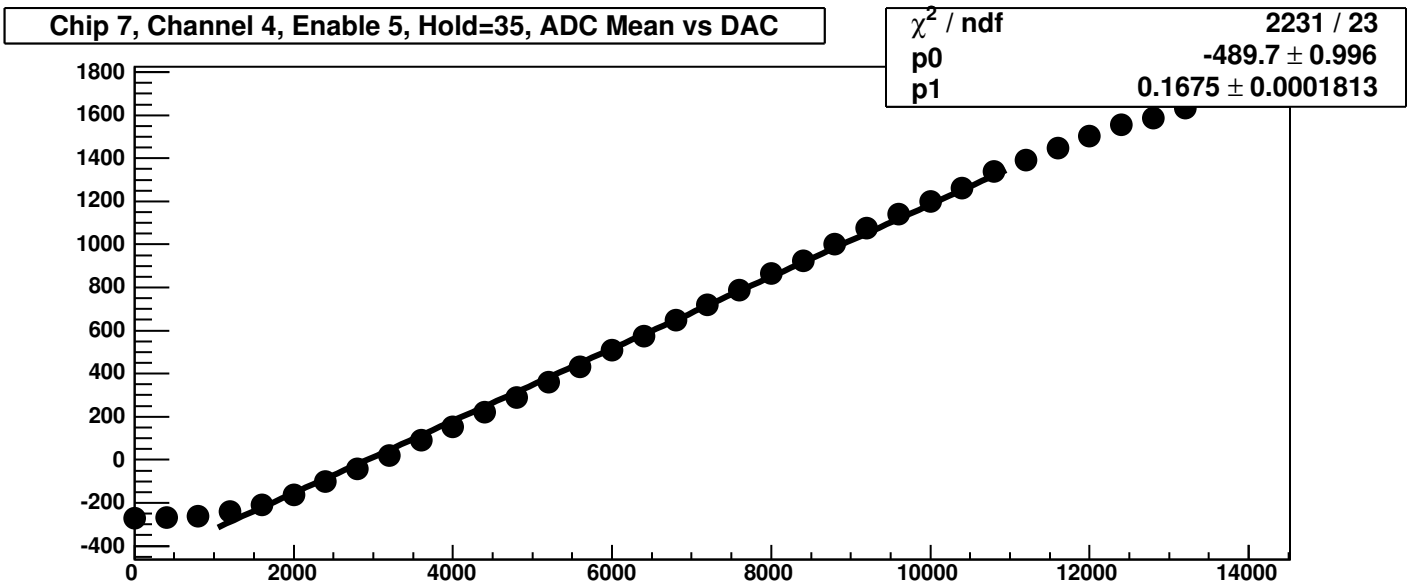


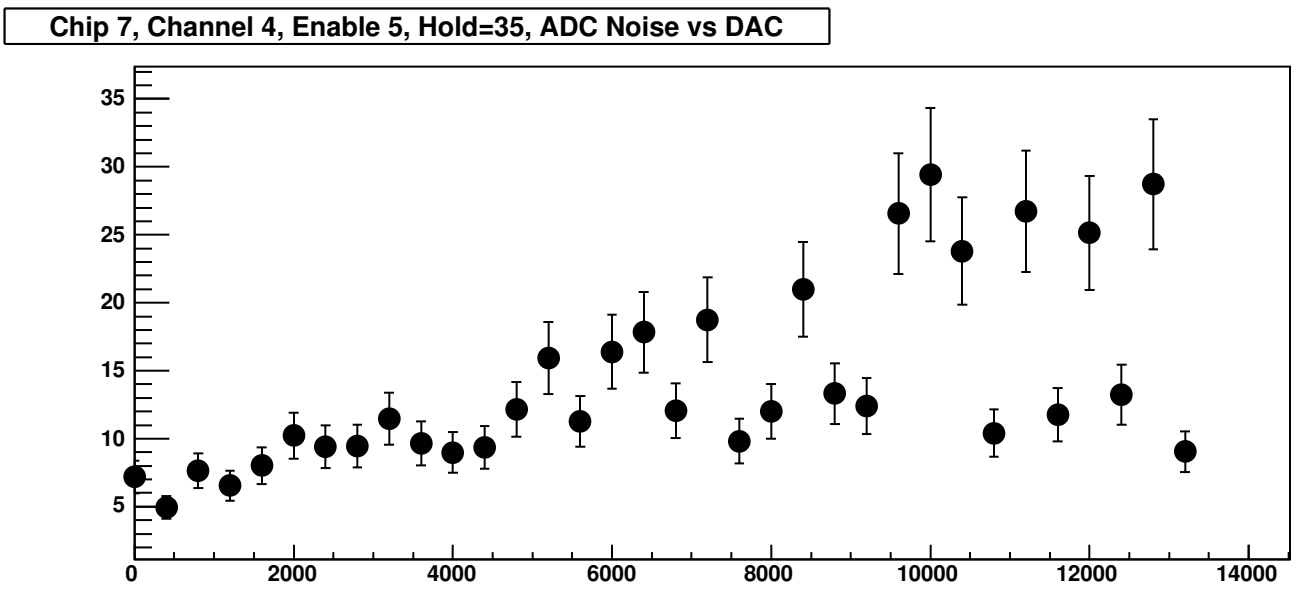
Chip 7, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC

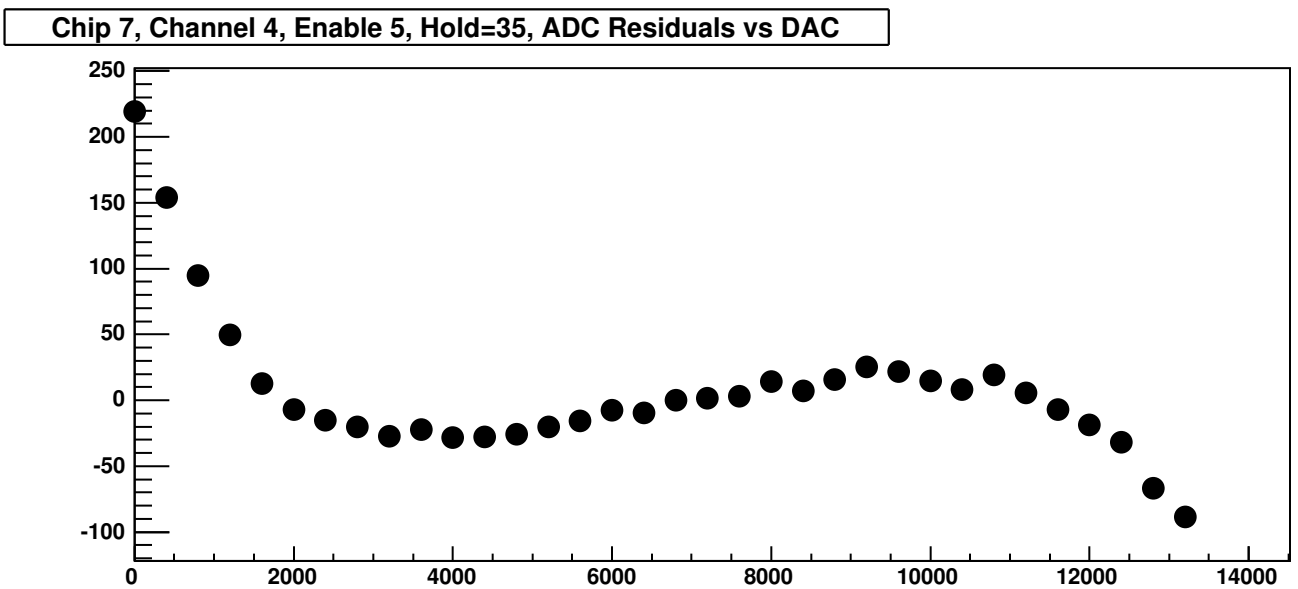


Chip 7, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC

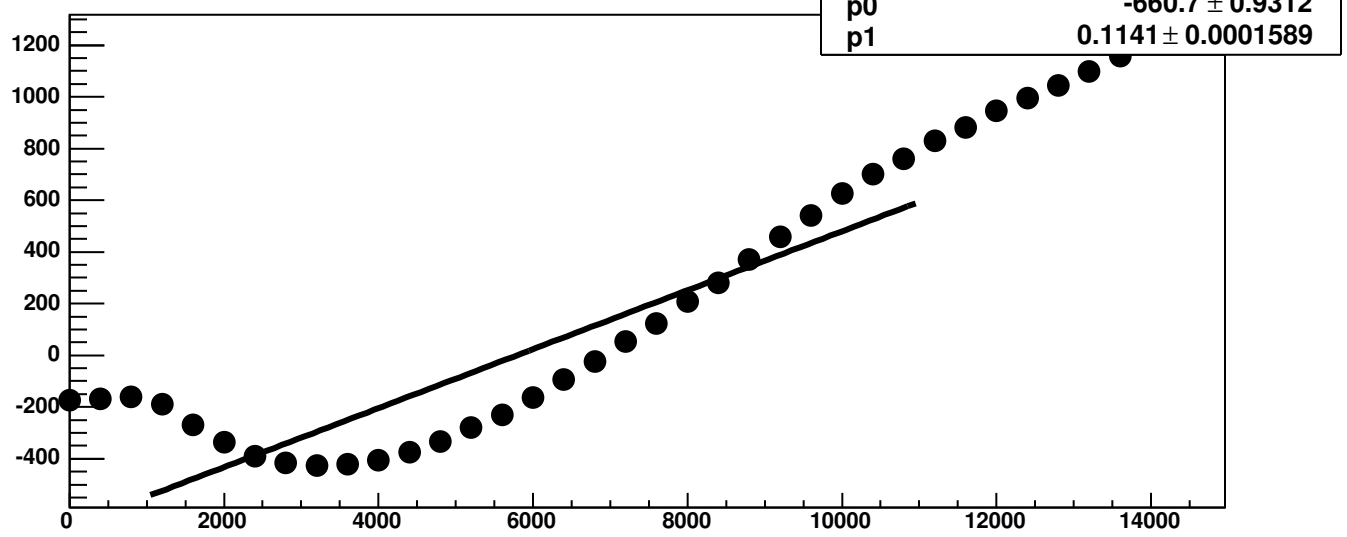



Chip 7, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2231 / 23
p0 -489.7 ± 0.996
p1 0.1675 ± 0.0001813
1800
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

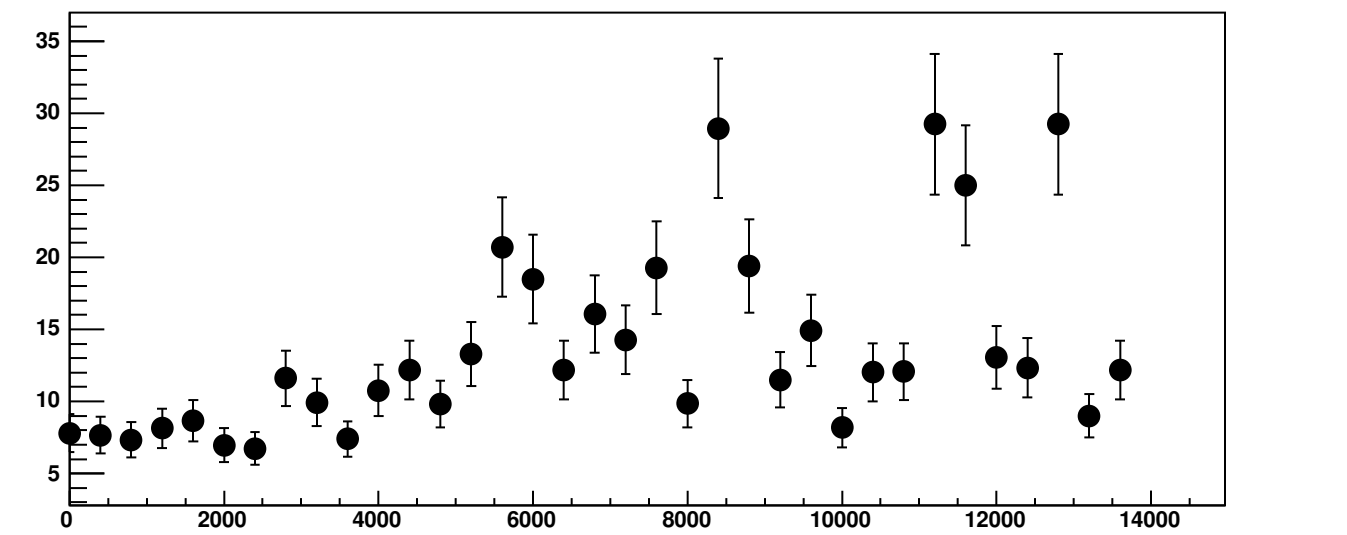

Chip 7, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

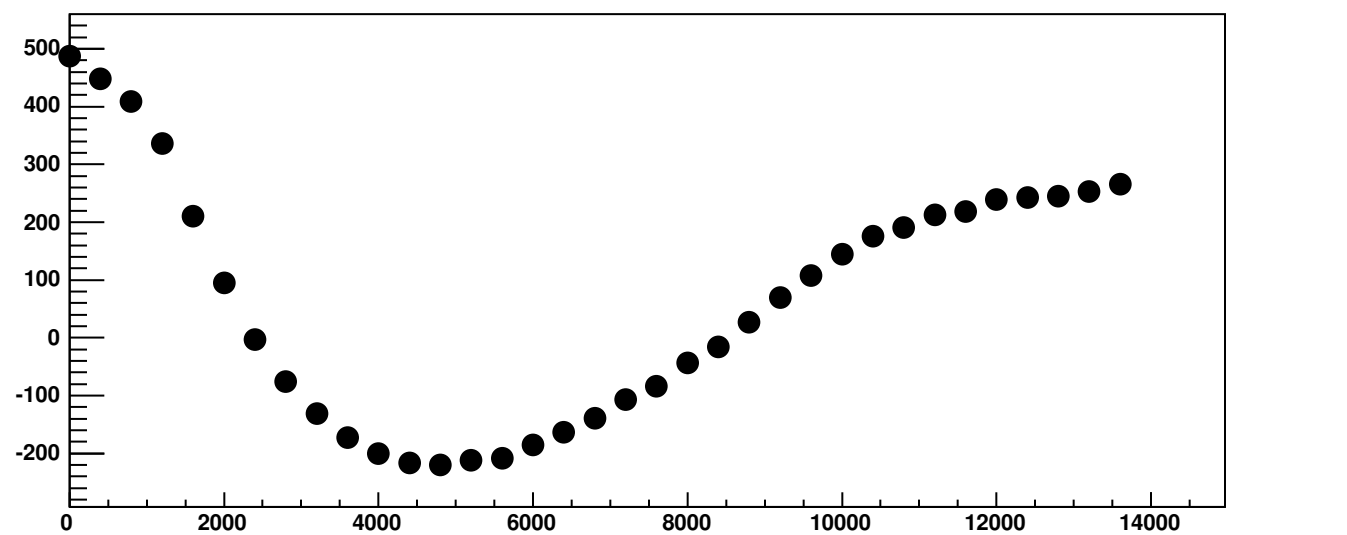
Chip 7, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC



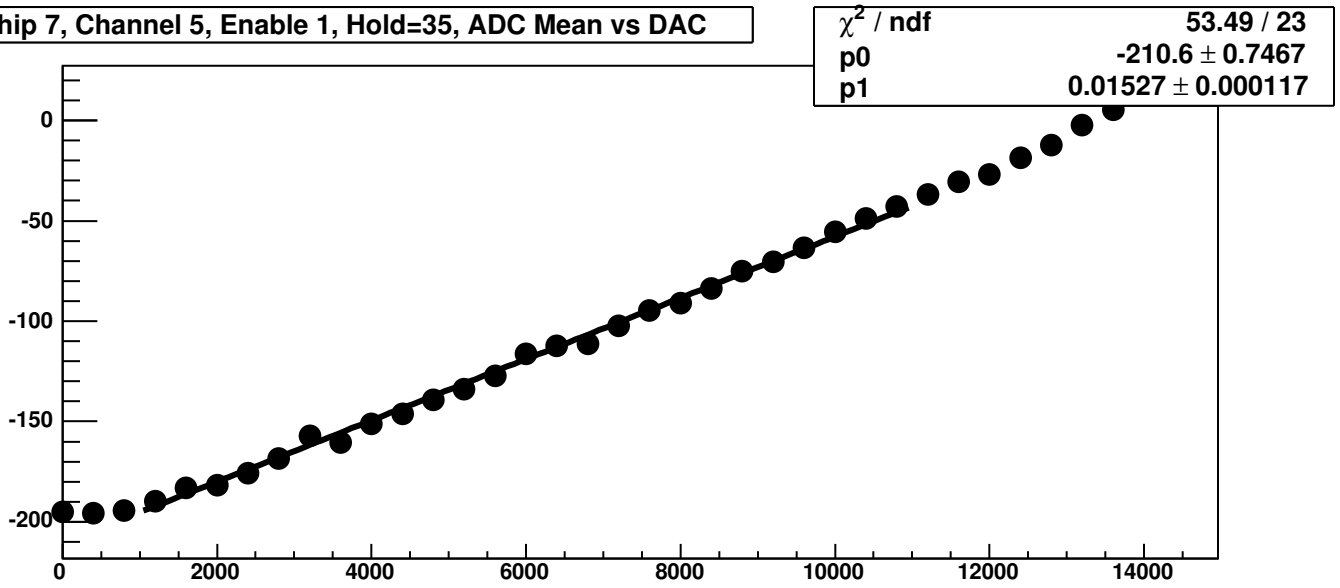
Chip 7, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC



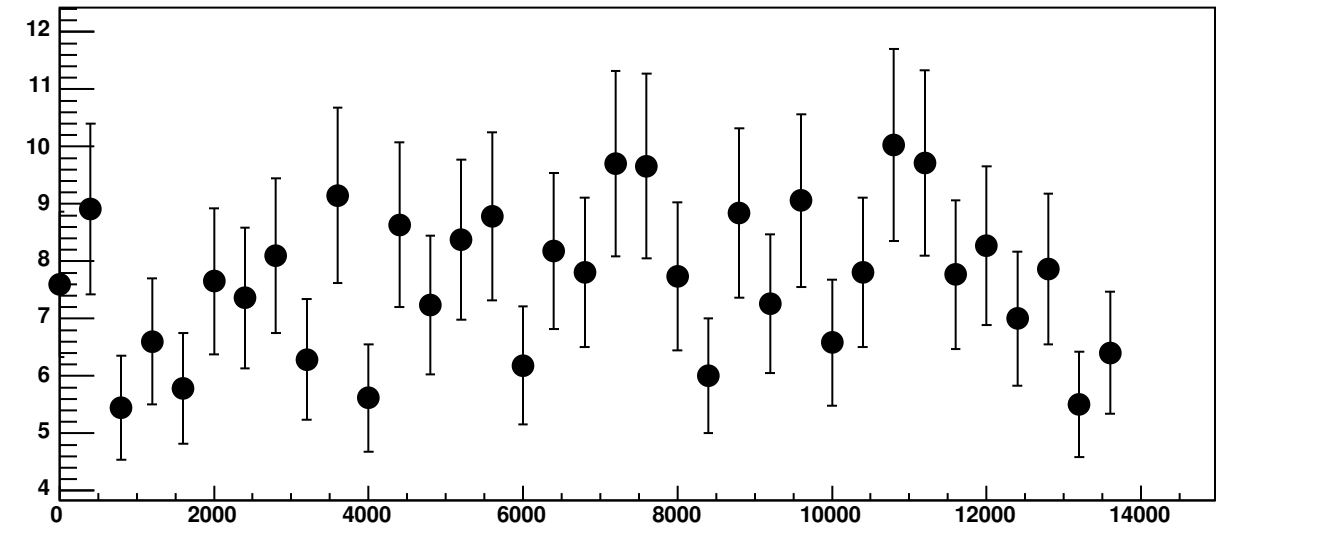
Chip 7, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC



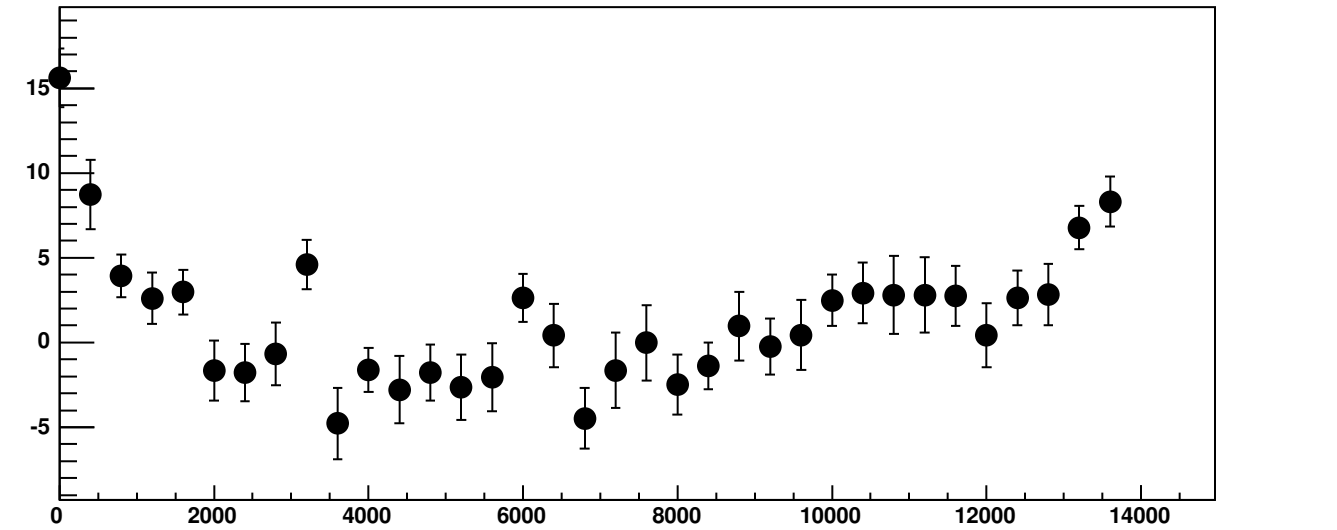
Chip 7, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

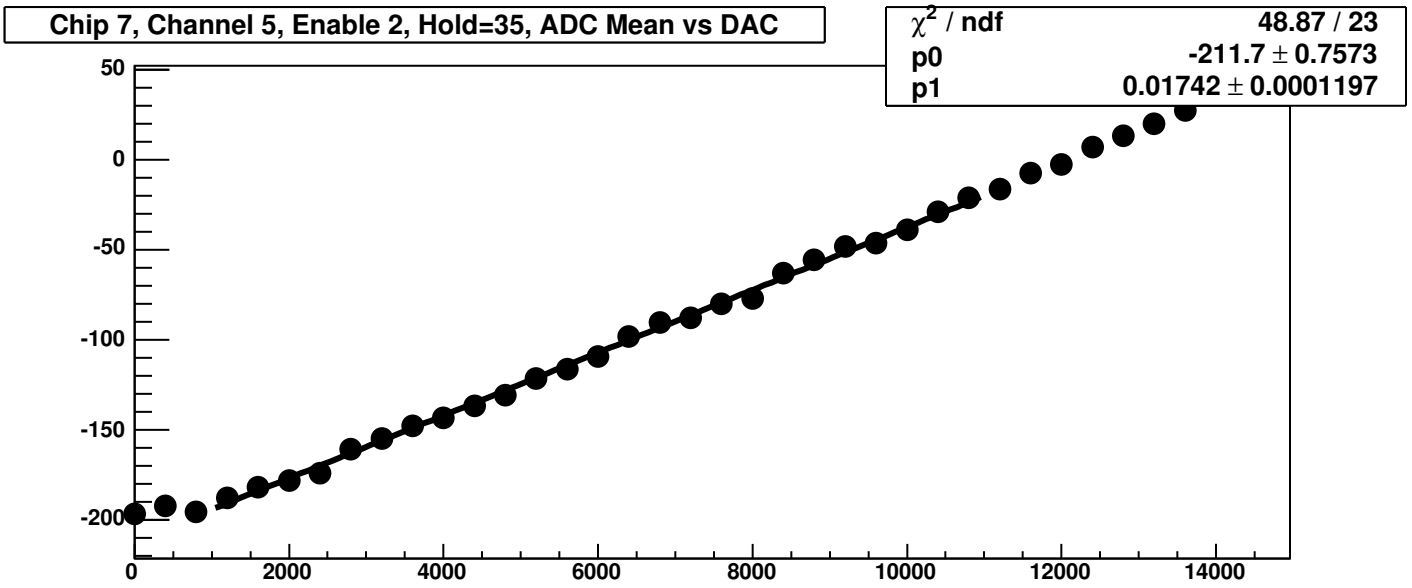


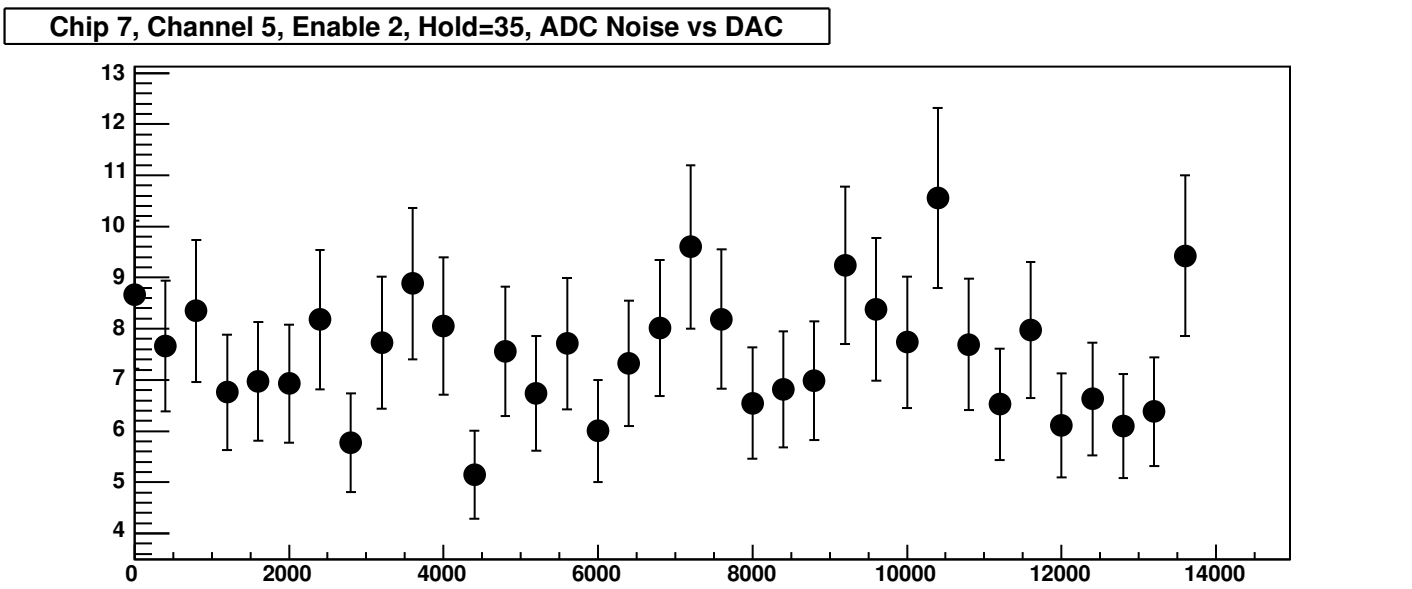
Chip 7, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC

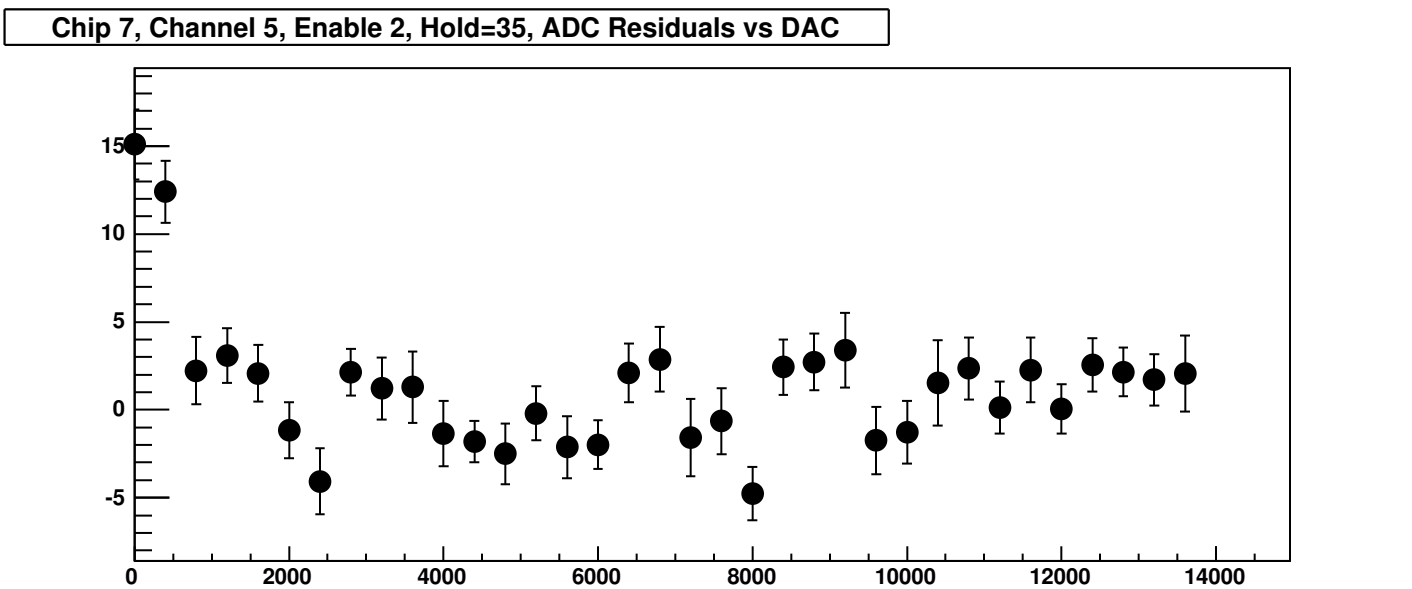


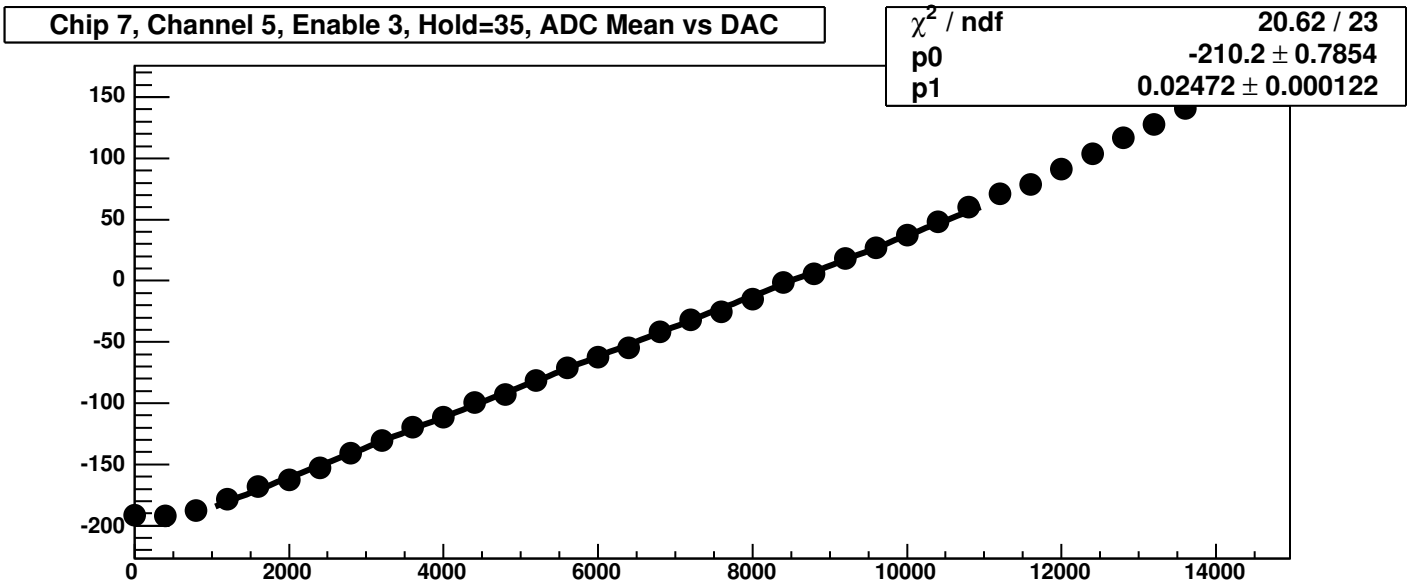
Chip 7, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

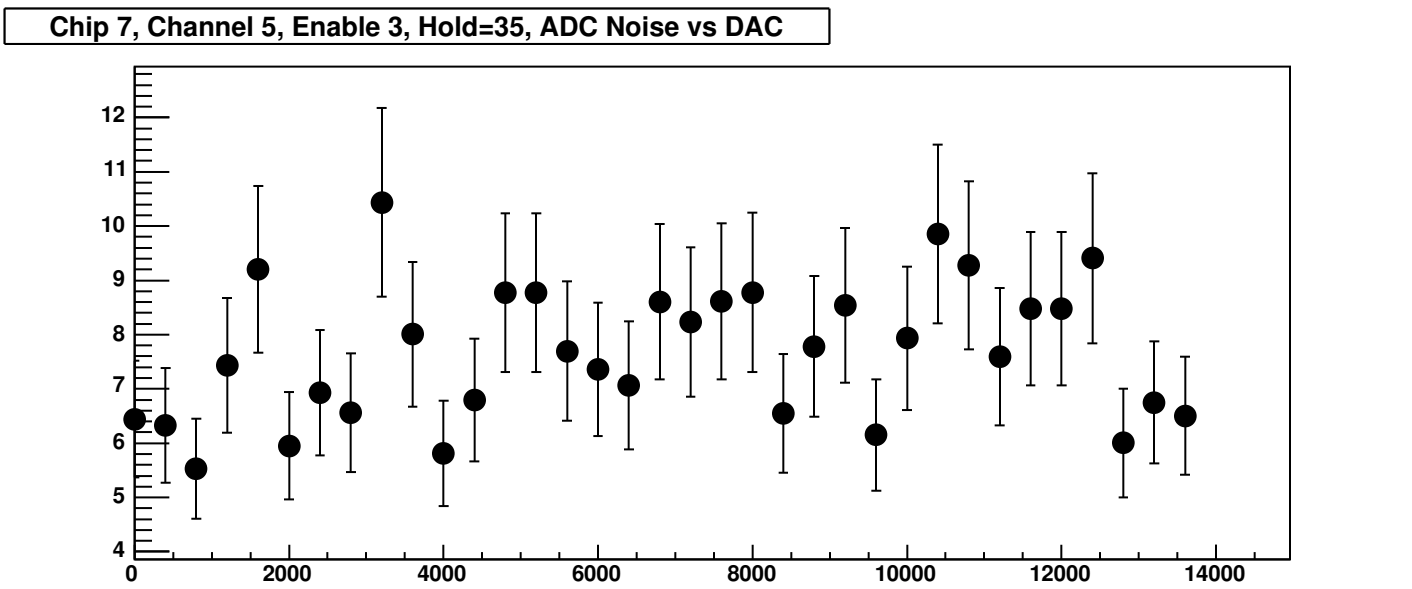


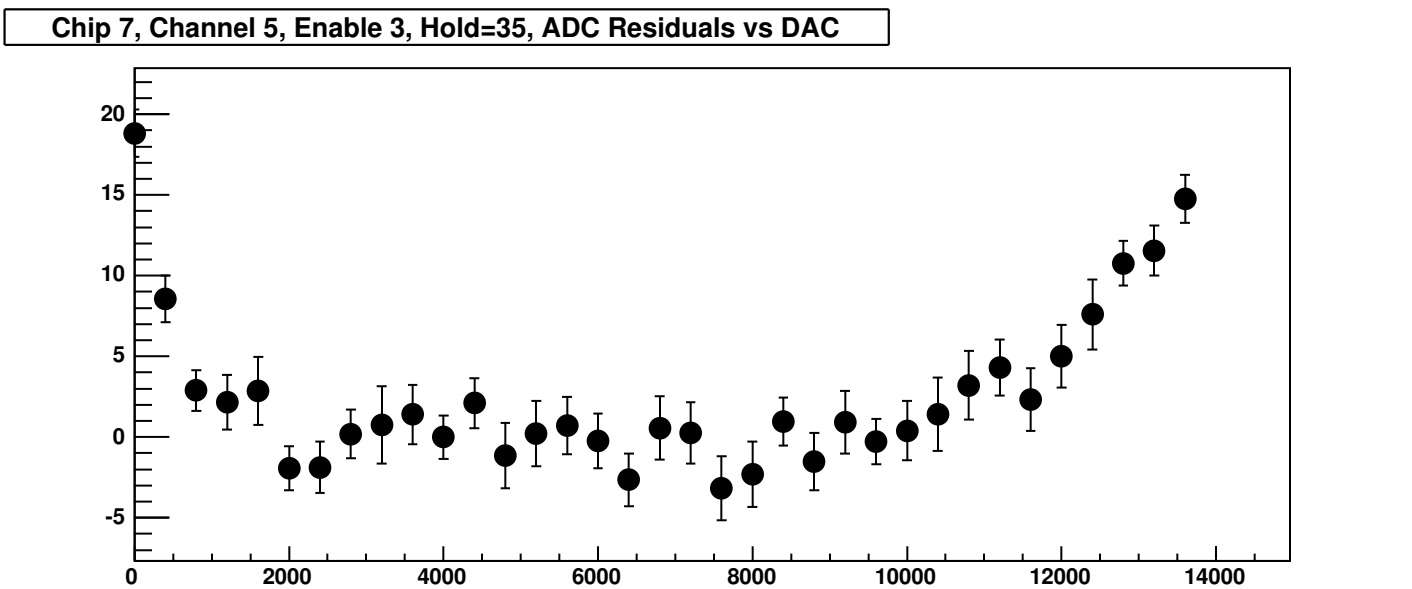

Chip 7, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 48.87 / 23
p0 -211.7 ± 0.7573
p1 0.01742 ± 0.0001197
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

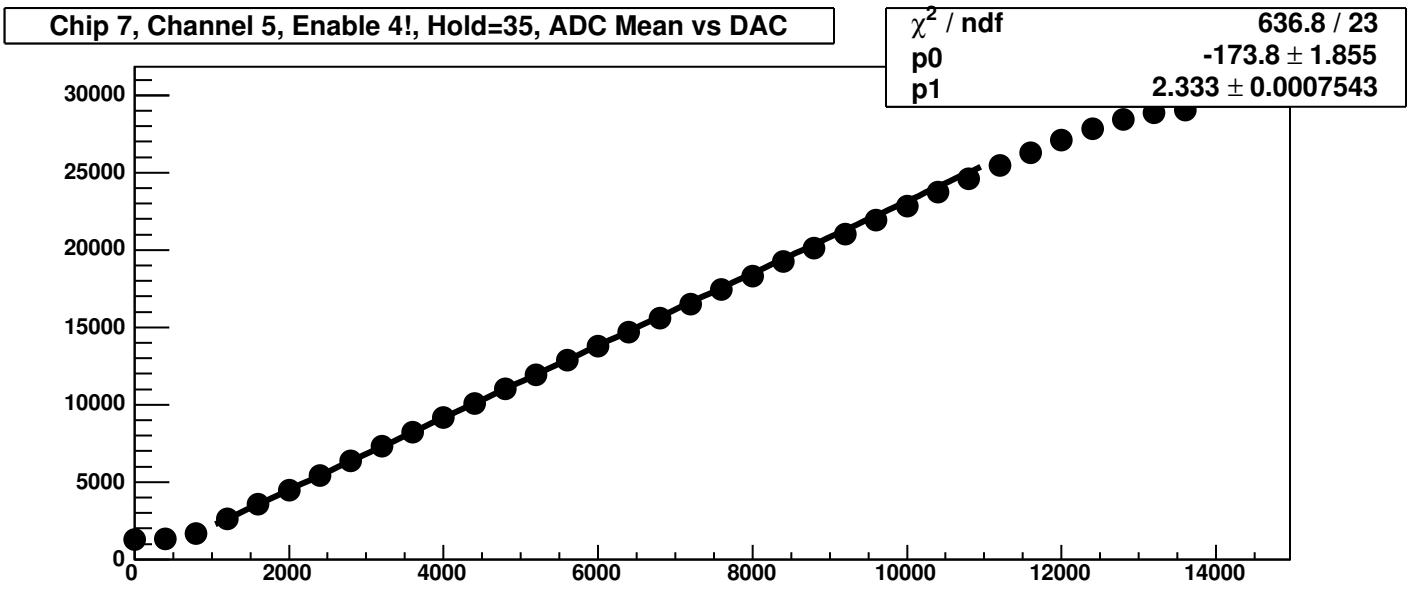

Chip 7, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

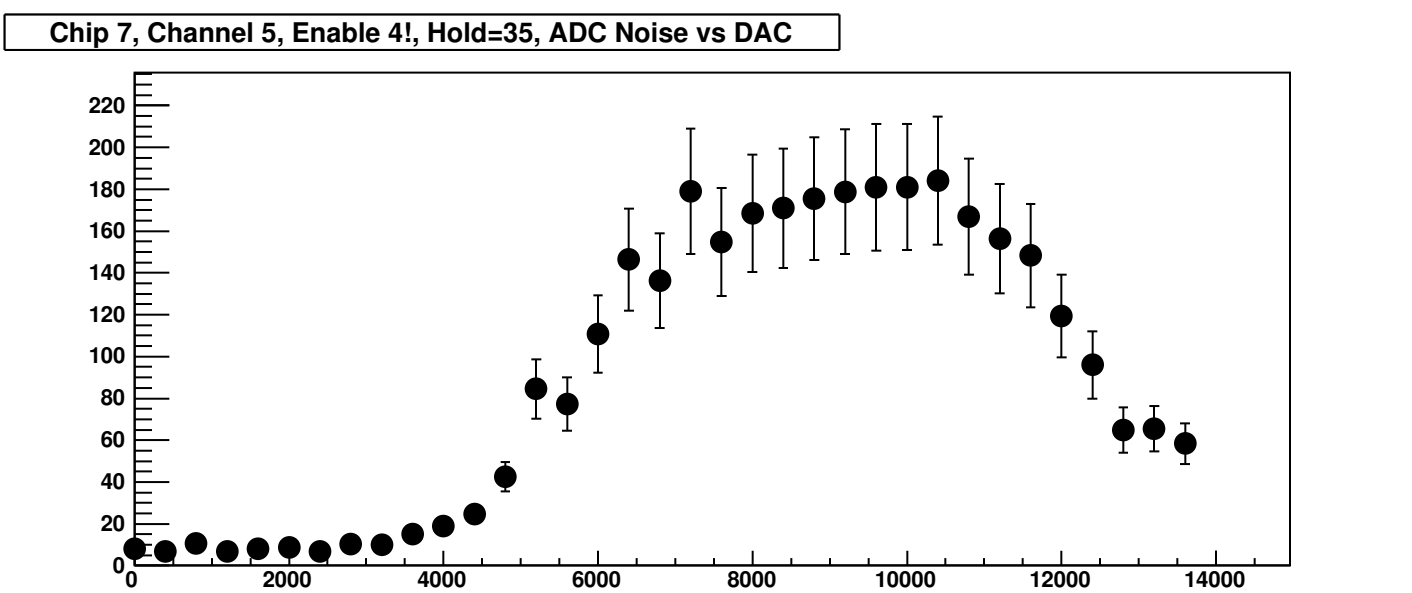

Chip 7, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

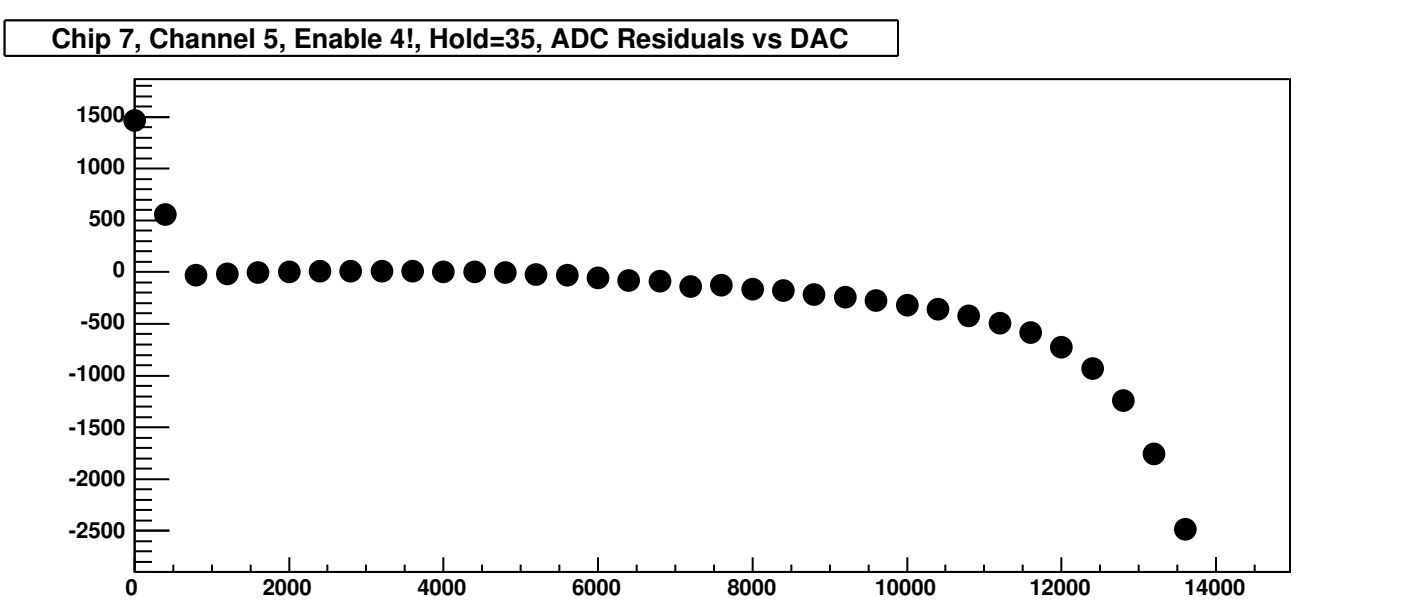

Chip 7, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 20.62 / 23
p0 -210.2 ± 0.7854
p1 0.02472 ± 0.000122
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

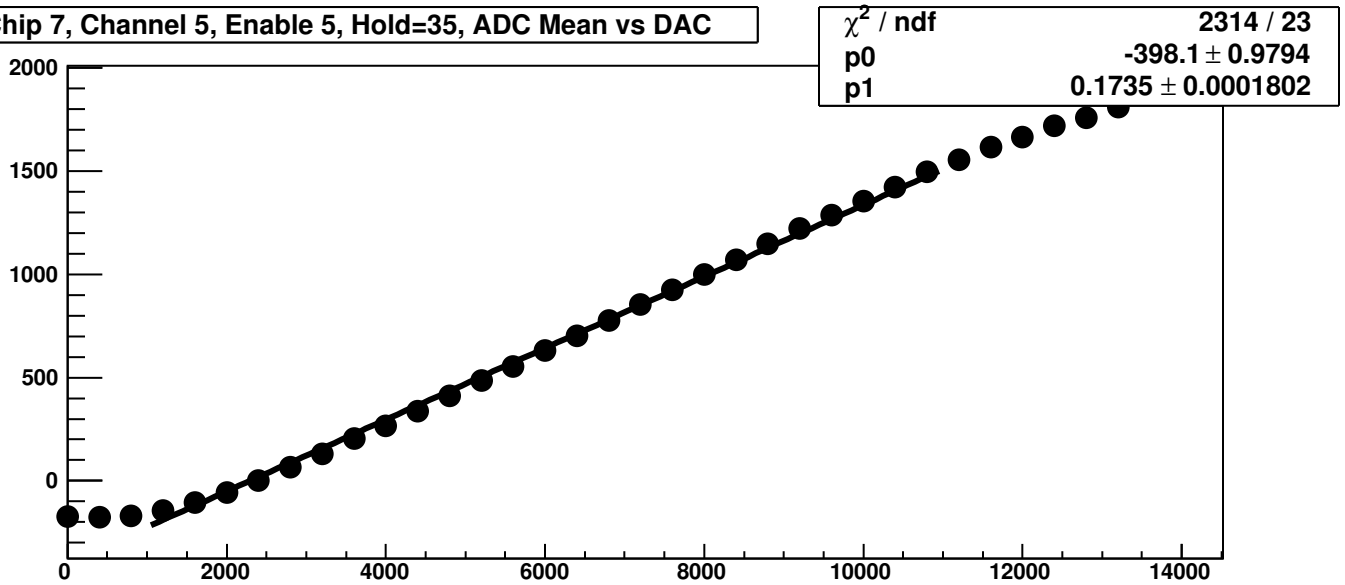

Chip 7, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 636.8 / 23
p0 -173.8 ± 1.855
p1 2.333 ± 0.0007543
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

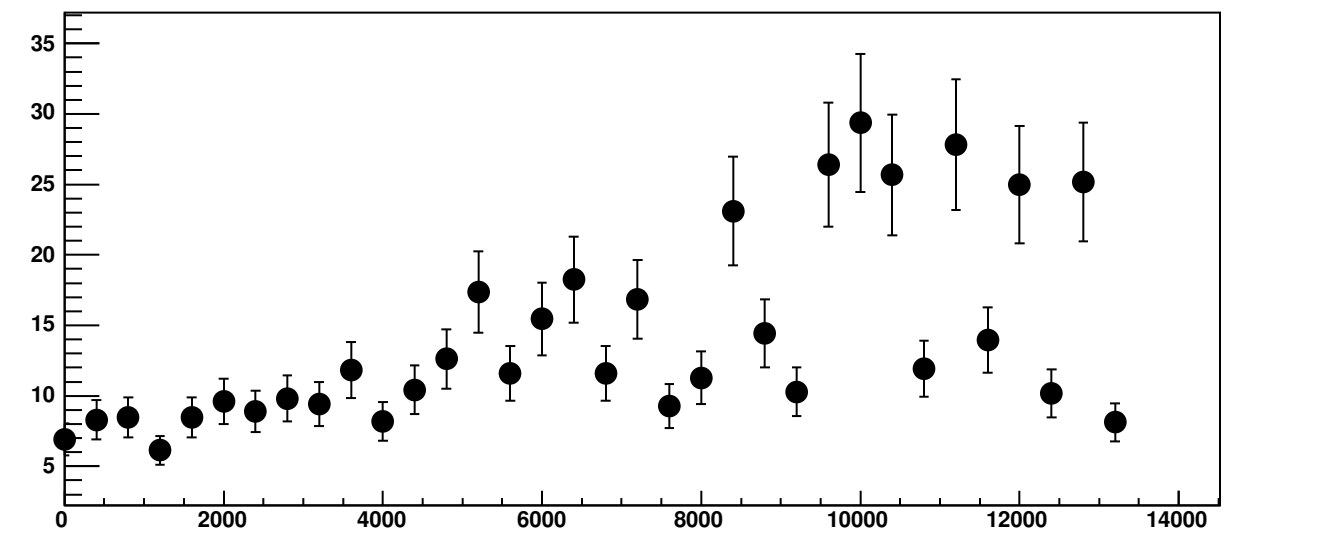

Chip 7, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

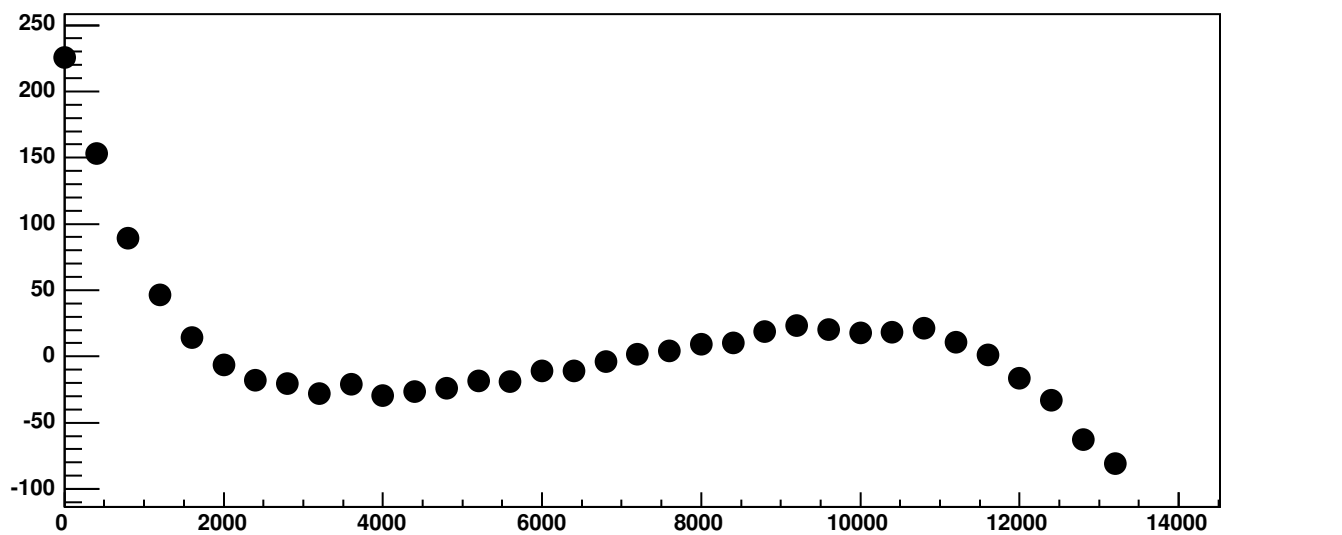
Chip 7, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC



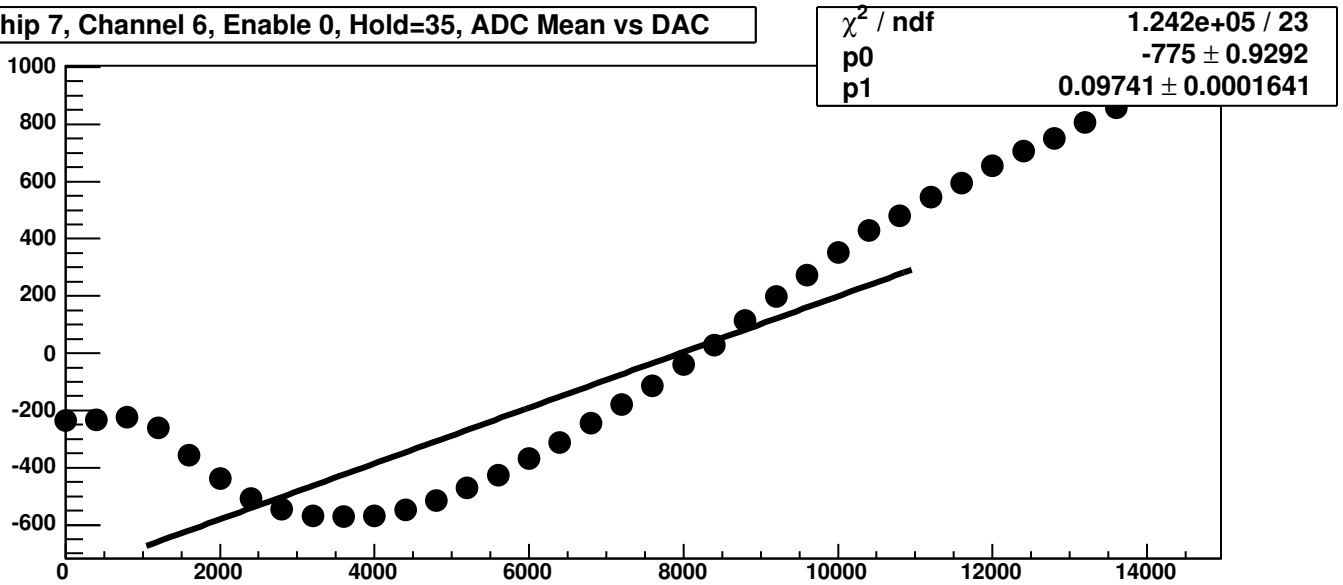
Chip 7, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC



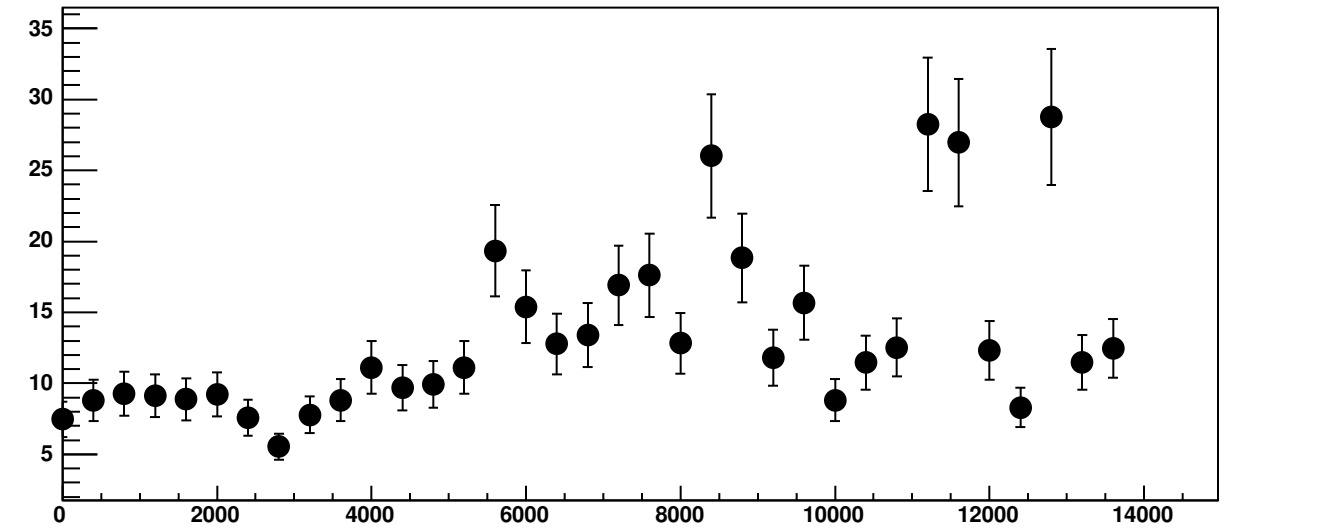
Chip 7, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC



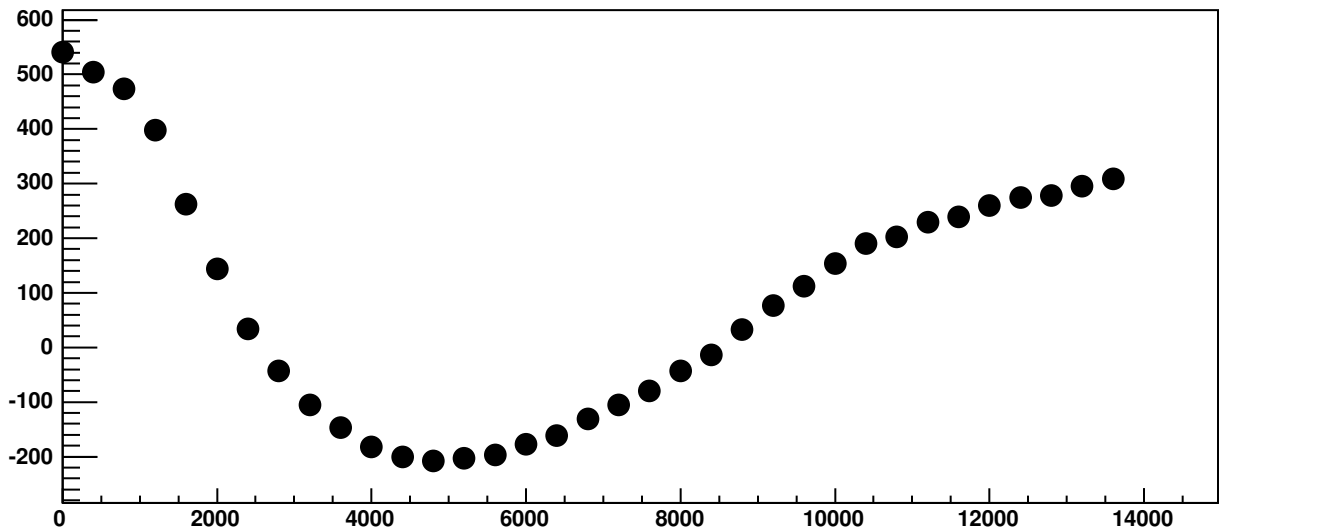
Chip 7, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

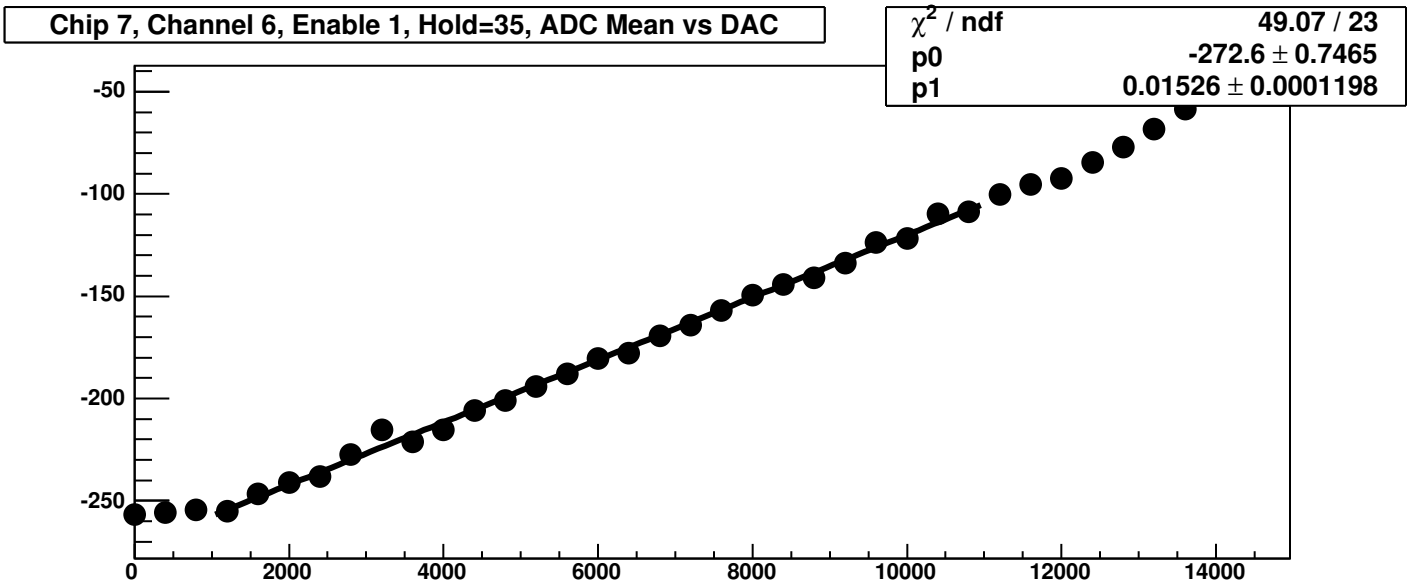


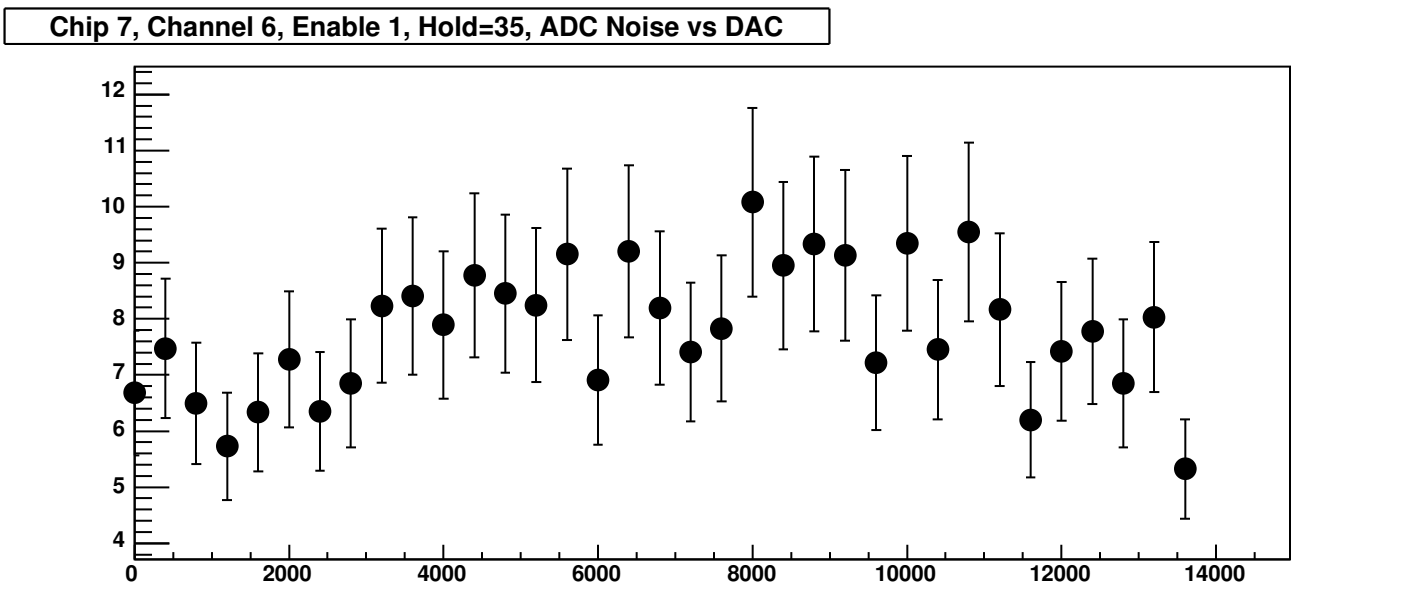
Chip 7, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC

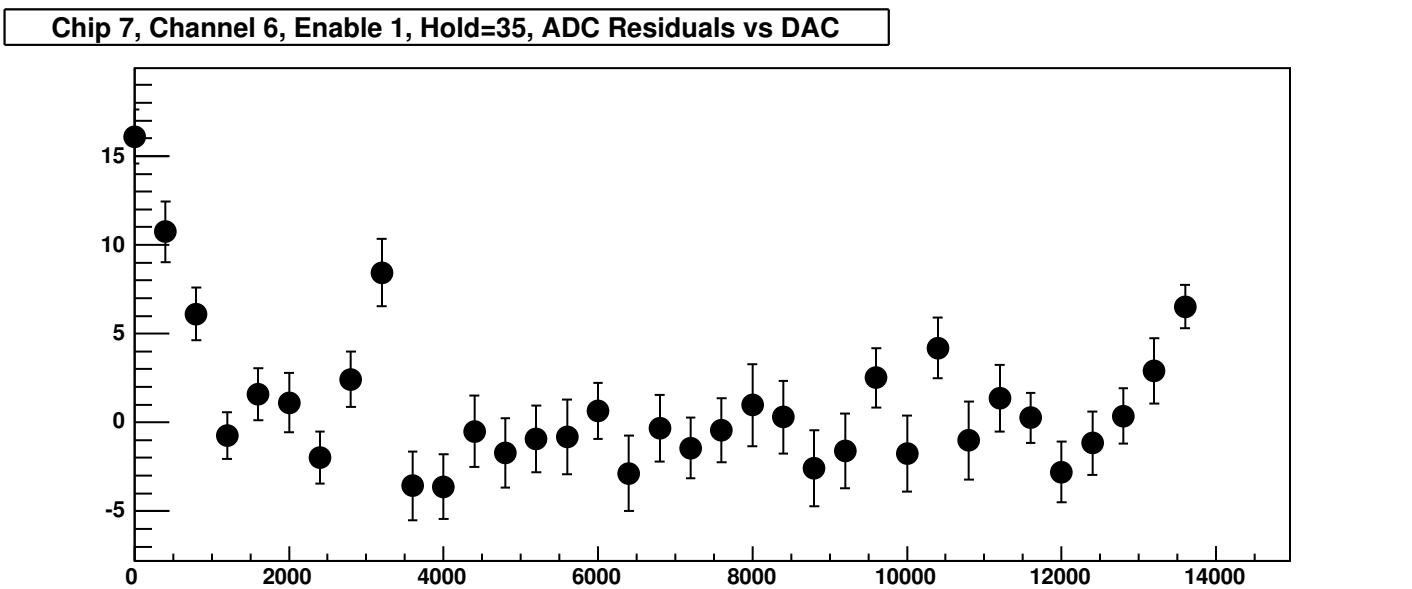


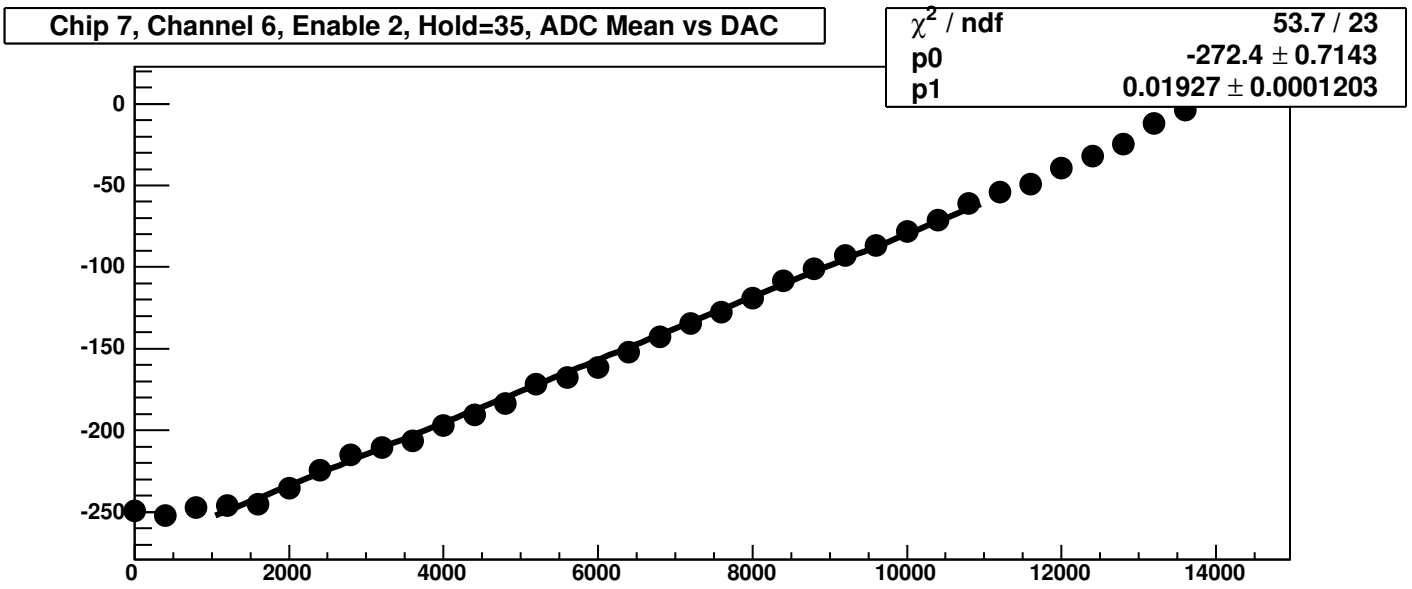
Chip 7, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

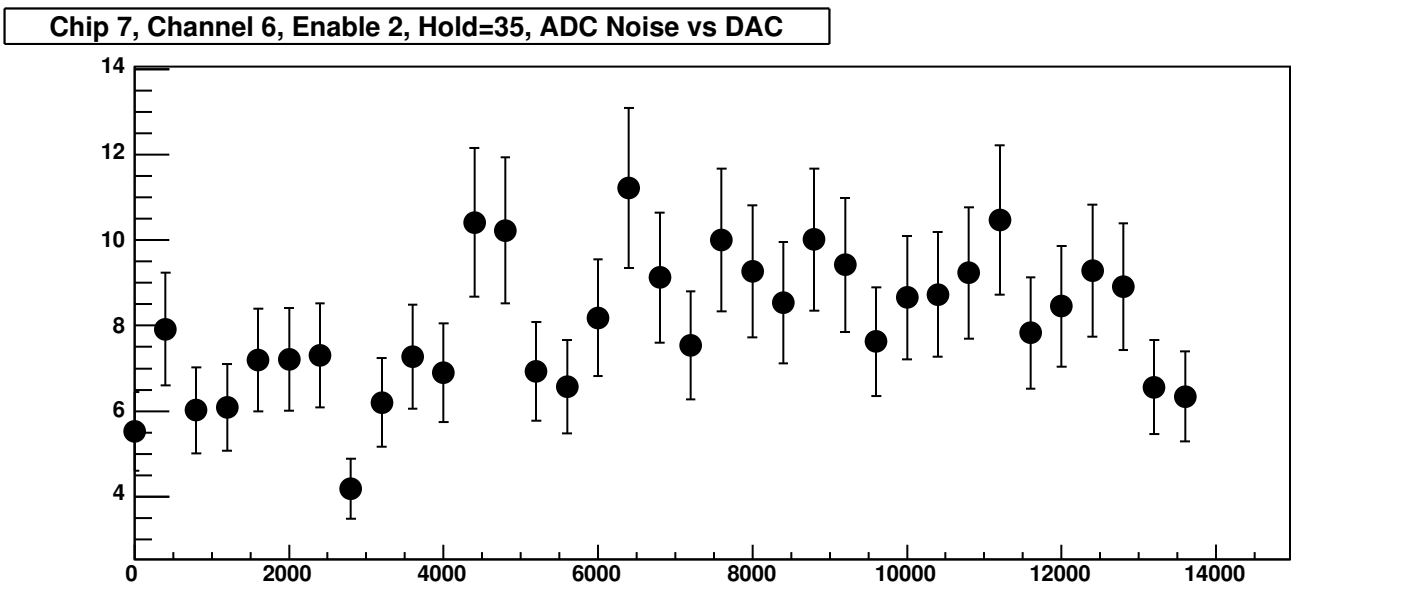


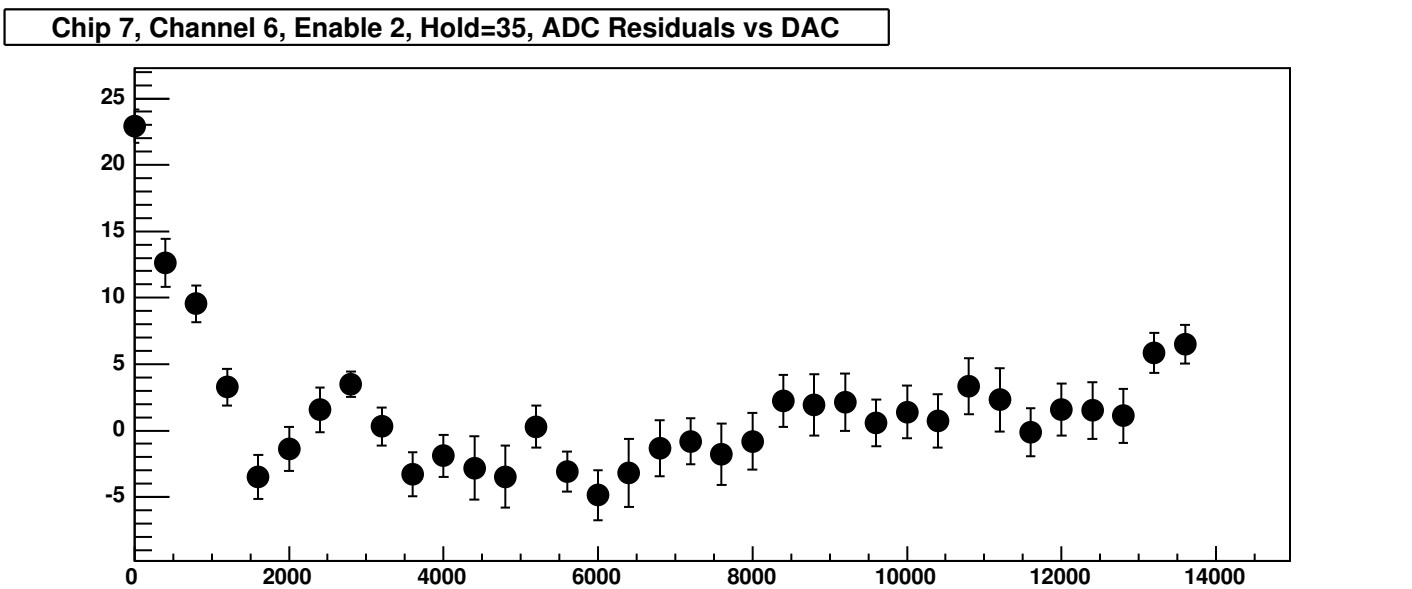

Chip 7, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 49.07 / 23
p0 -272.6 ± 0.7465
p1 0.01526 ± 0.0001198
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

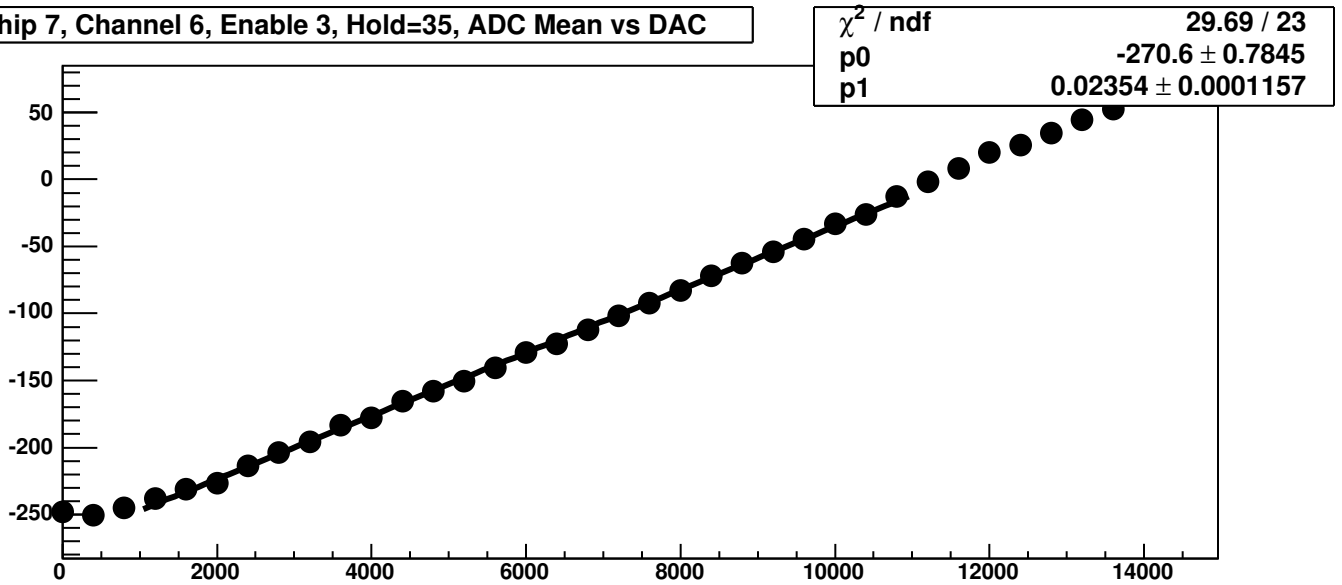

Chip 7, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 53.7 / 23
p0 -272.4 ± 0.7143
p1 0.01927 ± 0.0001203
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

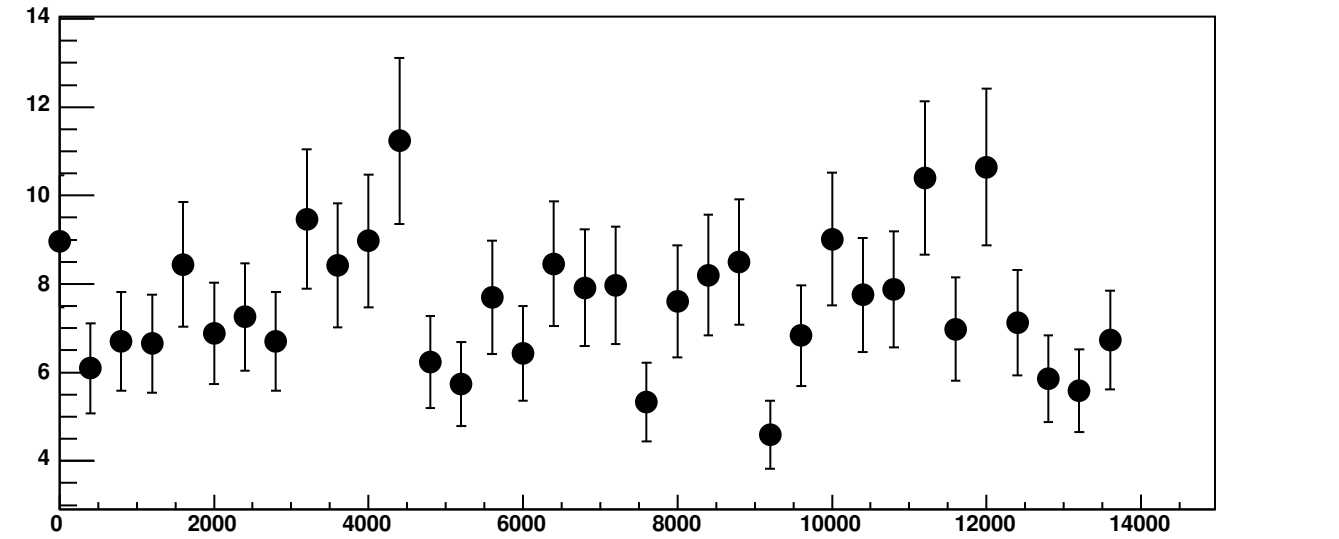

Chip 7, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

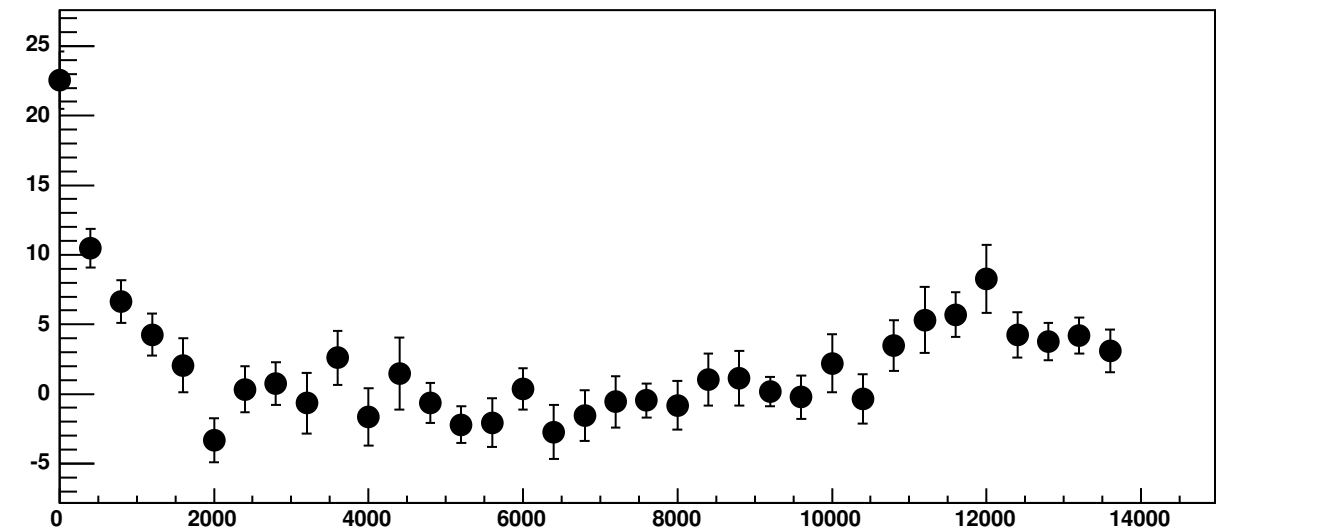
Chip 7, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

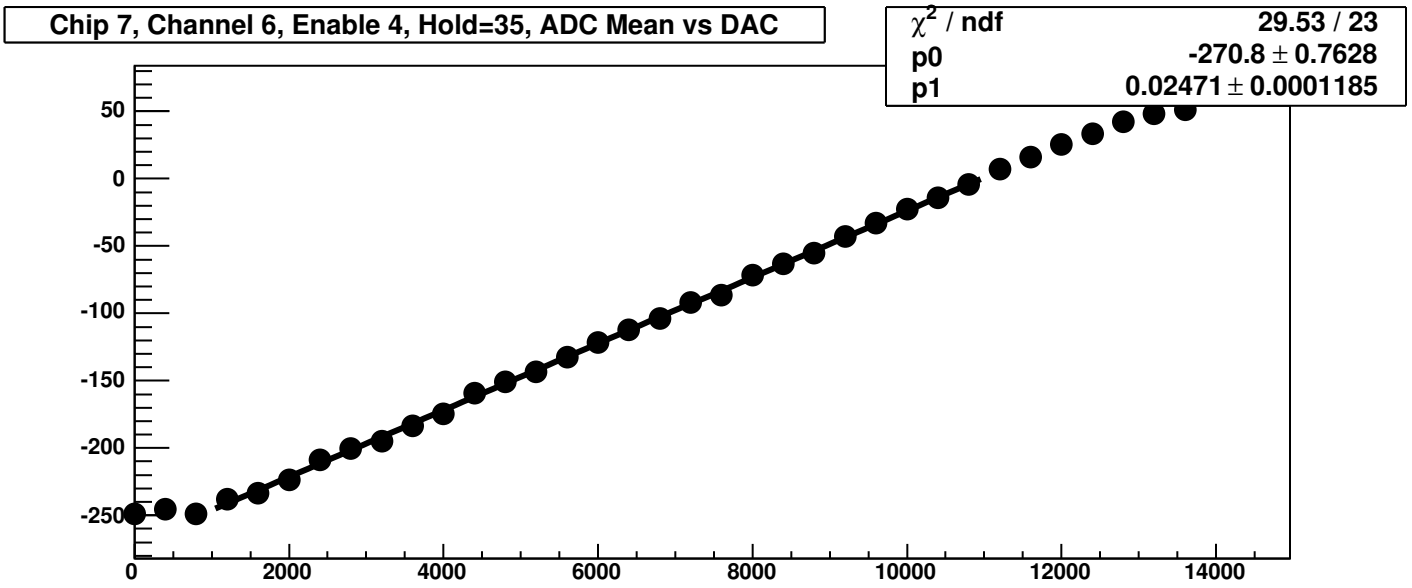


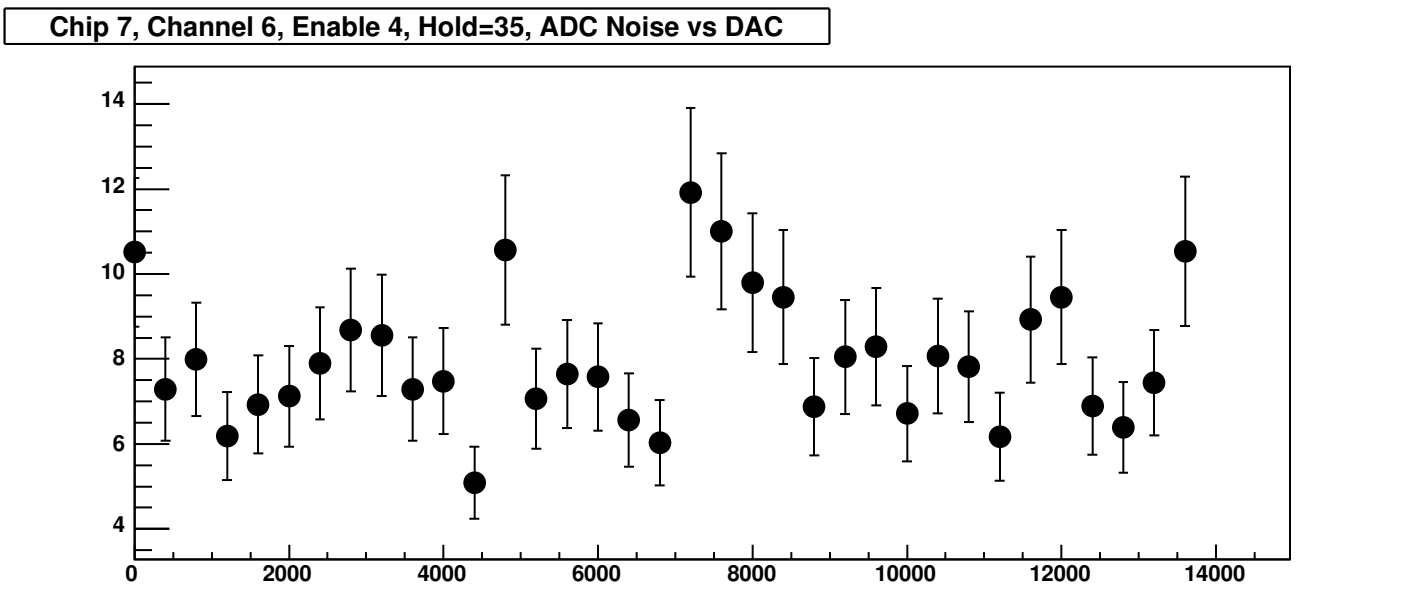
Chip 7, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC

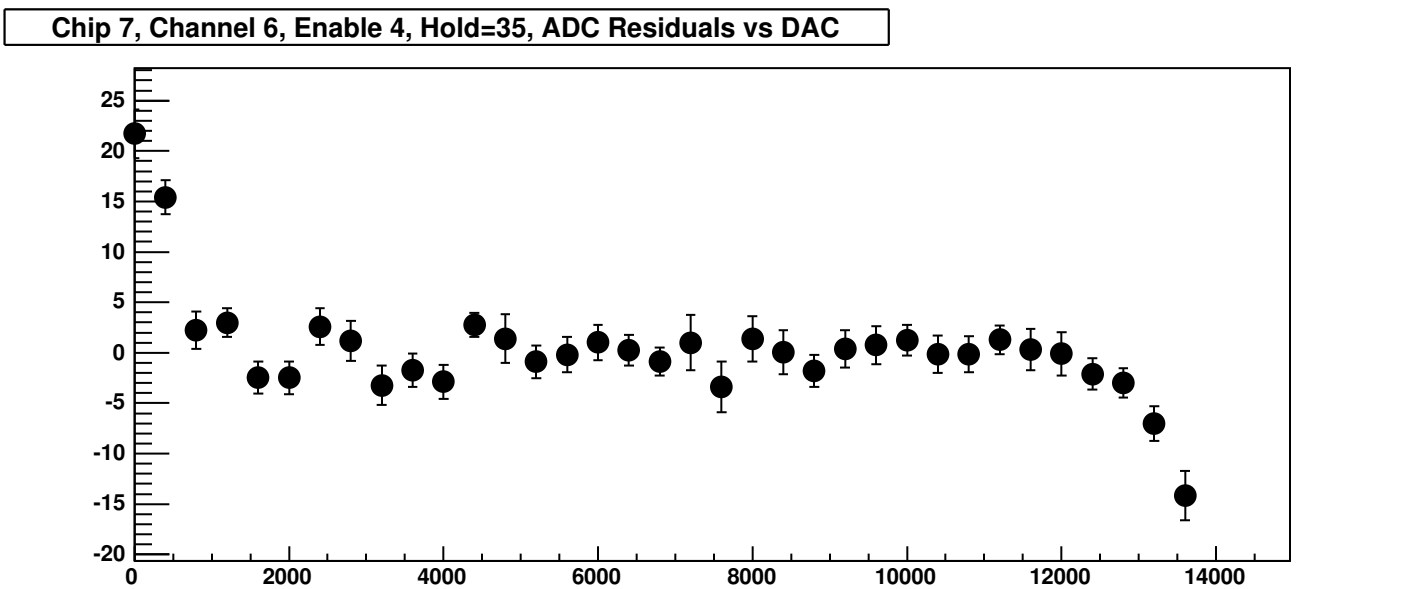


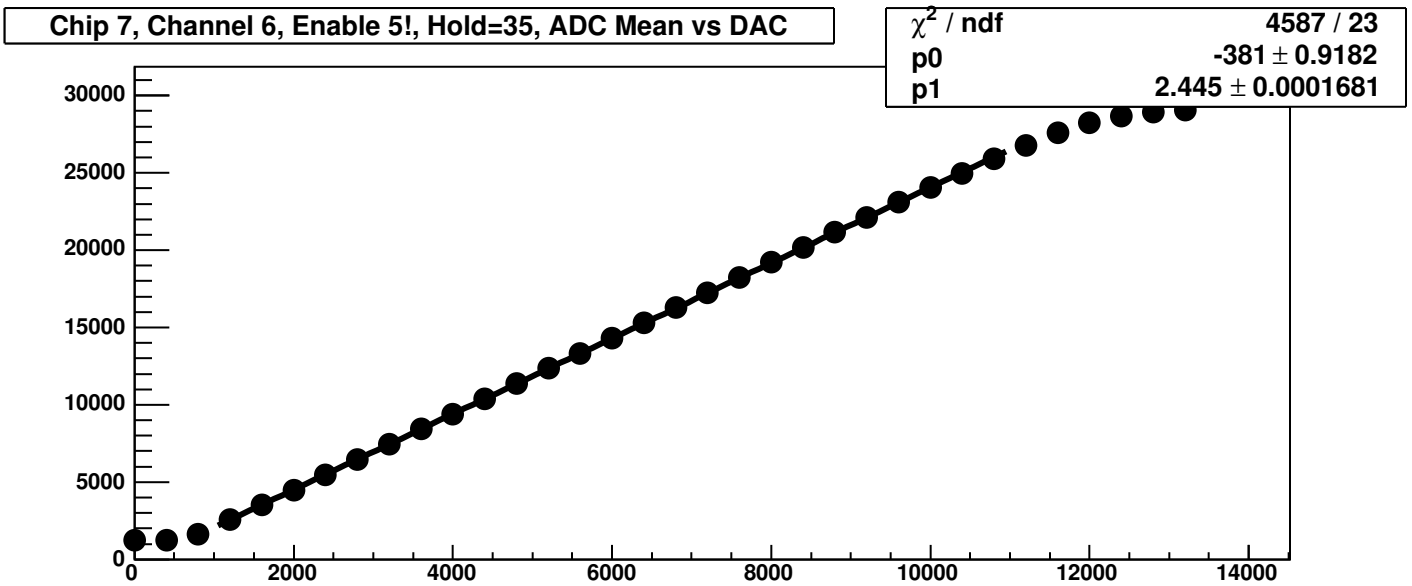
Chip 7, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

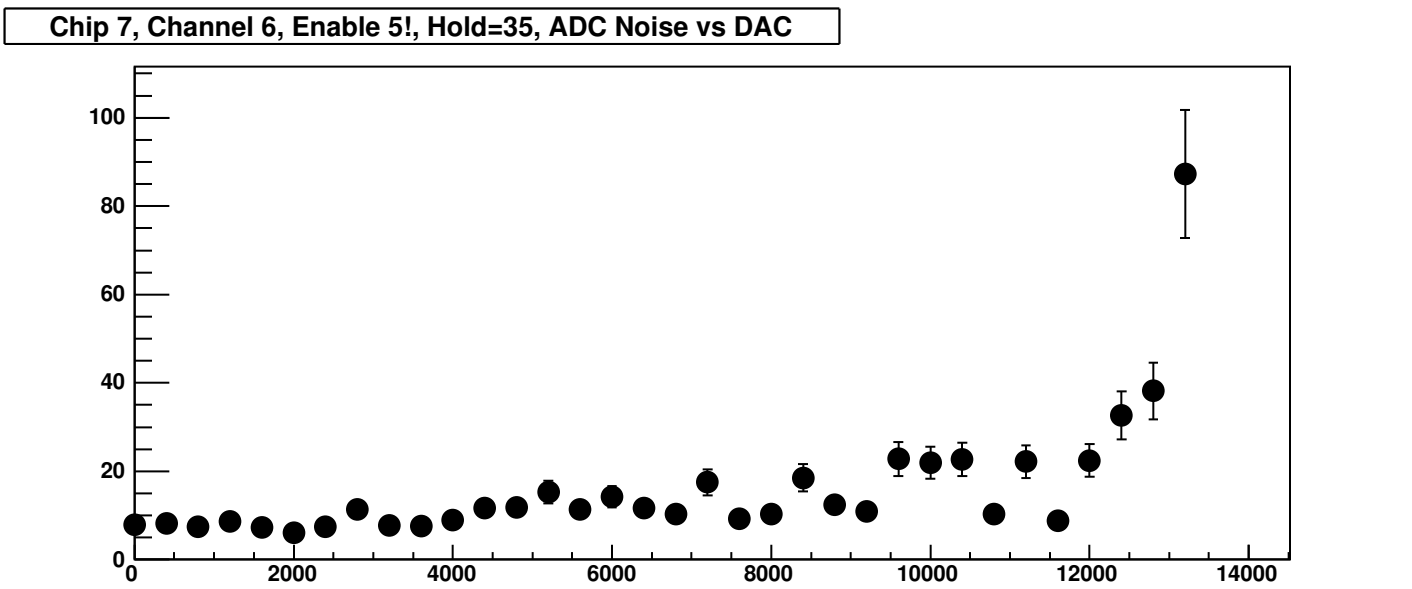


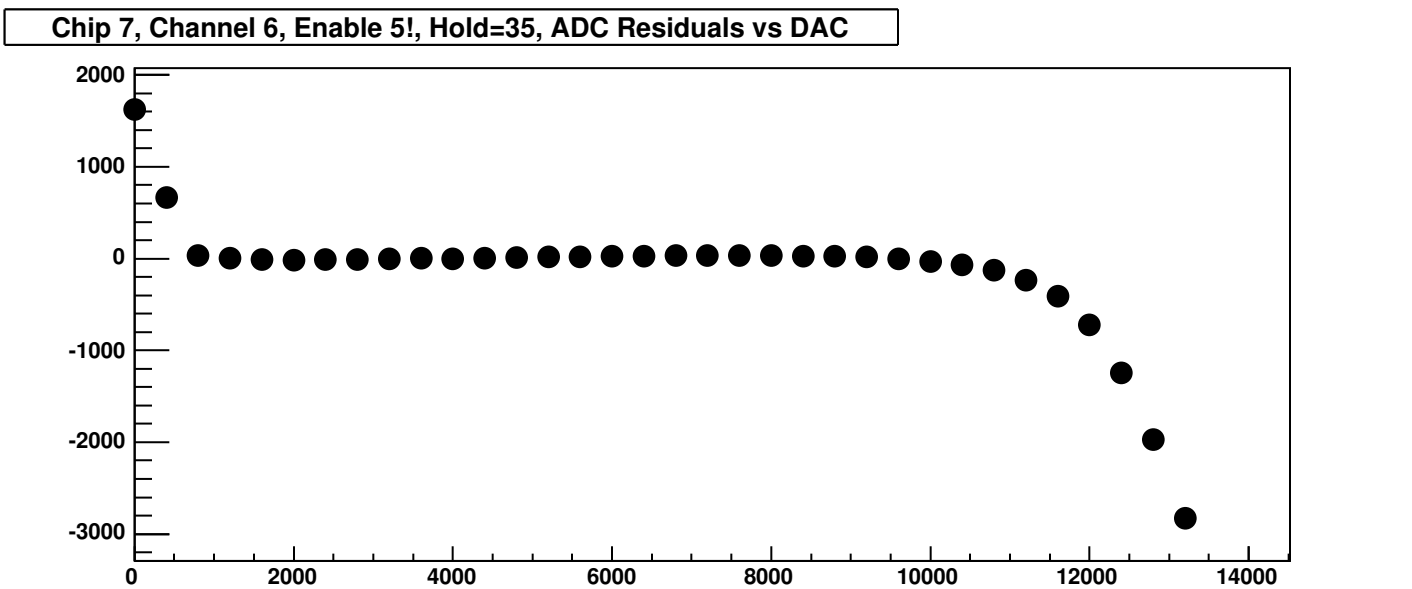

Chip 7, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.53 / 23
p0 -270.8 ± 0.7628
p1 0.02471 ± 0.0001185
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

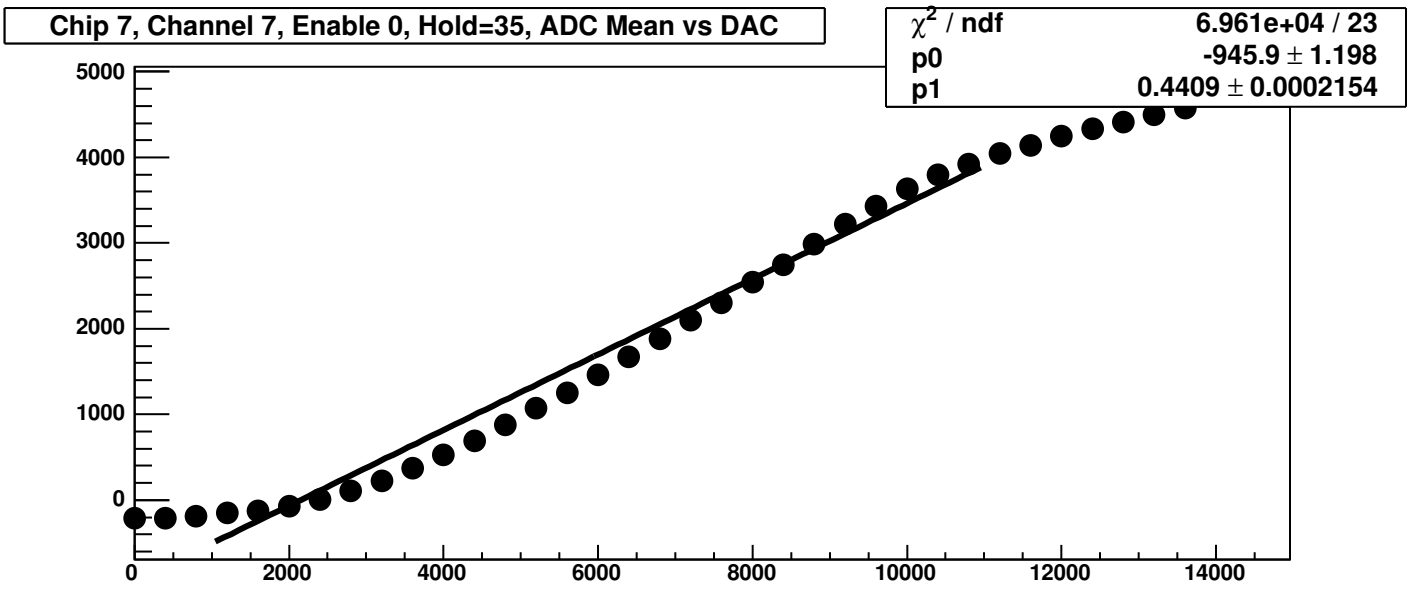

Chip 7, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

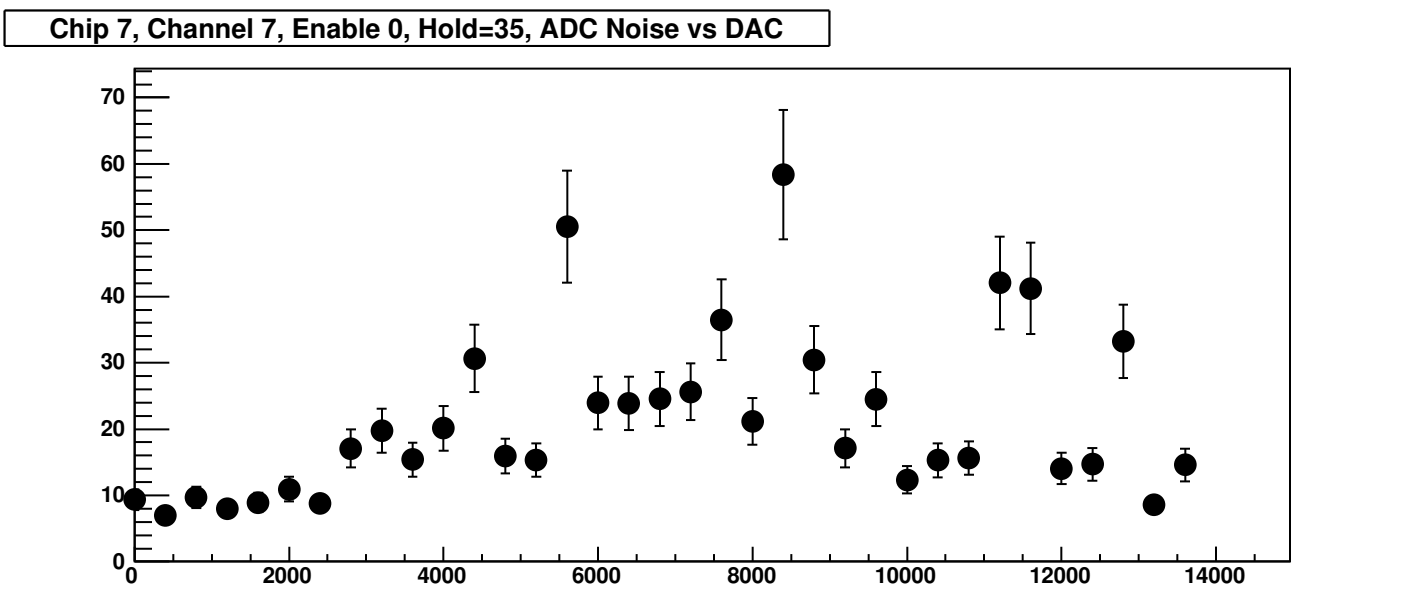

Chip 7, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

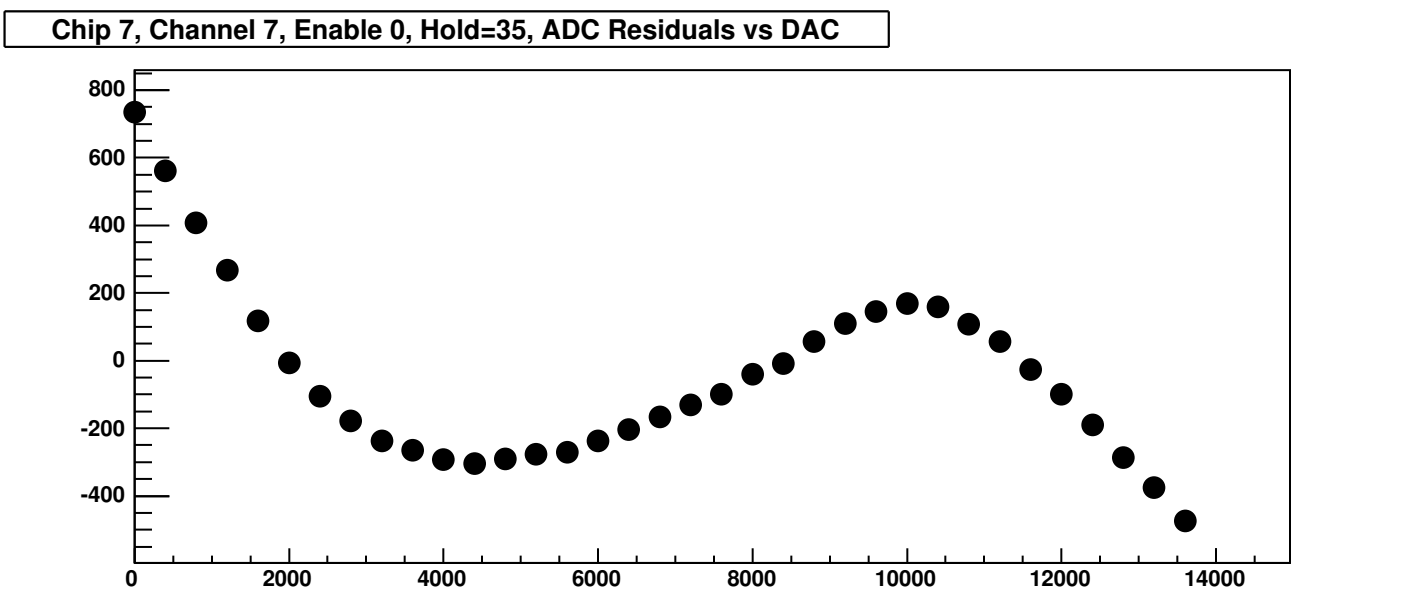

Chip 7, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4587 / 23
p0 -381 ± 0.9182
p1 2.445 ± 0.0001681
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

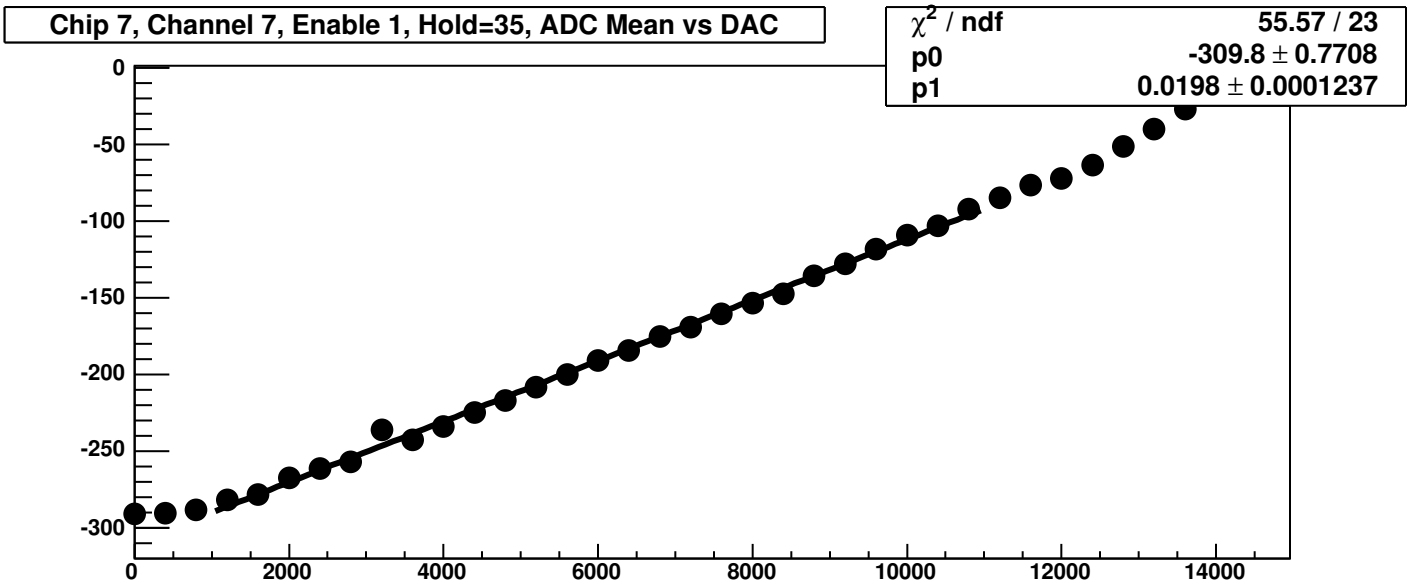

Chip 7, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

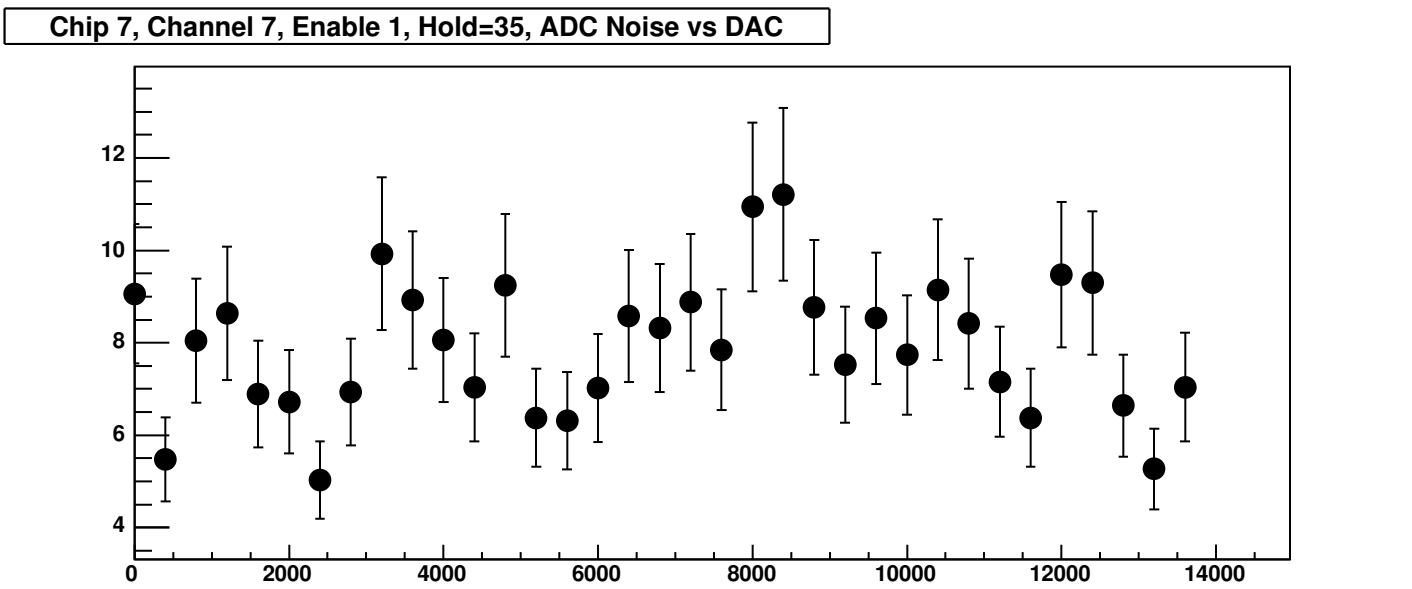

Chip 7, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

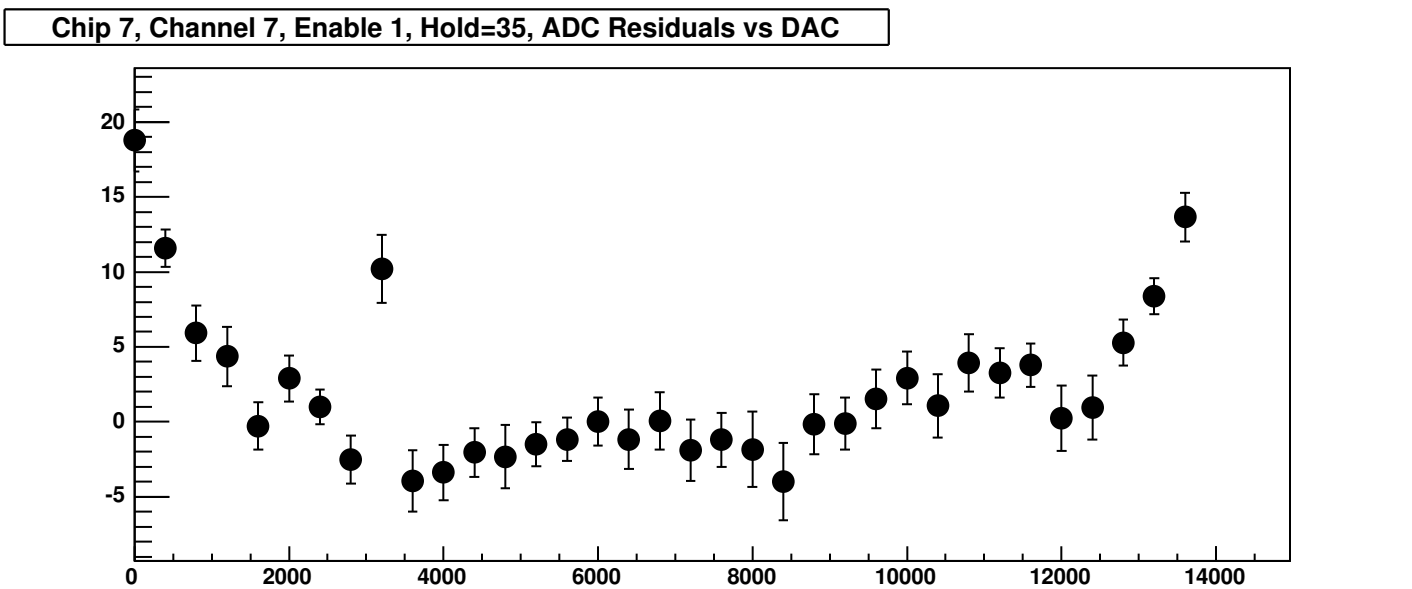

Chip 7, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 6.961e+04 / 23
p0 -945.9 ± 1.198
p1 0.4409 ± 0.0002154
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

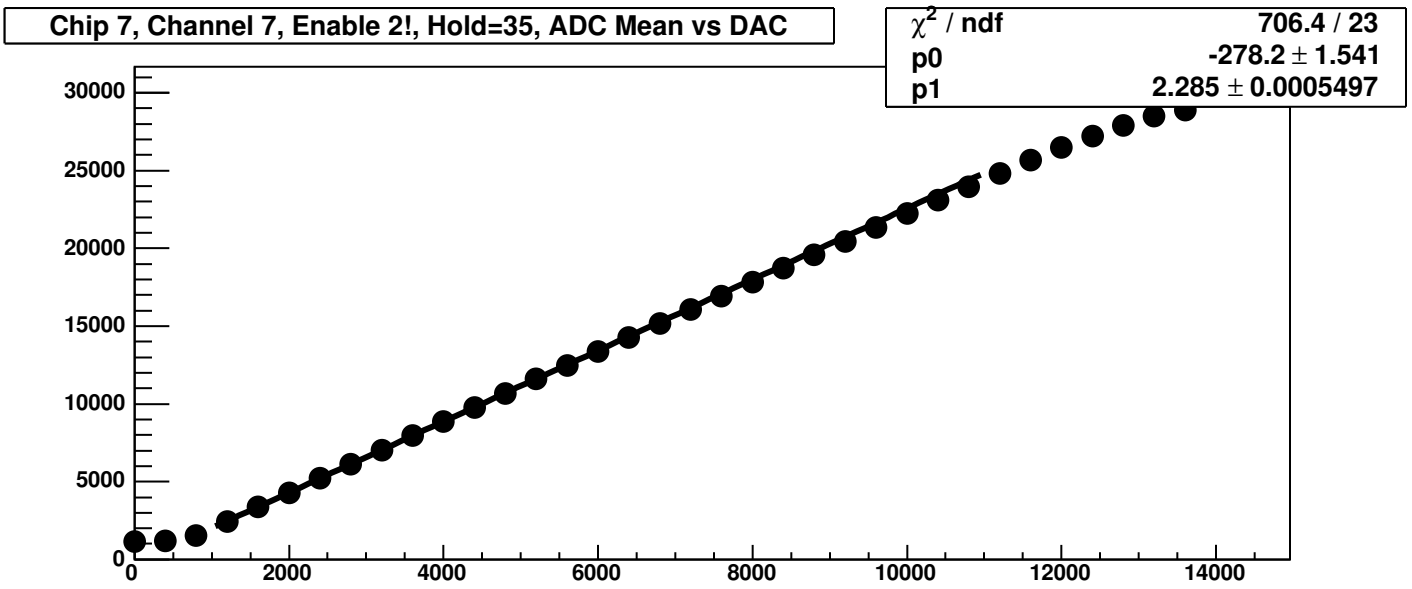

Chip 7, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

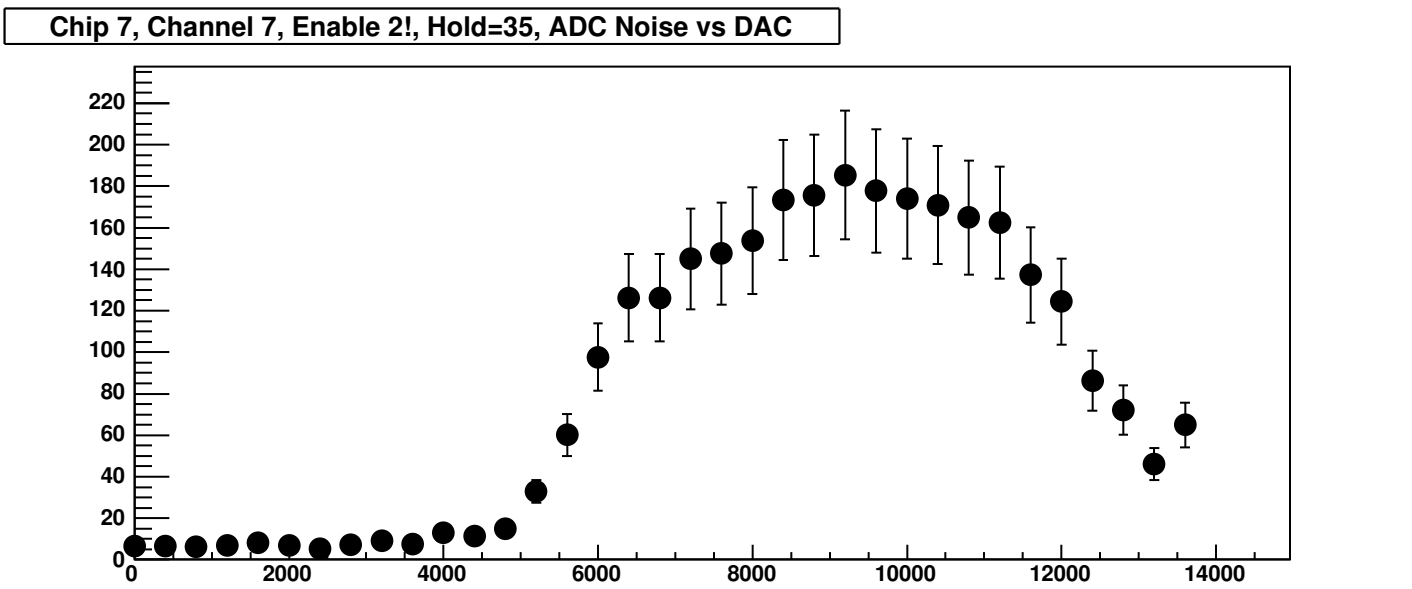

Chip 7, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

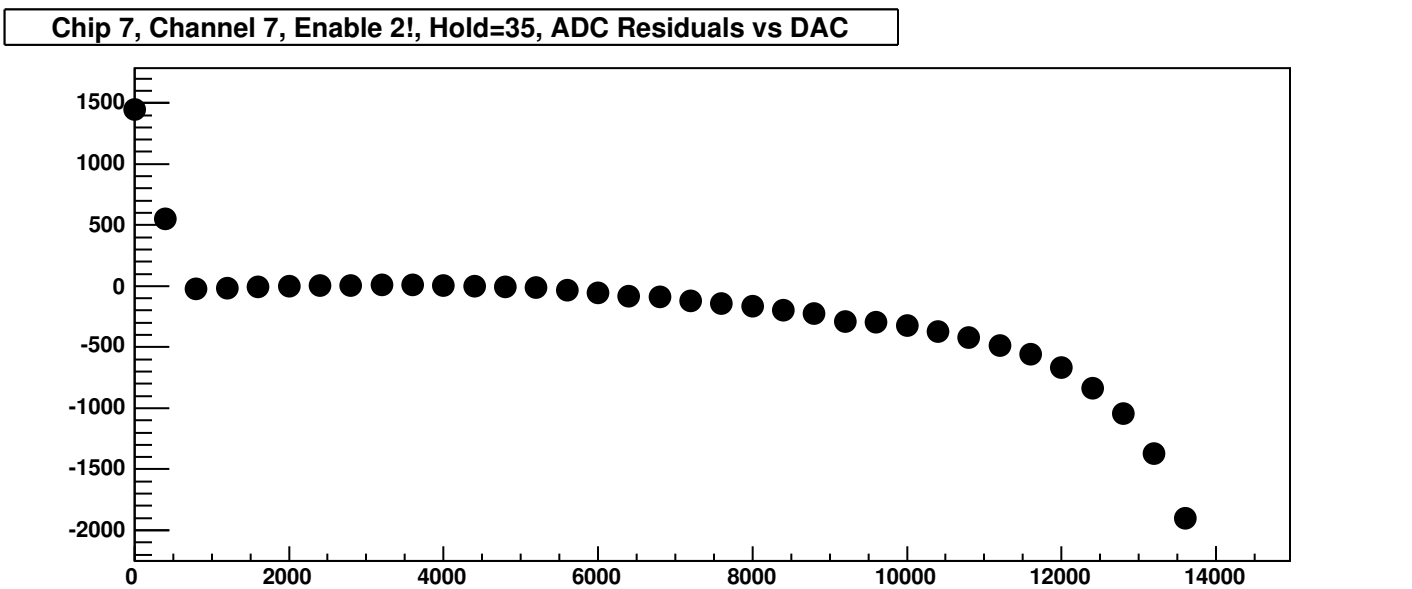

Chip 7, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 55.57 / 23
p0 -309.8 ± 0.7708
p1 0.0198 ± 0.0001237
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

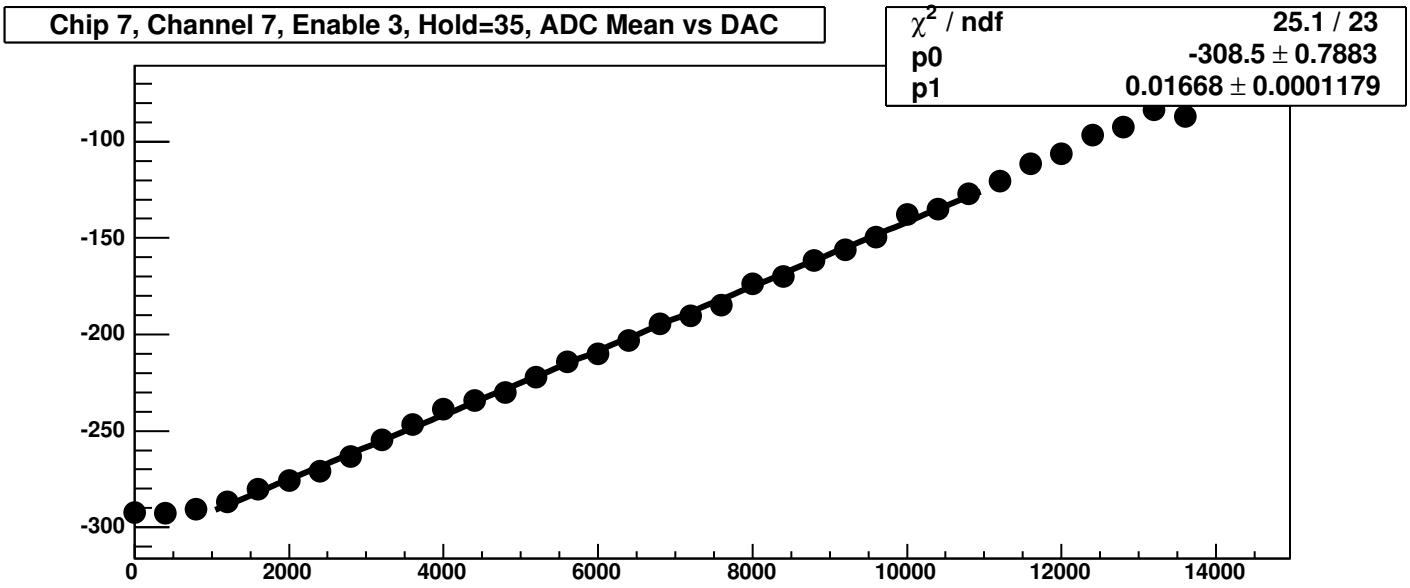

Chip 7, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

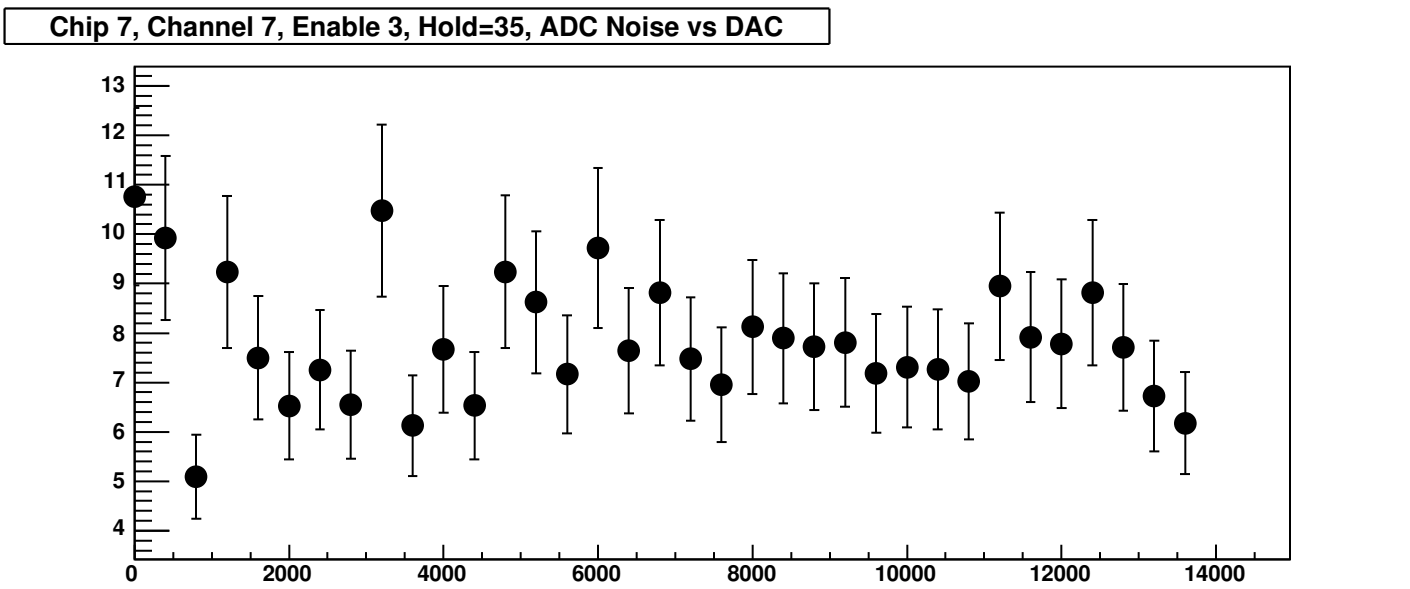

Chip 7, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

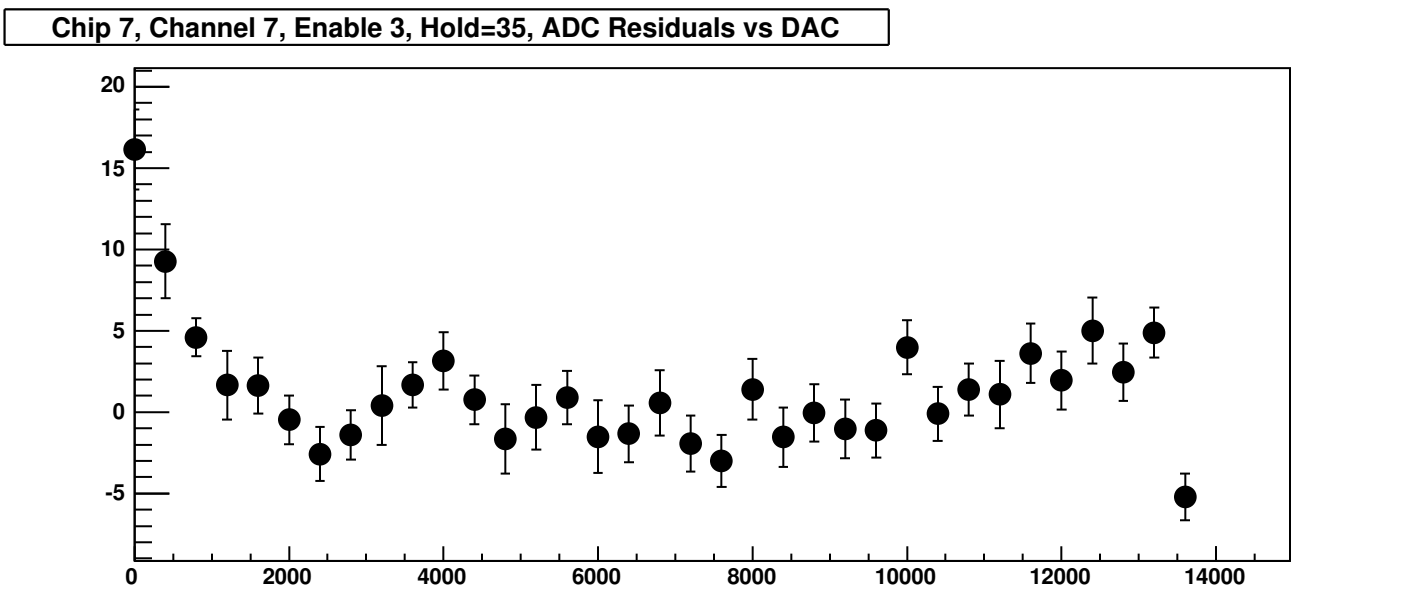

Chip 7, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 706.4 / 23
p0 -278.2 ± 1.541
p1 2.285 ± 0.0005497
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

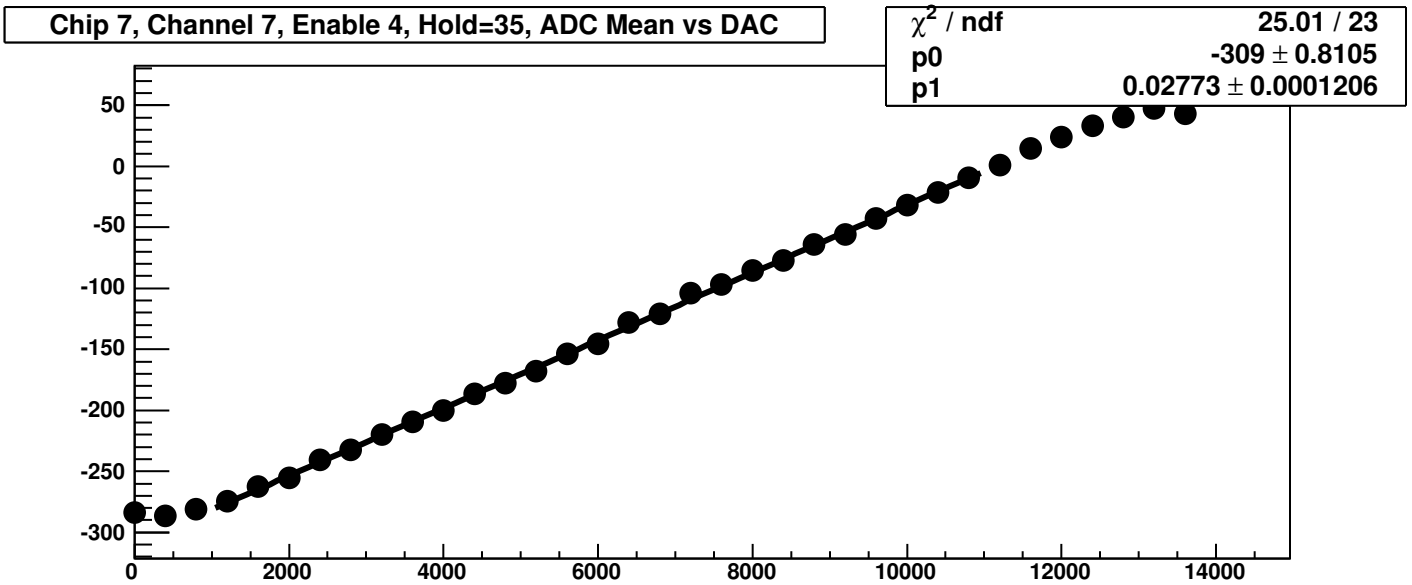

Chip 7, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

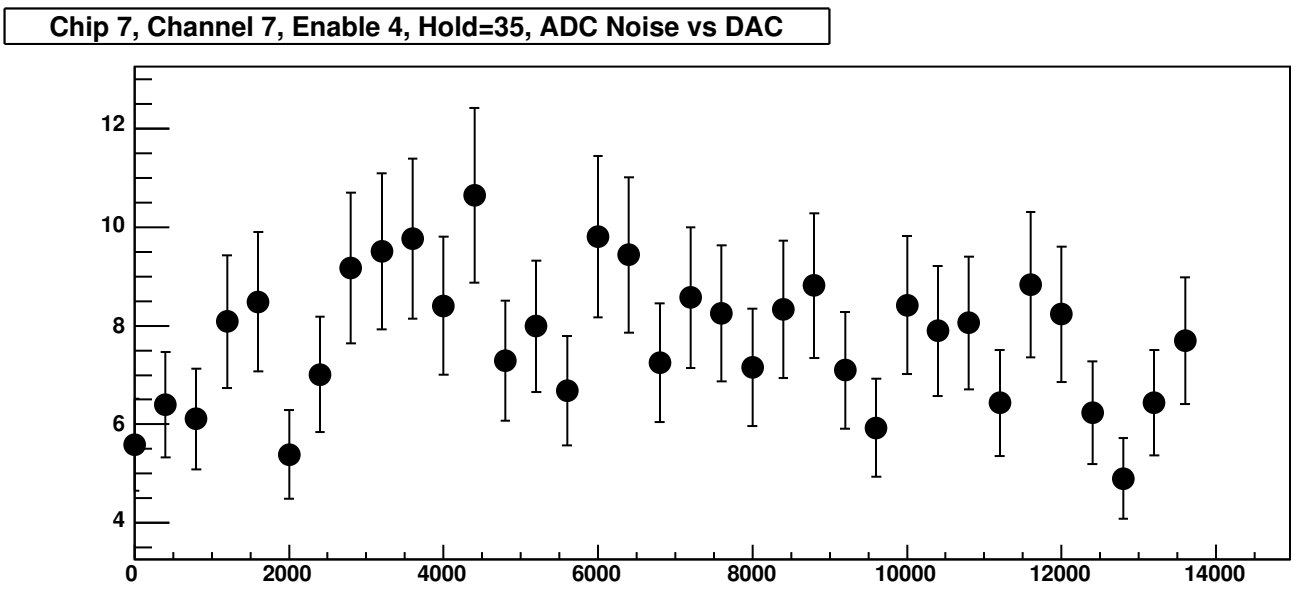

Chip 7, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

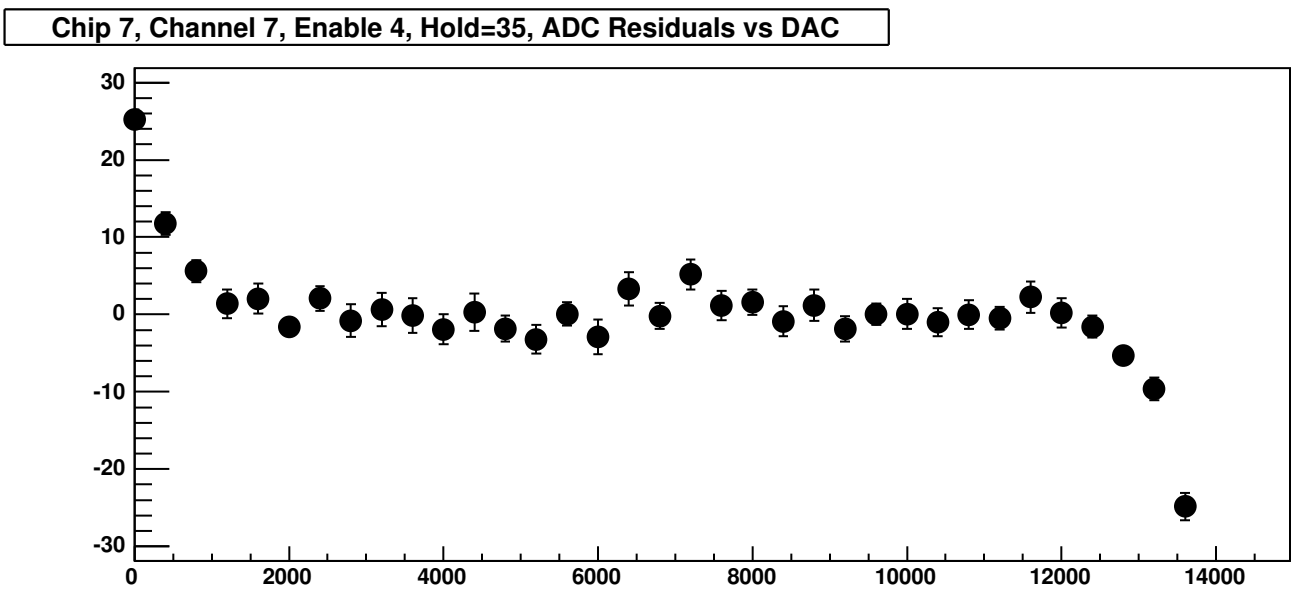

Chip 7, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.1 / 23
p0 -308.5 ± 0.7883
p1 0.01668 ± 0.0001179
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

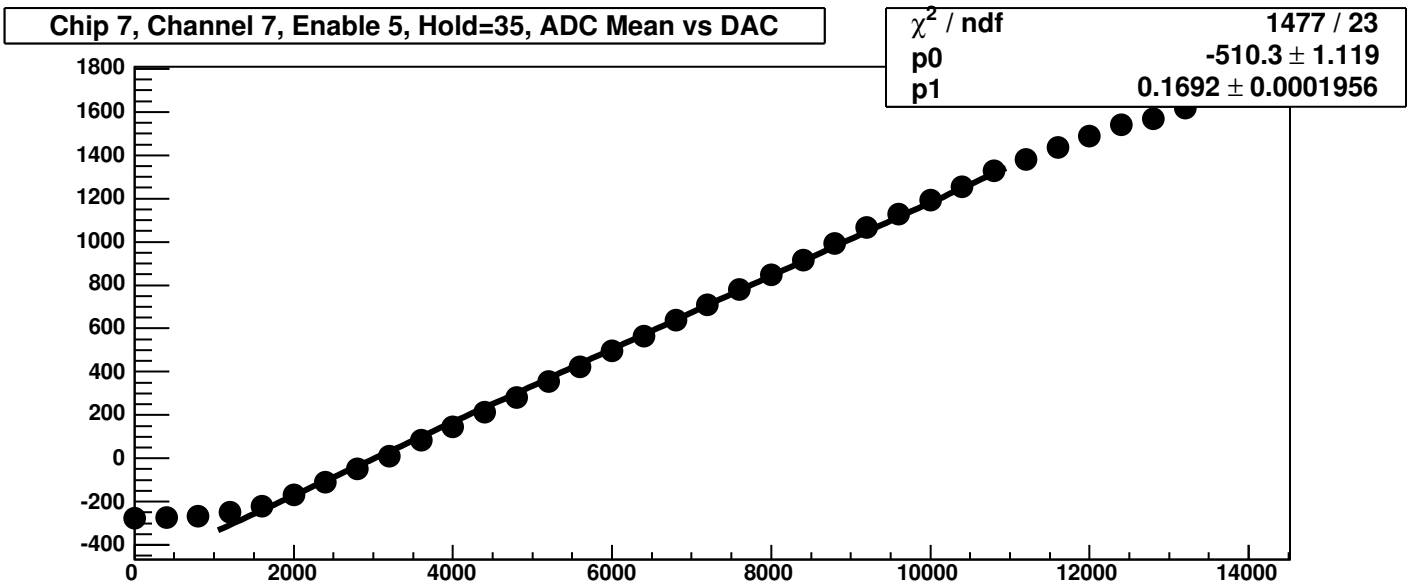

Chip 7, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

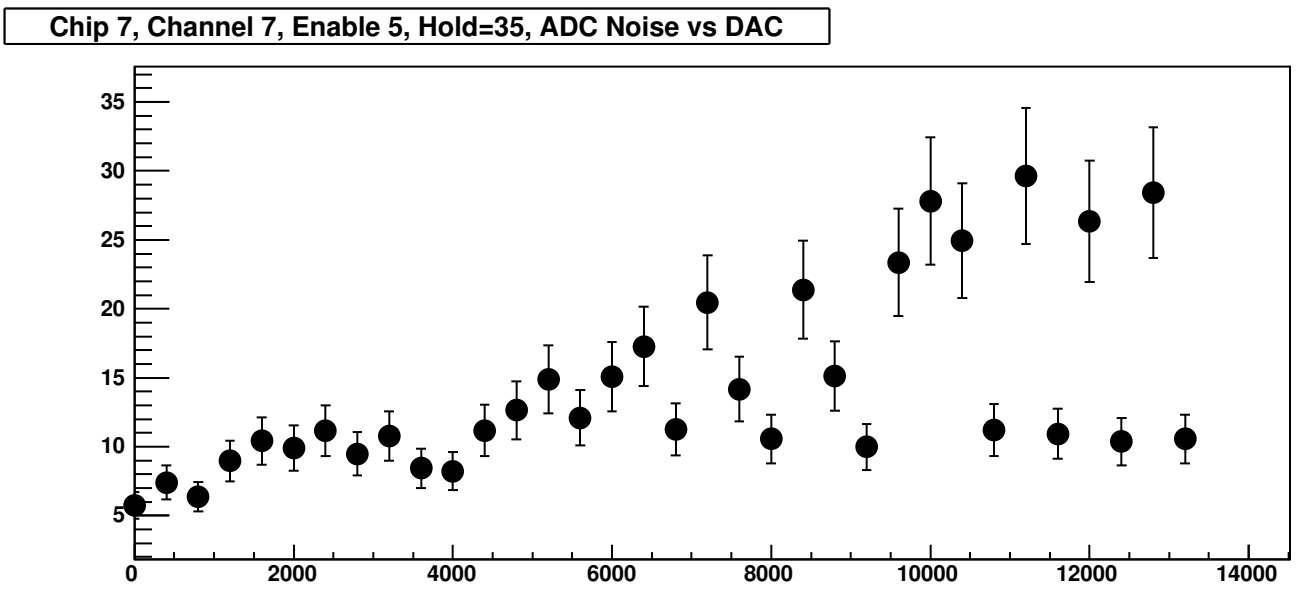

Chip 7, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

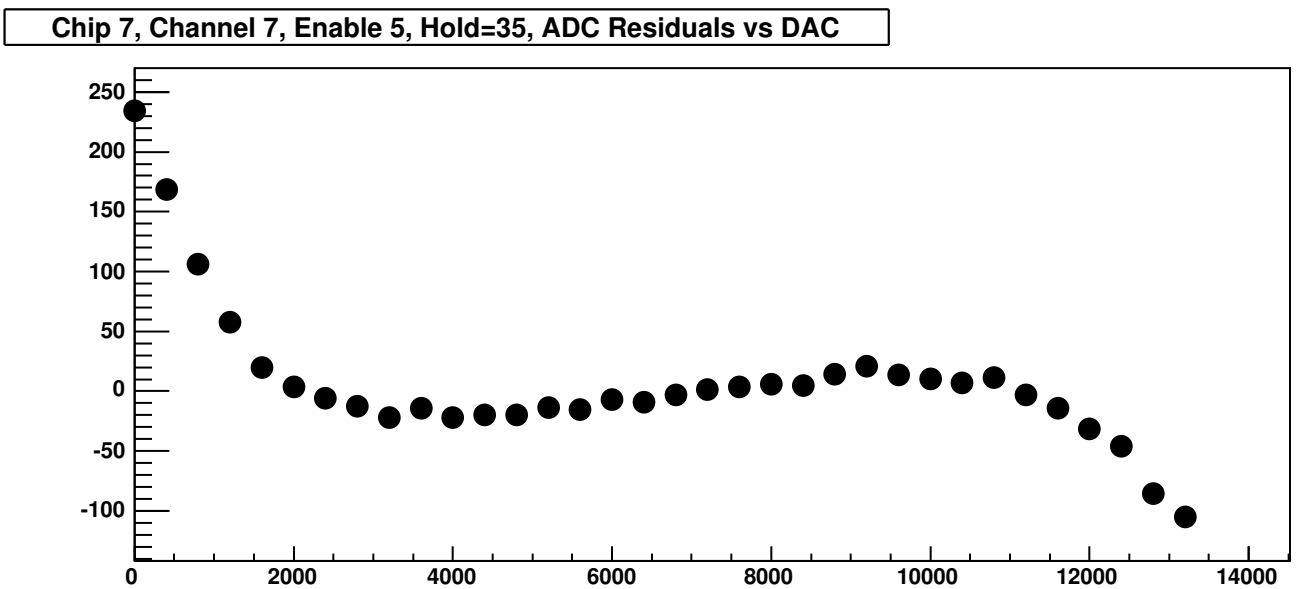

Chip 7, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 25.01 / 23
p0 -309 ± 0.8105
p1 0.02773 ± 0.0001206
50
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

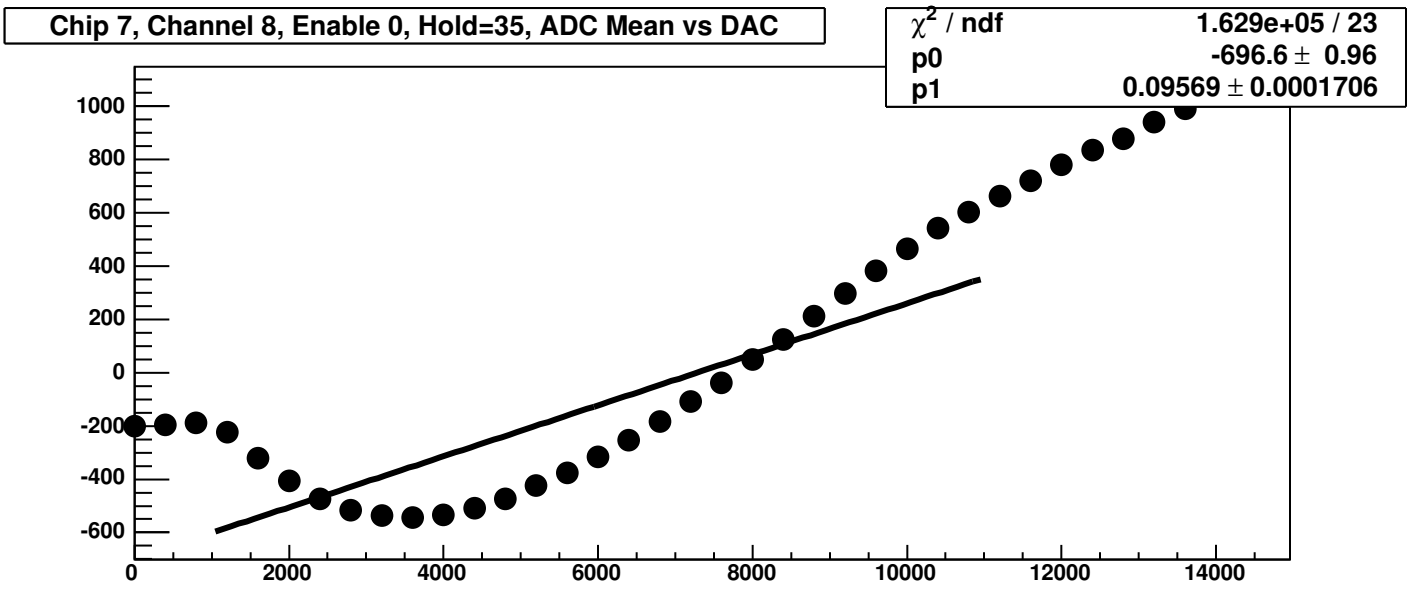

Chip 7, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

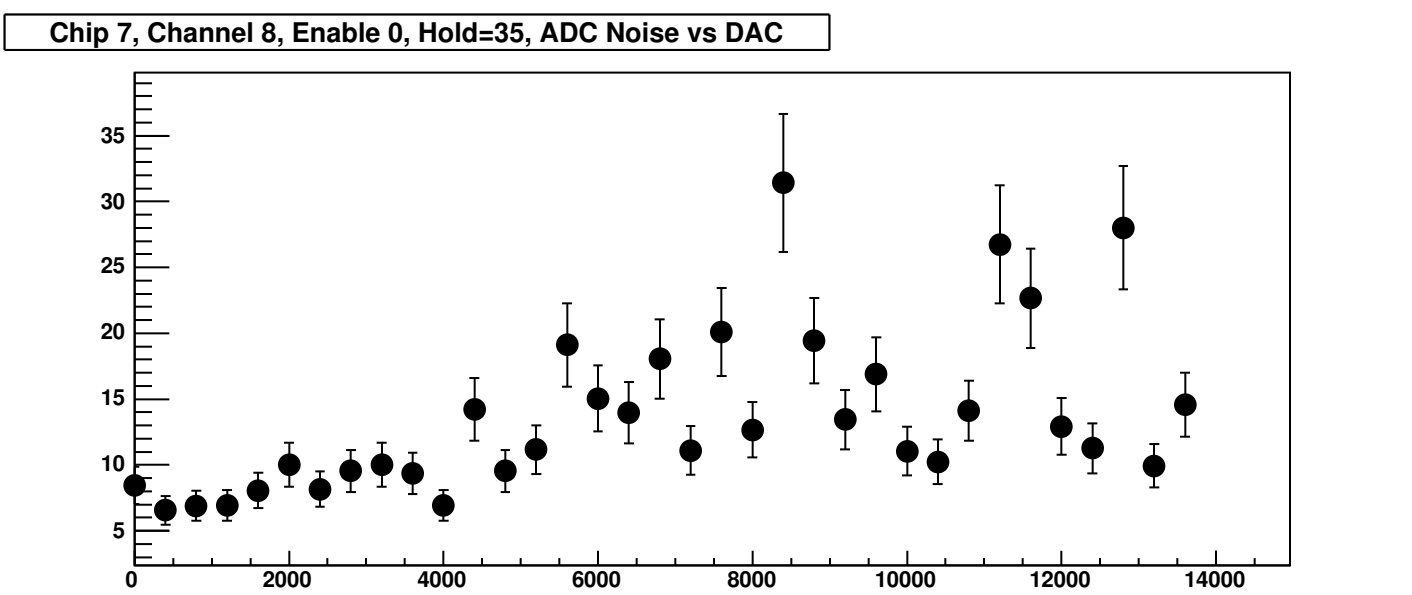

Chip 7, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

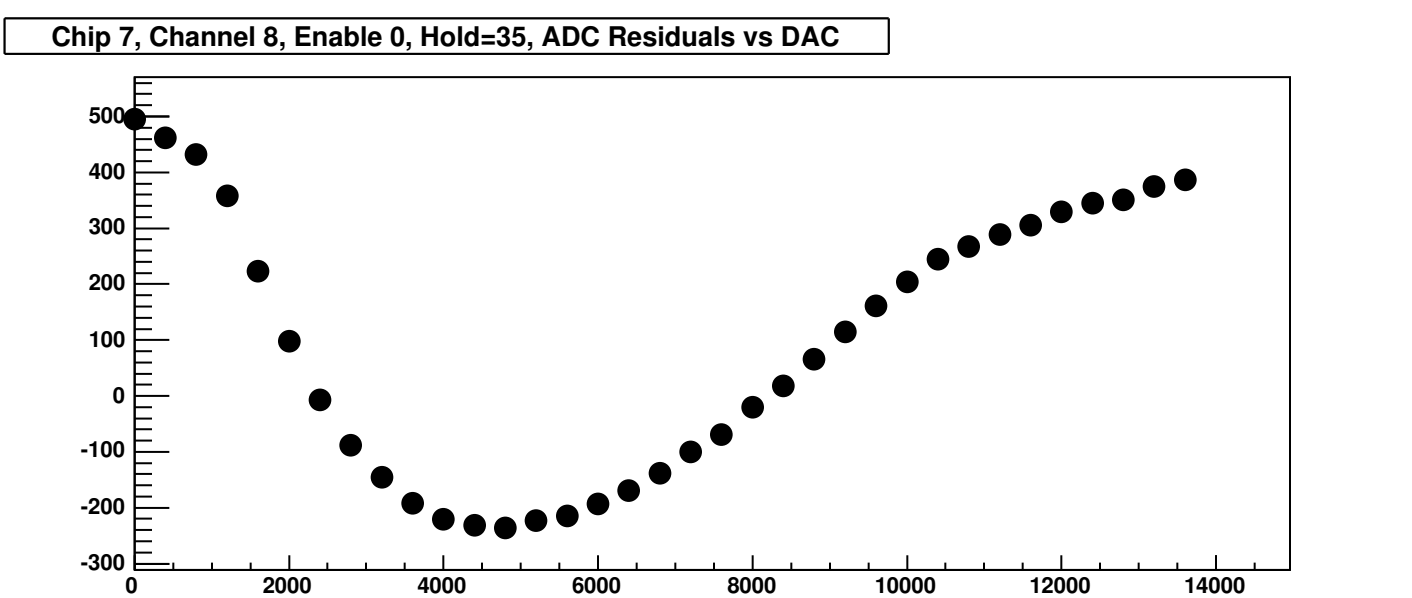

Chip 7, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1477 / 23
p0 -510.3 ± 1.119
p1 0.1692 ± 0.0001956
1800
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

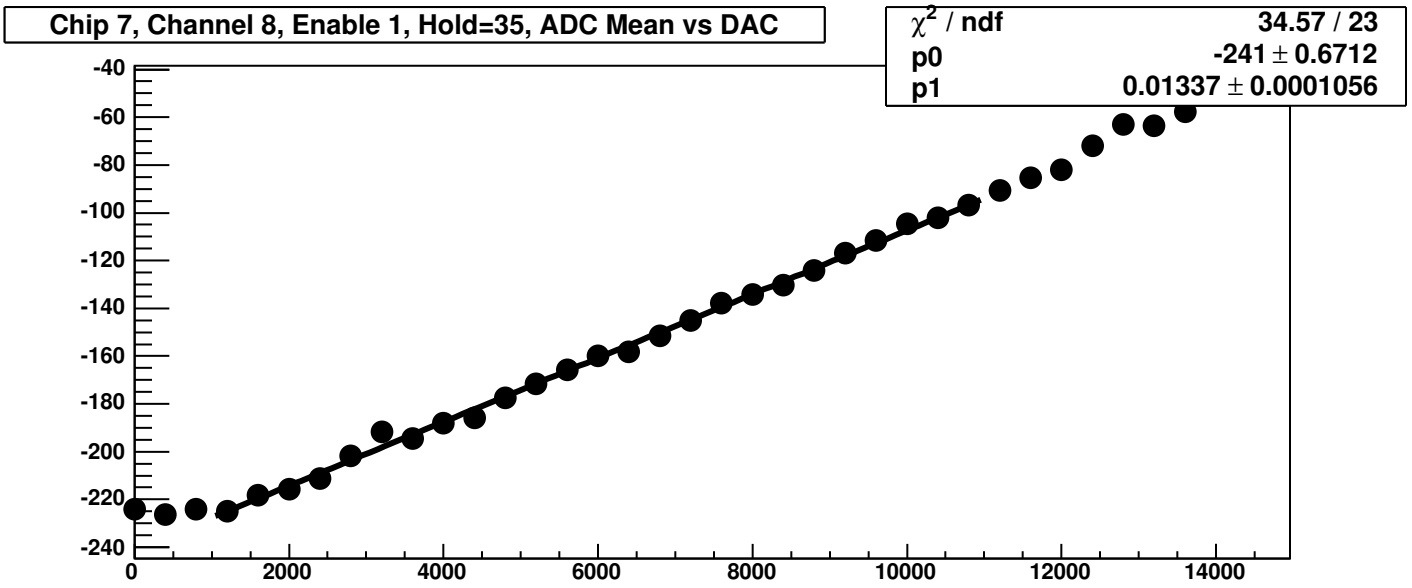

Chip 7, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

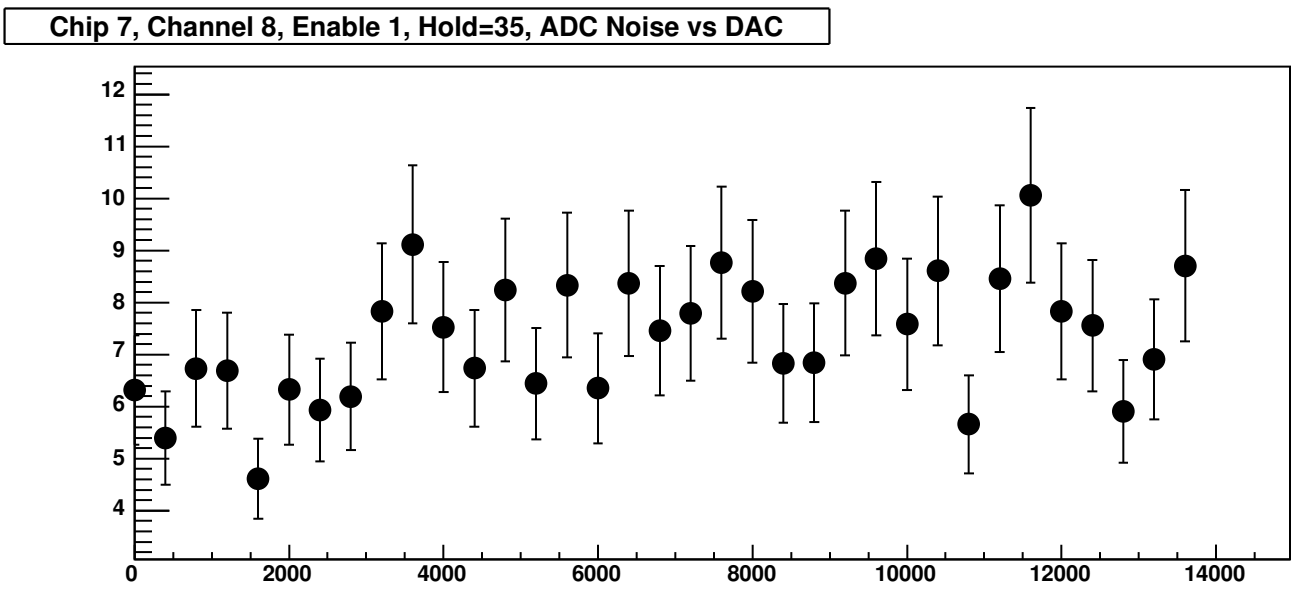

Chip 7, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

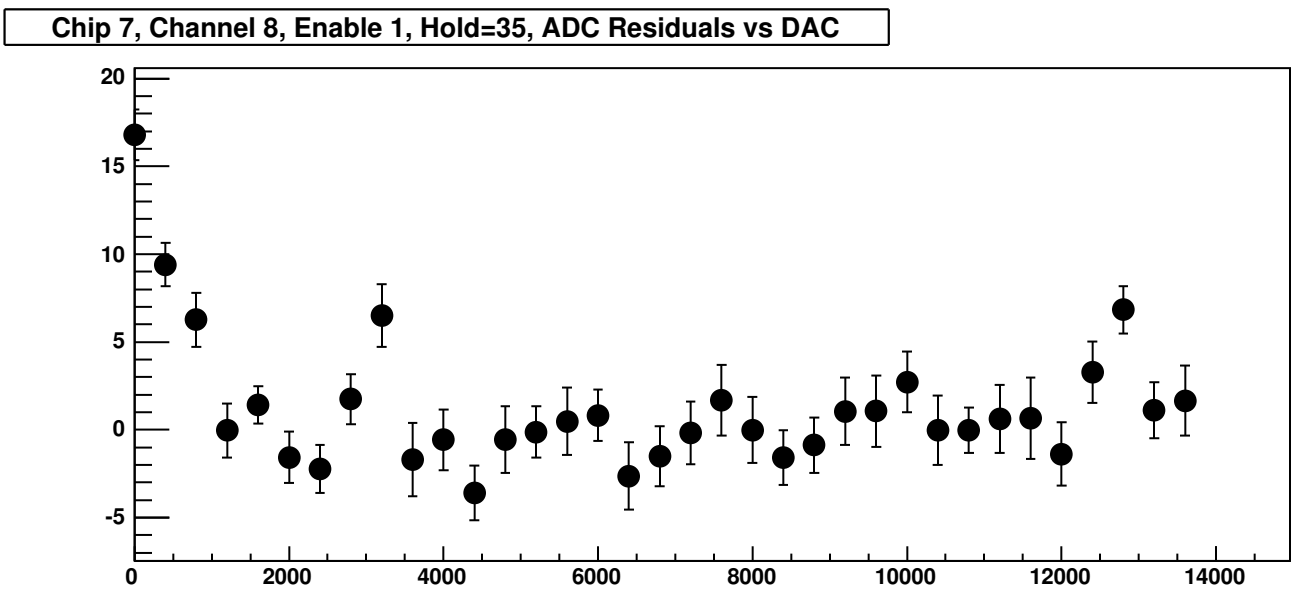

Chip 7, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.629e+05 / 23
p0 -696.6 ± 0.96
p1 0.09569 ± 0.0001706
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

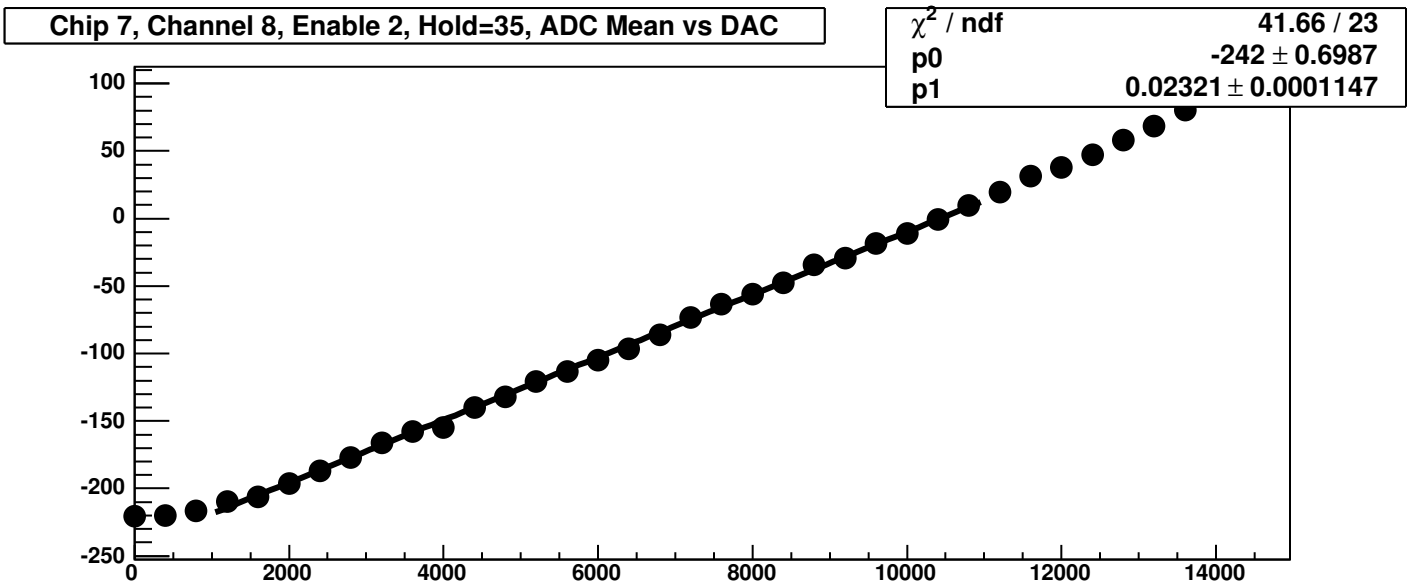

Chip 7, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

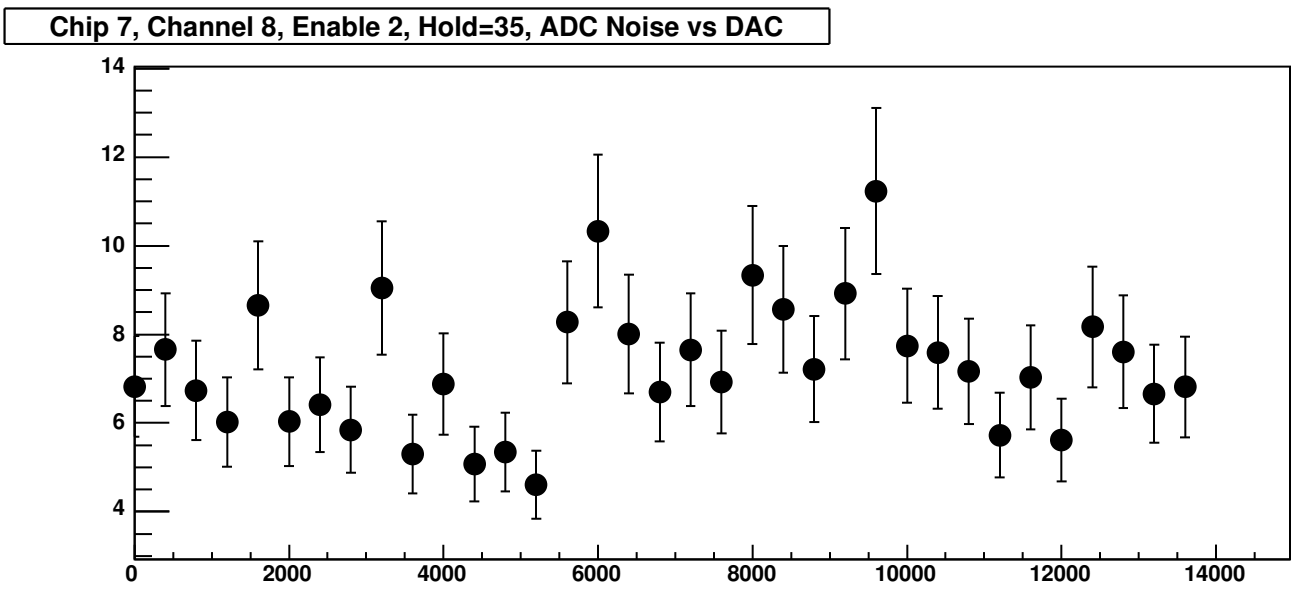

Chip 7, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

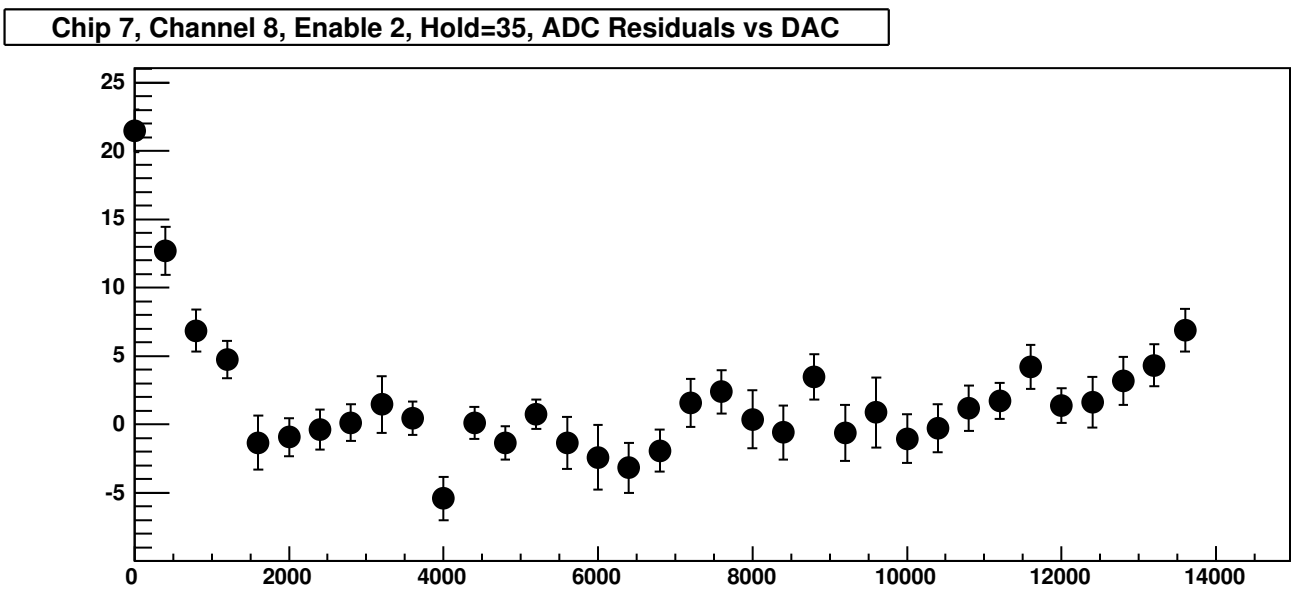

Chip 7, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 34.57 / 23
p0 -241 ± 0.6712
p1 0.01337 ± 0.0001056
-40
-60
-80
-100
-120
-140
-160
-180
-200
-220
-240
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

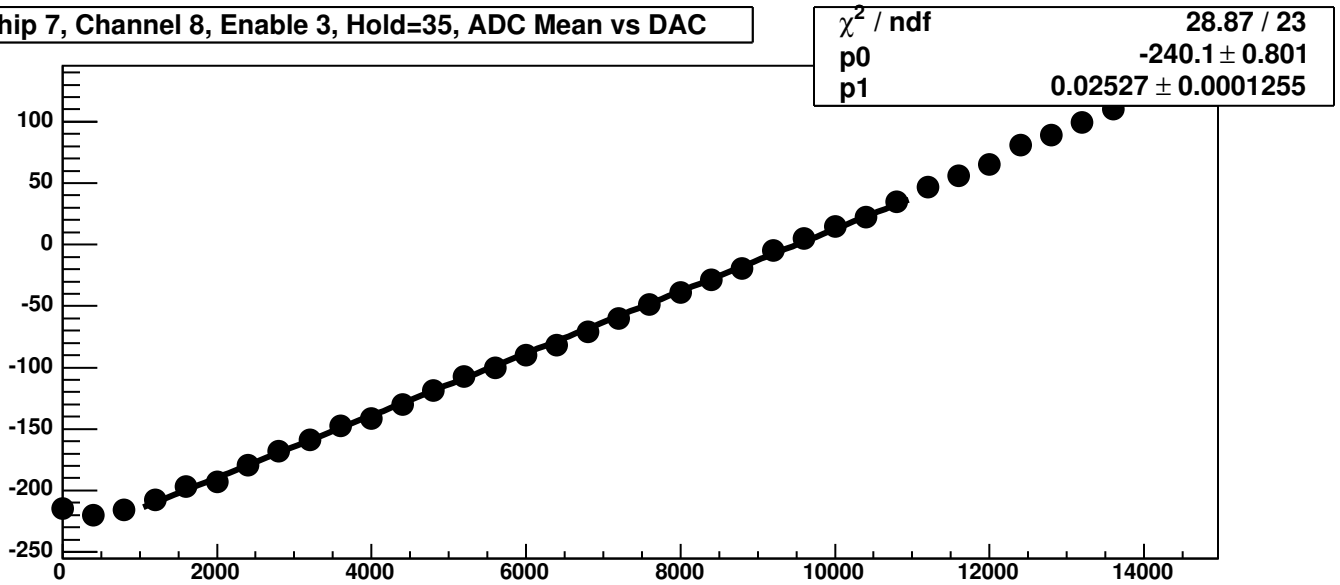

Chip 7, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 41.66 / 23
p0 -242 ± 0.6987
p1 0.02321 ± 0.0001147
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

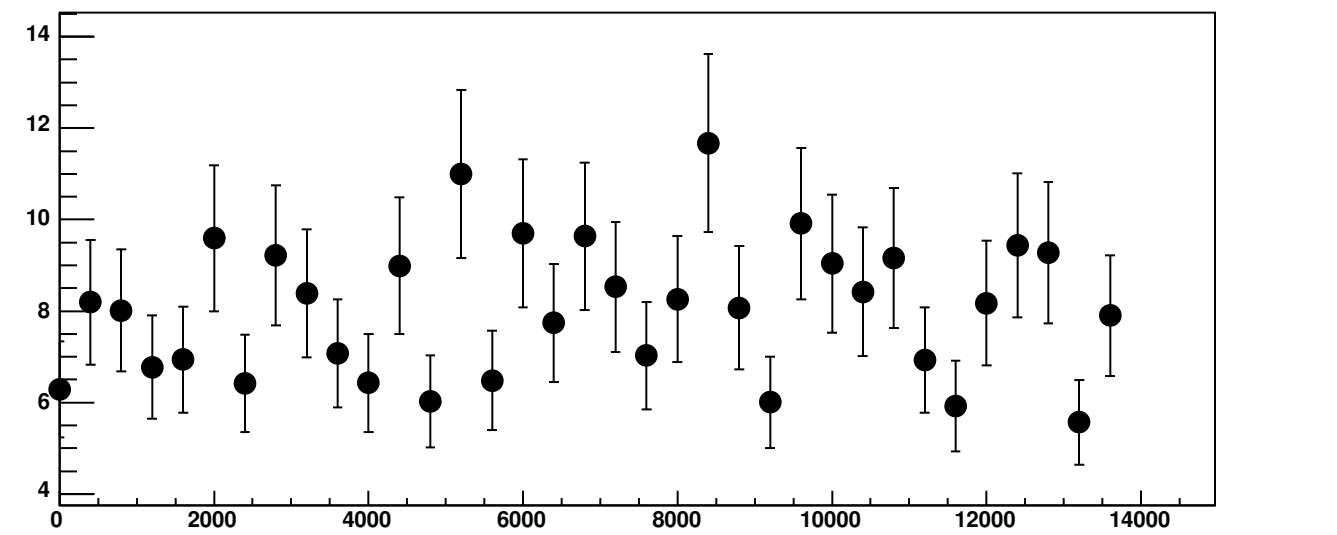

Chip 7, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

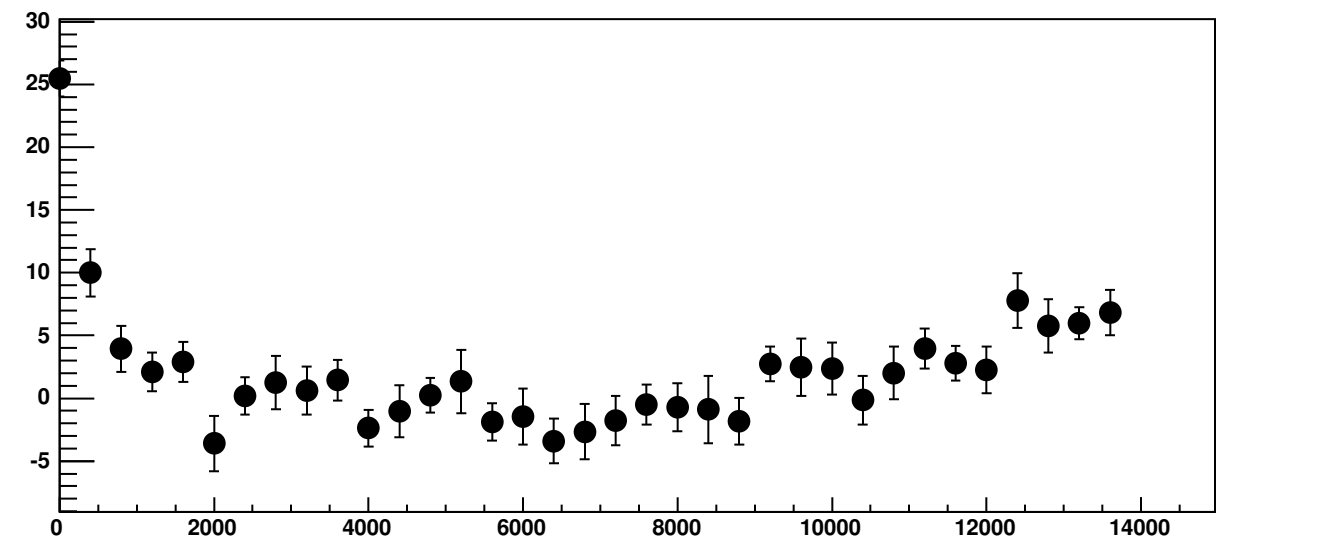
Chip 7, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC



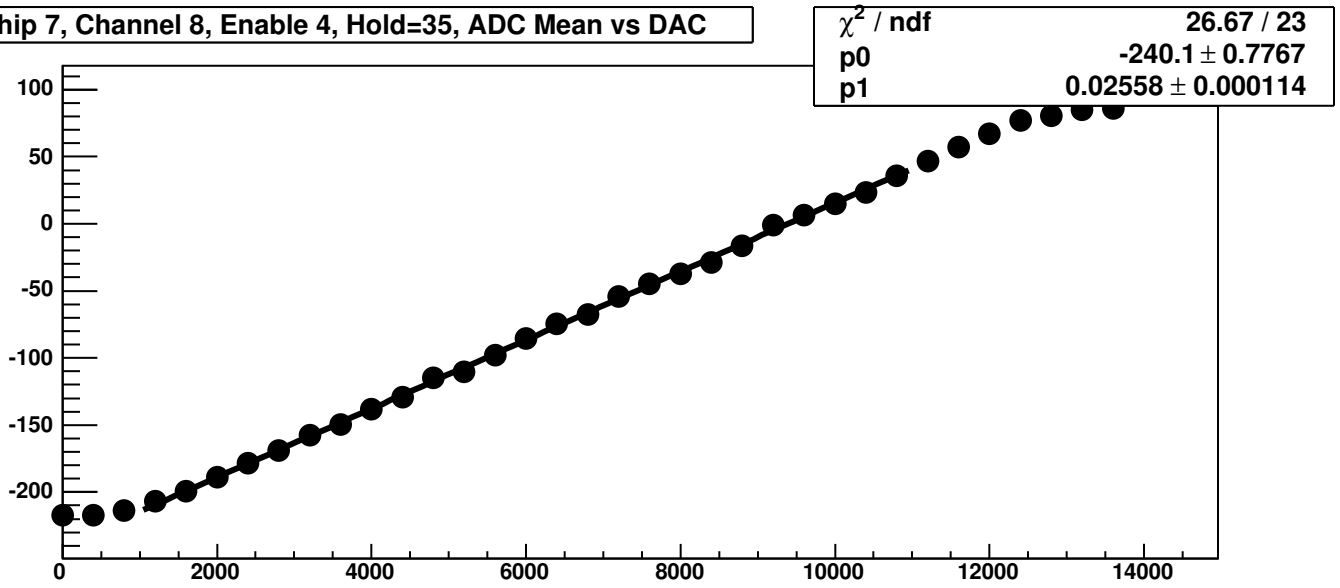
Chip 7, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC



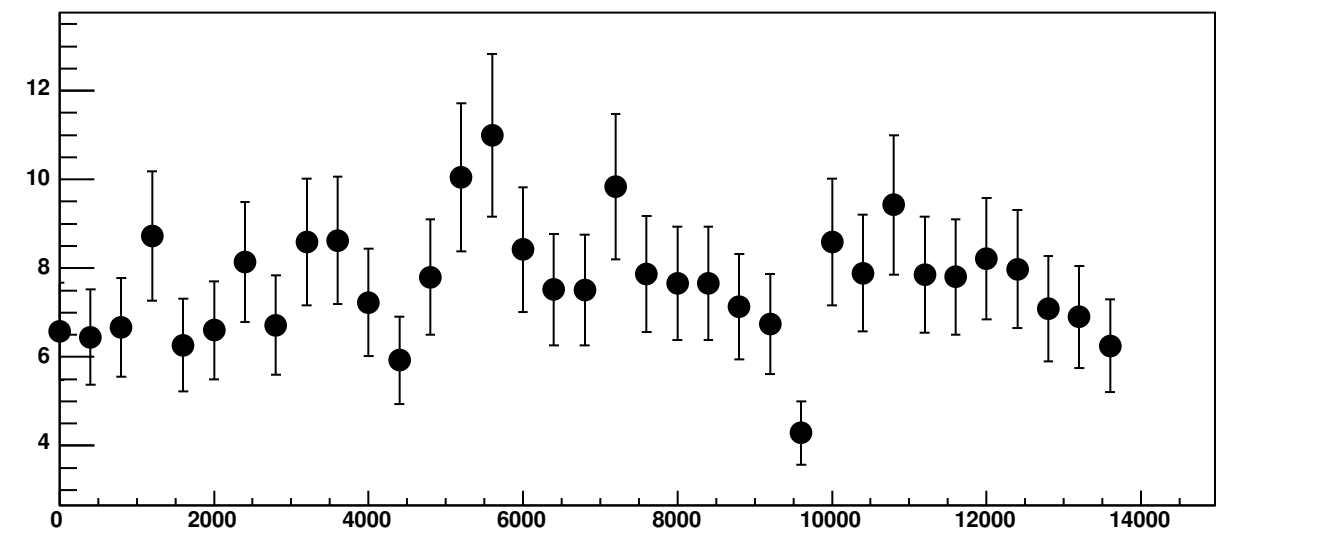
Chip 7, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



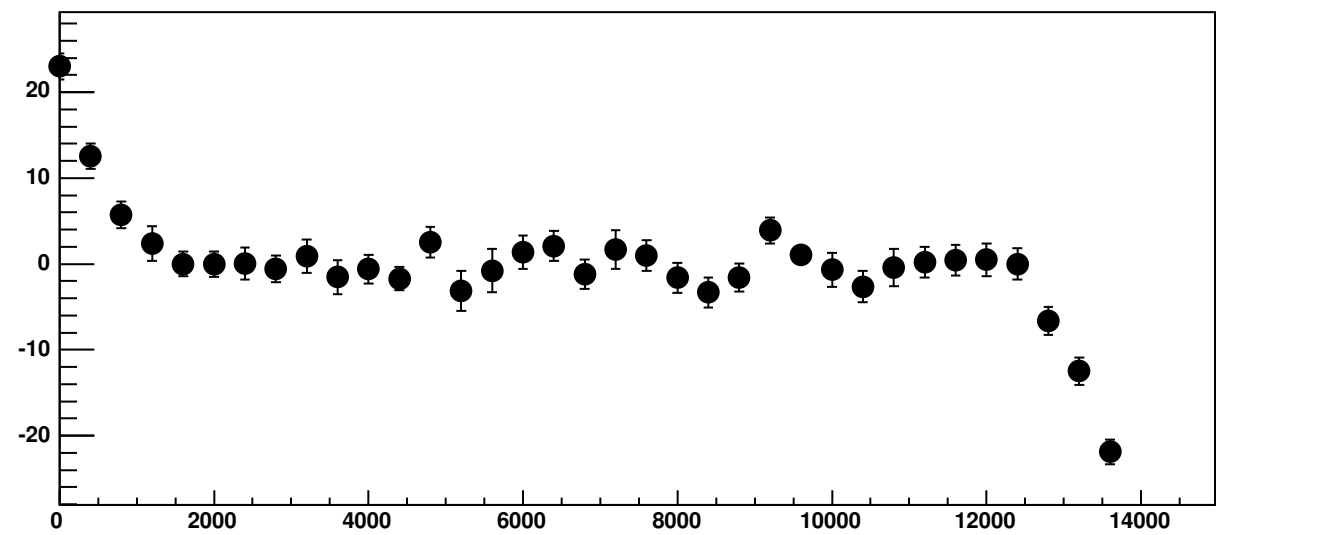
Chip 7, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

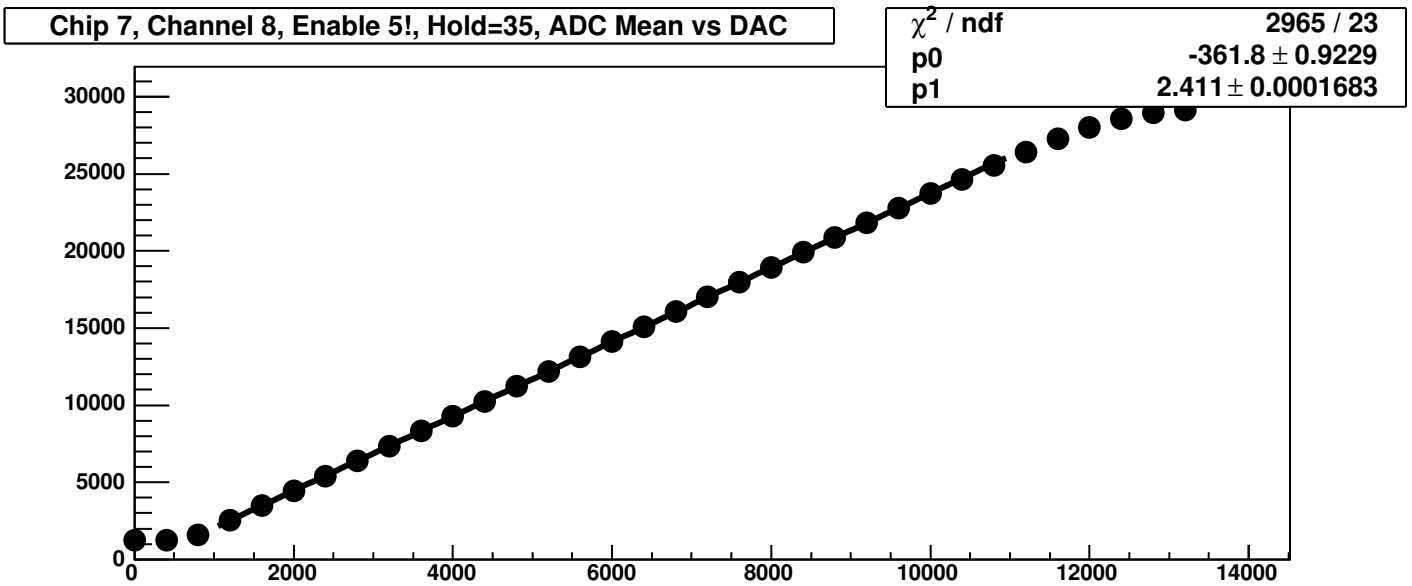


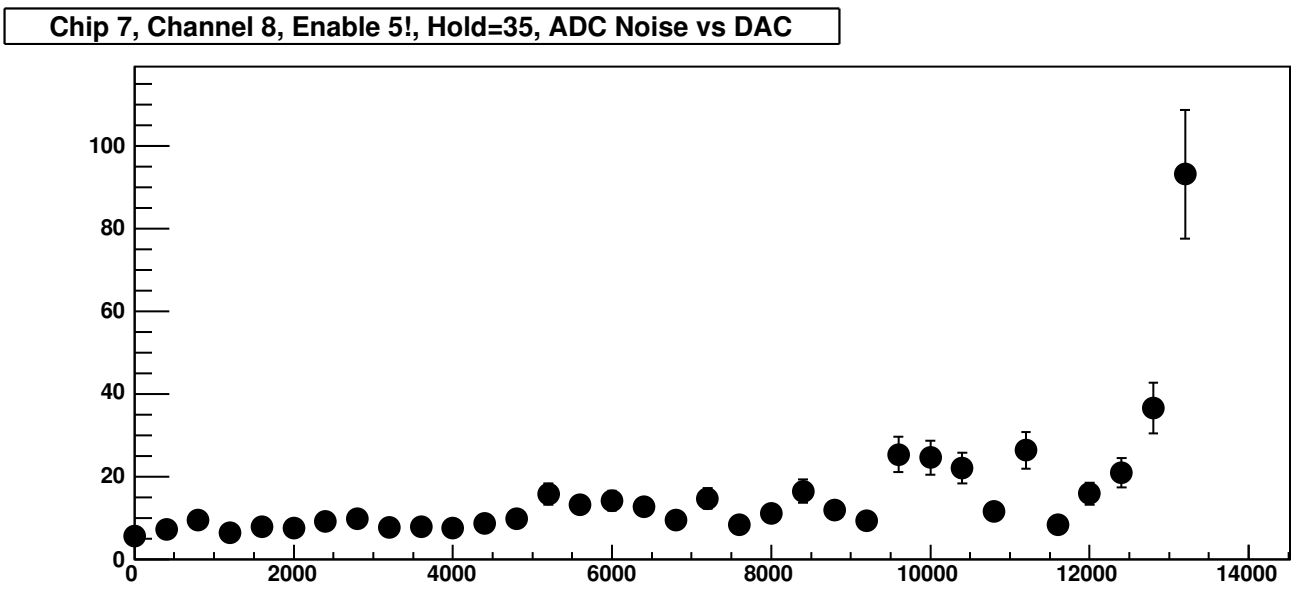
Chip 7, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC

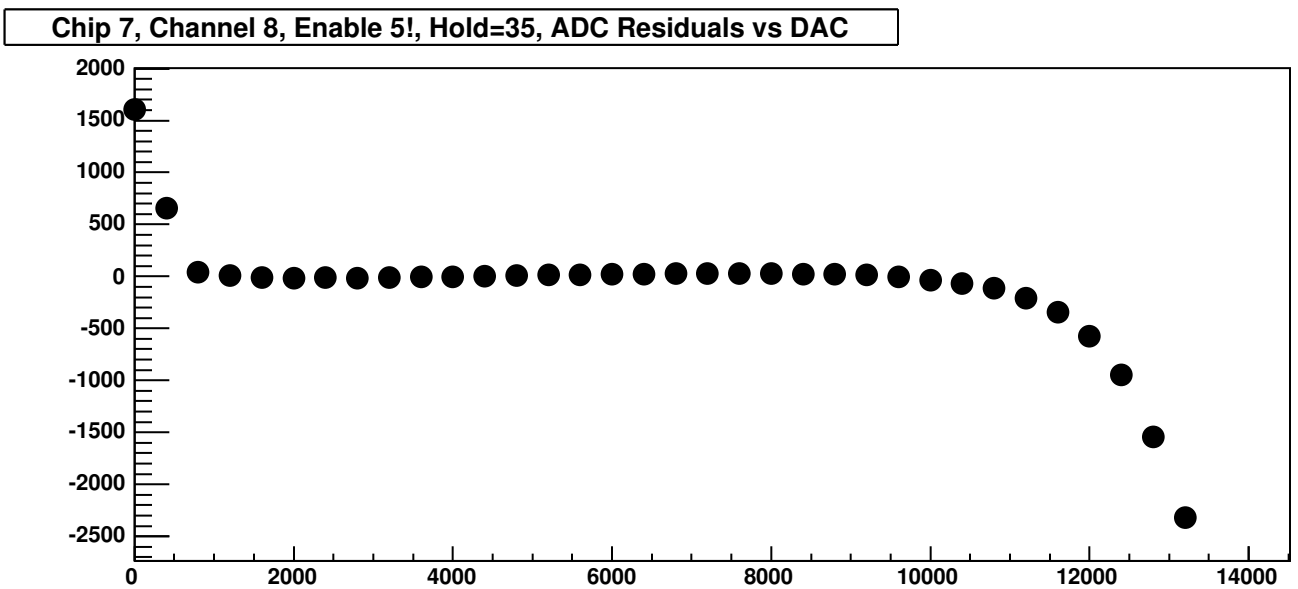


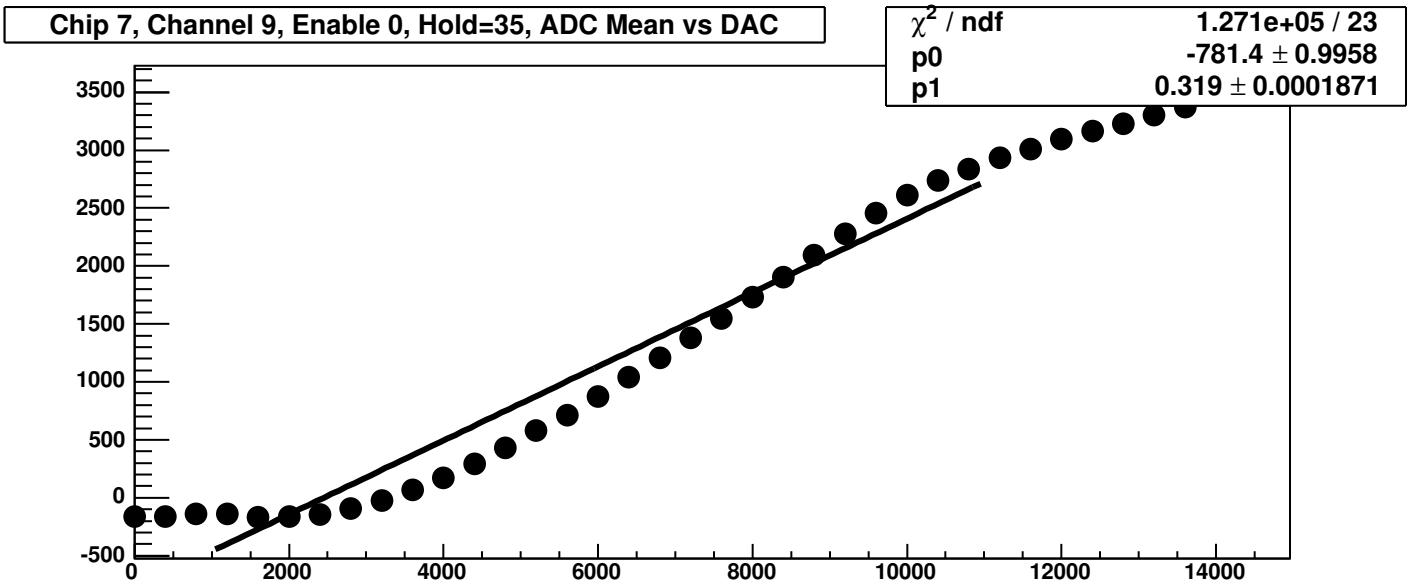
Chip 7, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

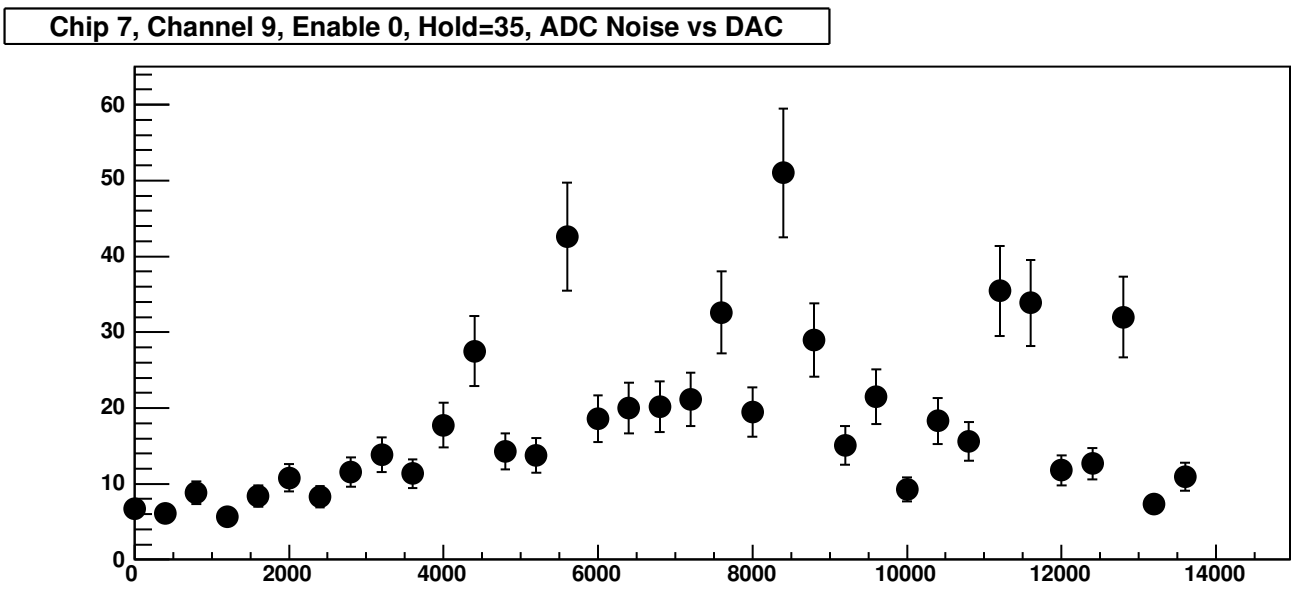


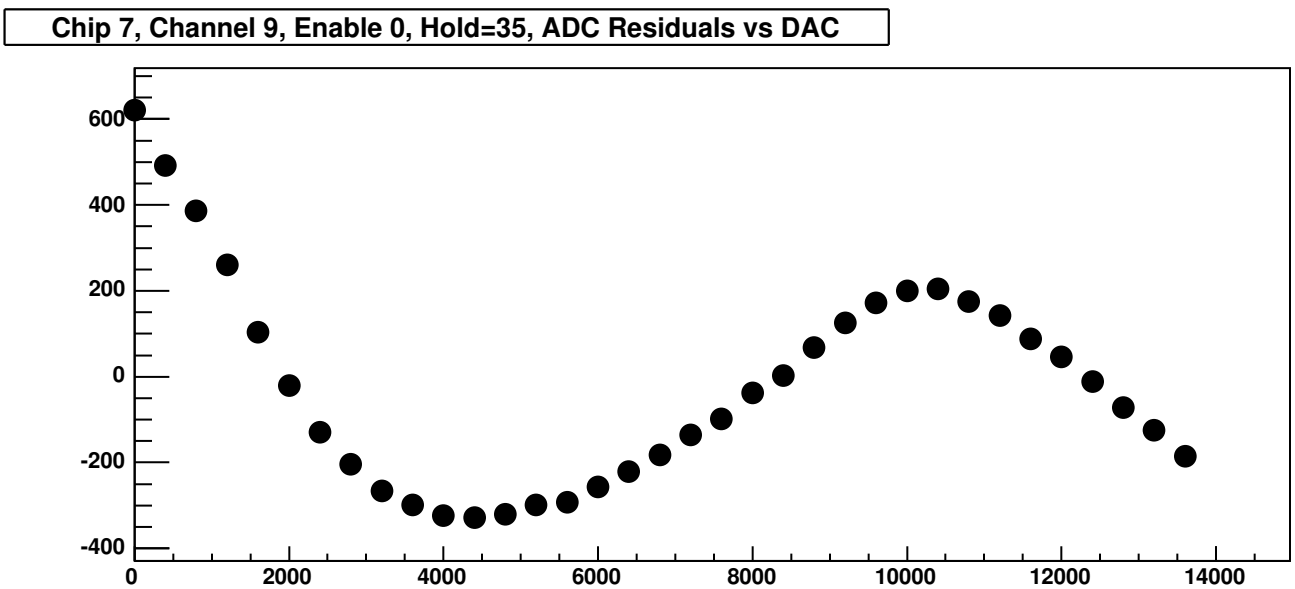

Chip 7, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2965 / 23
p0 -361.8 ± 0.9229
p1 2.411 ± 0.0001683
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

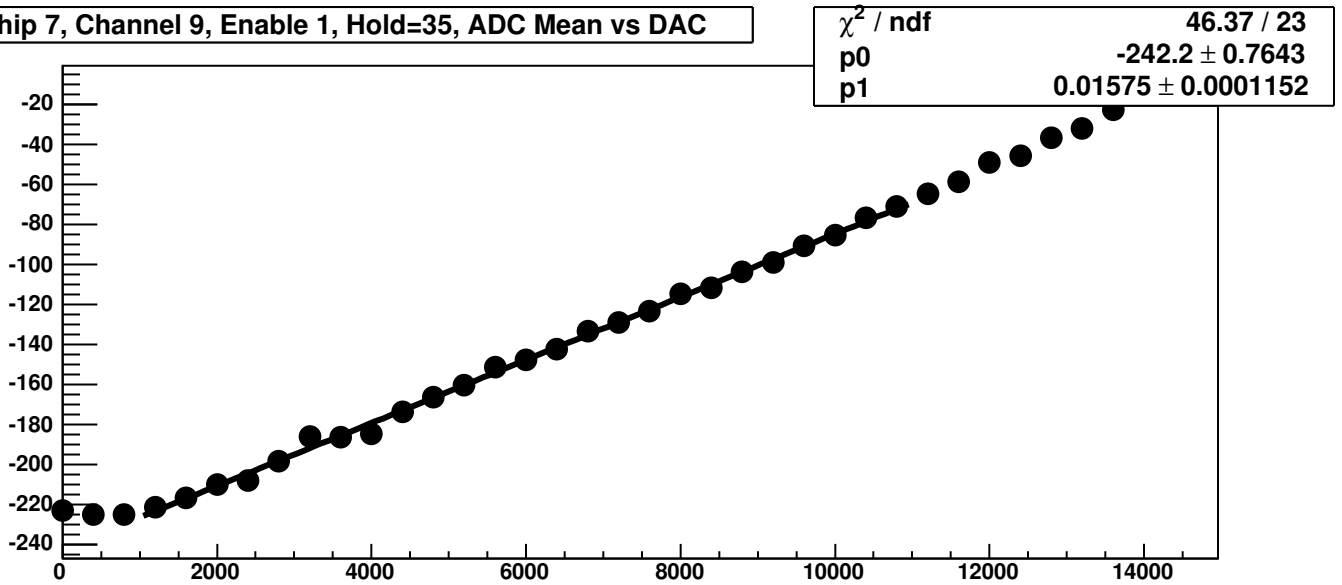

Chip 7, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.271e+05 / 23
p0 -781.4 ± 0.9958
p1 0.319 ± 0.0001871
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

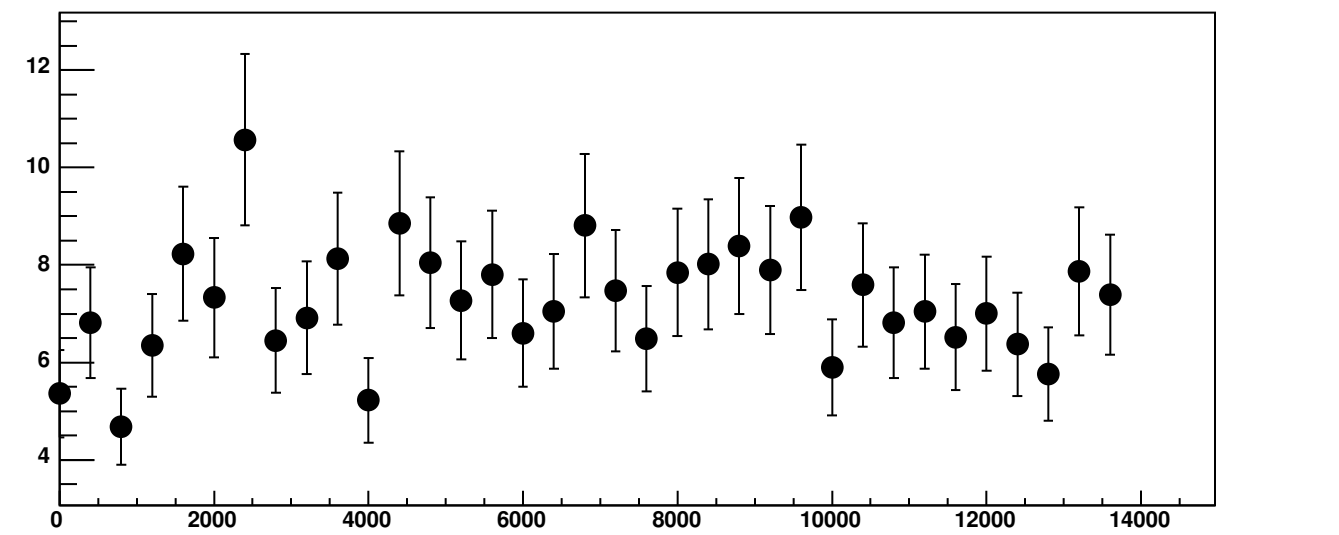

Chip 7, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

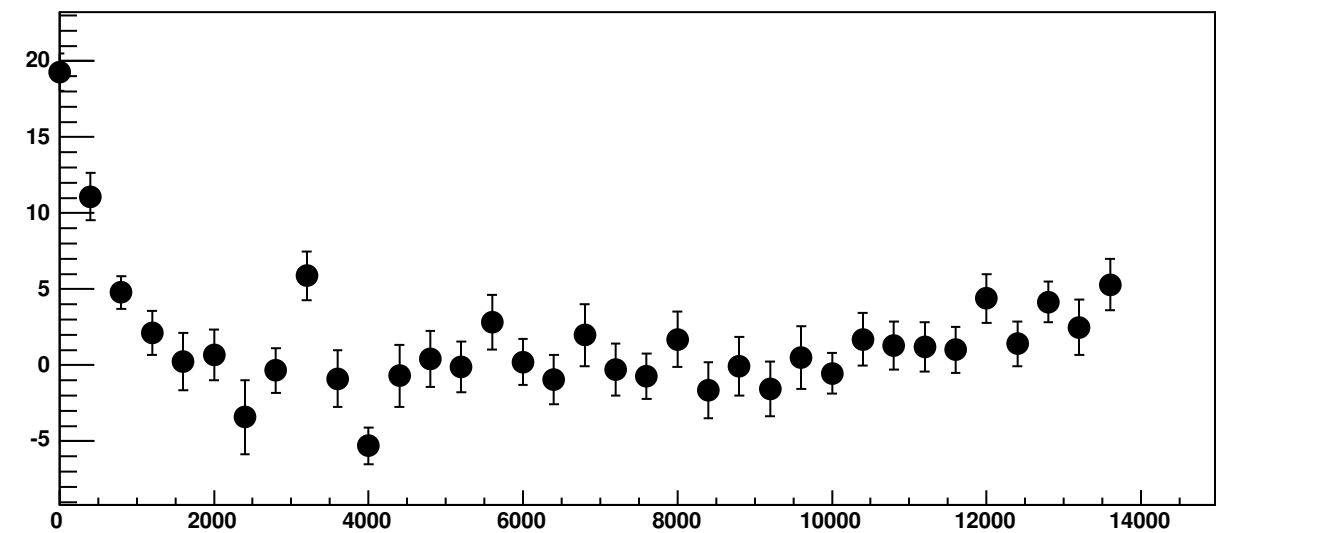
Chip 7, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

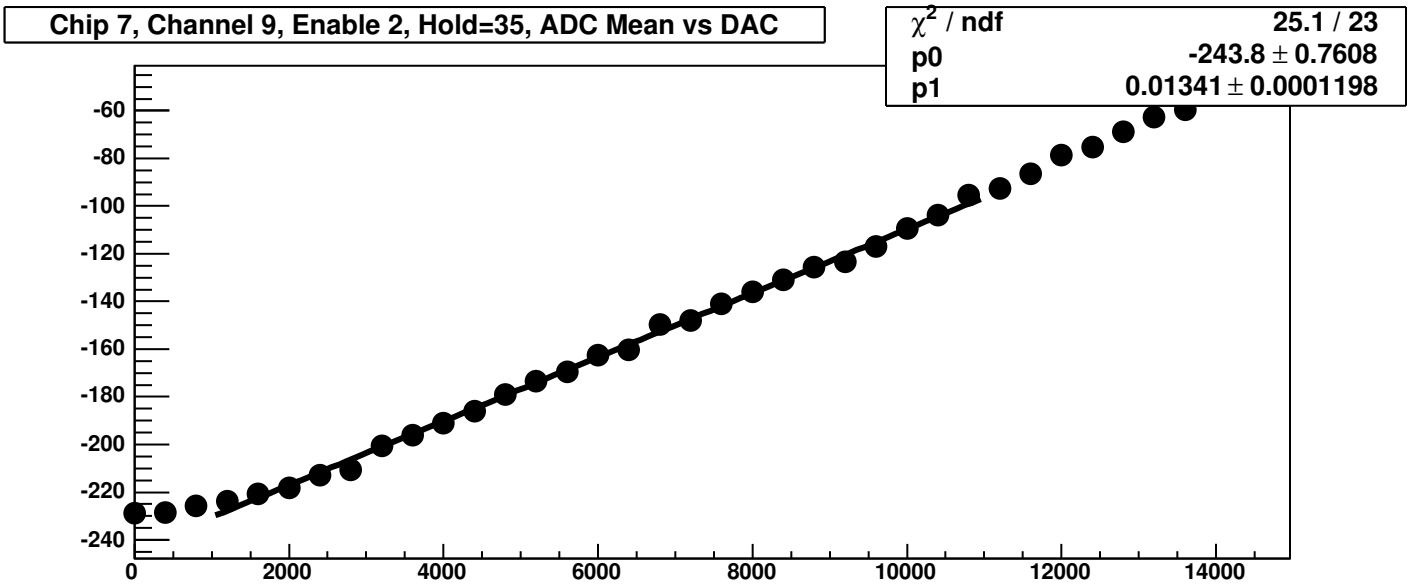


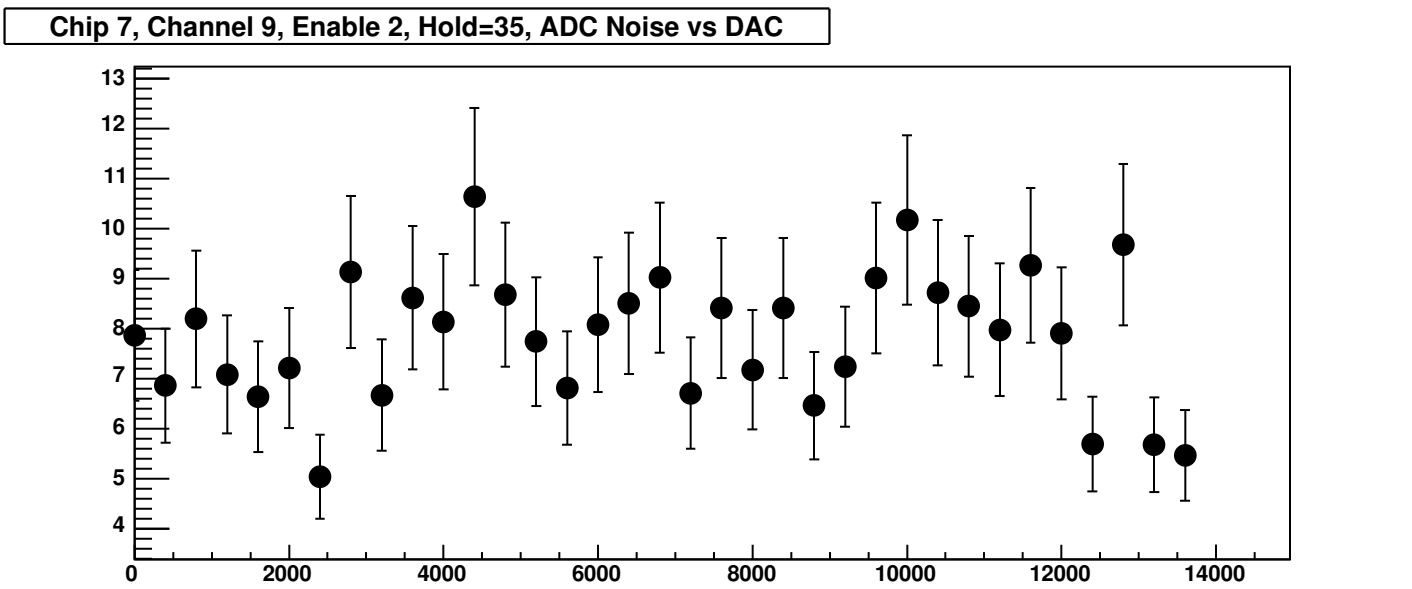
Chip 7, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC

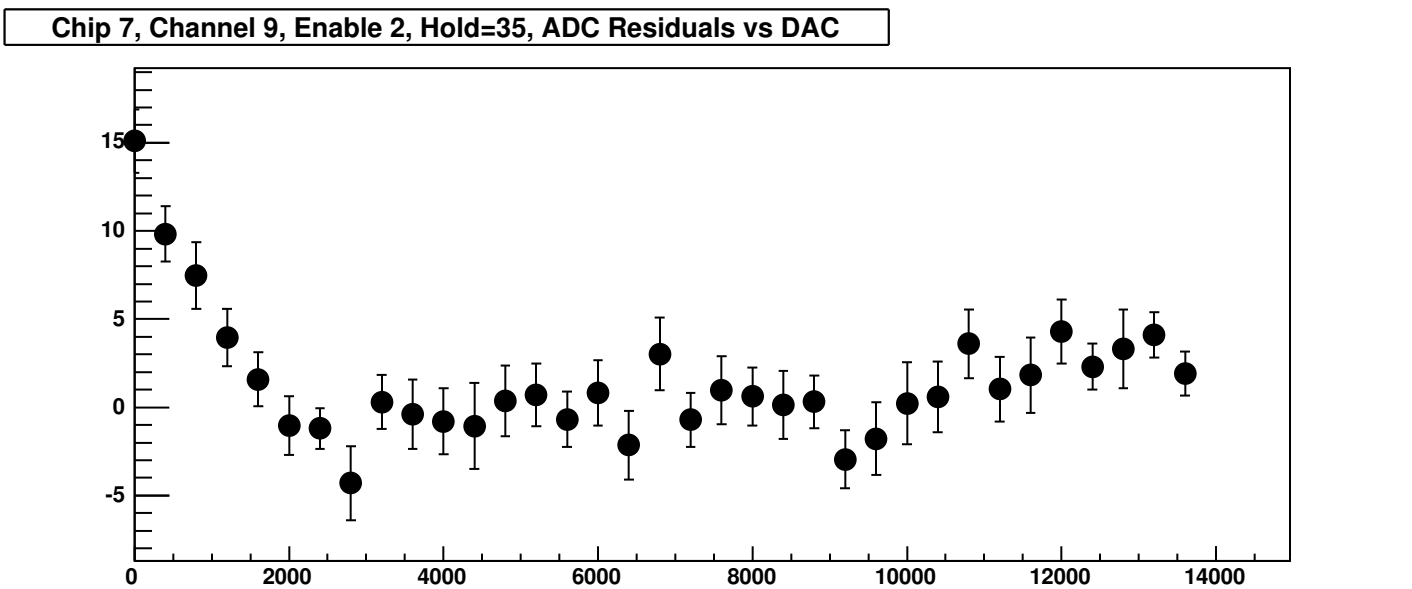


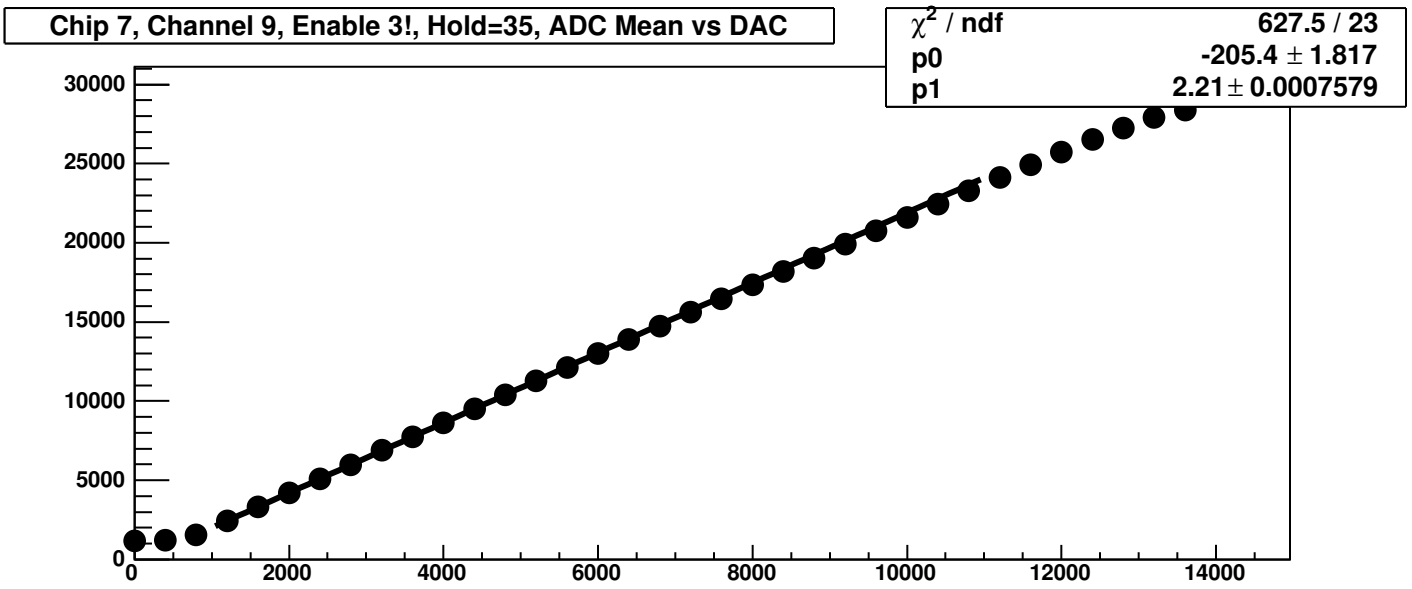
Chip 7, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

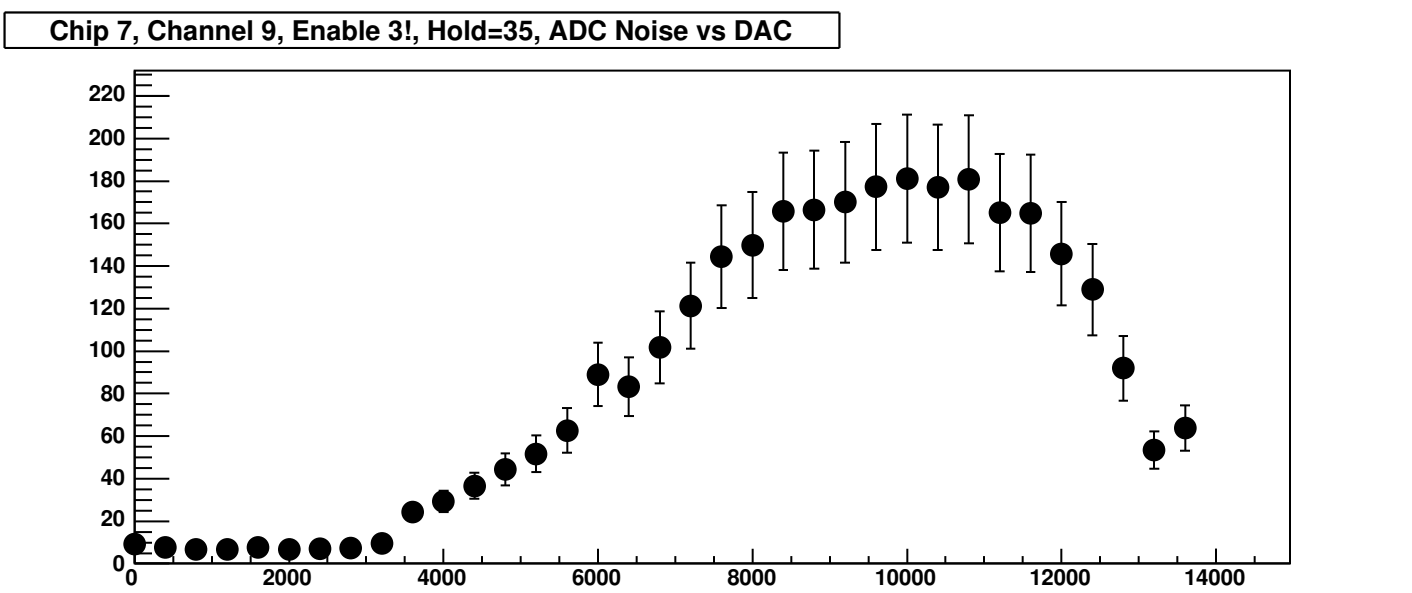


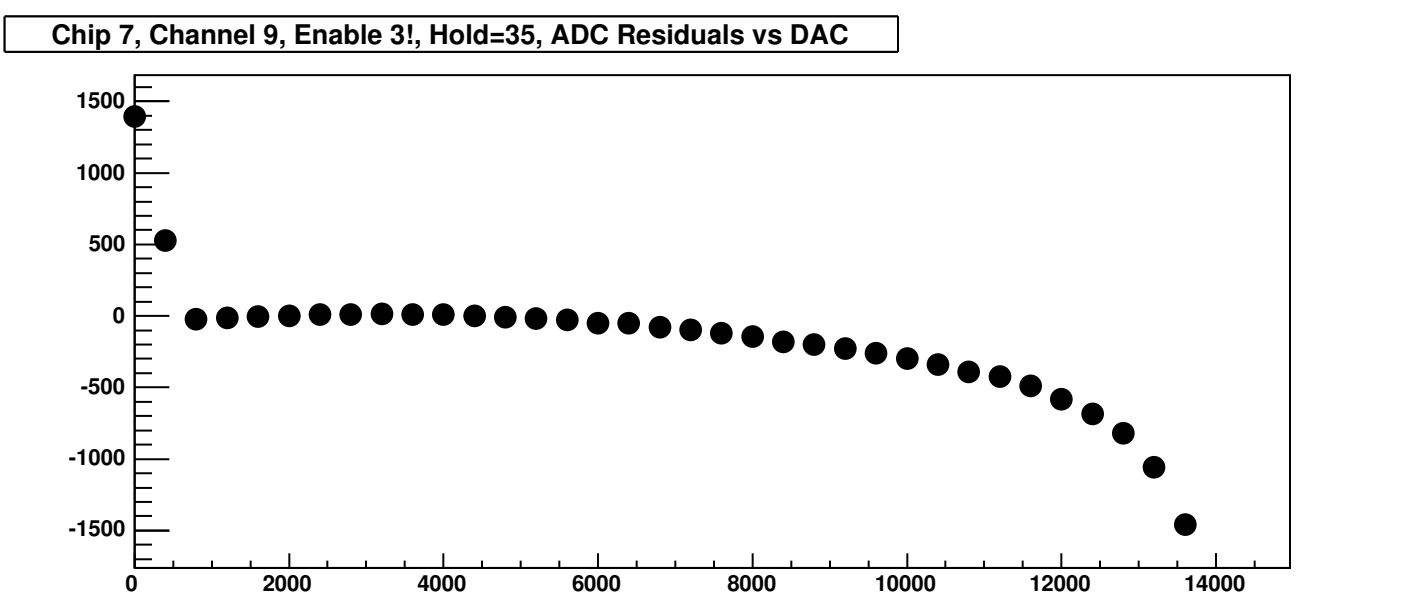

Chip 7, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 25.1 / 23
p0 -243.8 ± 0.7608
p1 0.01341 ± 0.0001198
-60
-80
-100
-120
-140
-160
-180
-200
-220
-240
0
2000
4000
6000
8000
10000
12000
14000

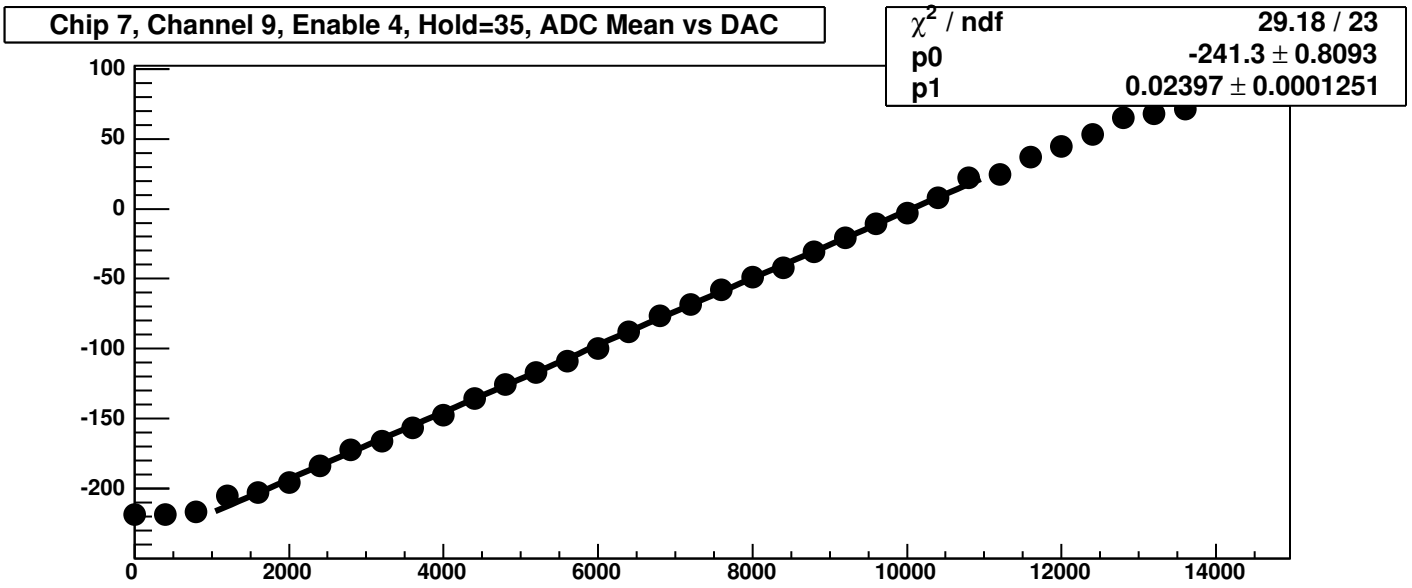

Chip 7, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

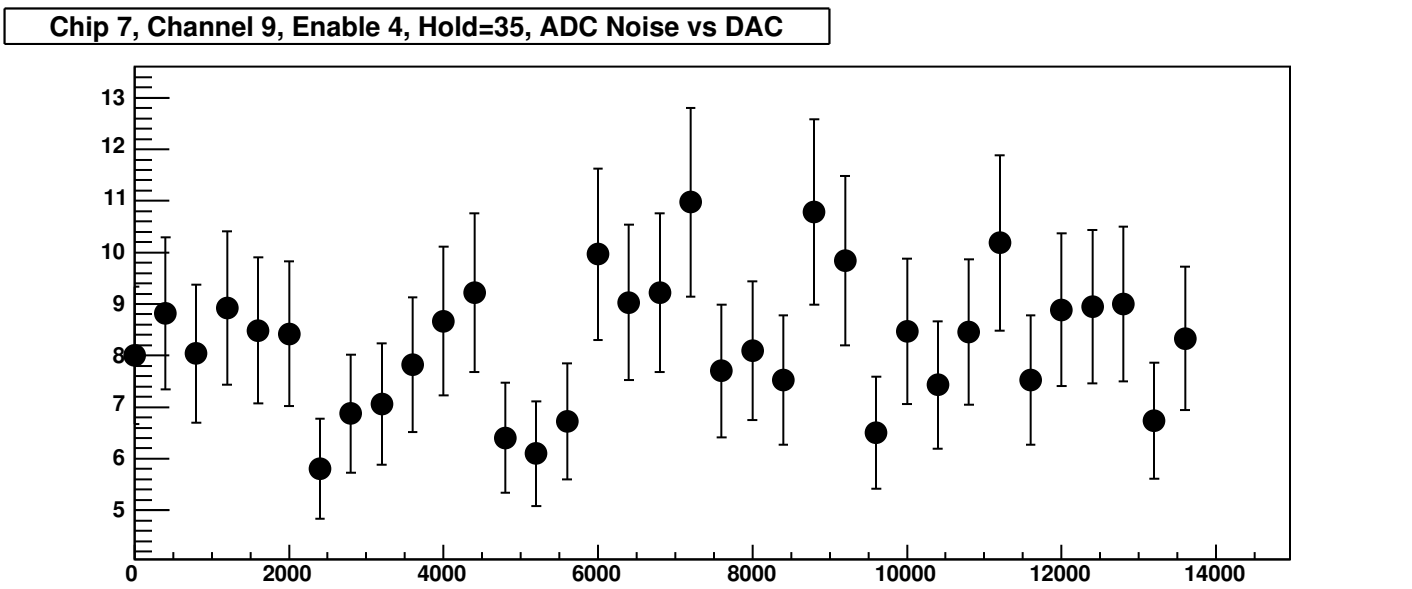

Chip 7, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

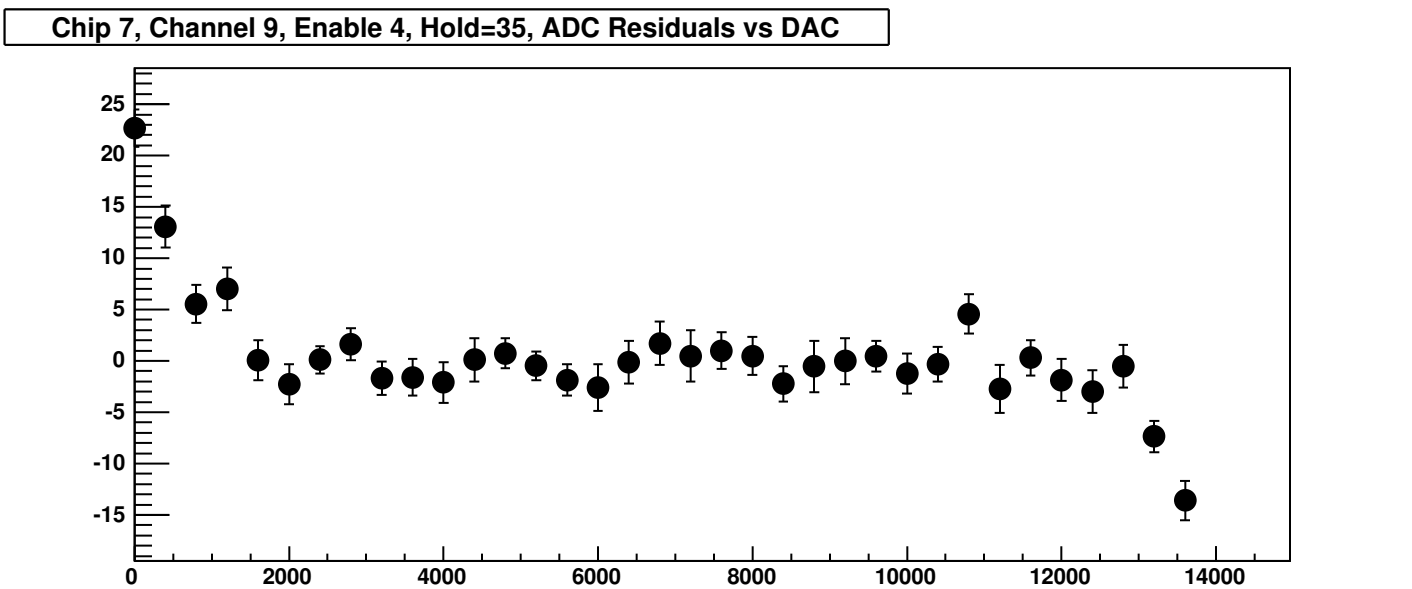

Chip 7, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 627.5 / 23
p0 -205.4 ± 1.817
p1 2.21 ± 0.0007579
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

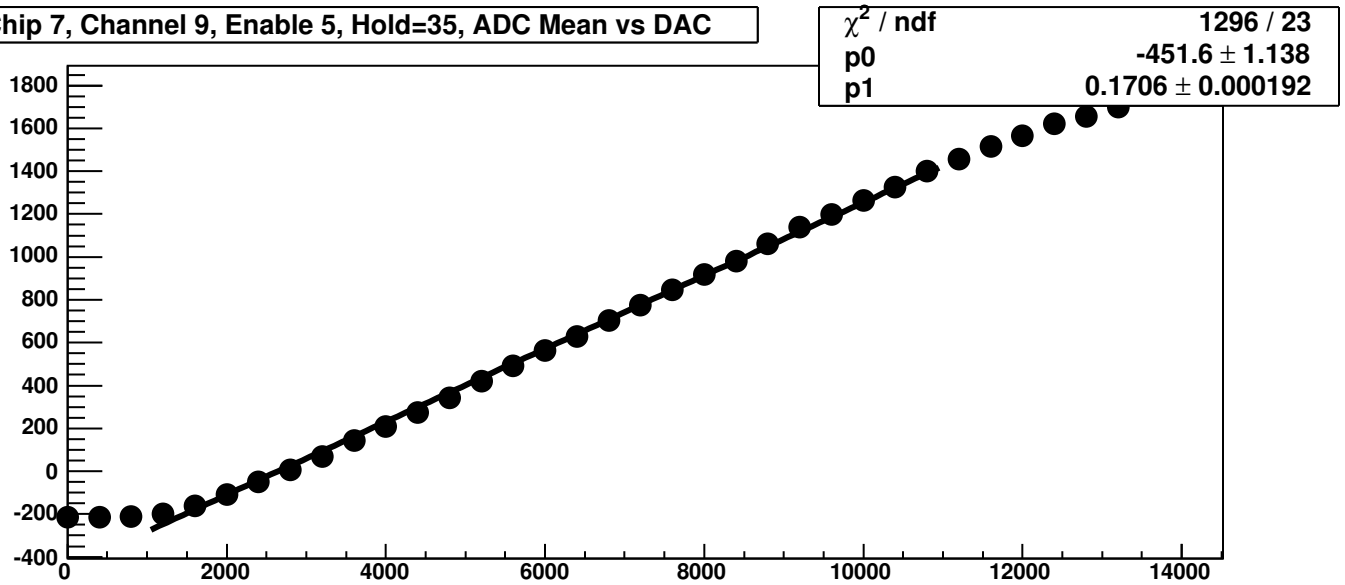

Chip 7, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.18 / 23
p0 -241.3 ± 0.8093
p1 0.02397 ± 0.0001251
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

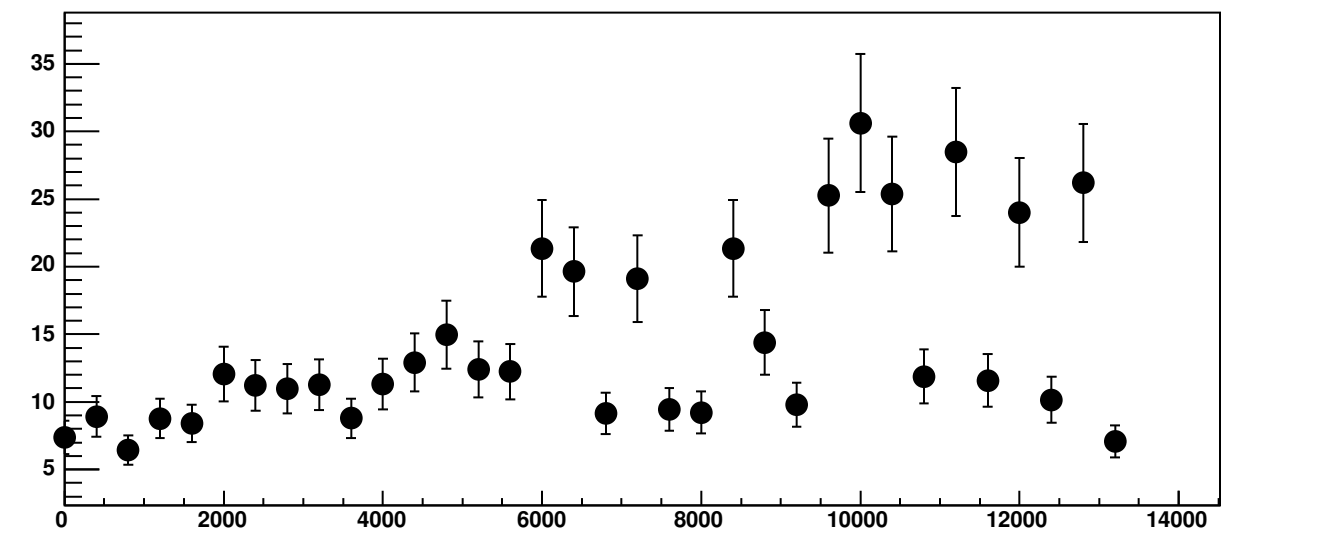

Chip 7, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

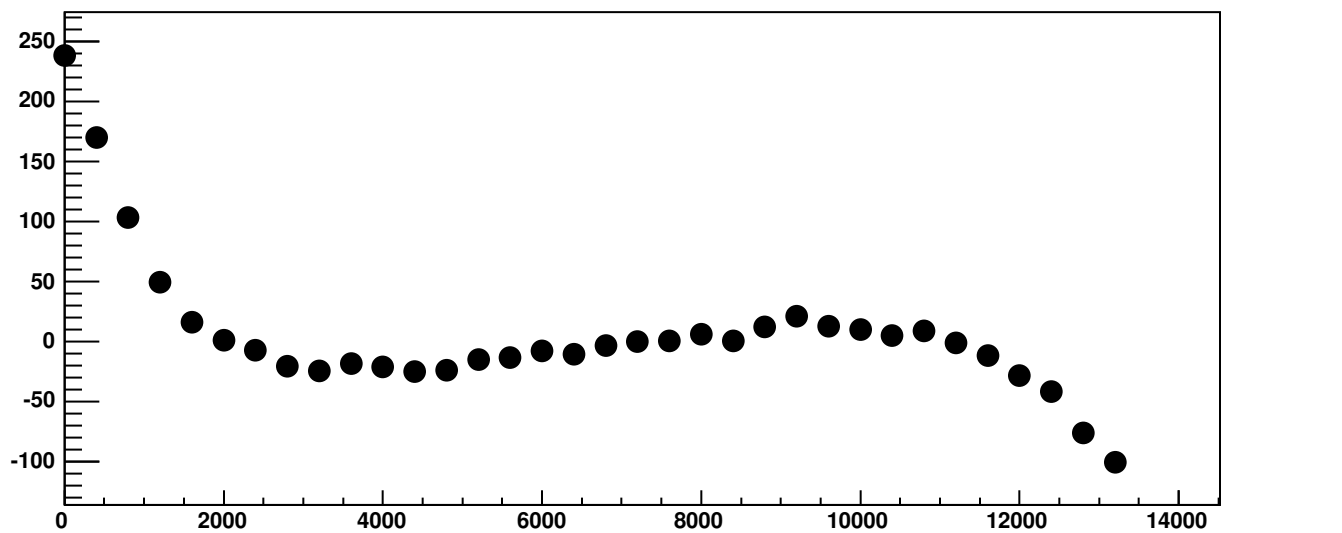
Chip 7, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

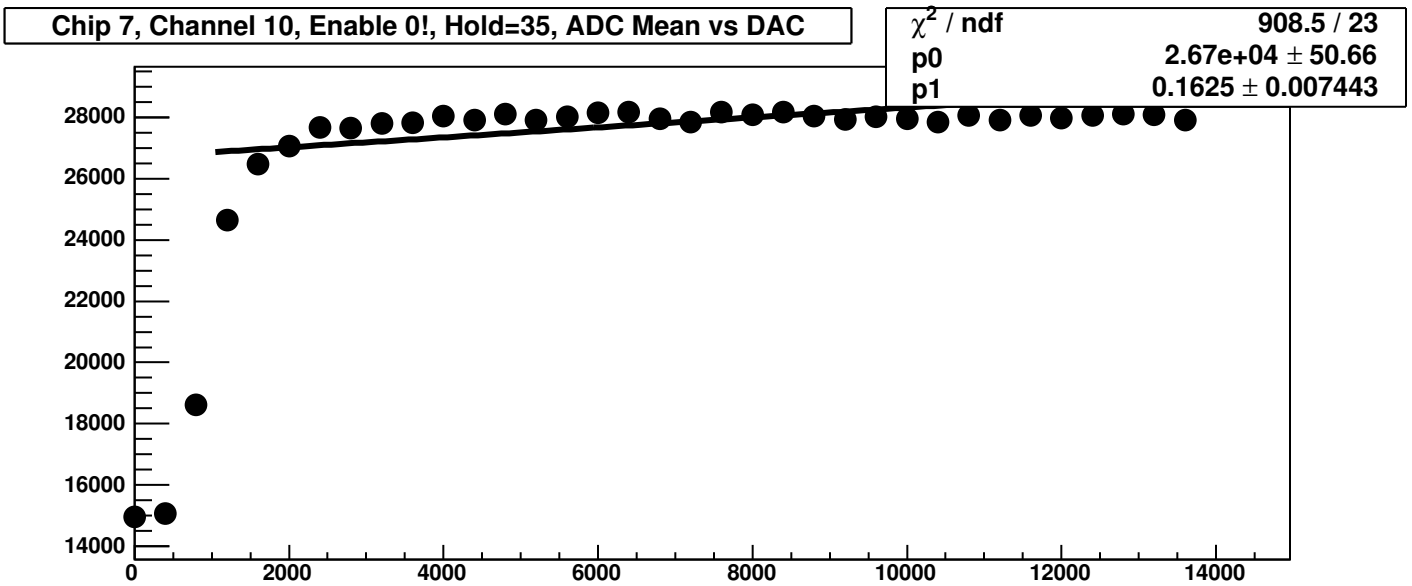


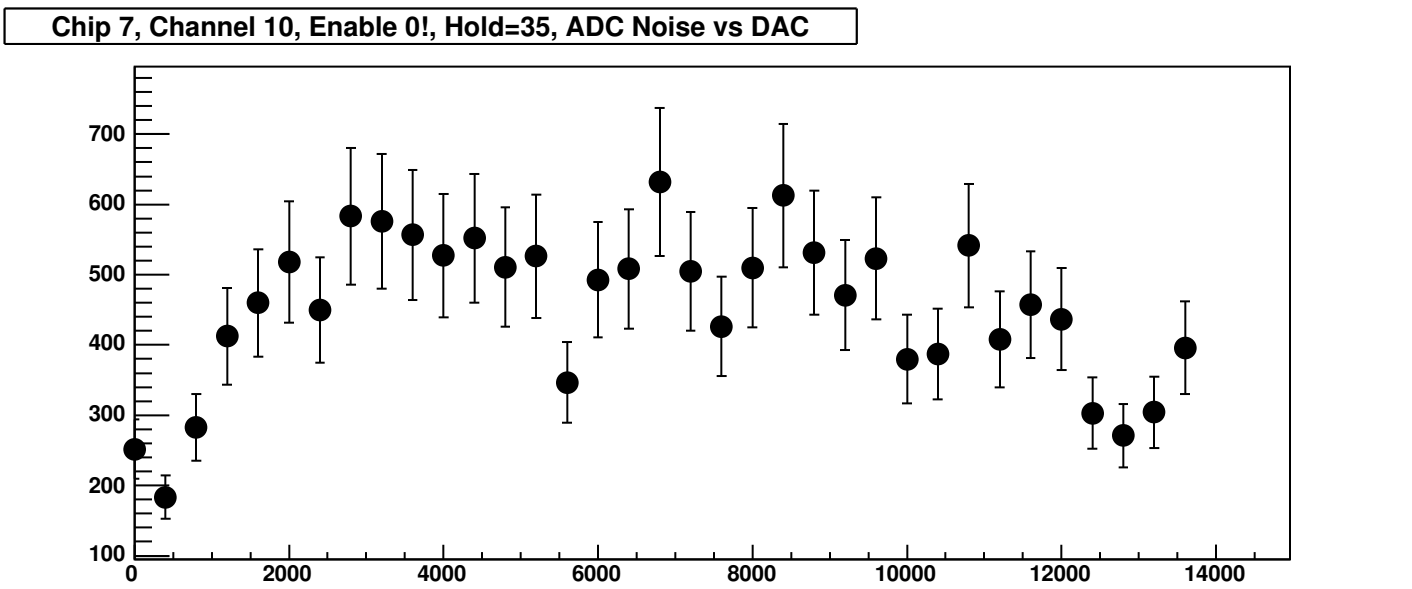
Chip 7, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

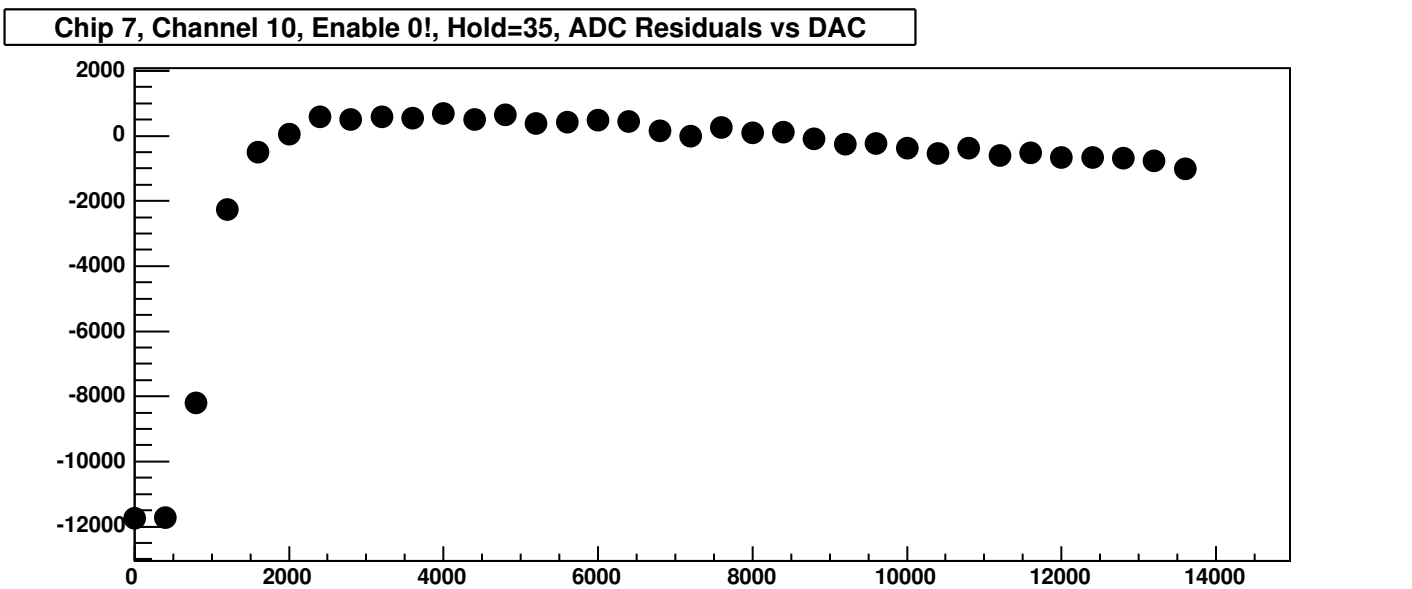


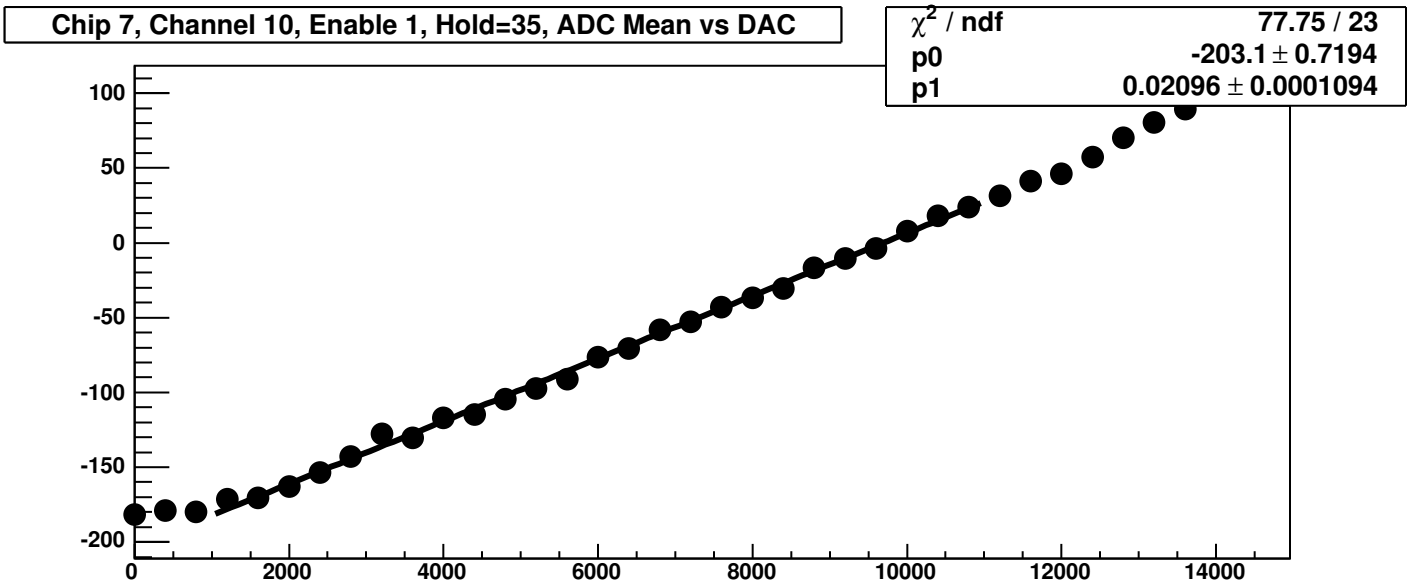
Chip 7, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

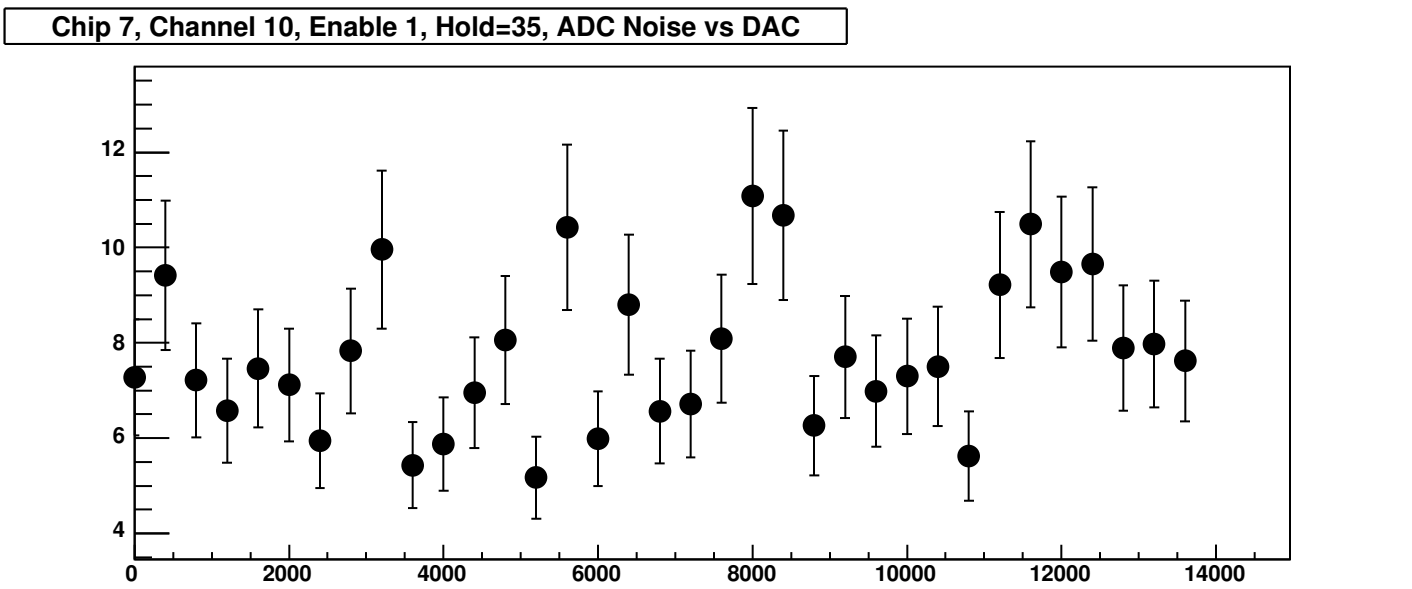


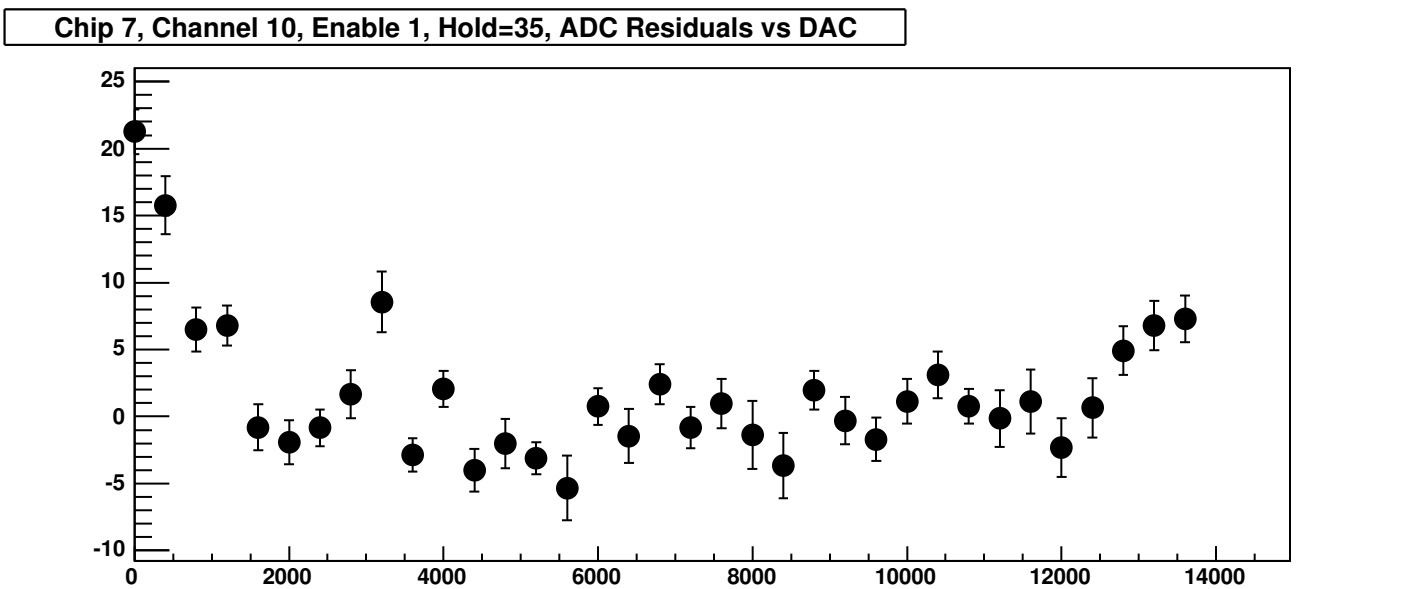

Chip 7, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 908.5 / 23
p0 2.67e+04 ± 50.66
p1 0.1625 ± 0.007443
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

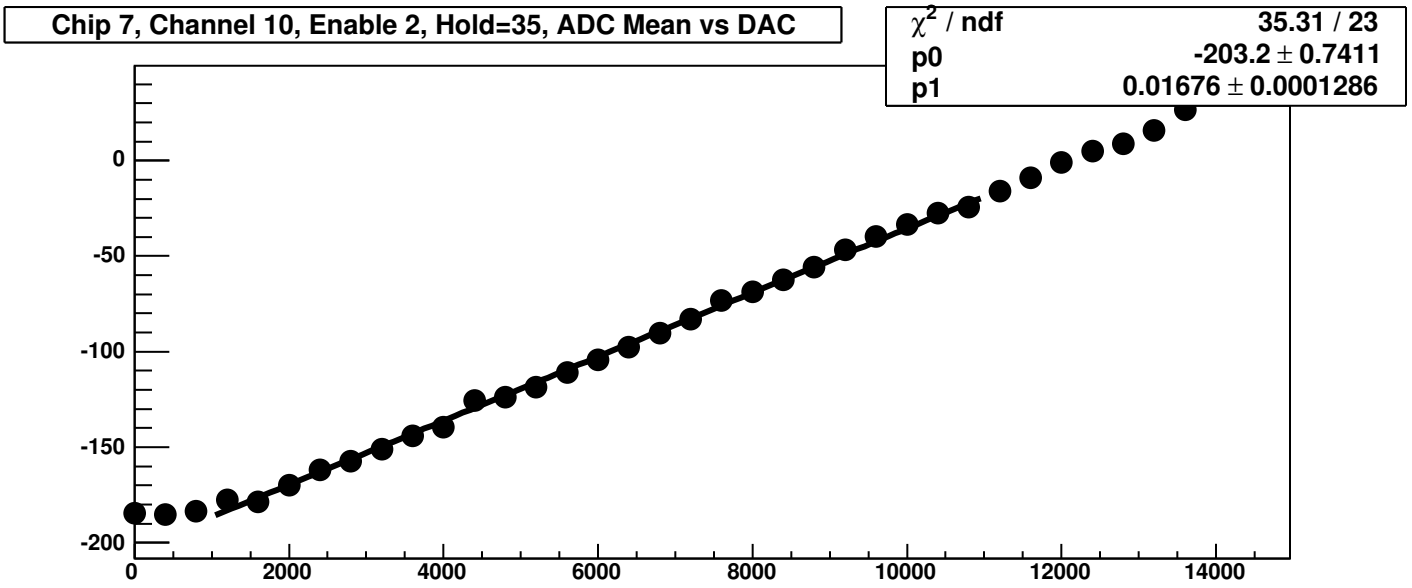

Chip 7, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
700
600
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000

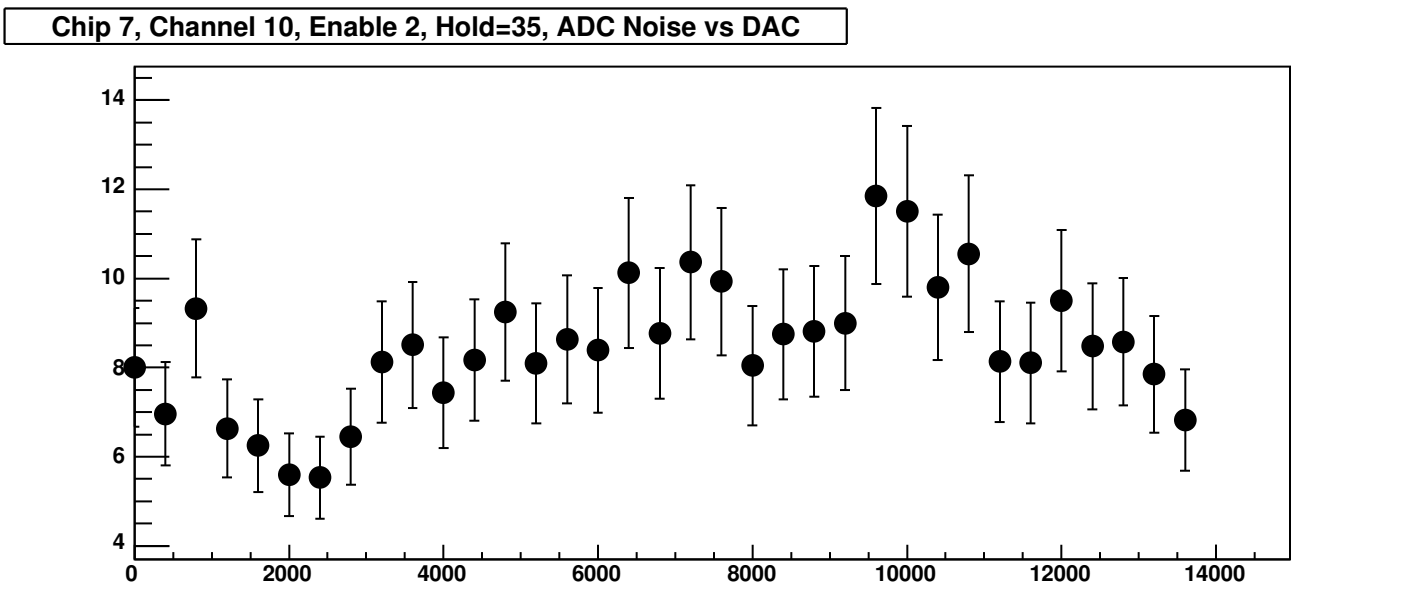

Chip 7, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
0
2000
4000
6000
8000
10000
12000
14000

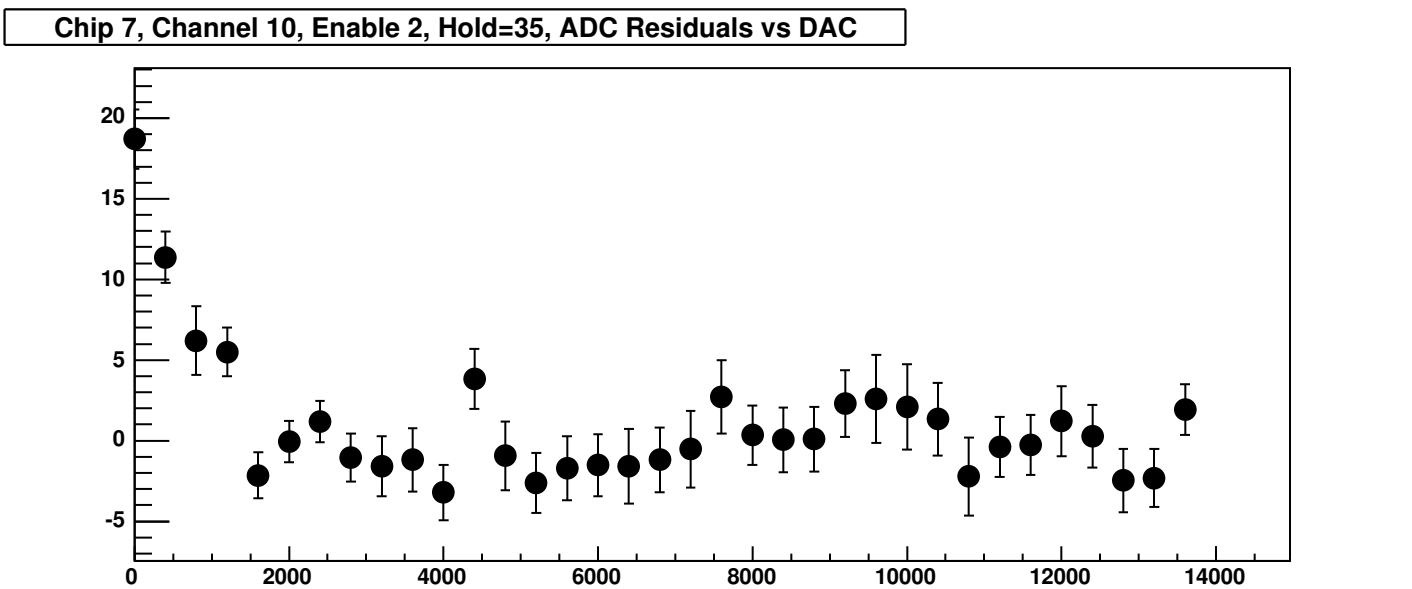

Chip 7, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 77.75 / 23
p0 -203.1 ± 0.7194
p1 0.02096 ± 0.0001094
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

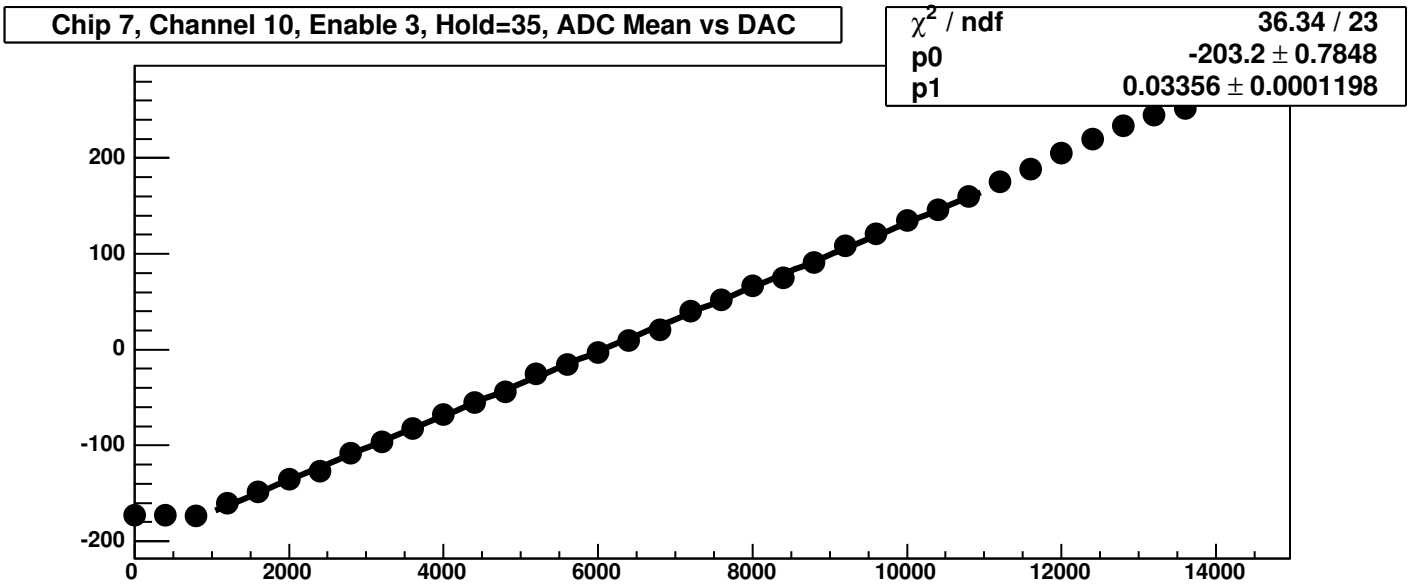

Chip 7, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

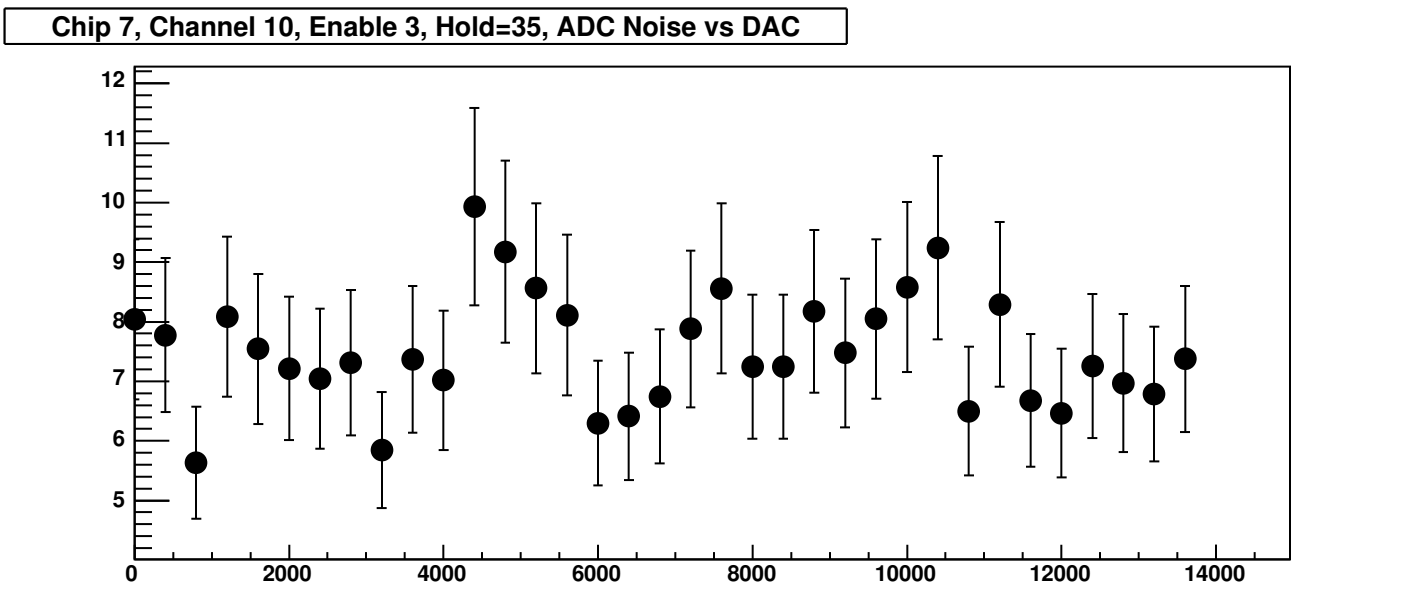

Chip 7, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

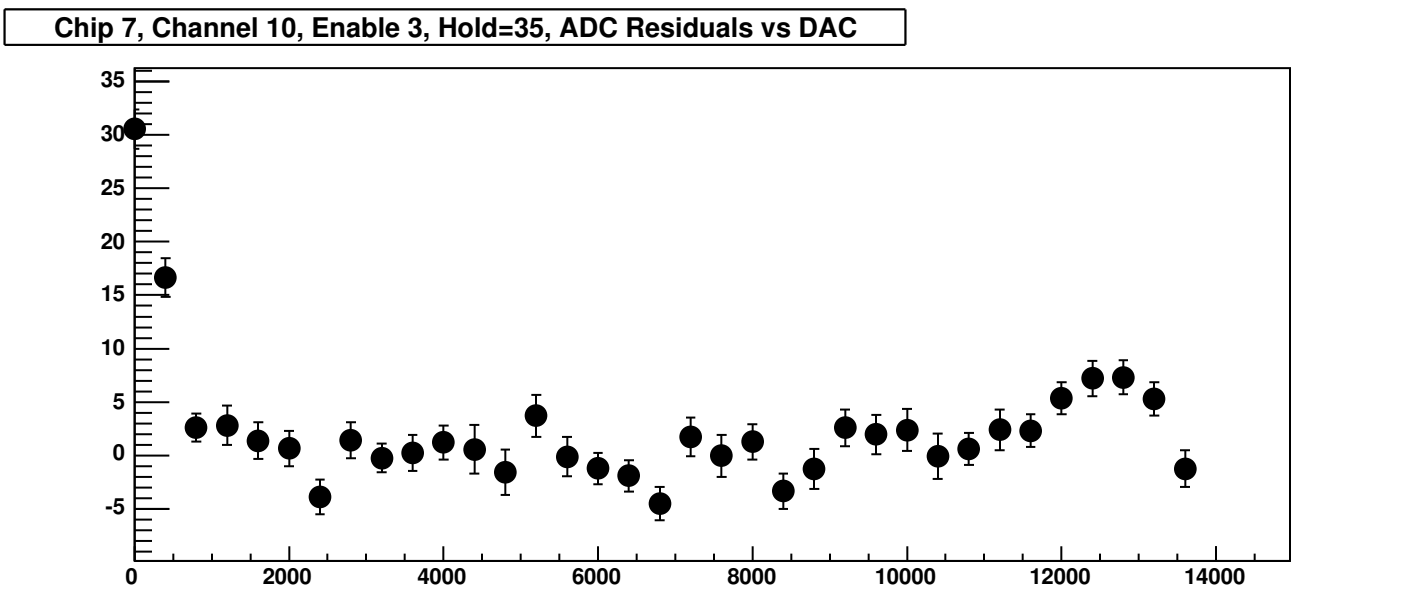

Chip 7, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 35.31 / 23
p0 -203.2 ± 0.7411
p1 0.01676 ± 0.0001286
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

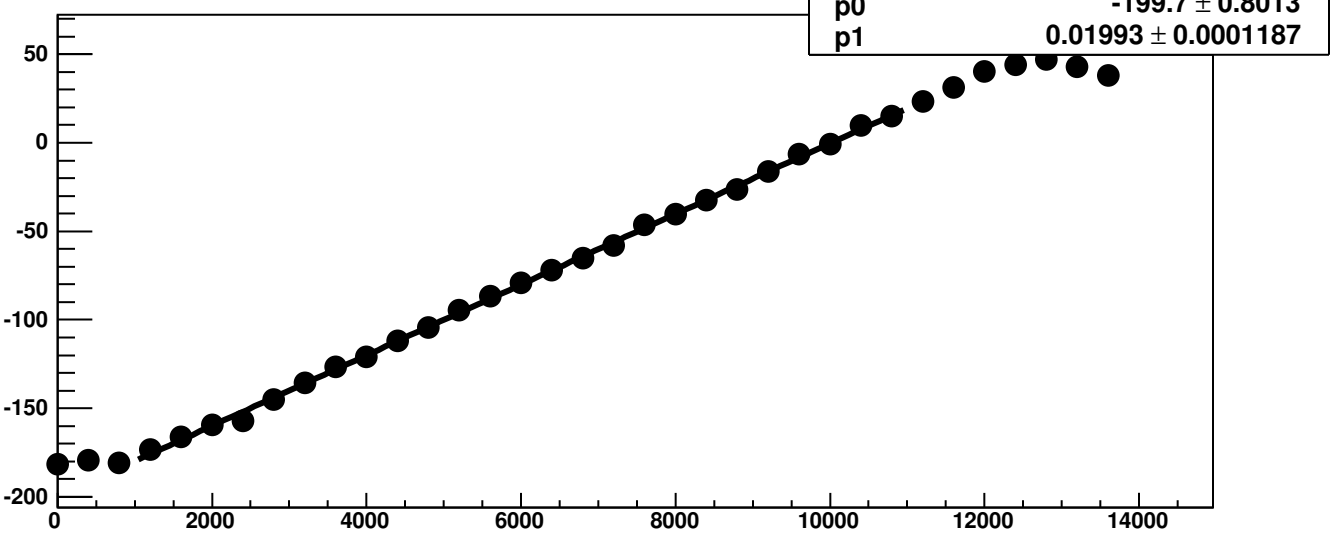

Chip 7, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.34 / 23
p0 -203.2 ± 0.7848
p1 0.03356 ± 0.0001198
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

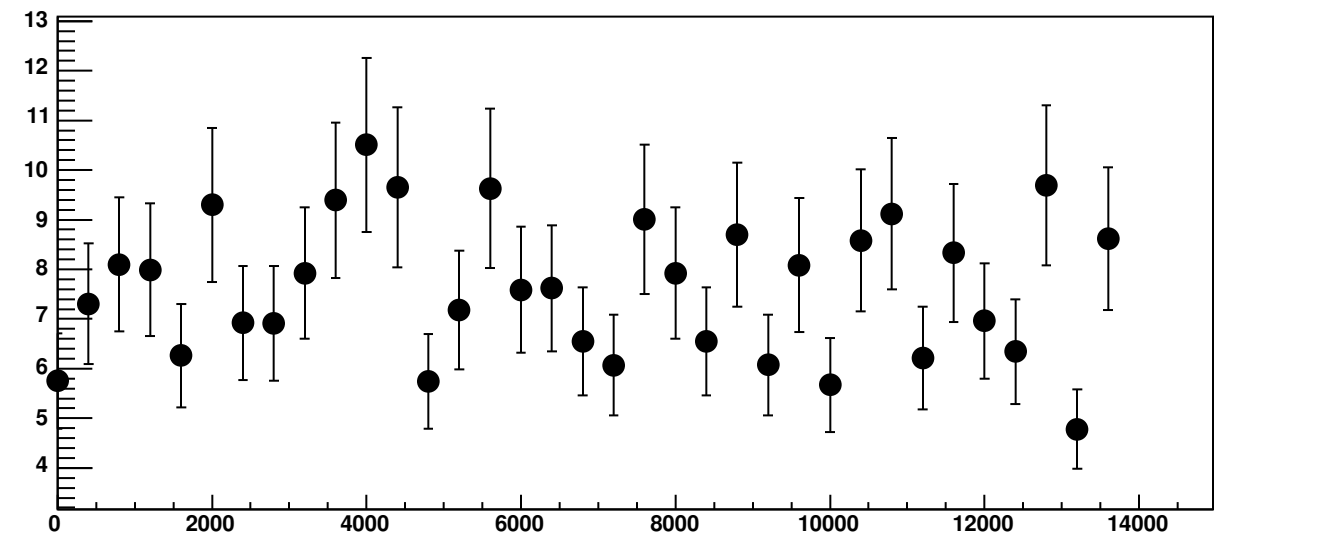

Chip 7, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

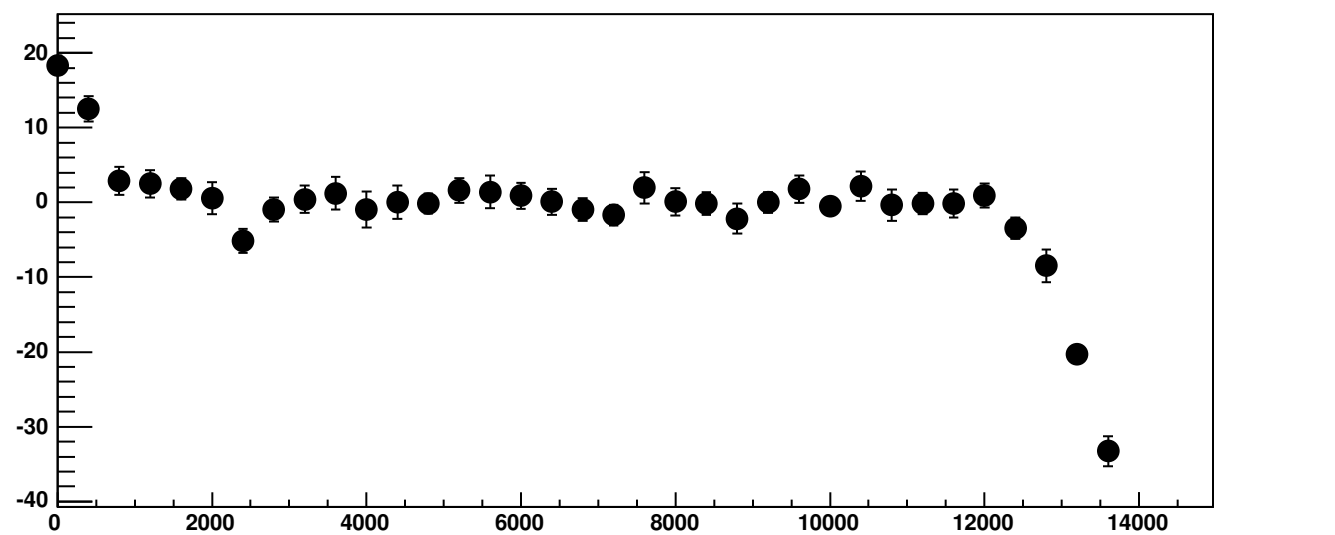
Chip 7, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

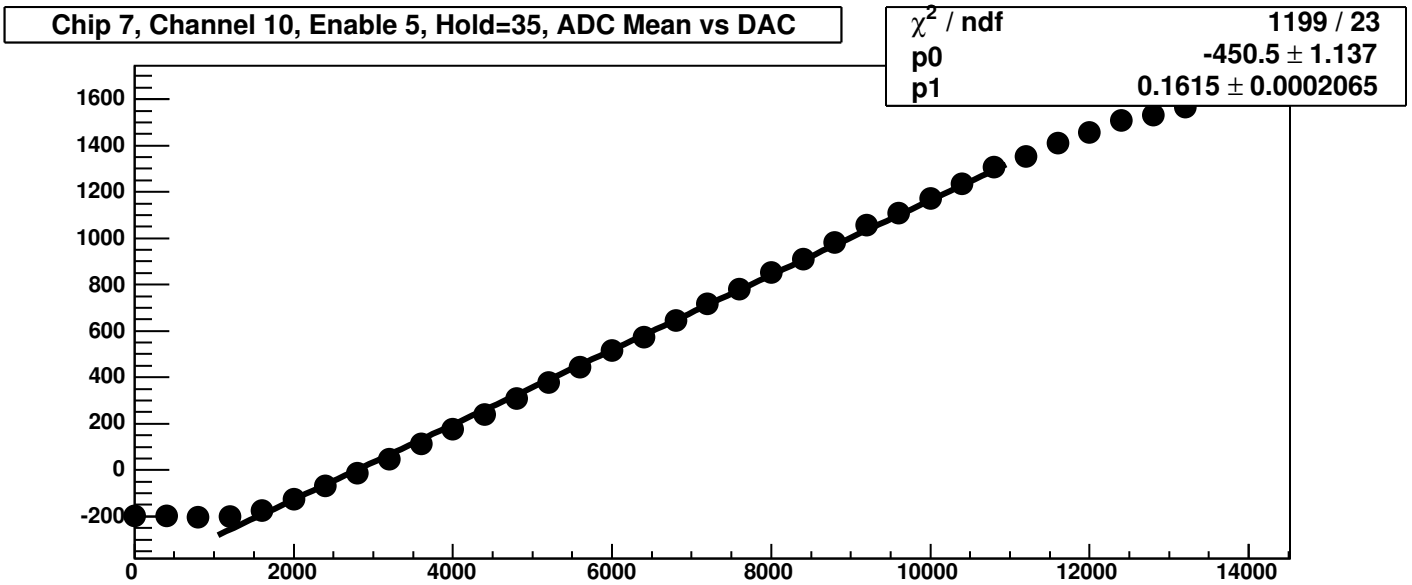


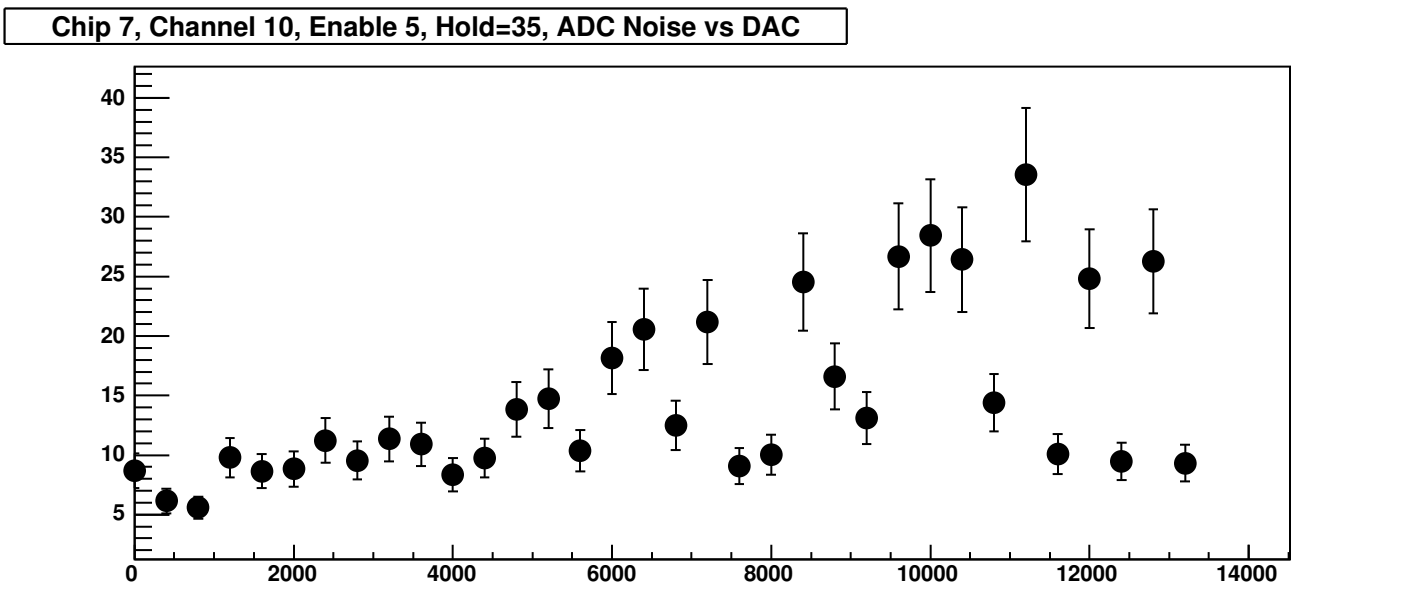
Chip 7, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

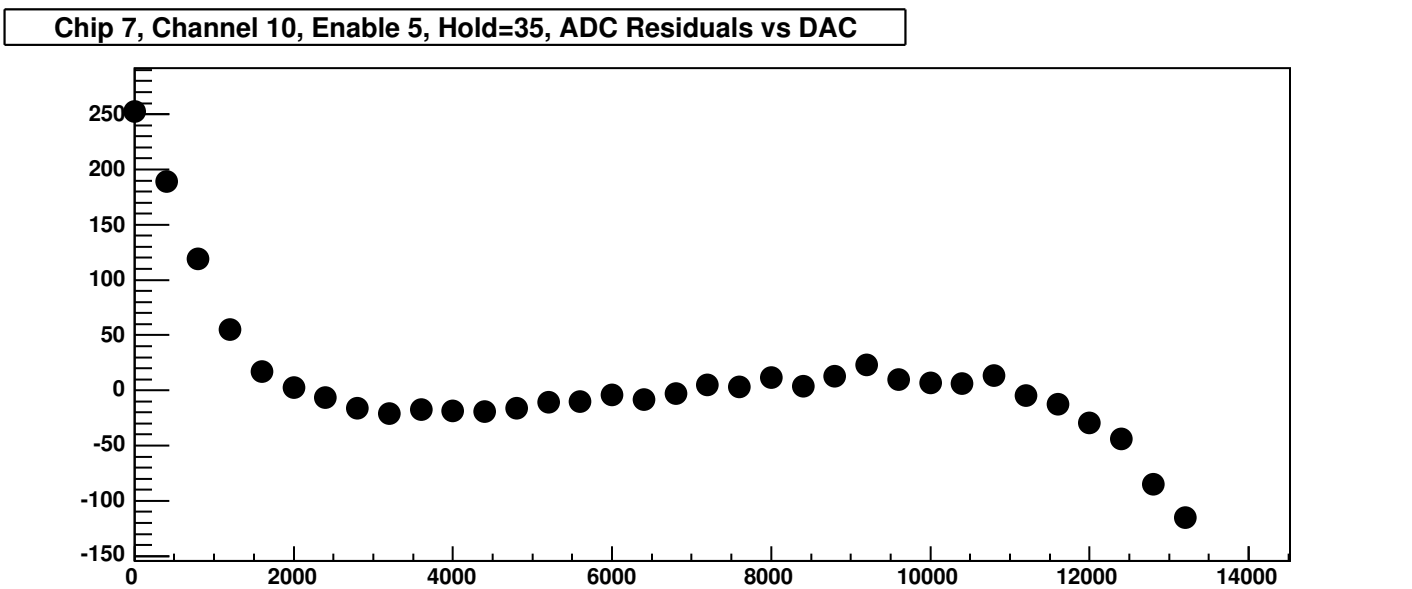


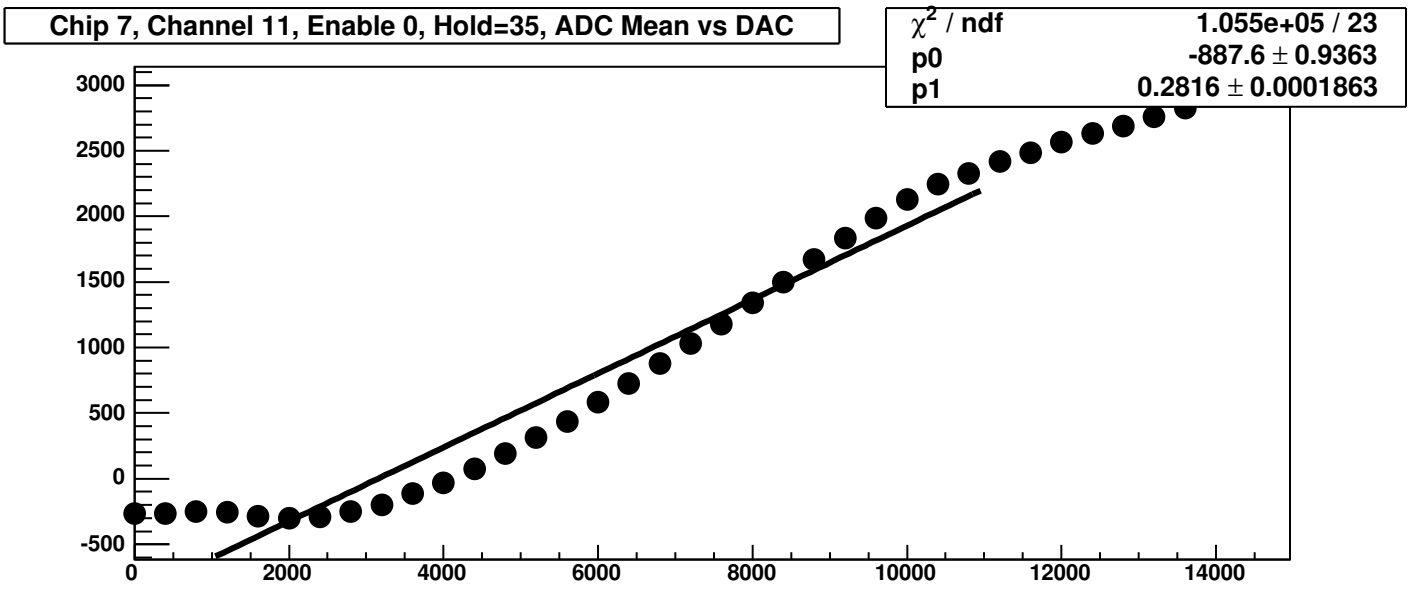
Chip 7, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

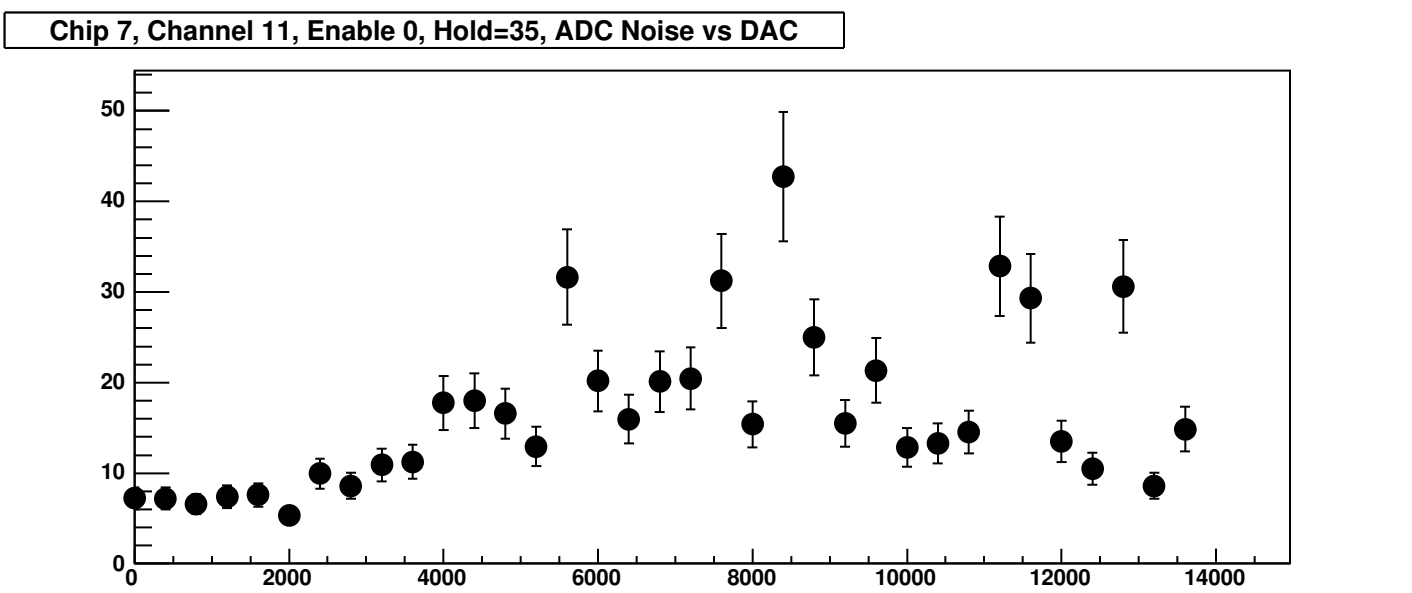


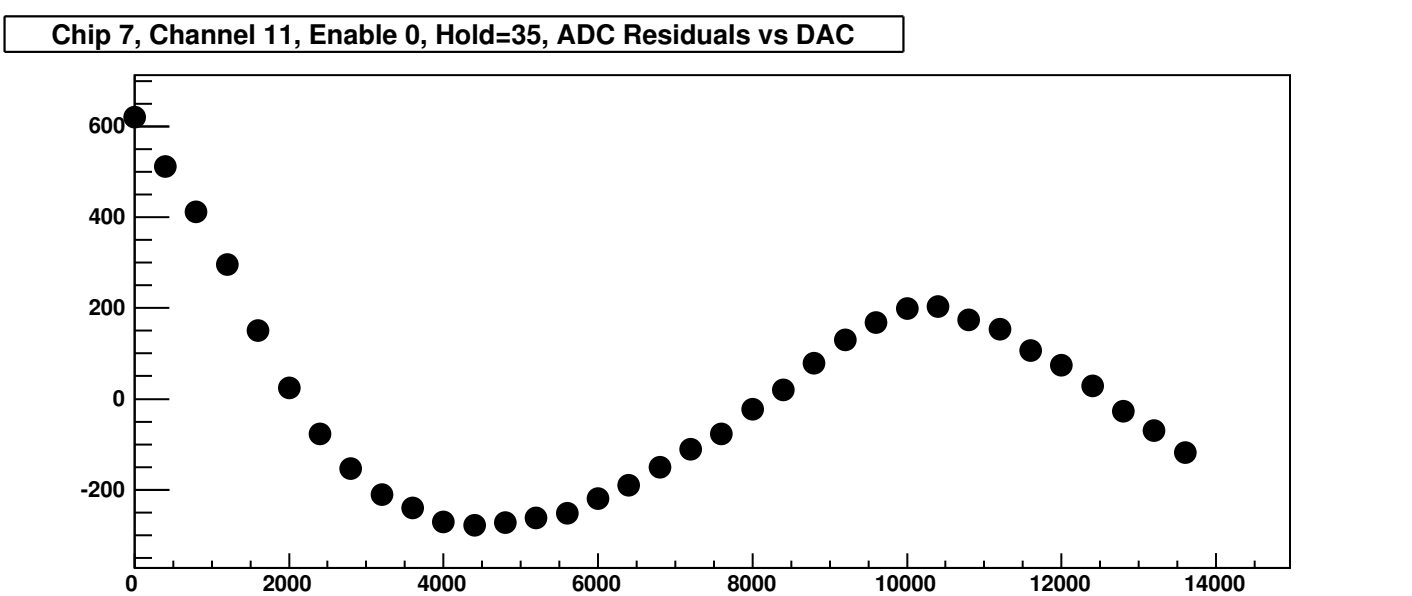

Chip 7, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1199 / 23
p0 -450.5 ± 1.137
p1 0.1615 ± 0.0002065
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

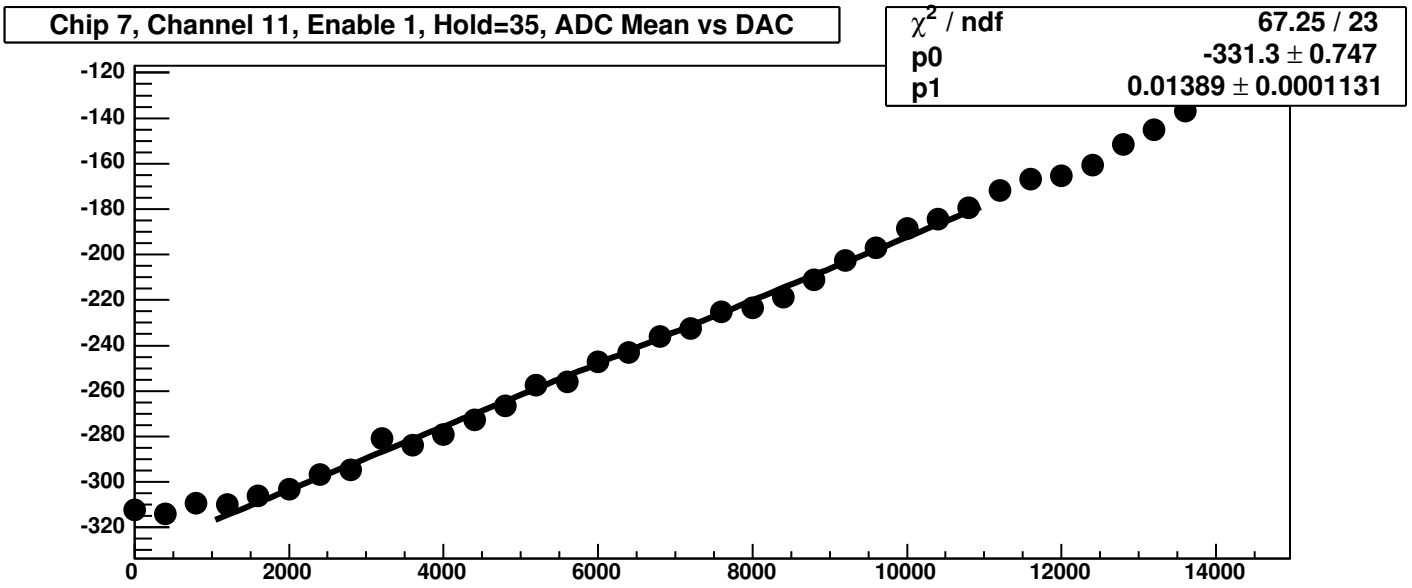

Chip 7, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

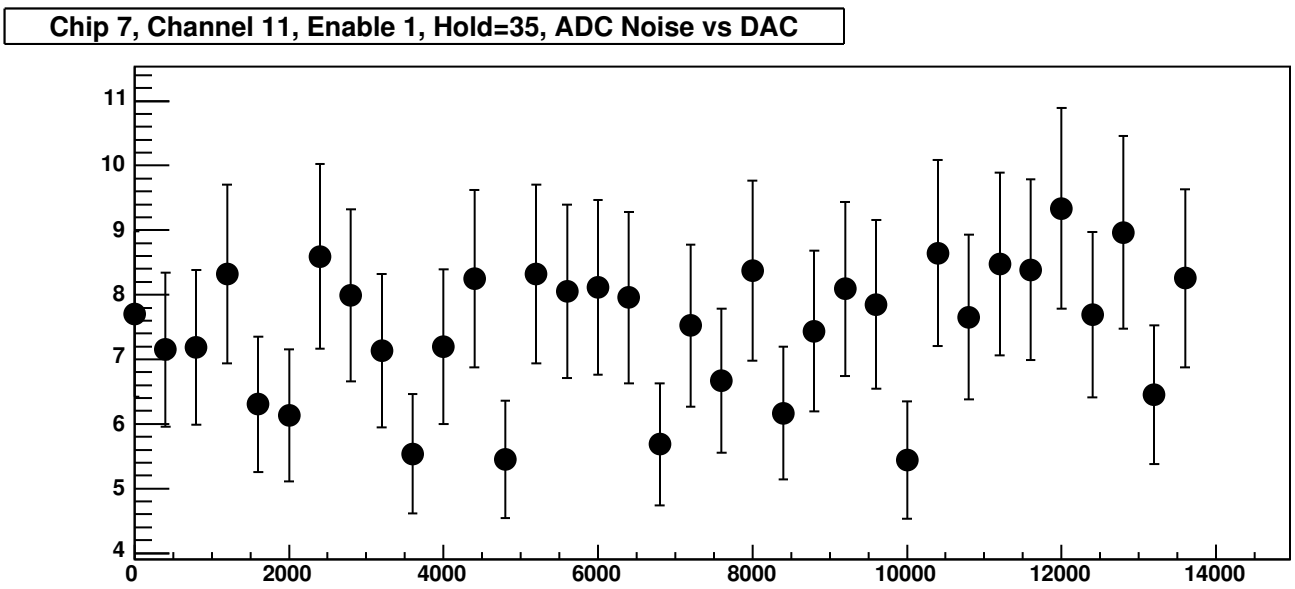

Chip 7, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

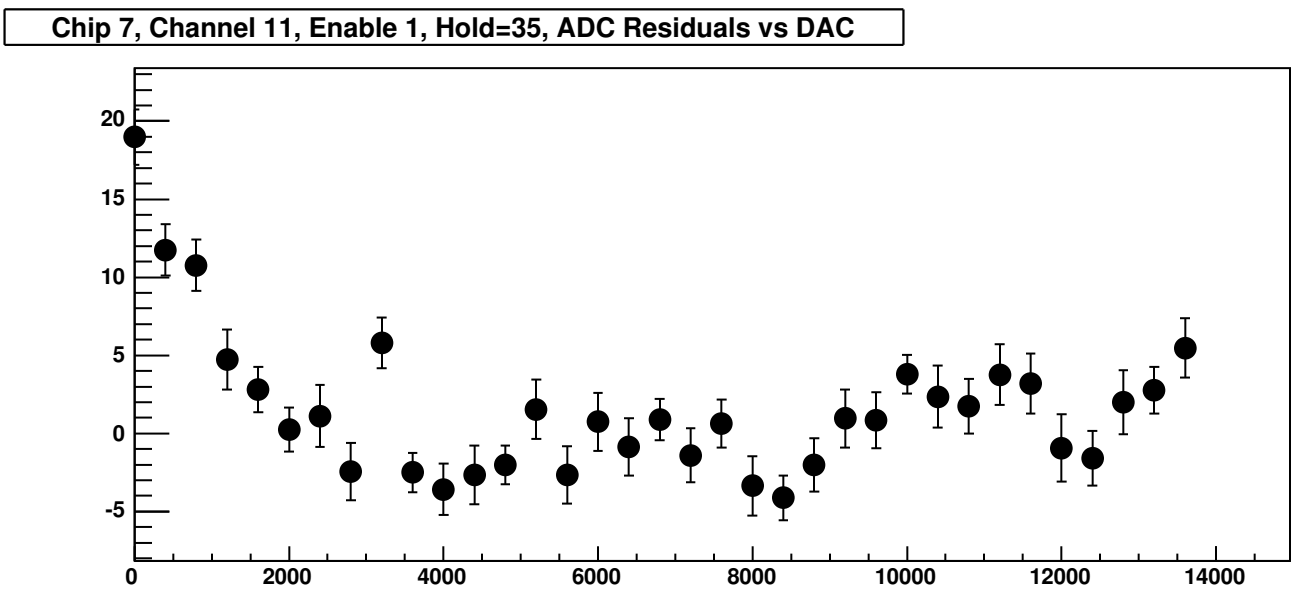

Chip 7, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.055e+05 / 23
p0 -887.6 ± 0.9363
p1 0.2816 ± 0.0001863
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

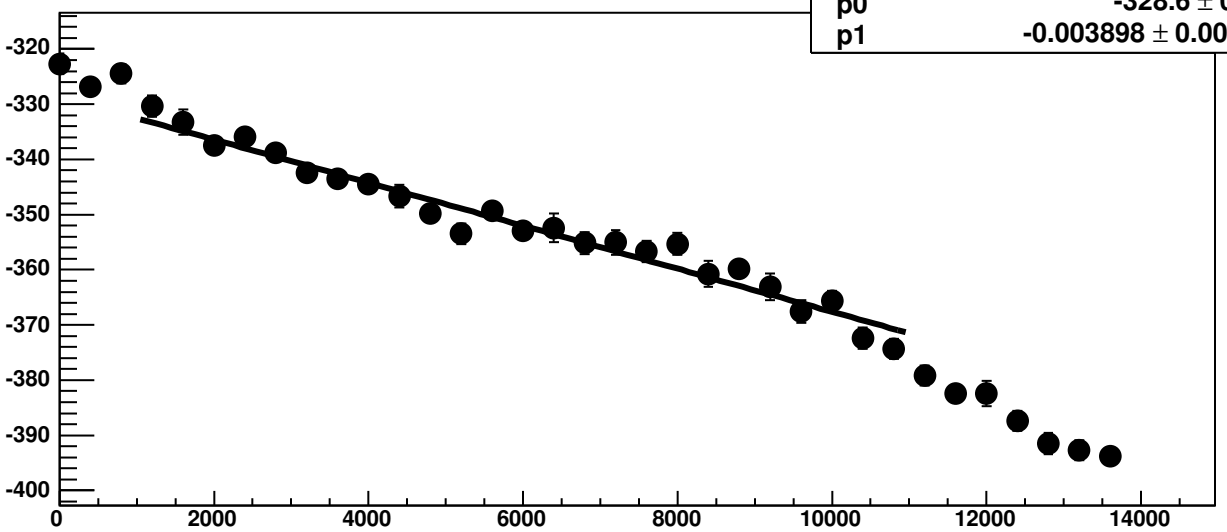

Chip 7, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 67.25 / 23
p0 -331.3 ± 0.747
p1 0.01389 ± 0.0001131
-120
-140
-160
-180
-200
-220
-240
-260
-280
-300
-320
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

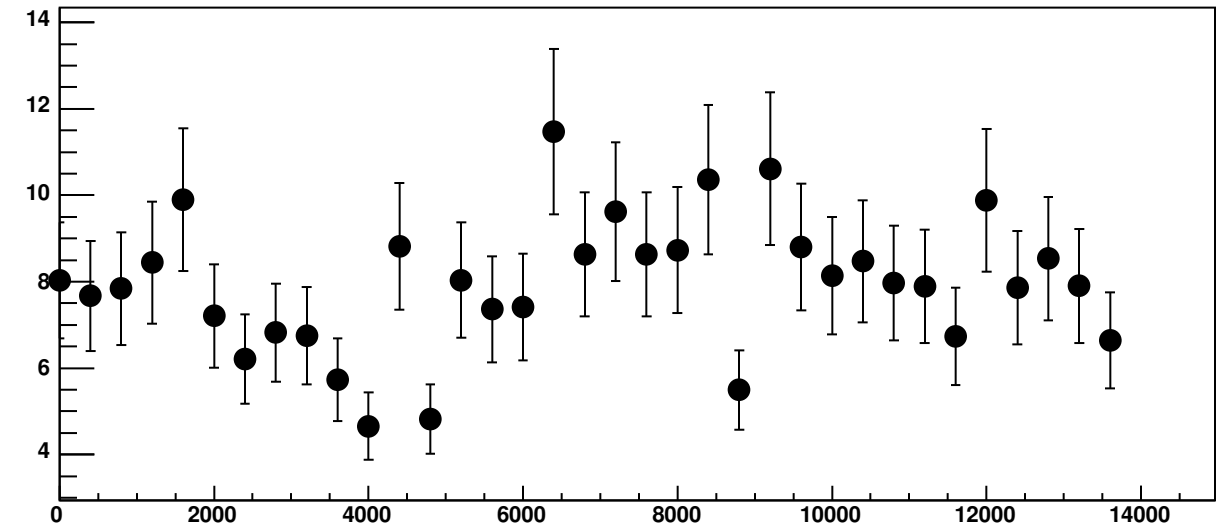

Chip 7, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

Chip 7, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

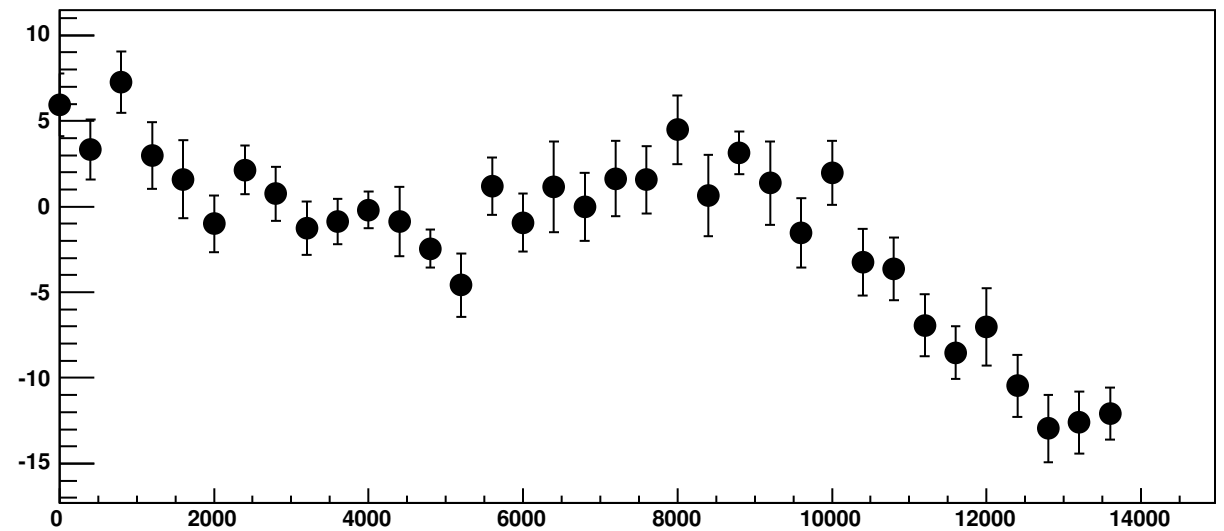
χ^2 / ndf	40.39 / 23
p0	-328.6 ± 0.7605
p1	-0.003898 ± 0.0001228

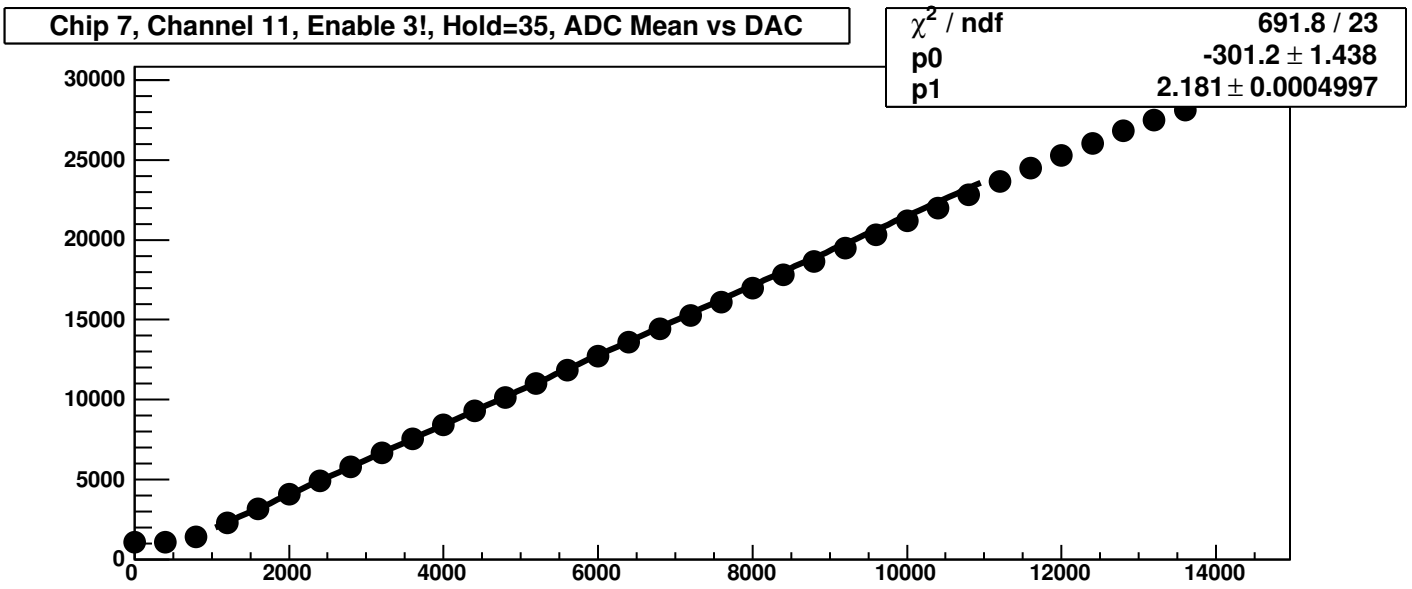


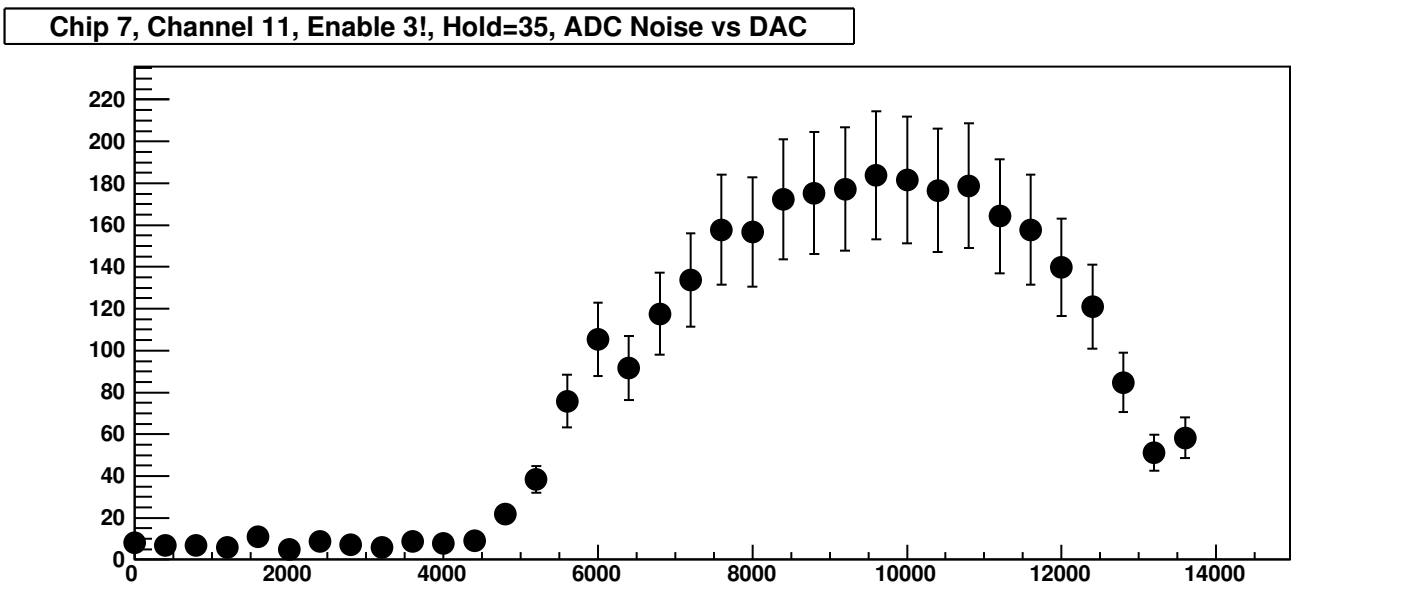
Chip 7, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

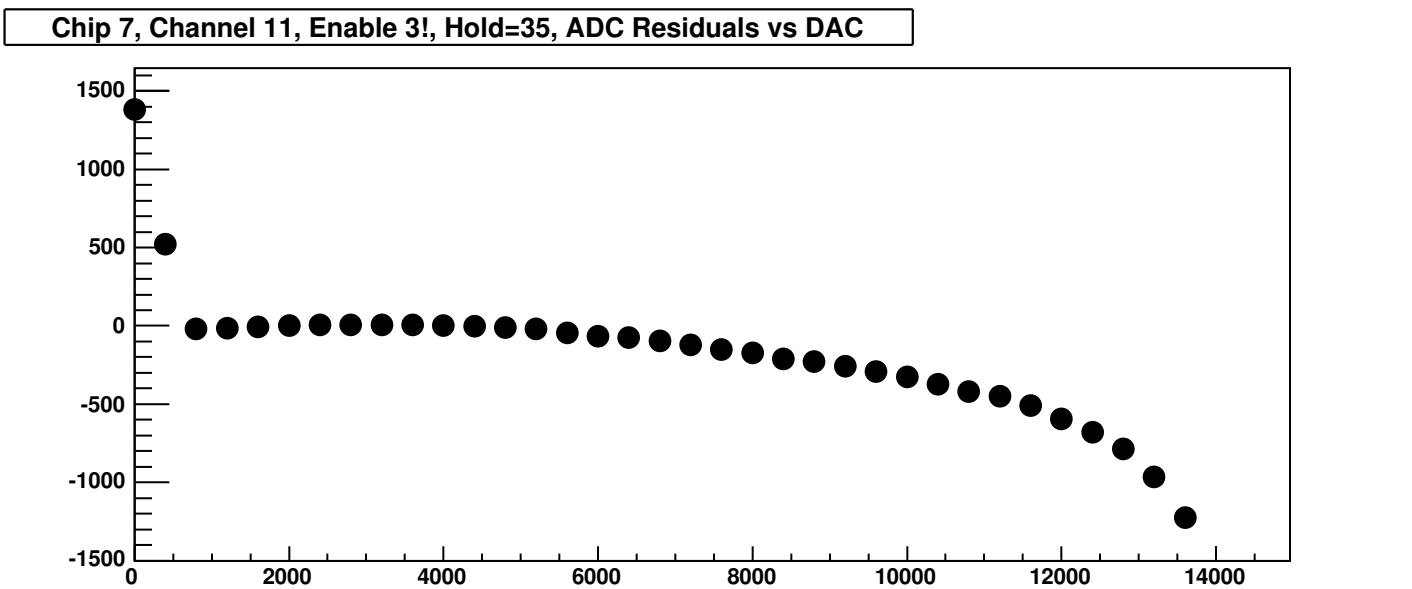


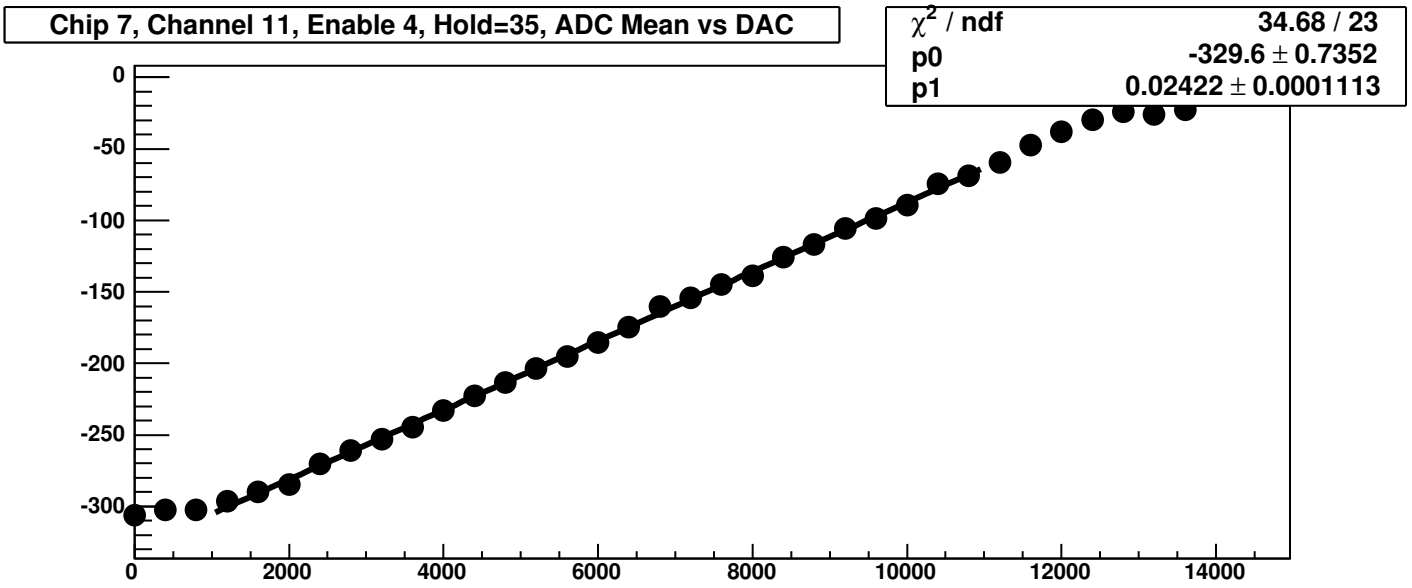
Chip 7, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

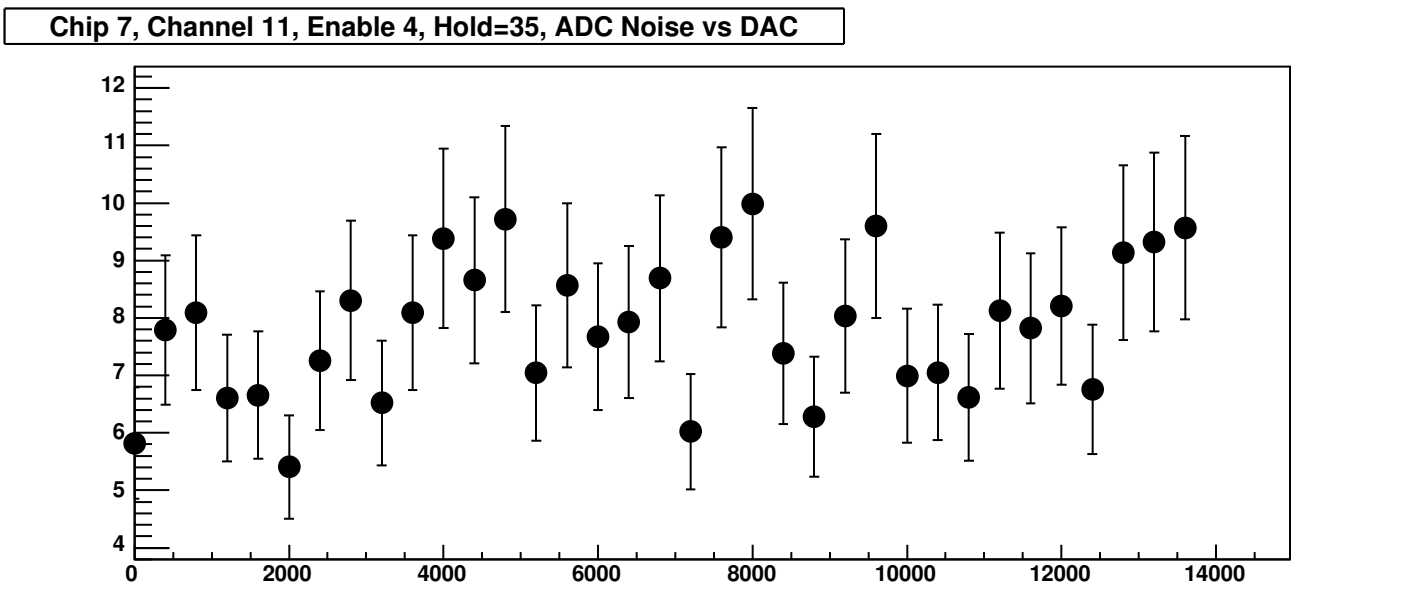


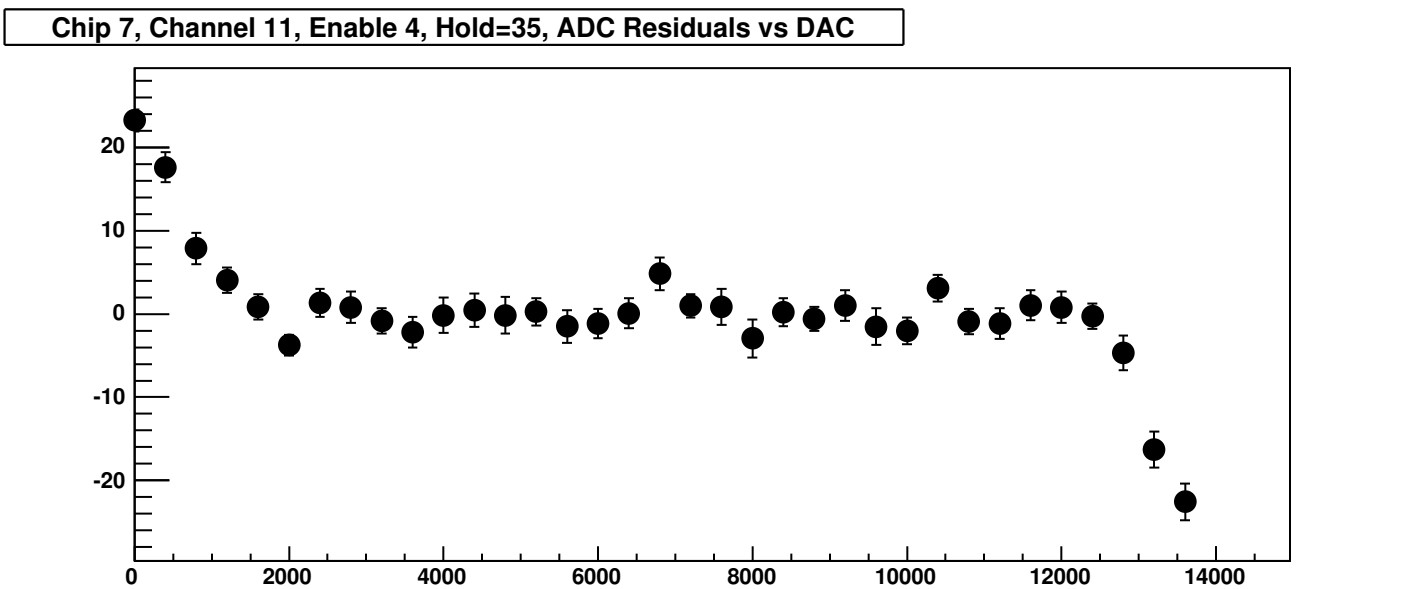

Chip 7, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 691.8 / 23
p0 -301.2 ± 1.438
p1 2.181 ± 0.0004997
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

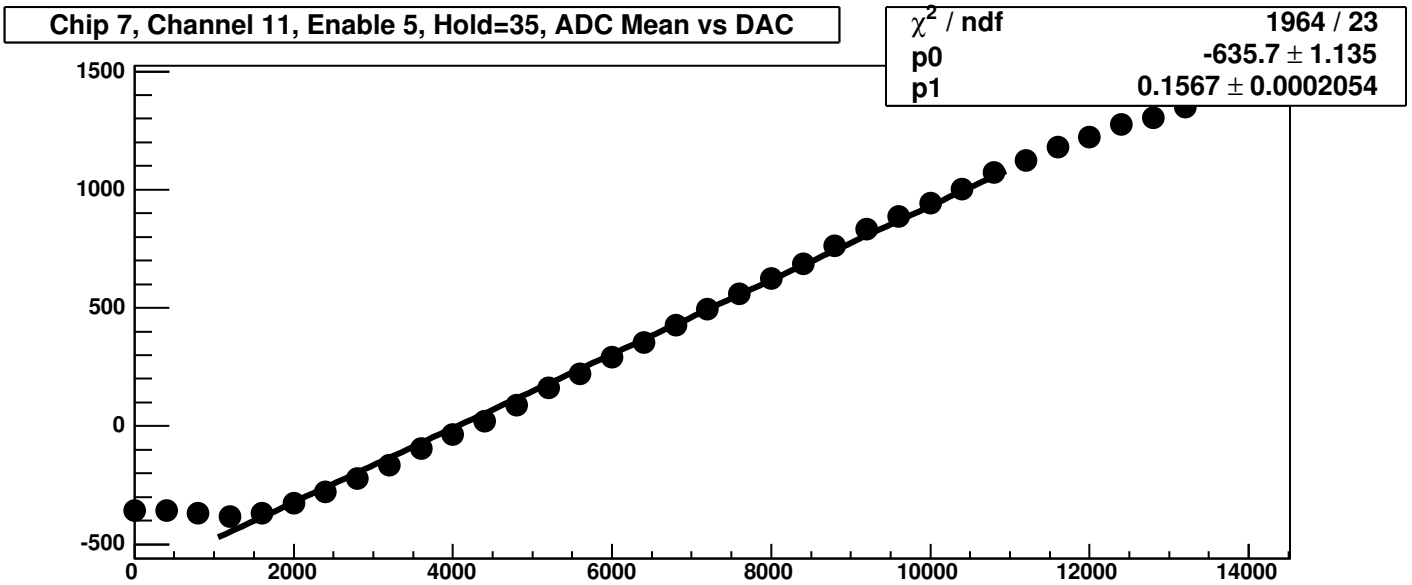

Chip 7, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

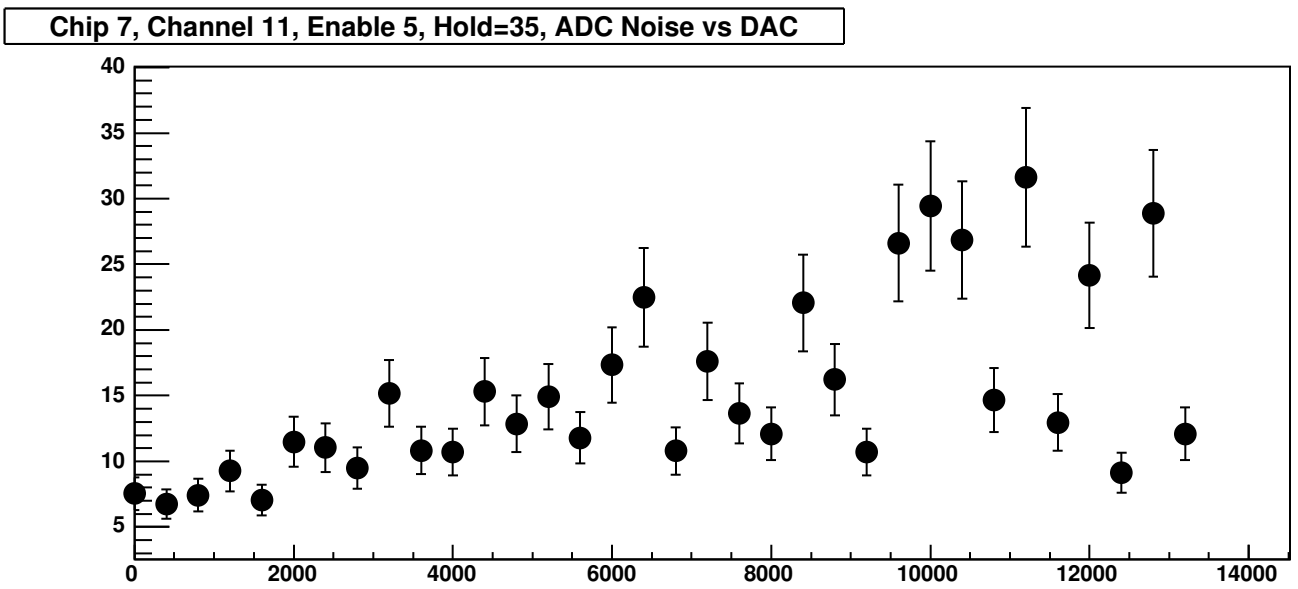

Chip 7, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

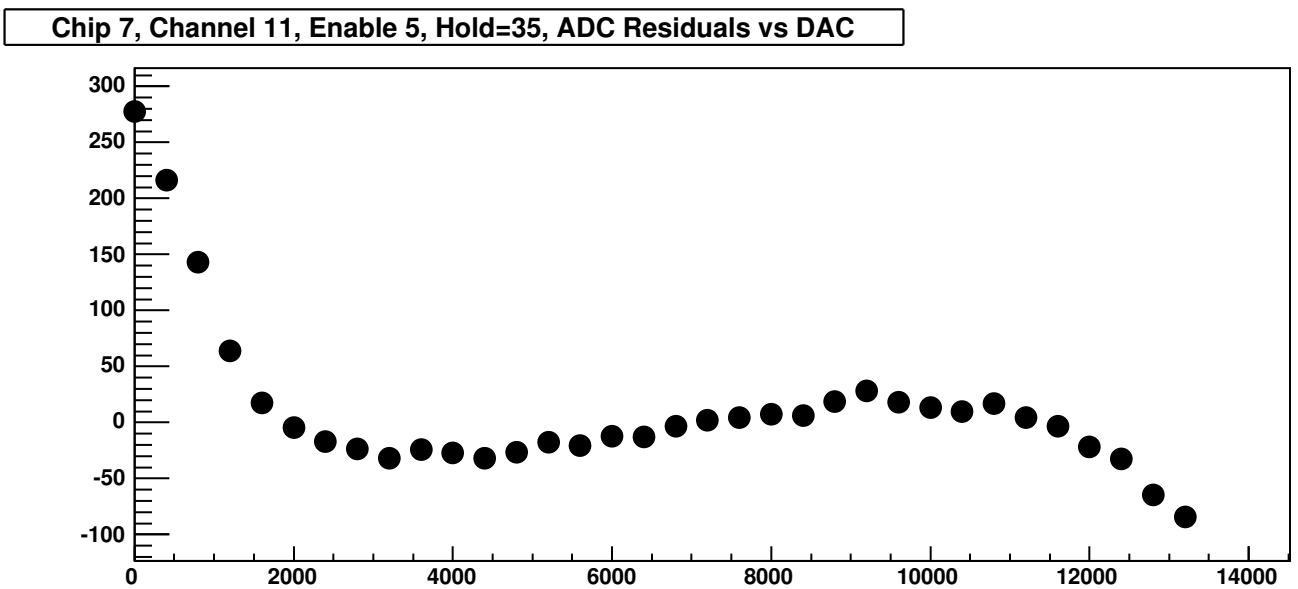

Chip 7, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 34.68 / 23
p0 -329.6 ± 0.7352
p1 0.02422 ± 0.0001113
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

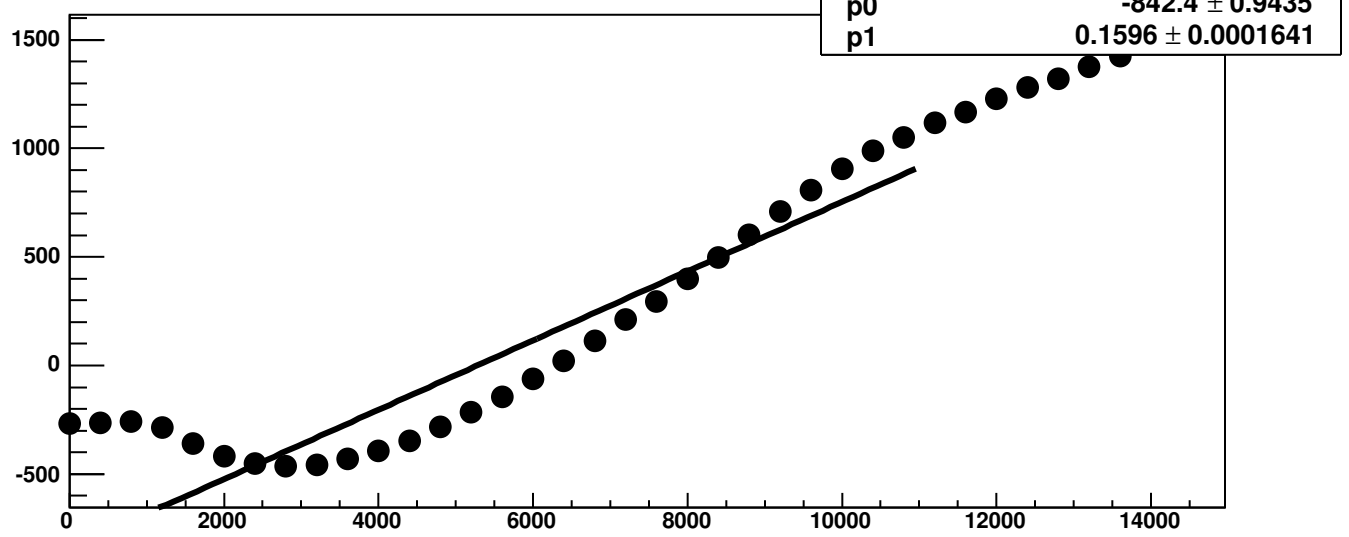

Chip 7, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1964 / 23
p0 -635.7 ± 1.135
p1 0.1567 ± 0.0002054
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

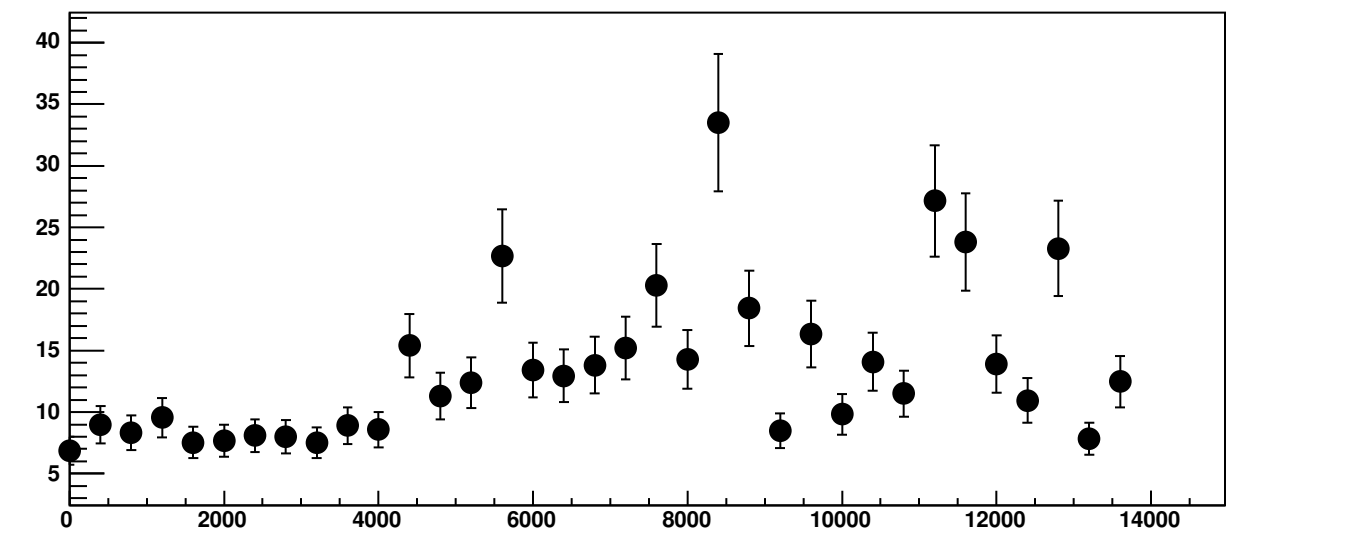

Chip 7, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

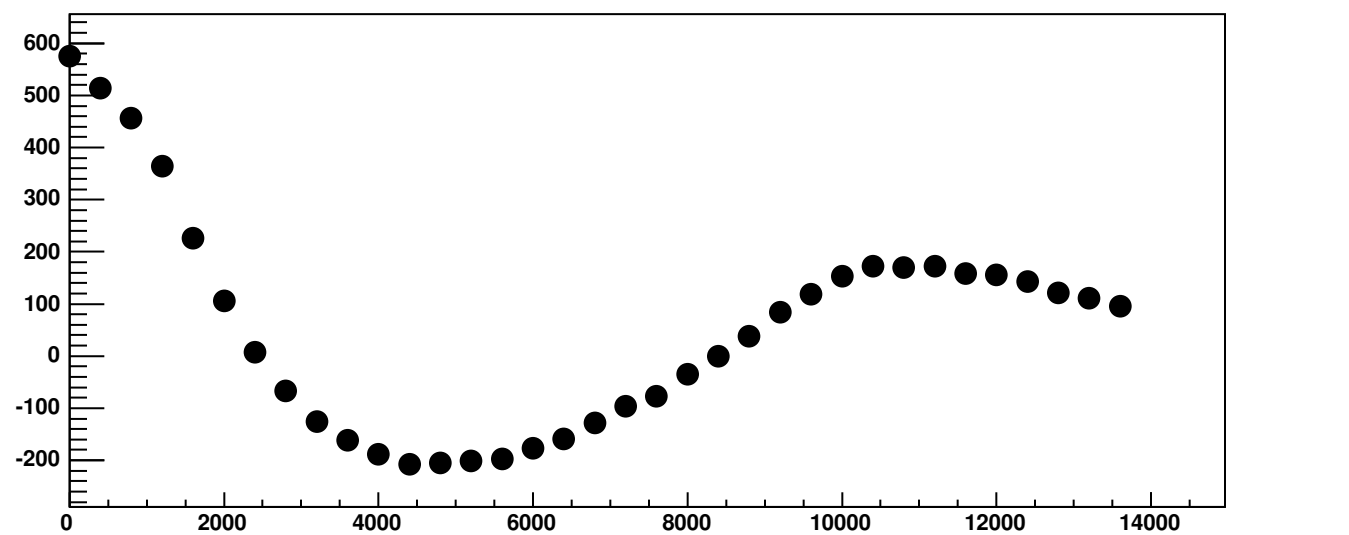
Chip 7, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

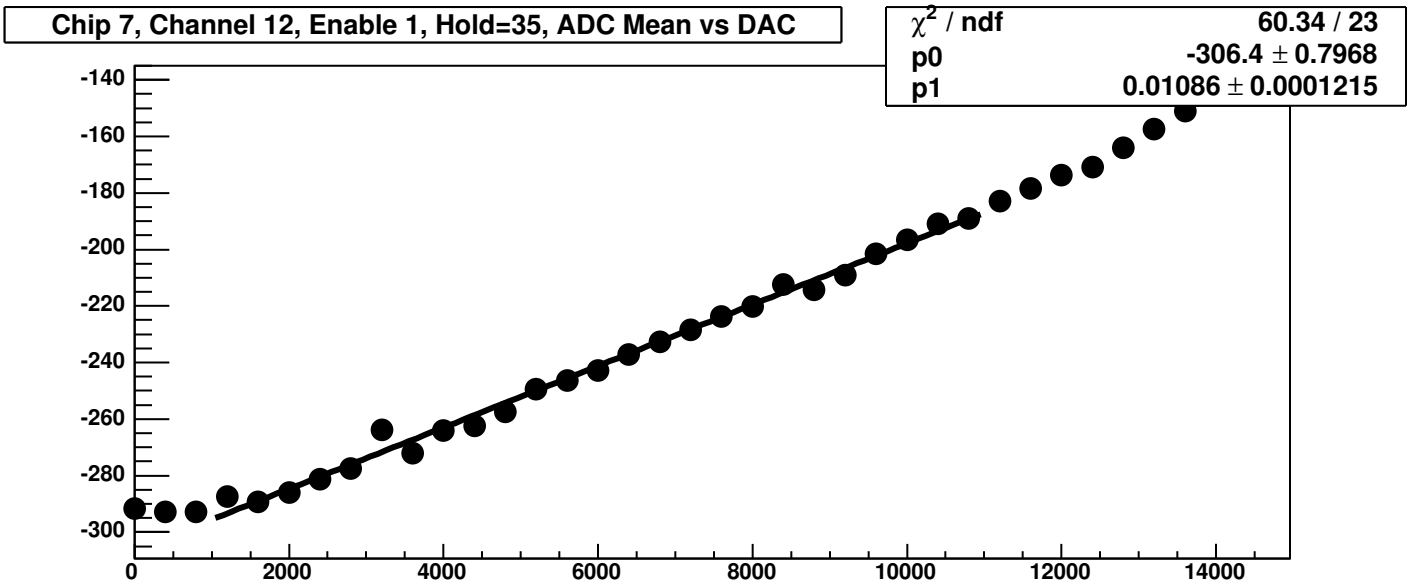


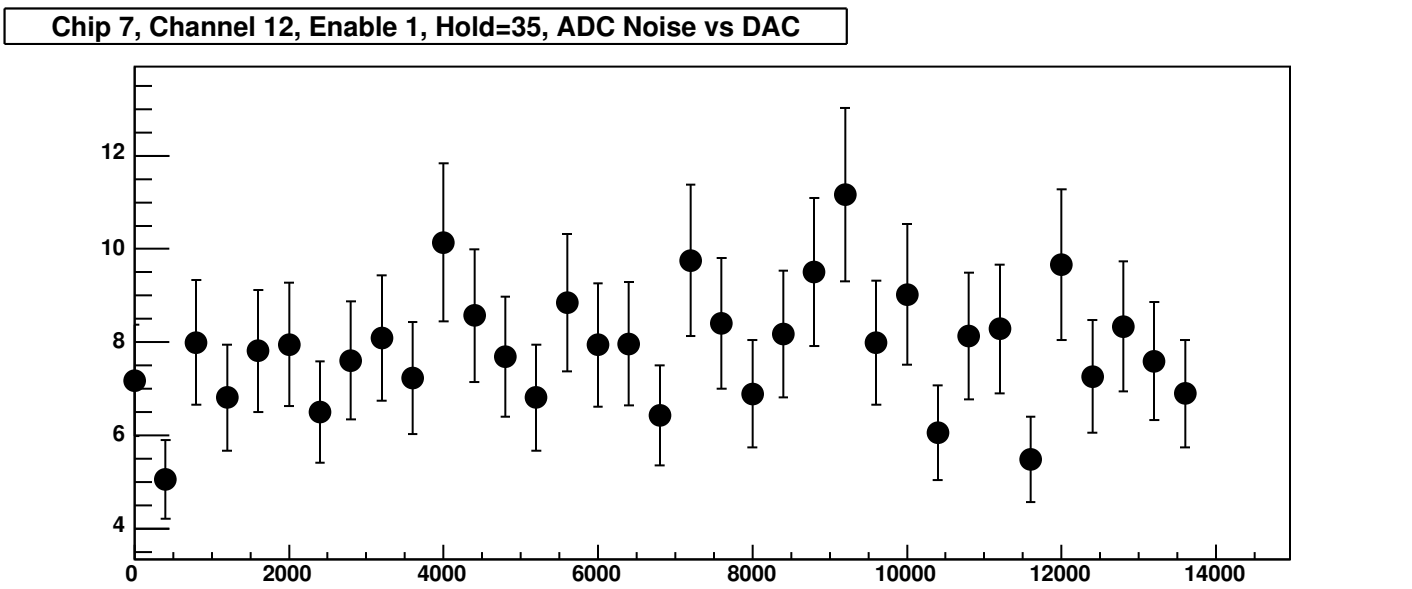
Chip 7, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

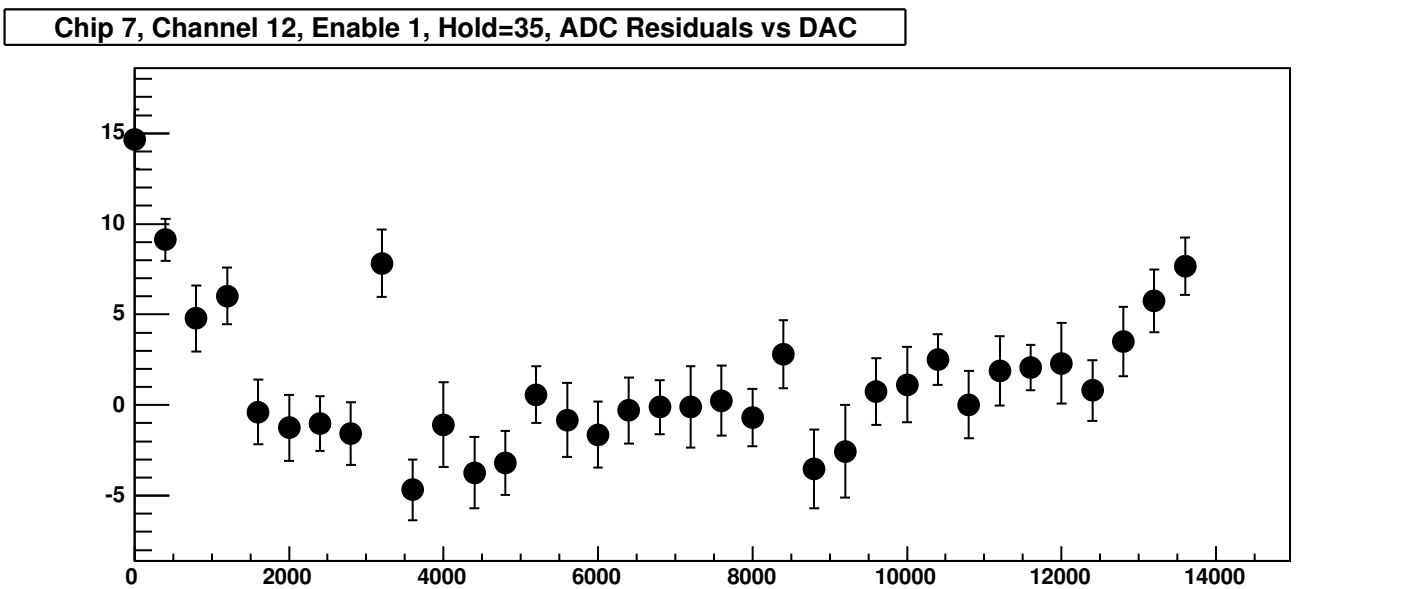


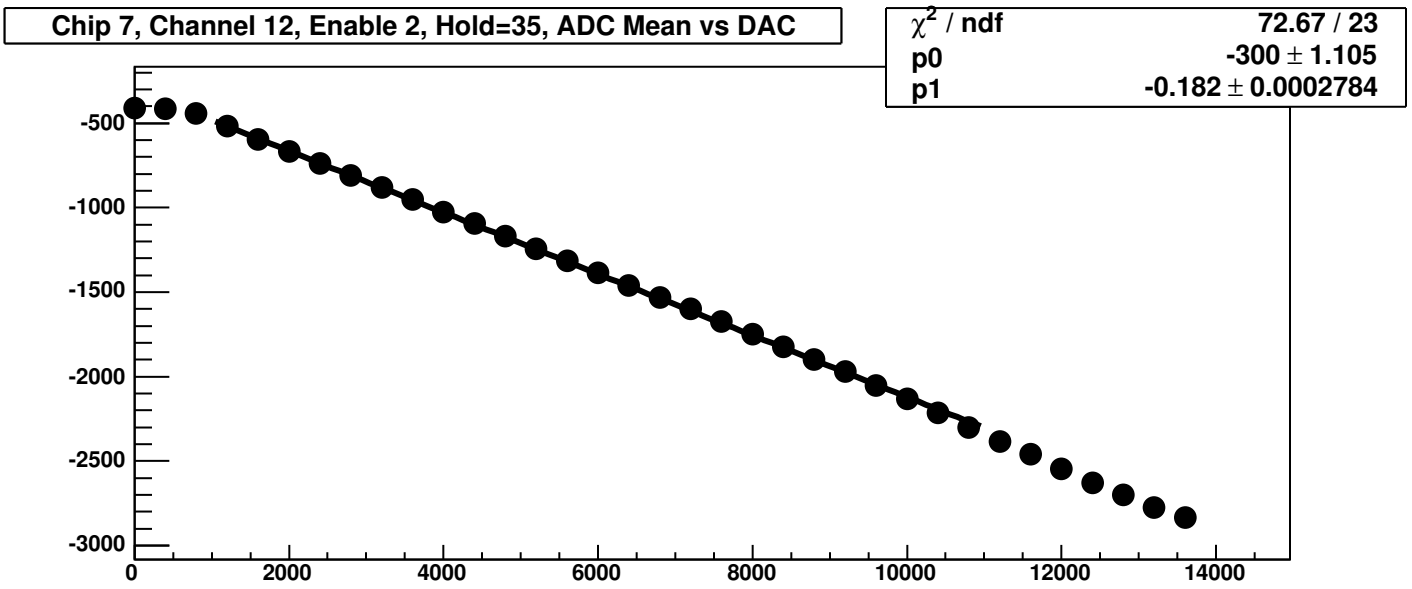
Chip 7, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

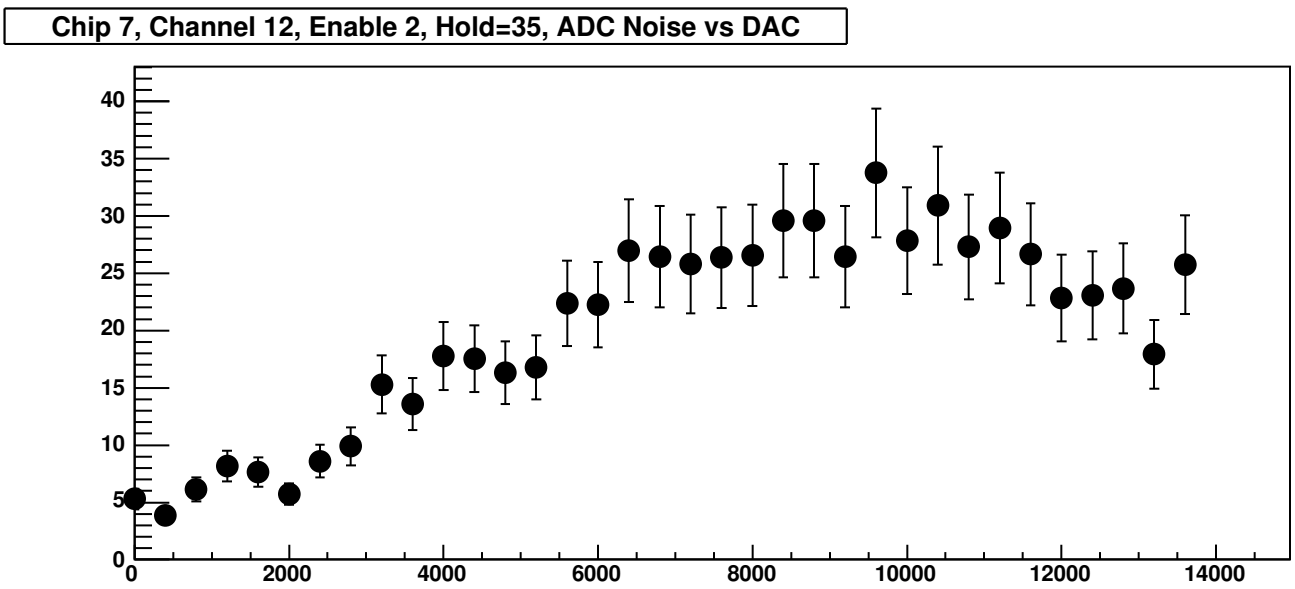


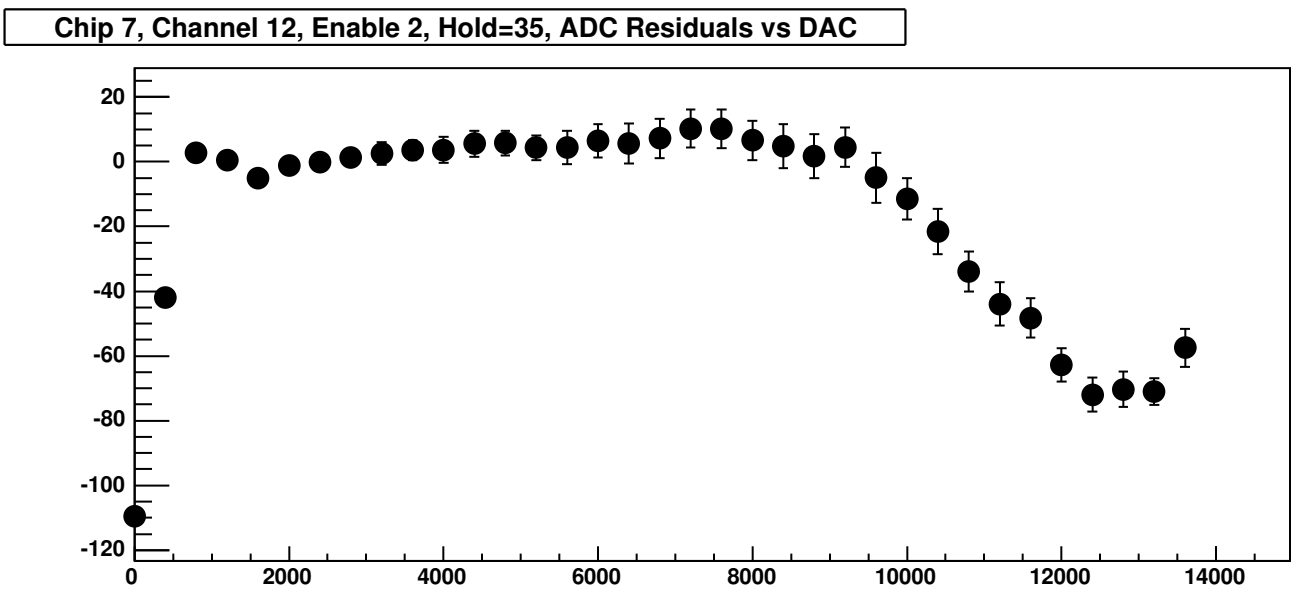

Chip 7, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 60.34 / 23
p0 -306.4 ± 0.7968
p1 0.01086 ± 0.0001215
-140
-160
-180
-200
-220
-240
-260
-280
-300
0
2000
4000
6000
8000
10000
12000
14000

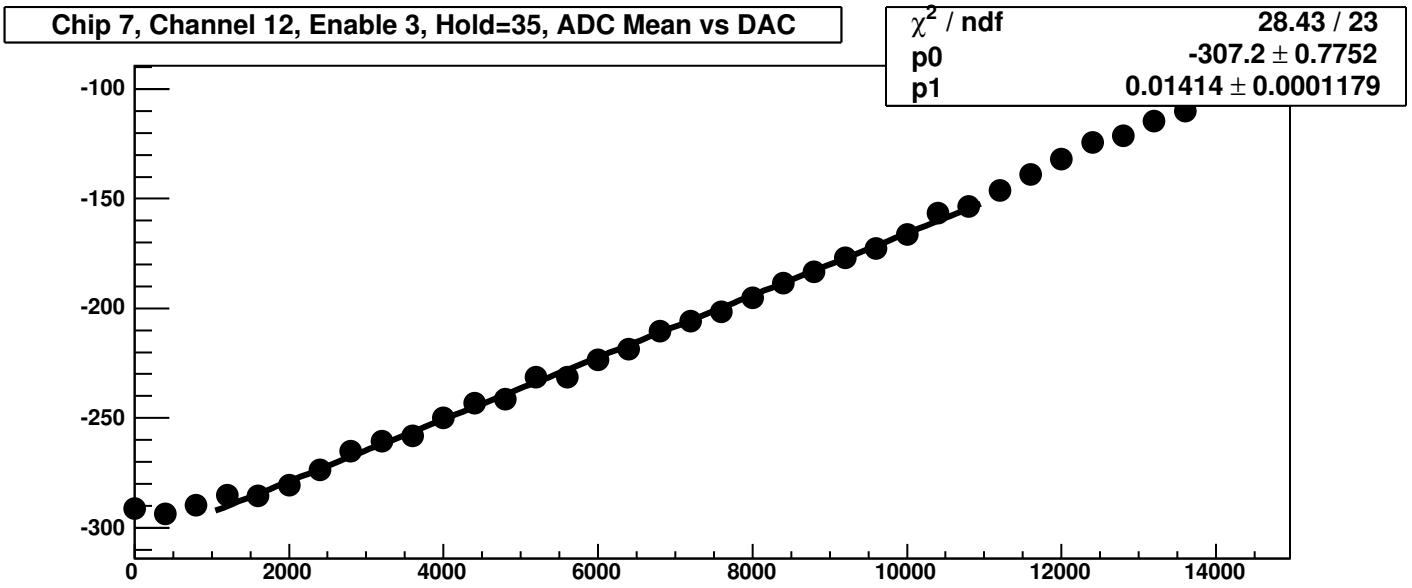

Chip 7, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

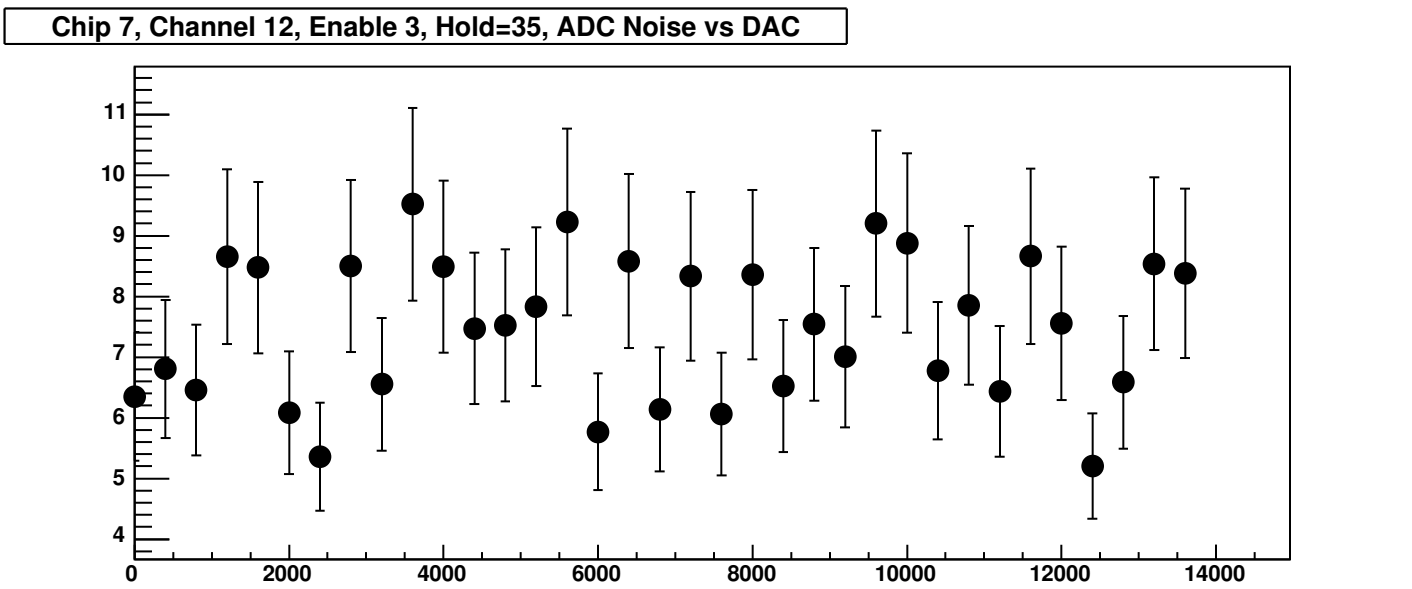

Chip 7, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

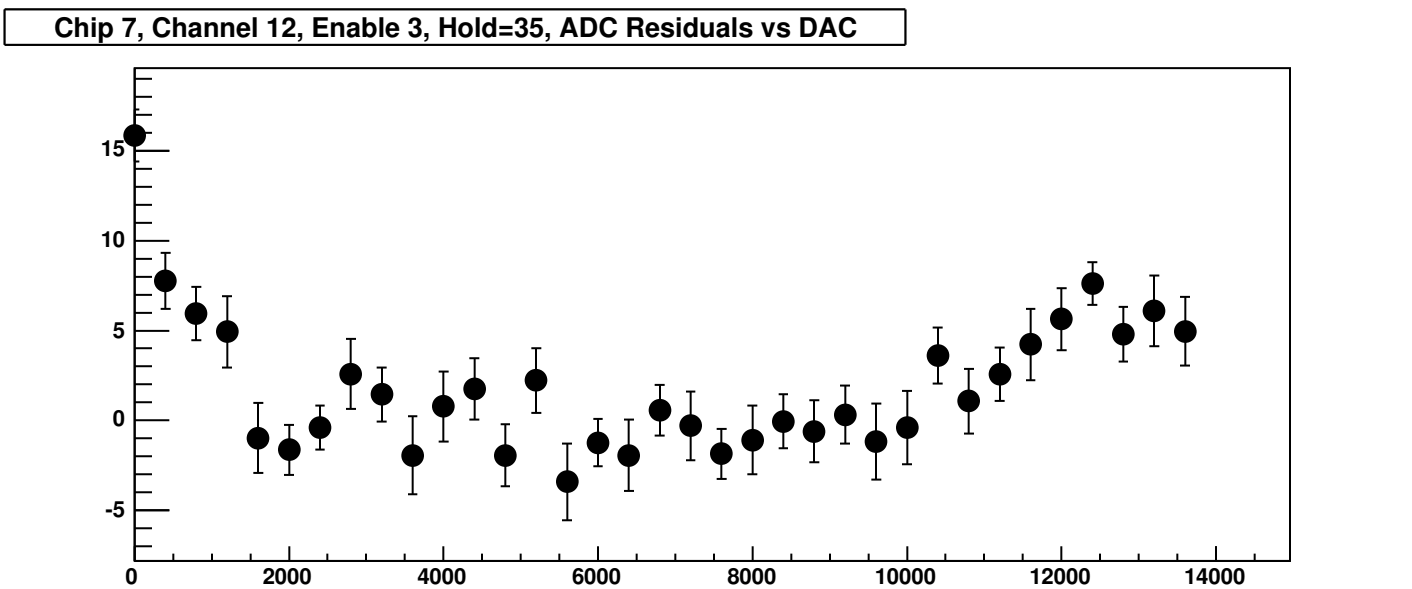

Chip 7, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 72.67 / 23
p0 -300 ± 1.105
p1 -0.182 ± 0.0002784
-500
-1000
-1500
-2000
-2500
-3000
0
2000
4000
6000
8000
10000
12000
14000

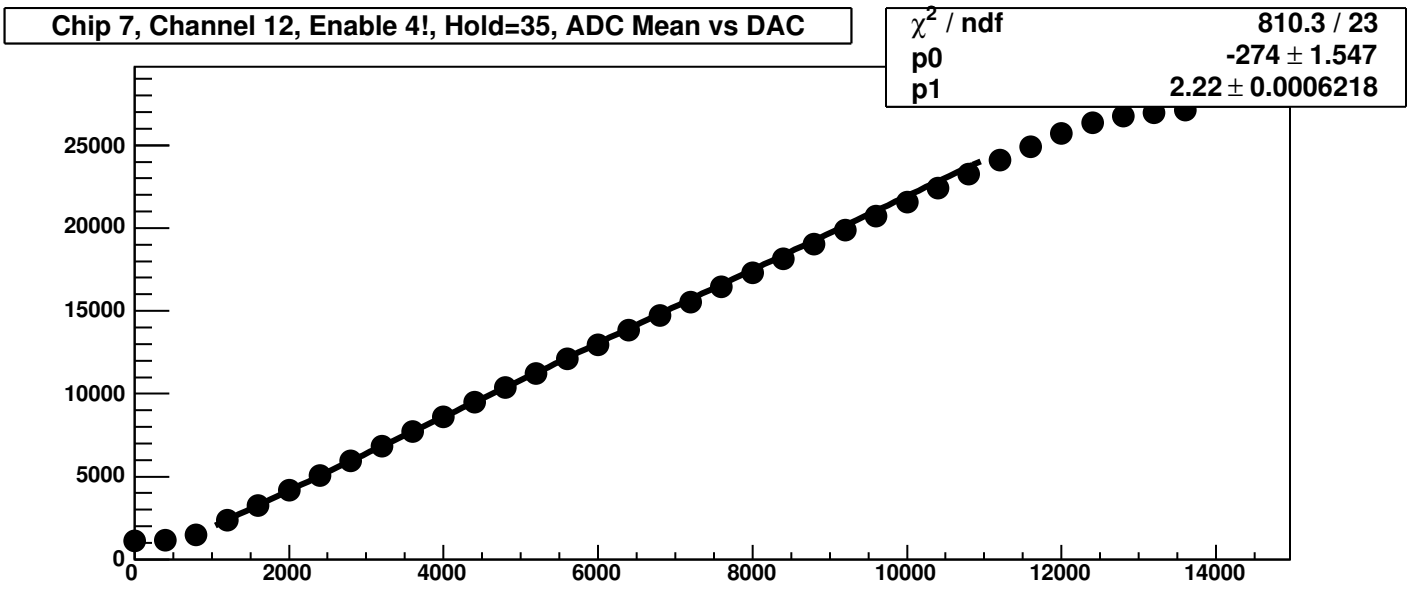

Chip 7, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
0
2000
4000
6000
8000
10000
12000
14000

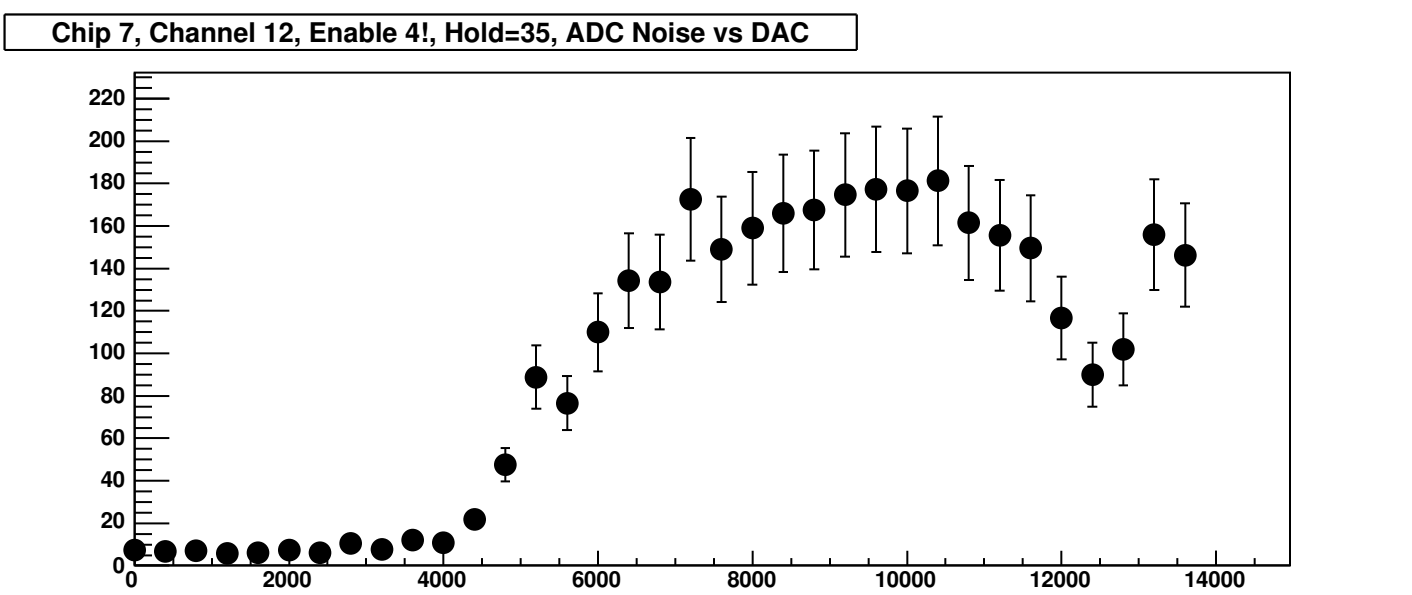

Chip 7, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
2000
4000
6000
8000
10000
12000
14000

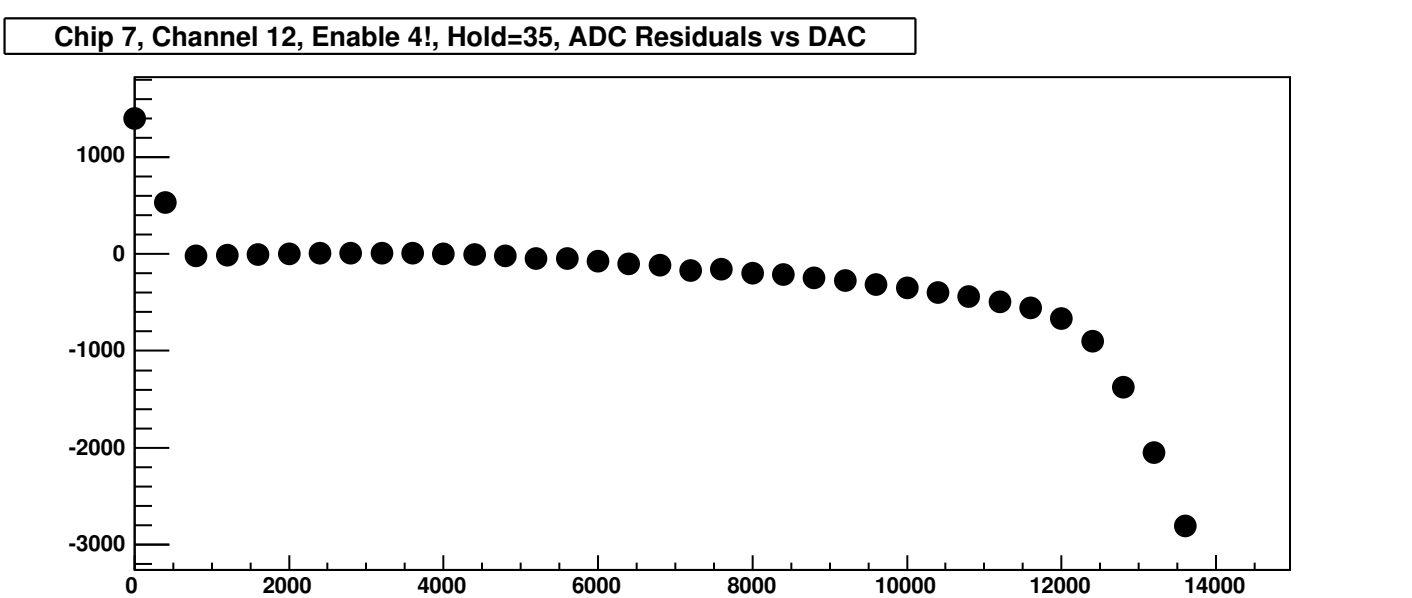

Chip 7, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.43 / 23
p0 -307.2 ± 0.7752
p1 0.01414 ± 0.0001179
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

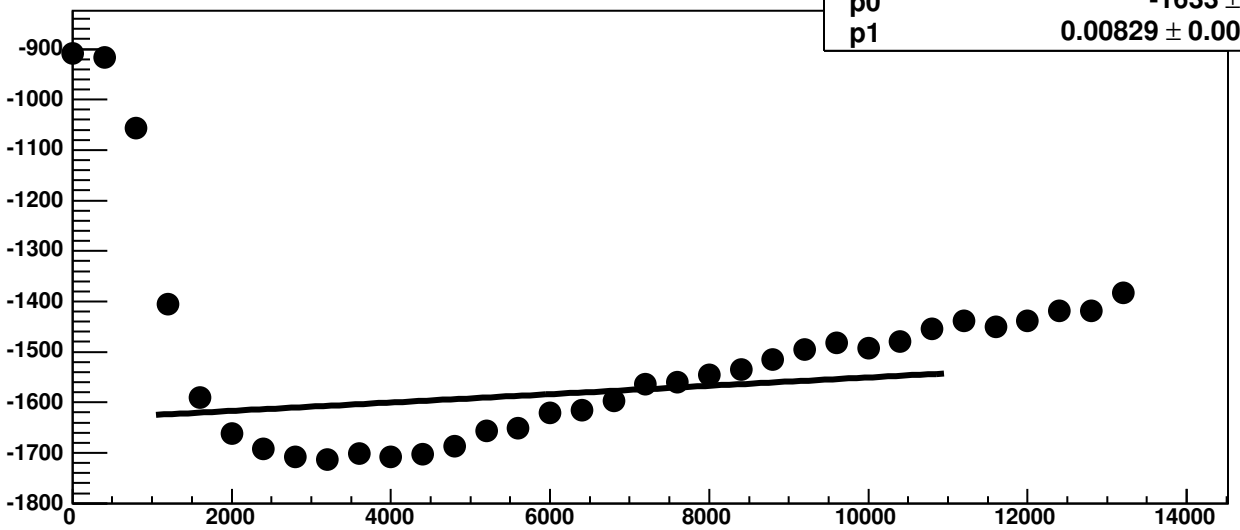

Chip 7, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 810.3 / 23
p0 -274 ± 1.547
p1 2.22 ± 0.0006218
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

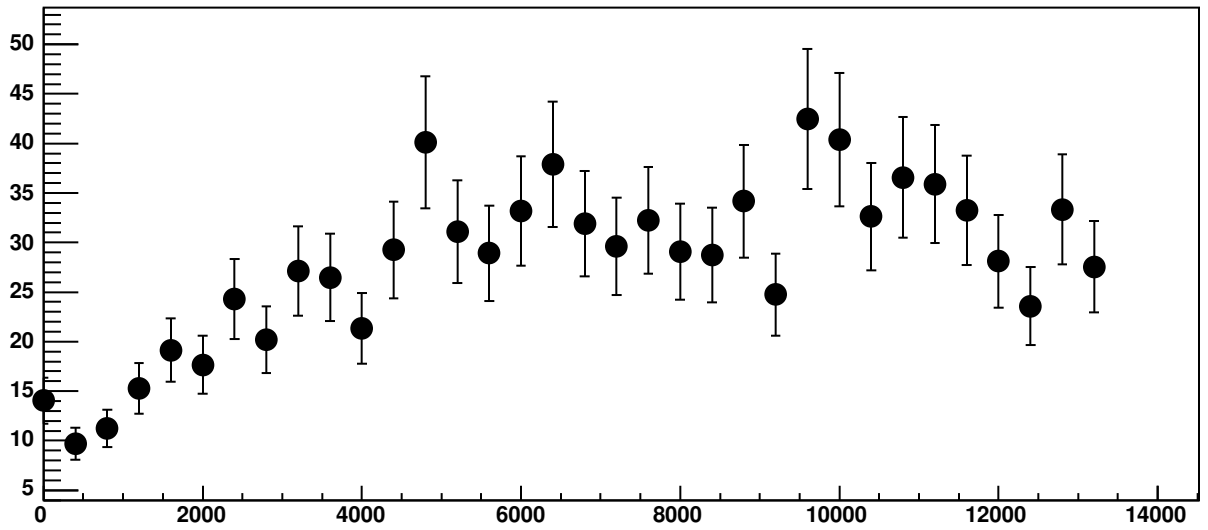

Chip 7, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

Chip 7, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

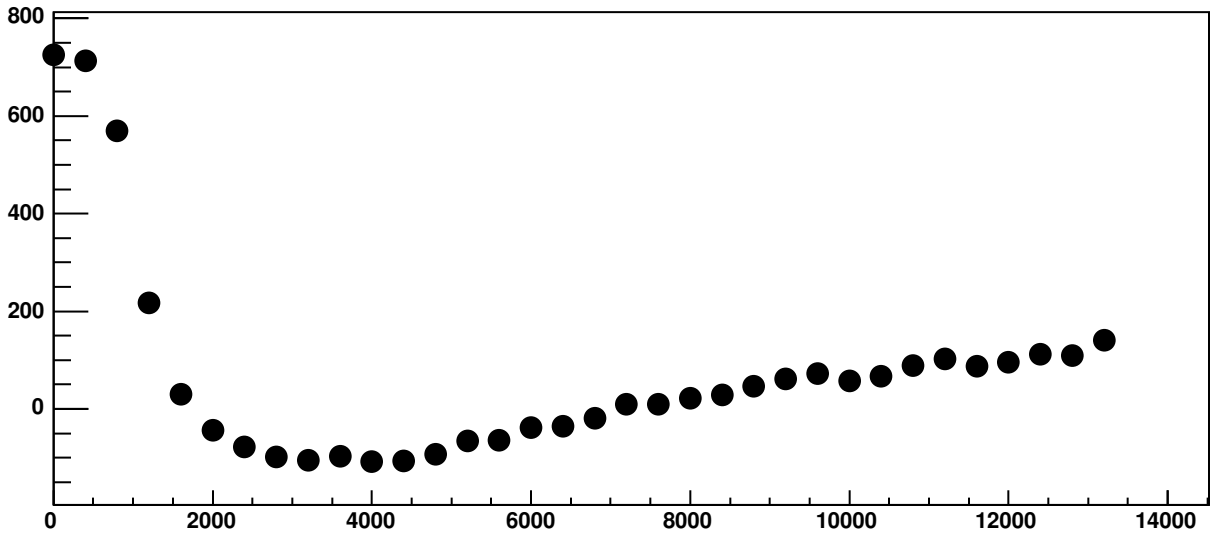
χ^2 / ndf	6736 / 23
p0	-1633 ± 2.277
p1	0.00829 ± 0.0004082

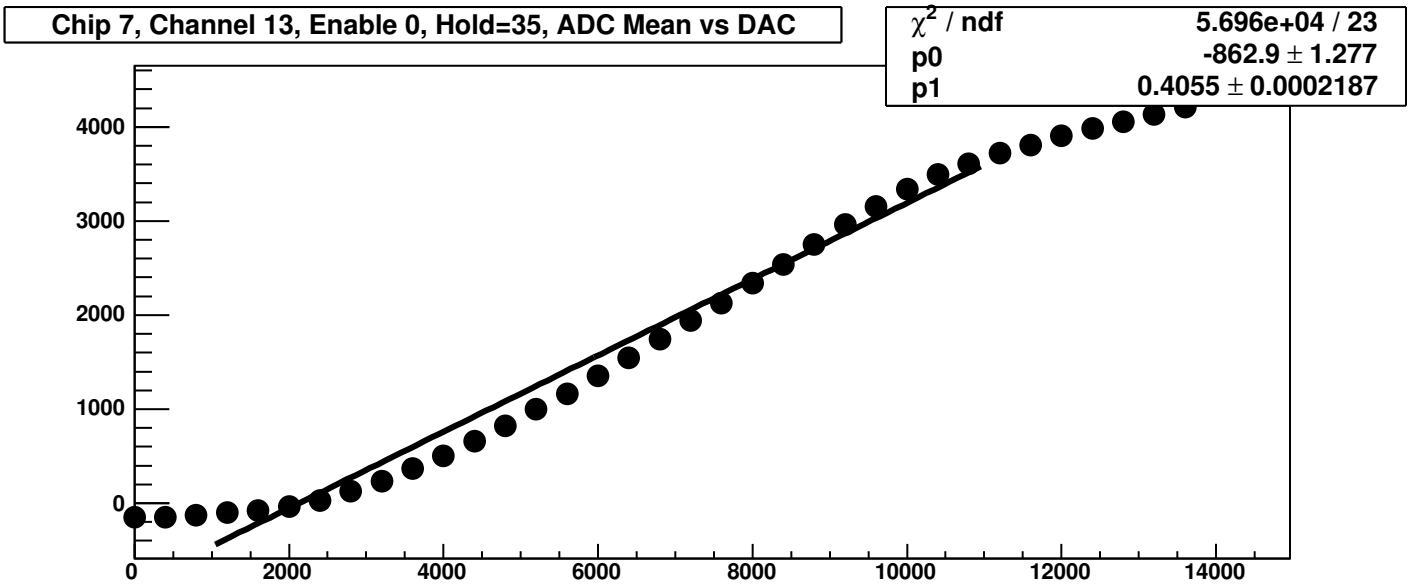


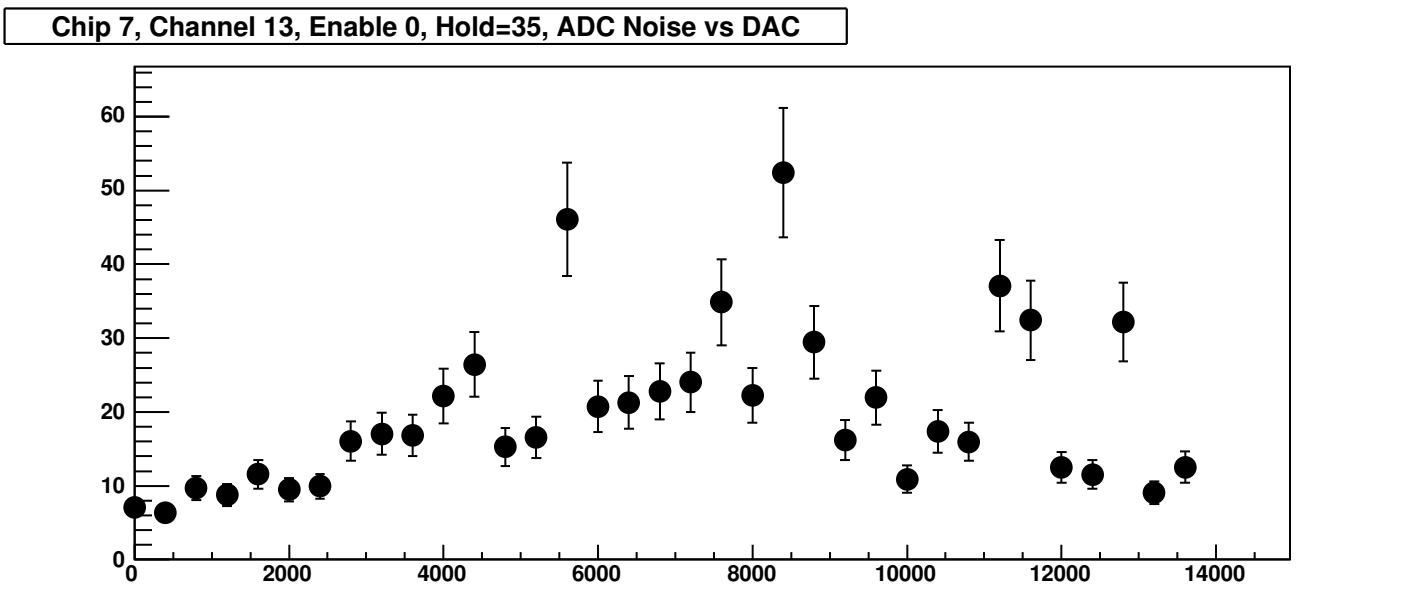
Chip 7, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

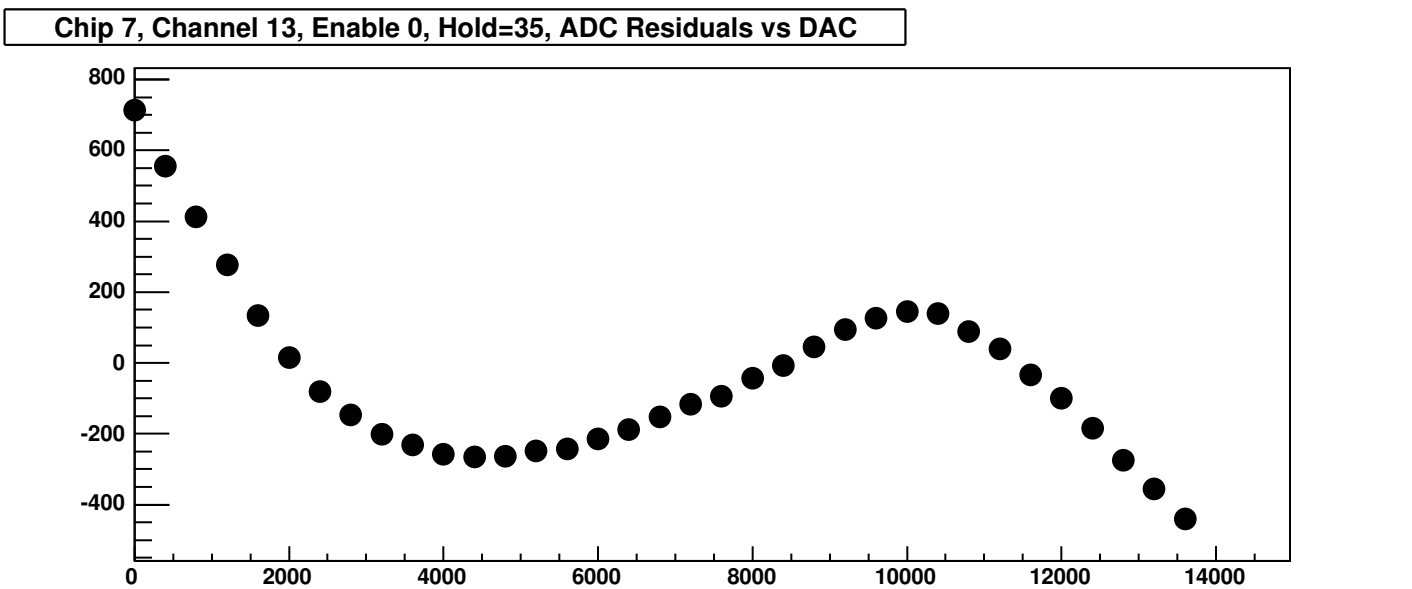


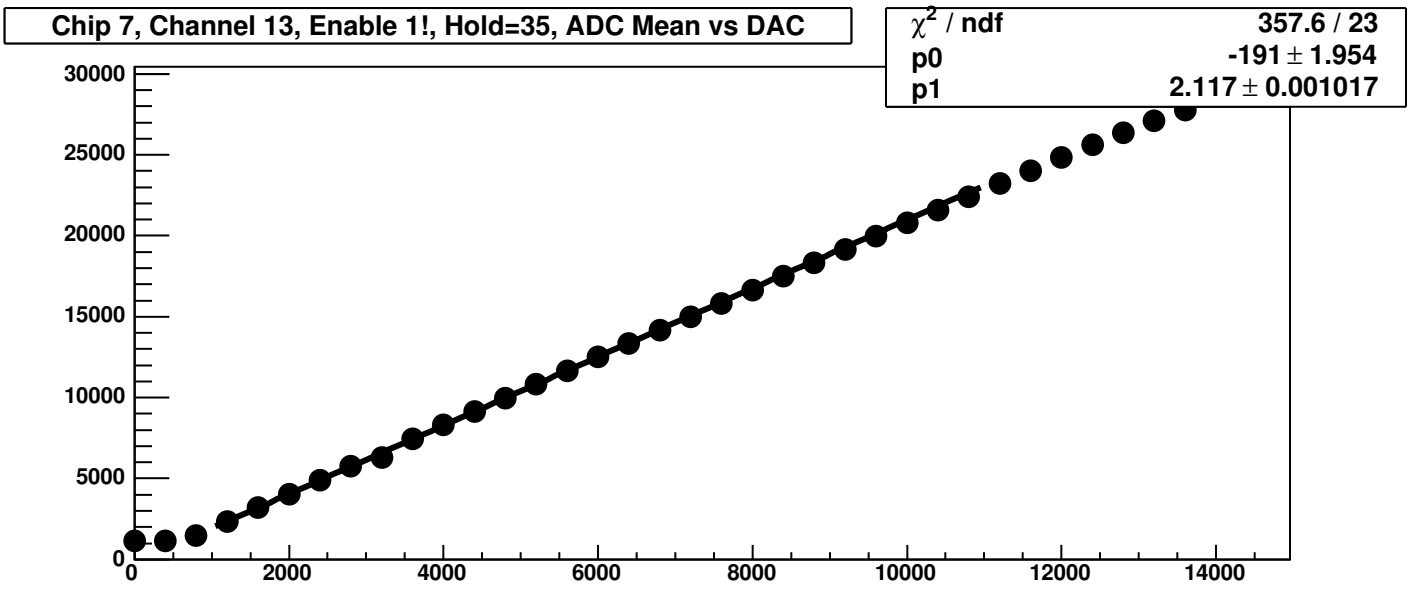
Chip 7, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

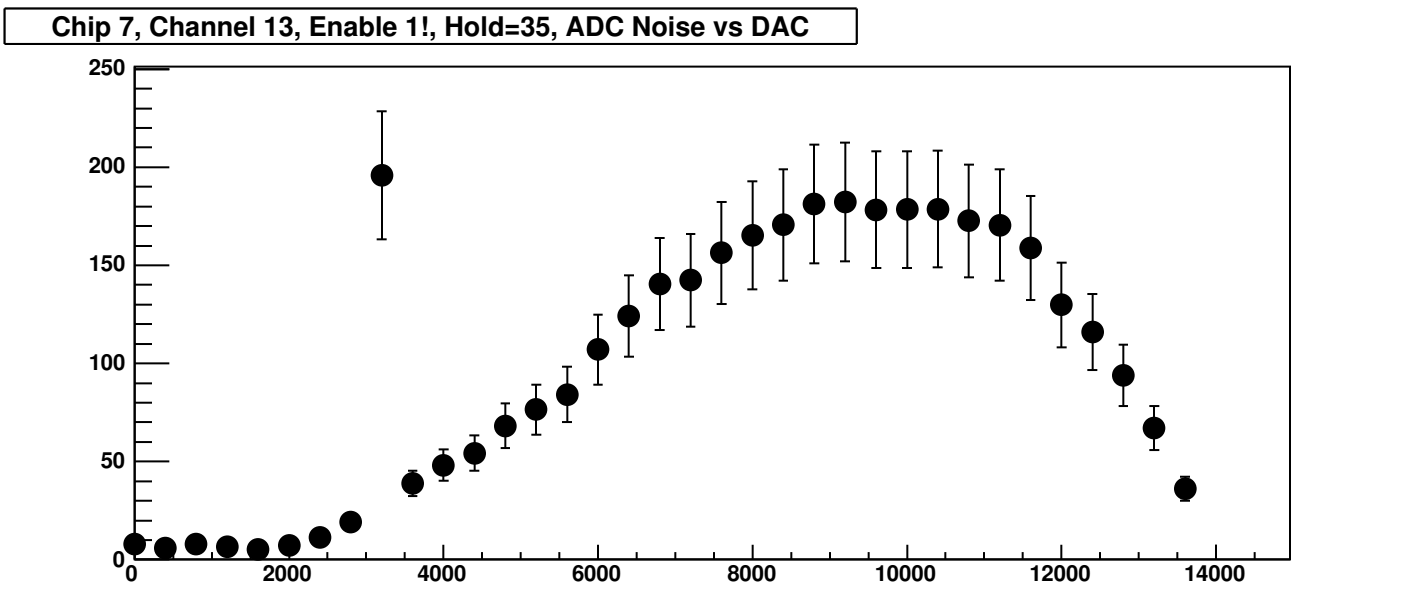


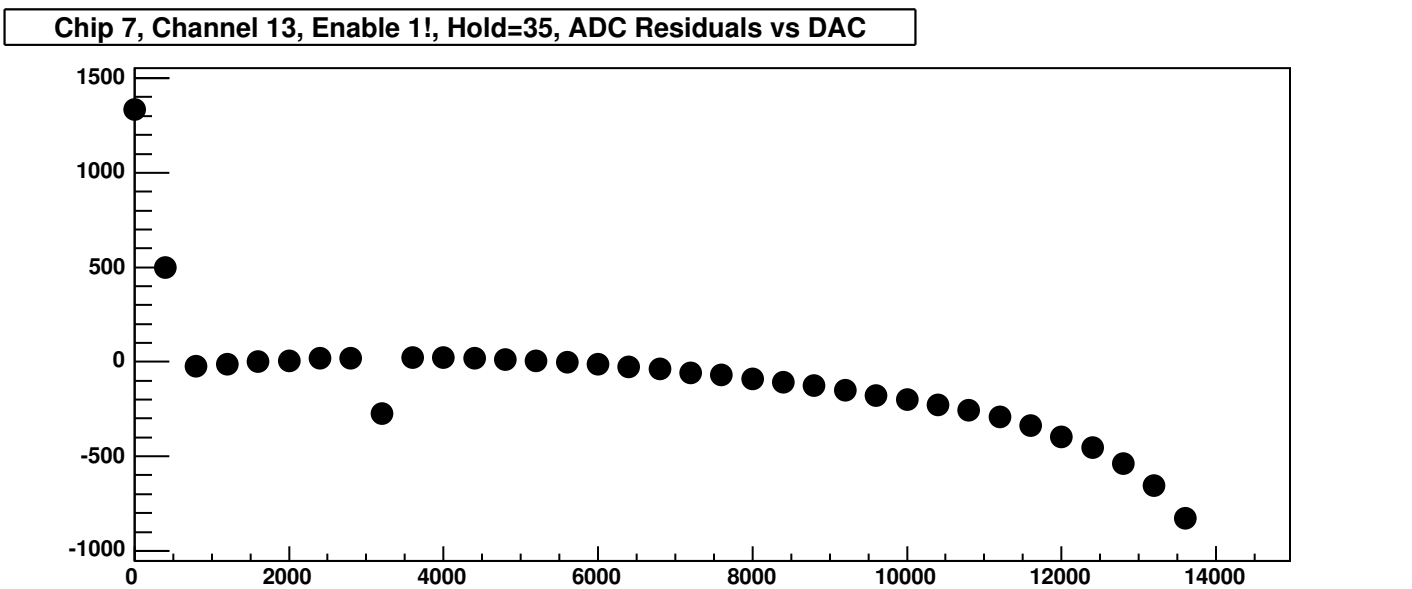

Chip 7, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.696e+04 / 23
p0 -862.9 ± 1.277
p1 0.4055 ± 0.0002187
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

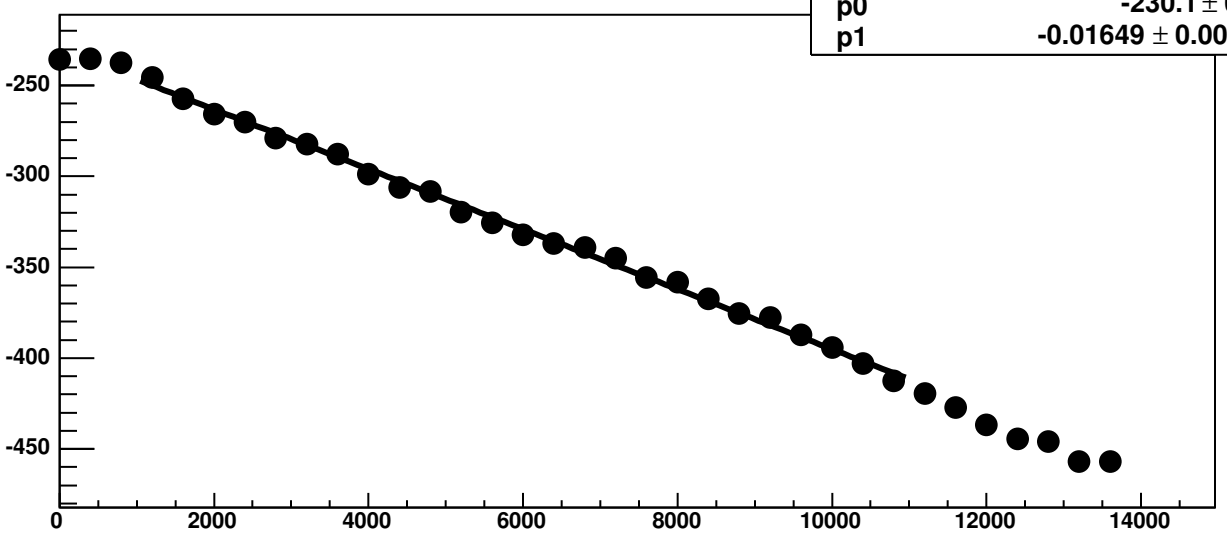

Chip 7, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 357.6 / 23
p0 -191 ± 1.954
p1 2.117 ± 0.001017
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

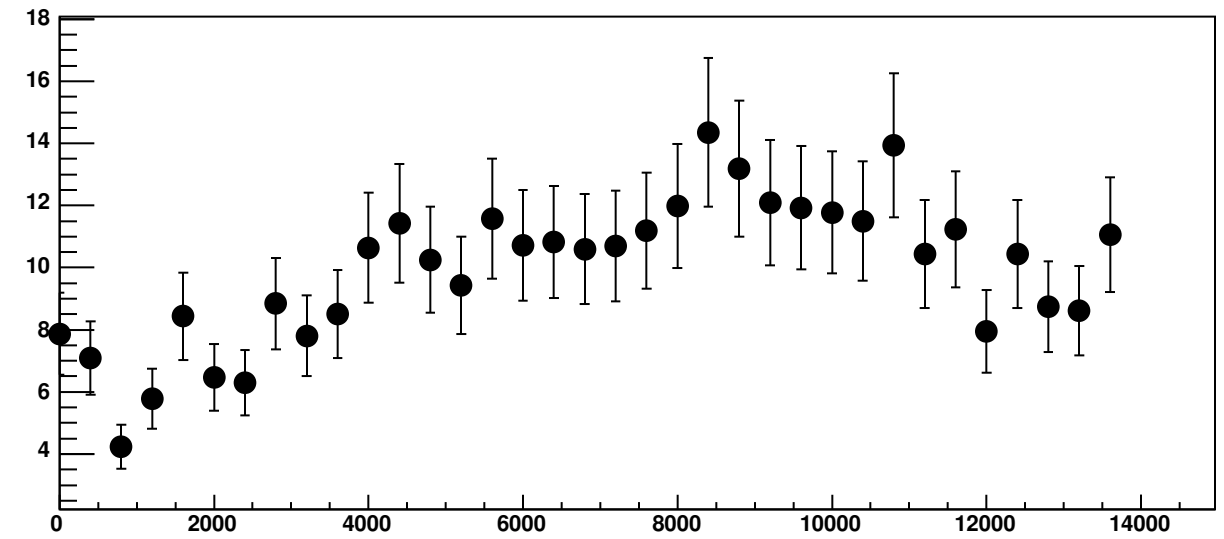

Chip 7, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

Chip 7, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

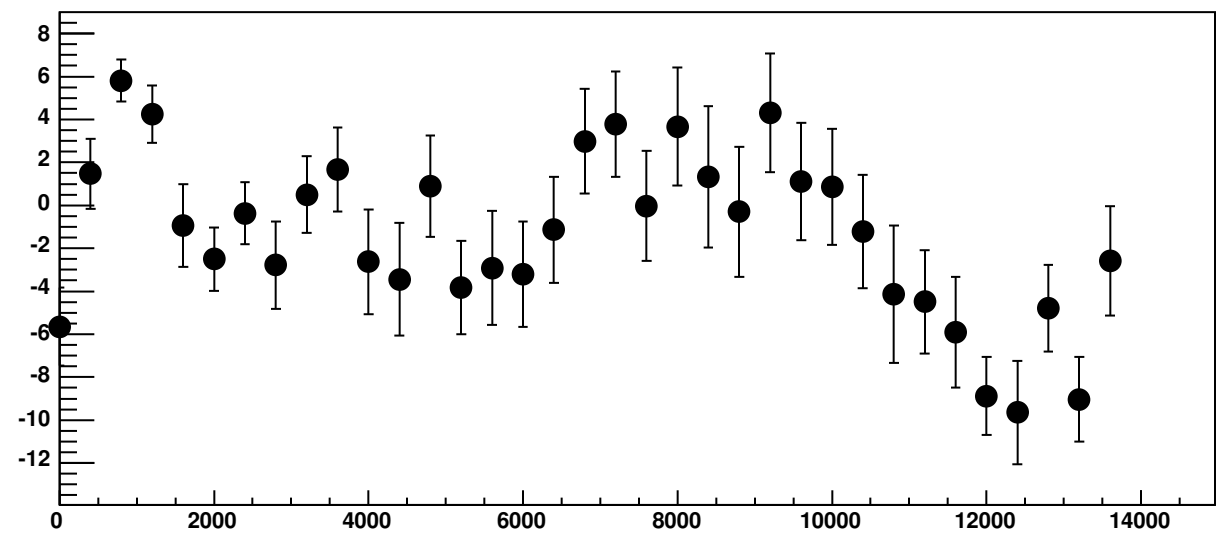
χ^2 / ndf	35.82 / 23
p0	-230.1 ± 0.8341
p1	-0.01649 ± 0.0001509

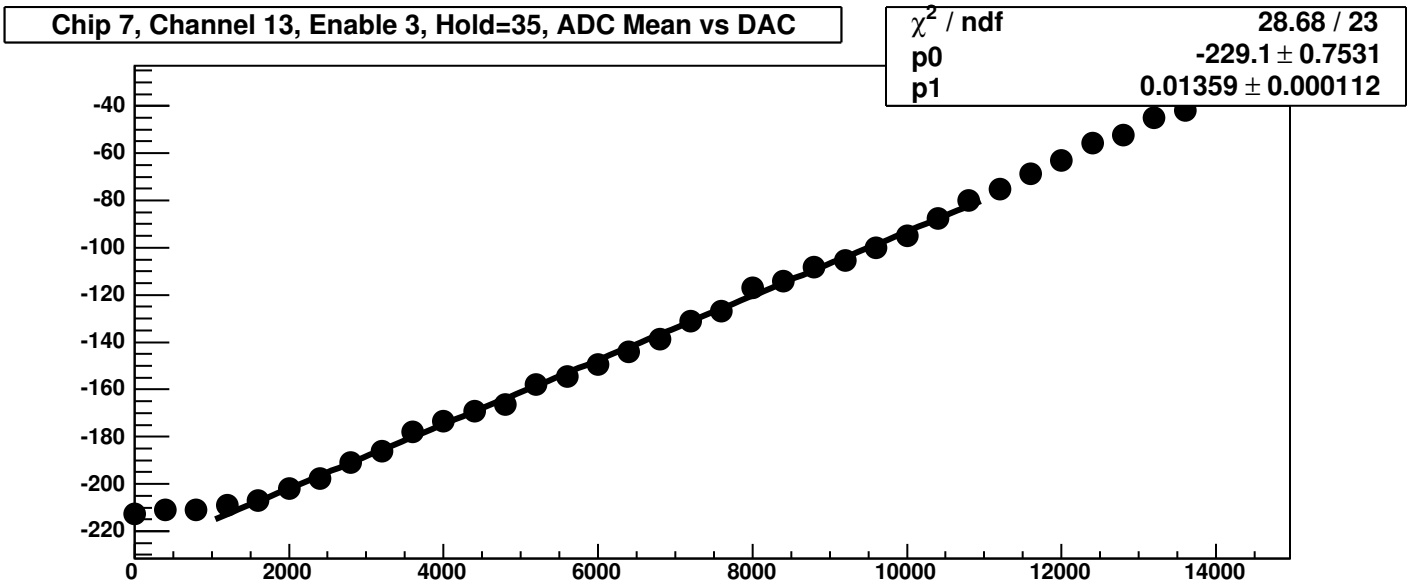


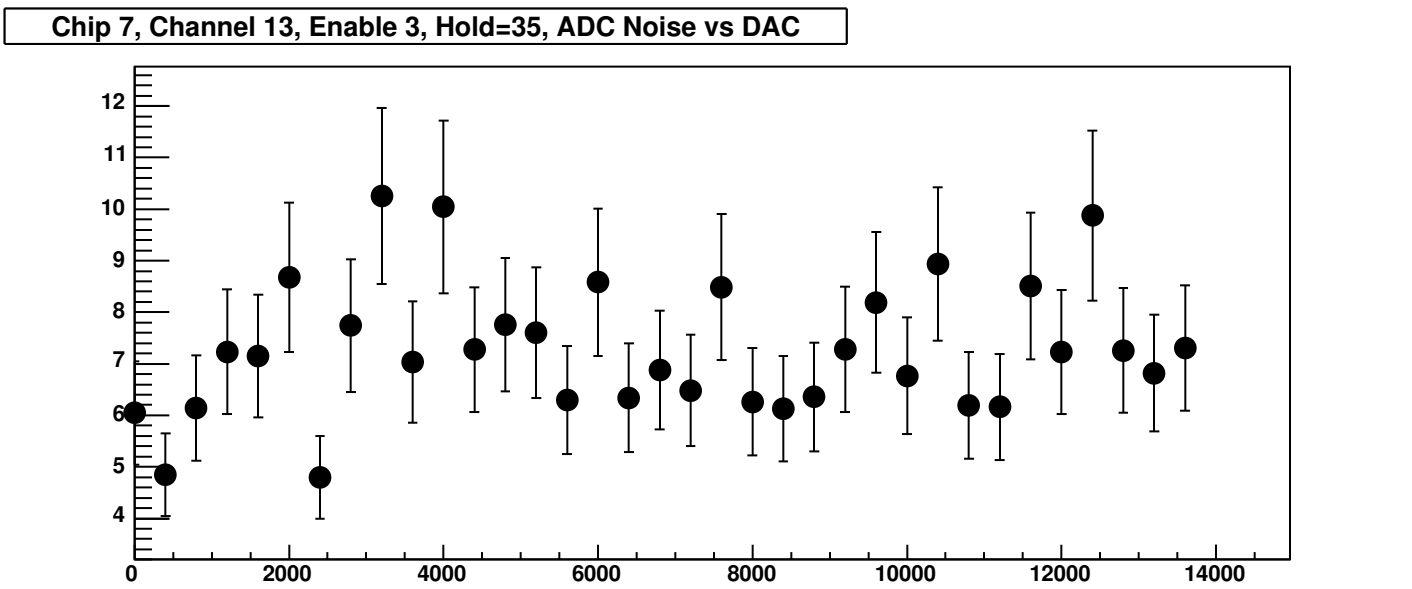
Chip 7, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC

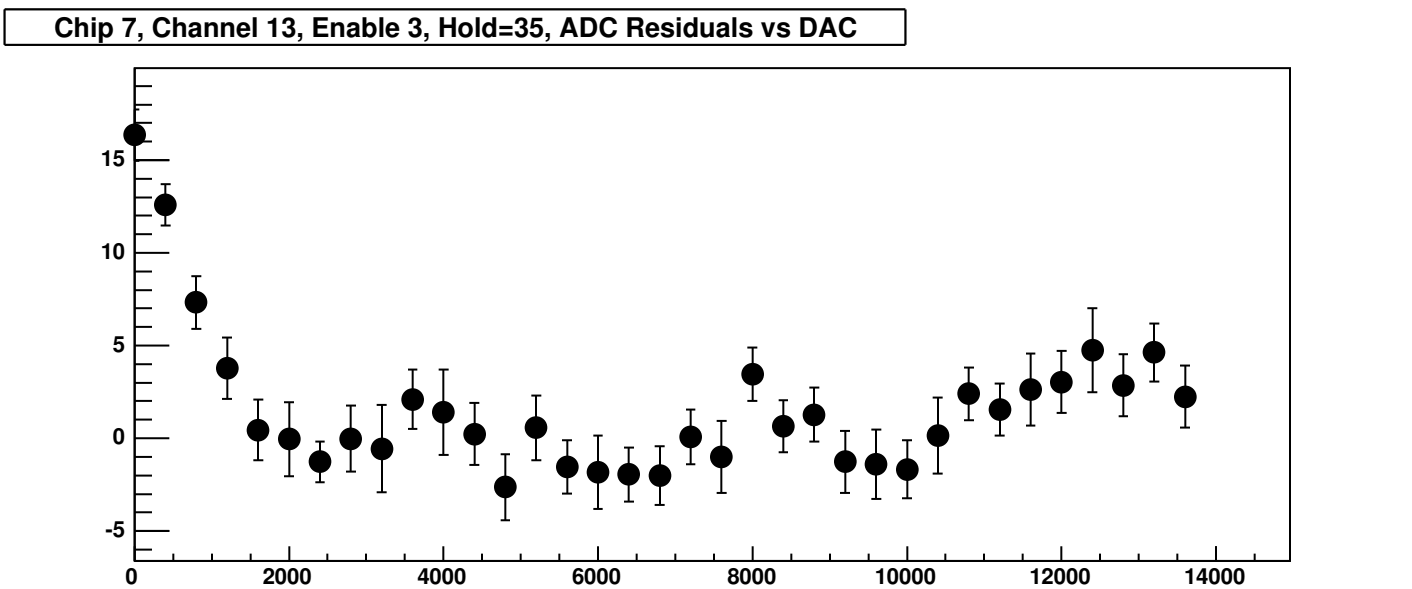


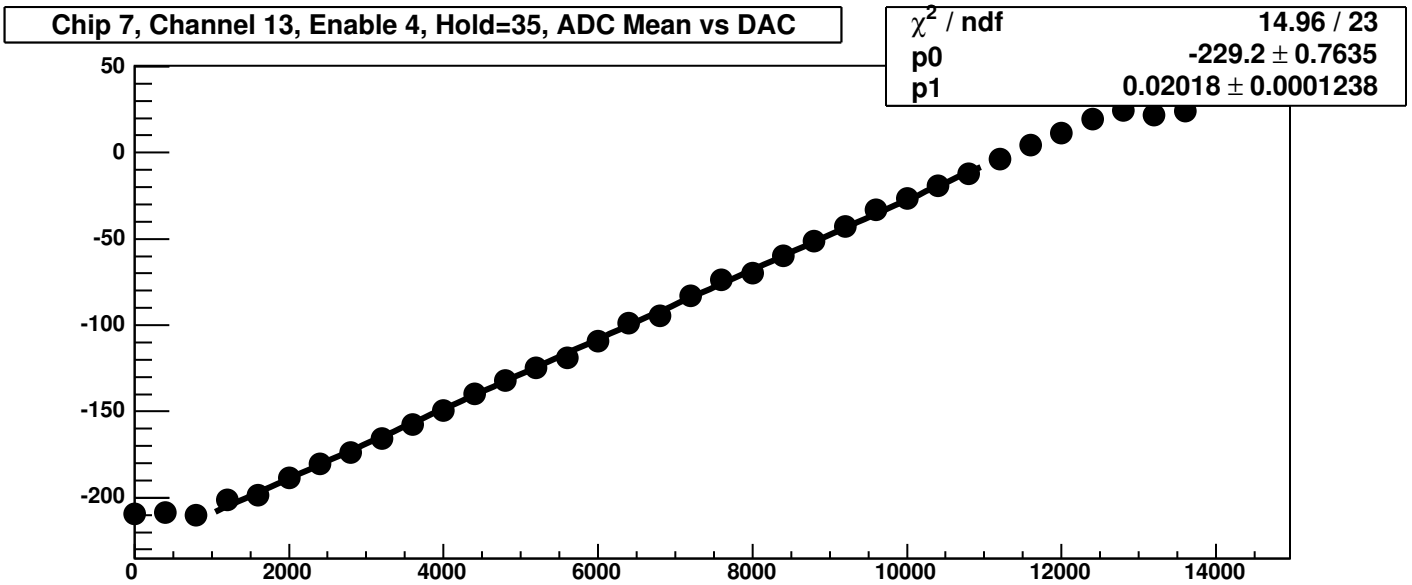
Chip 7, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

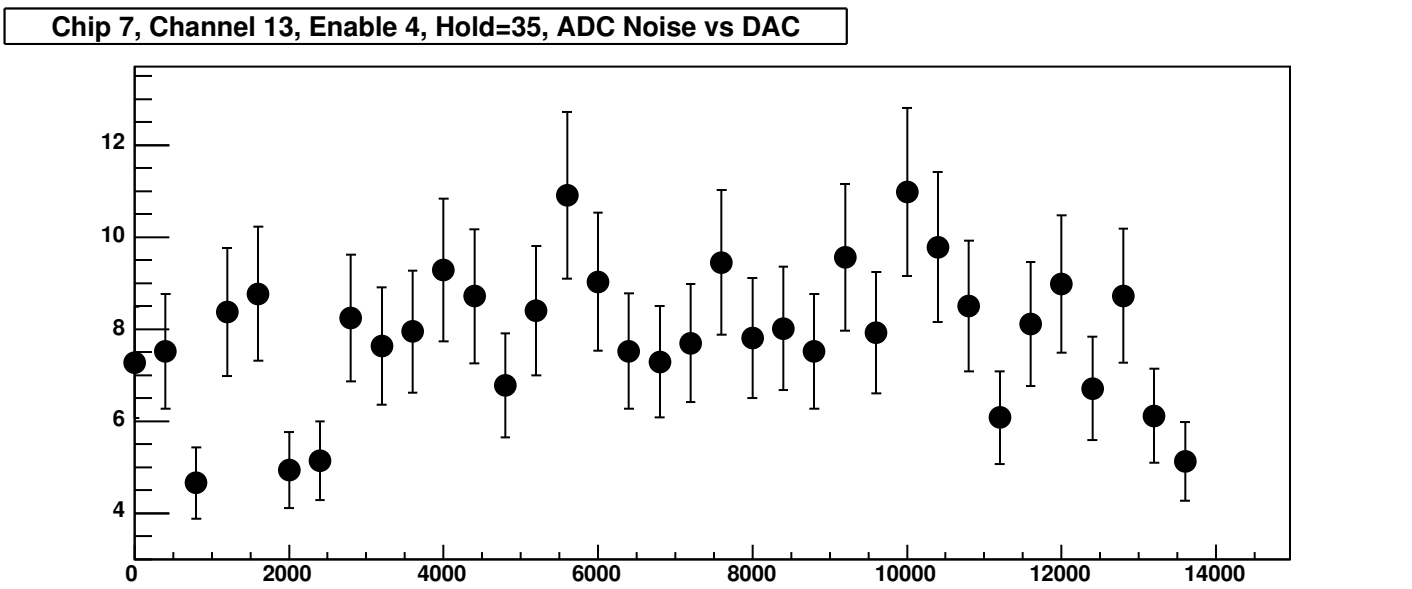


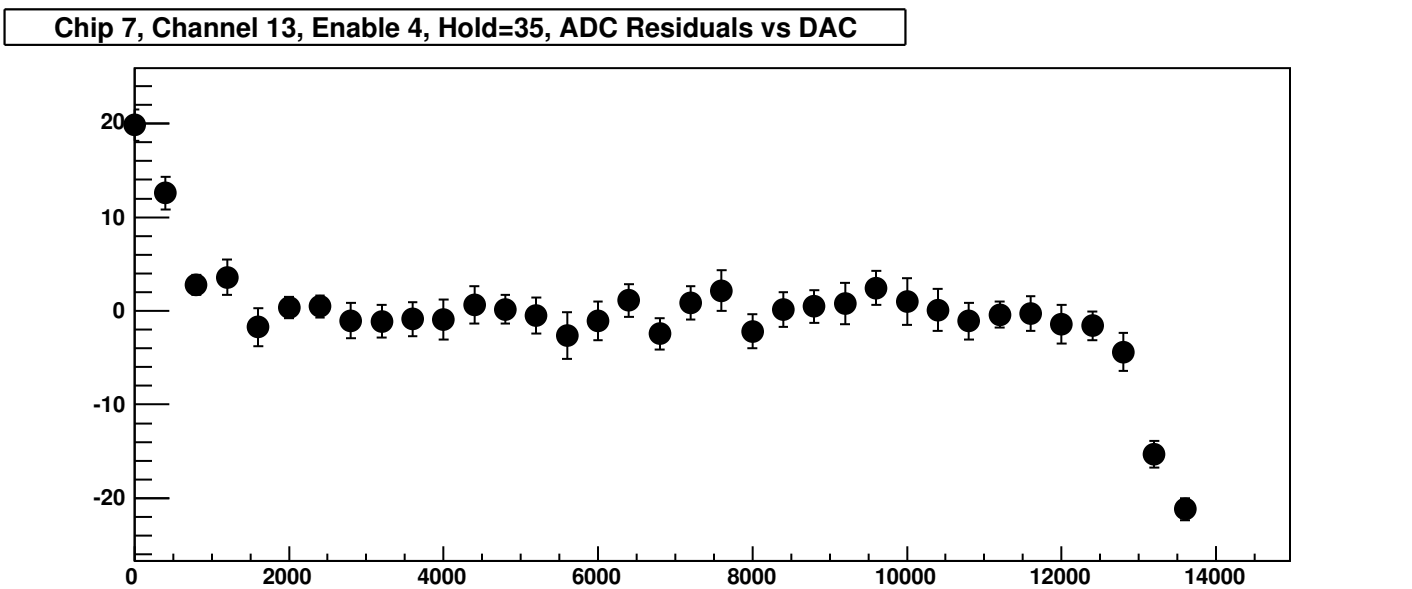

Chip 7, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.68 / 23
p0 -229.1 ± 0.7531
p1 0.01359 ± 0.000112
-40
-60
-80
-100
-120
-140
-160
-180
-200
-220
0
2000
4000
6000
8000
10000
12000
14000

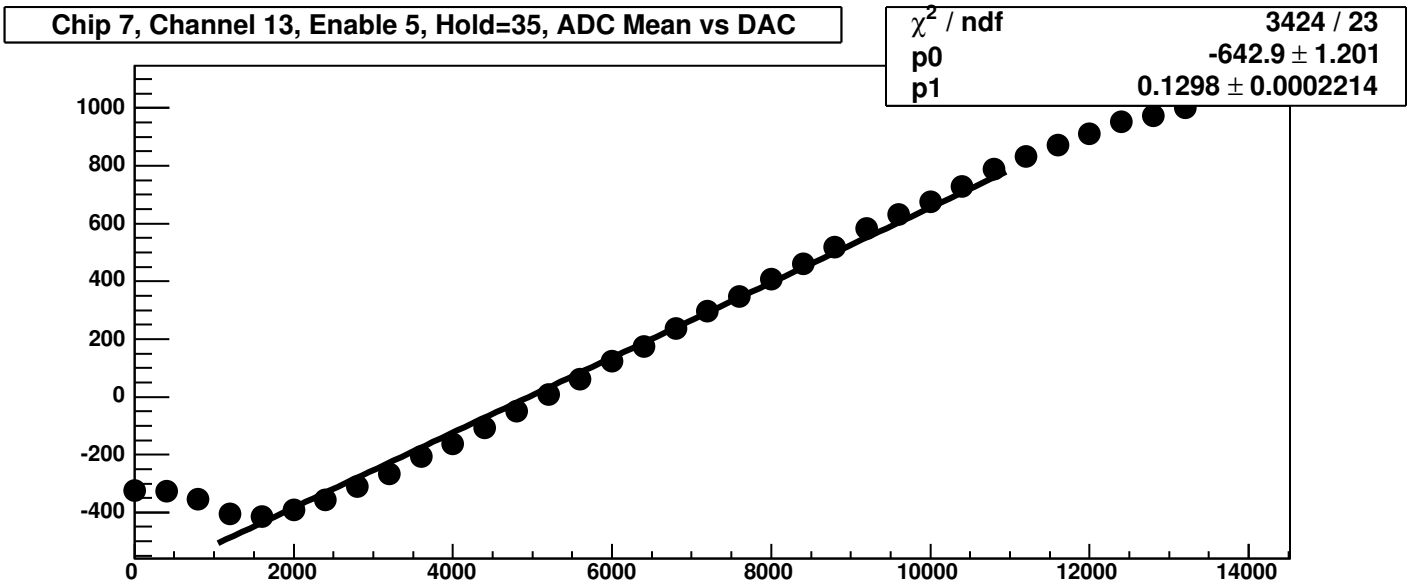

Chip 7, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

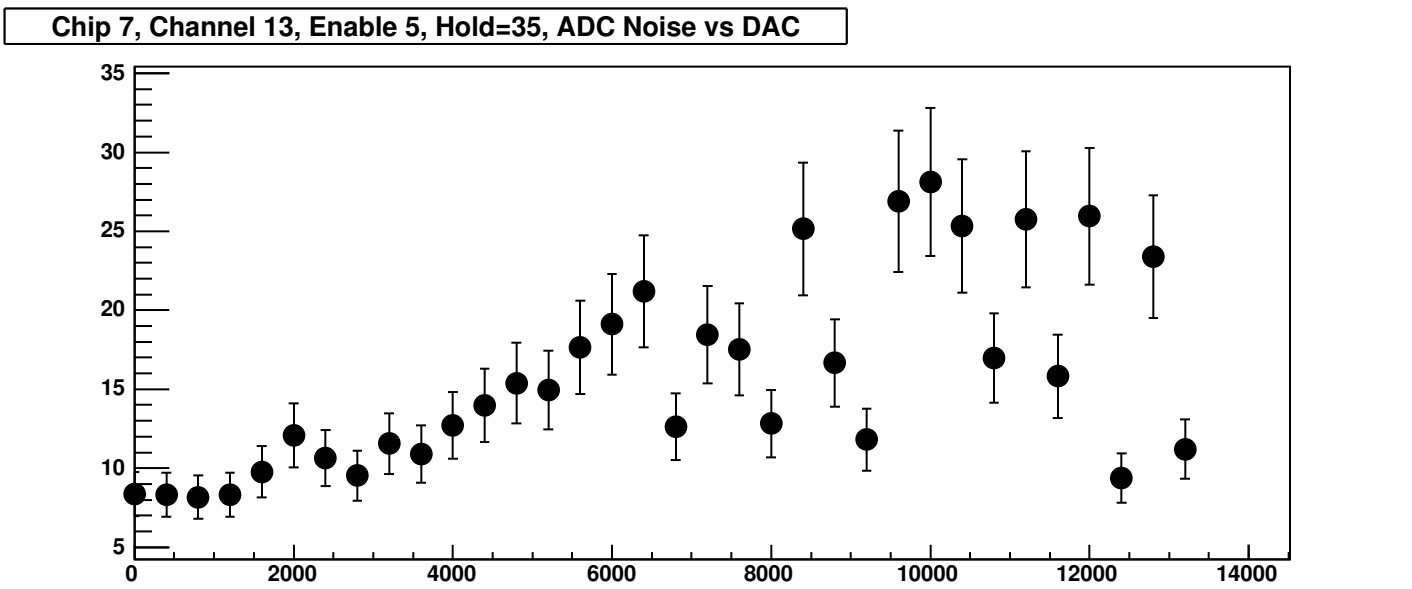

Chip 7, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

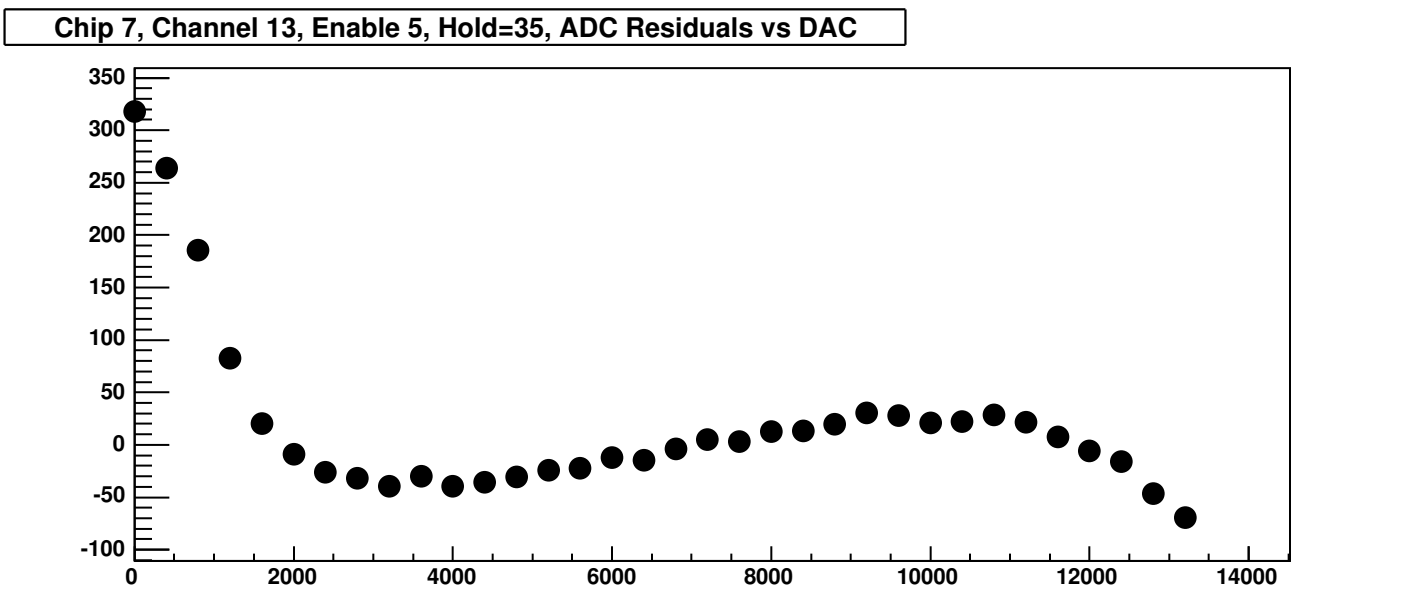

Chip 7, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 14.96 / 23
p0 -229.2 ± 0.7635
p1 0.02018 ± 0.0001238
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

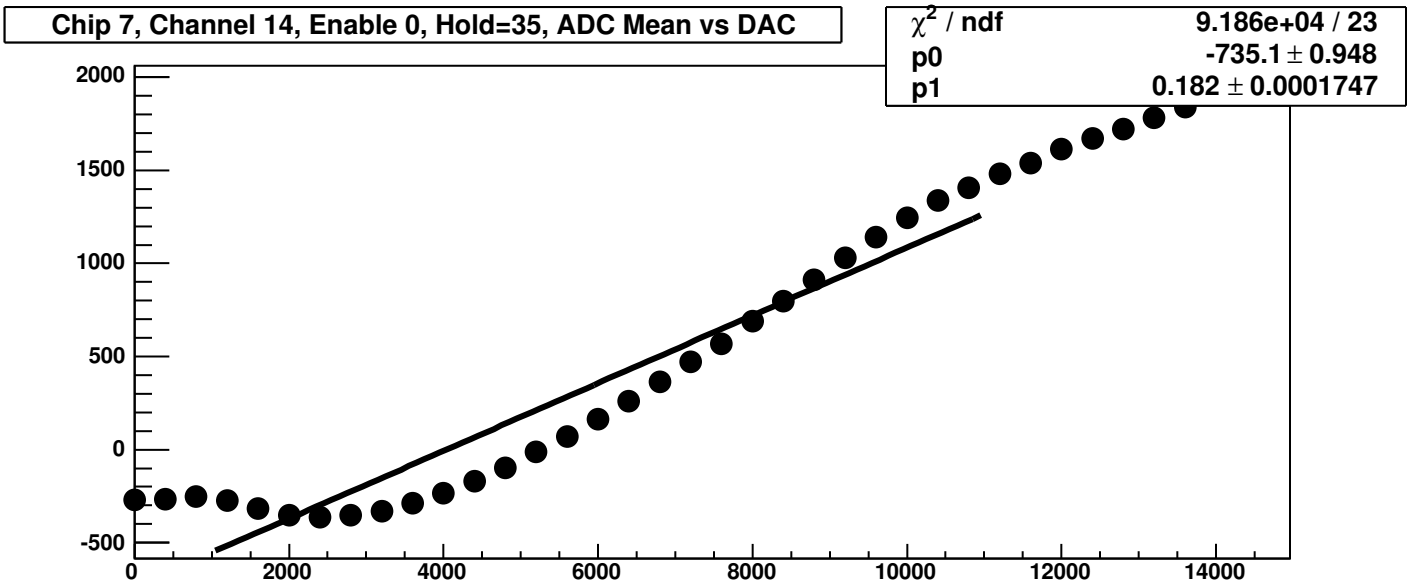

Chip 7, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

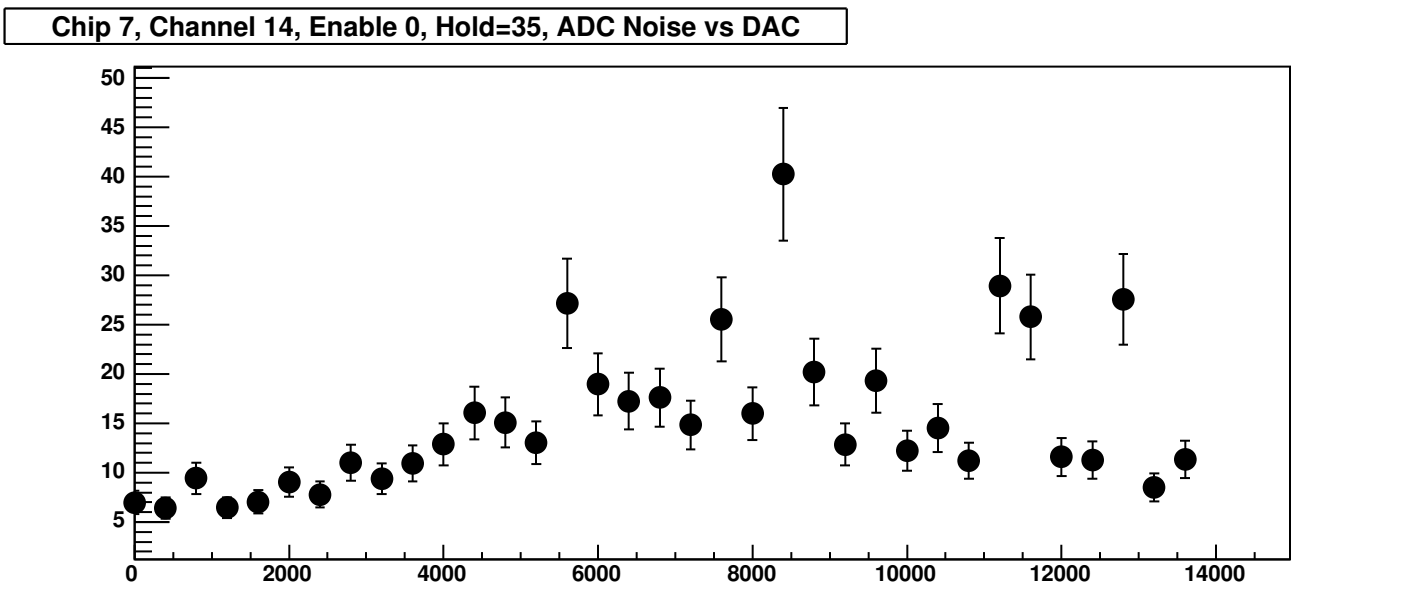

Chip 7, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

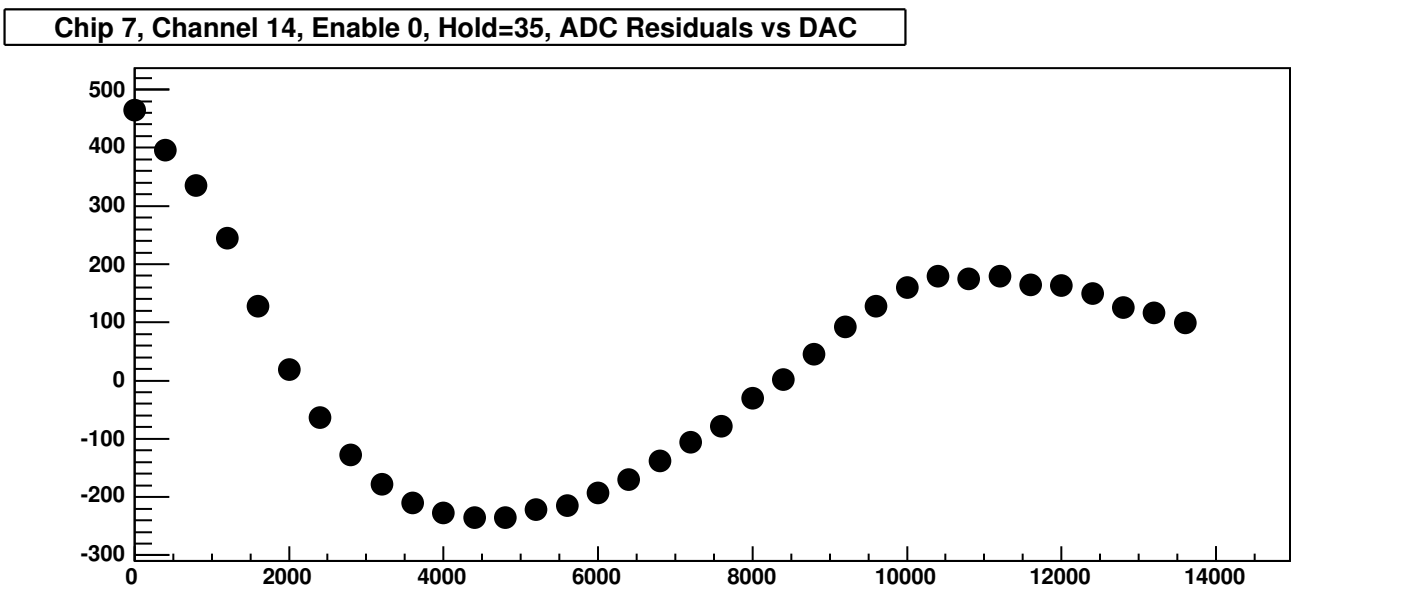

Chip 7, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3424 / 23
p0 -642.9 ± 1.201
p1 0.1298 ± 0.0002214
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

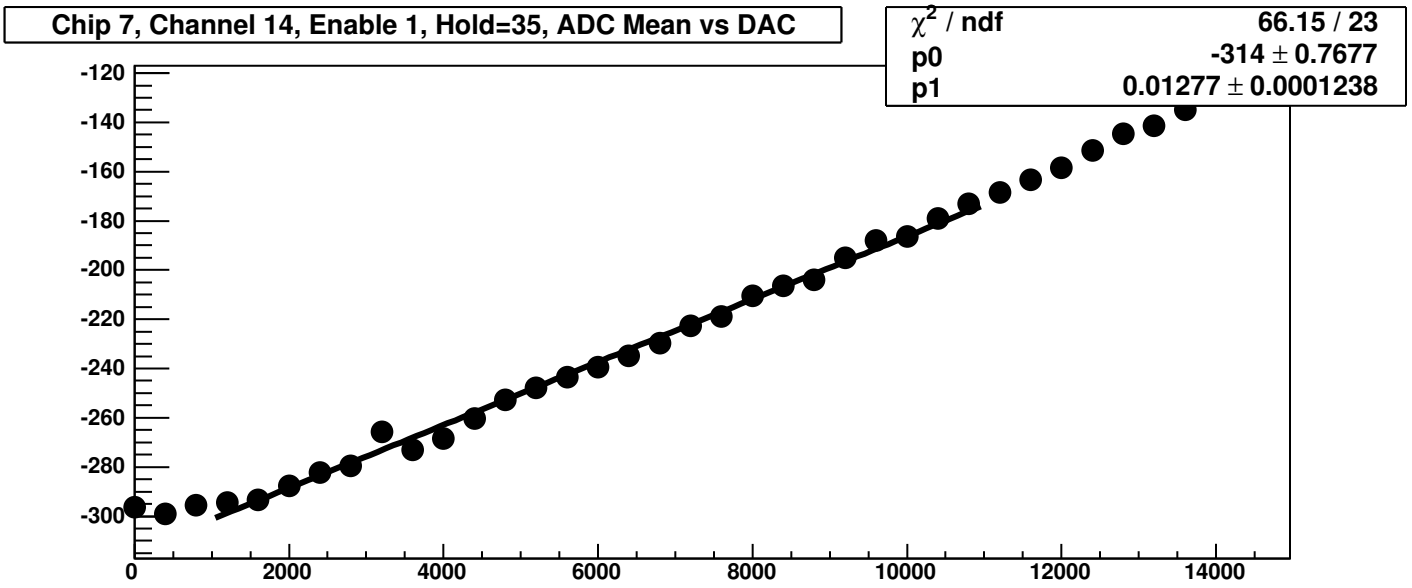

Chip 7, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

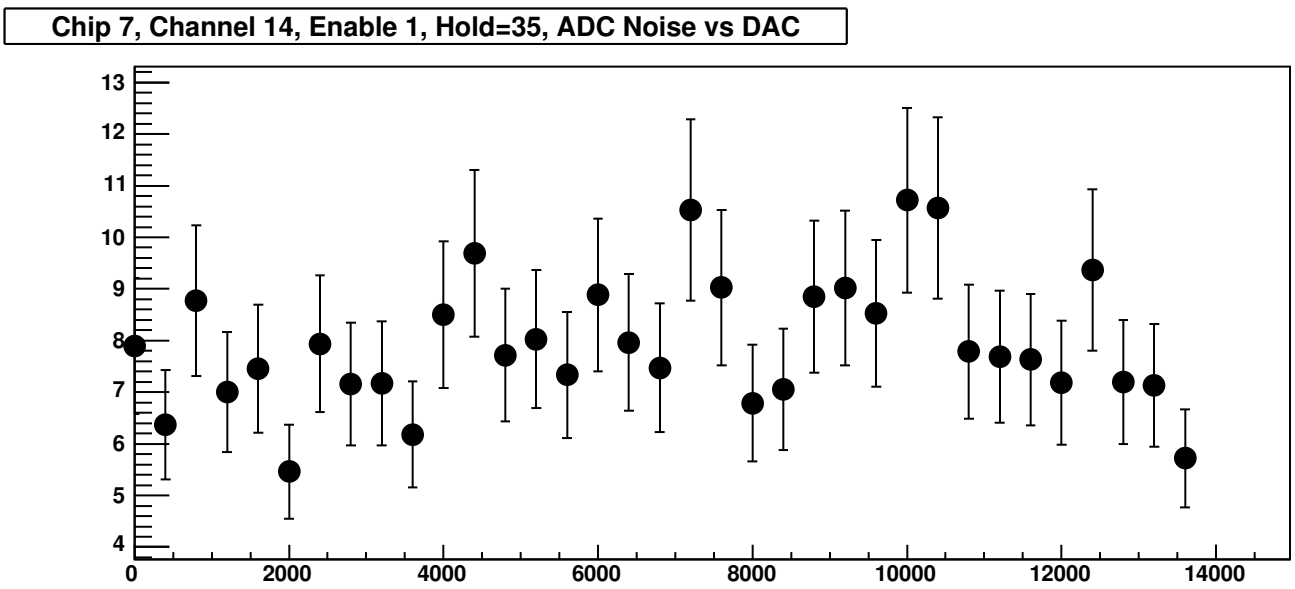

Chip 7, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

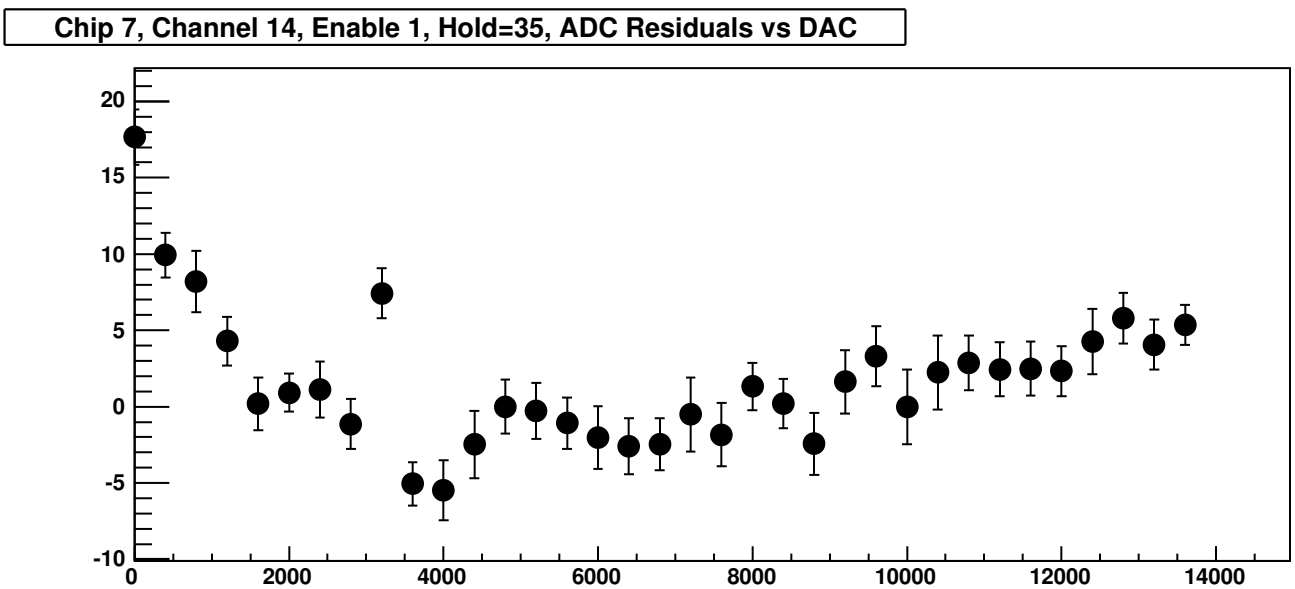

Chip 7, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 9.186e+04 / 23
p0 -735.1 ± 0.948
p1 0.182 ± 0.0001747
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

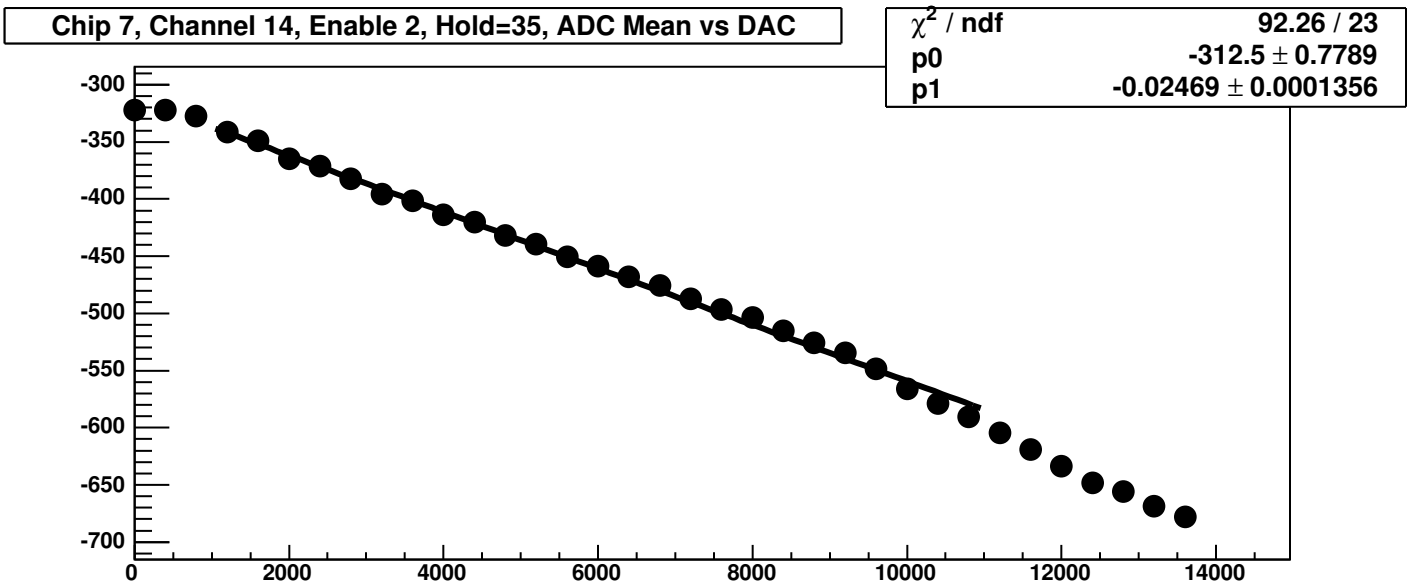

Chip 7, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

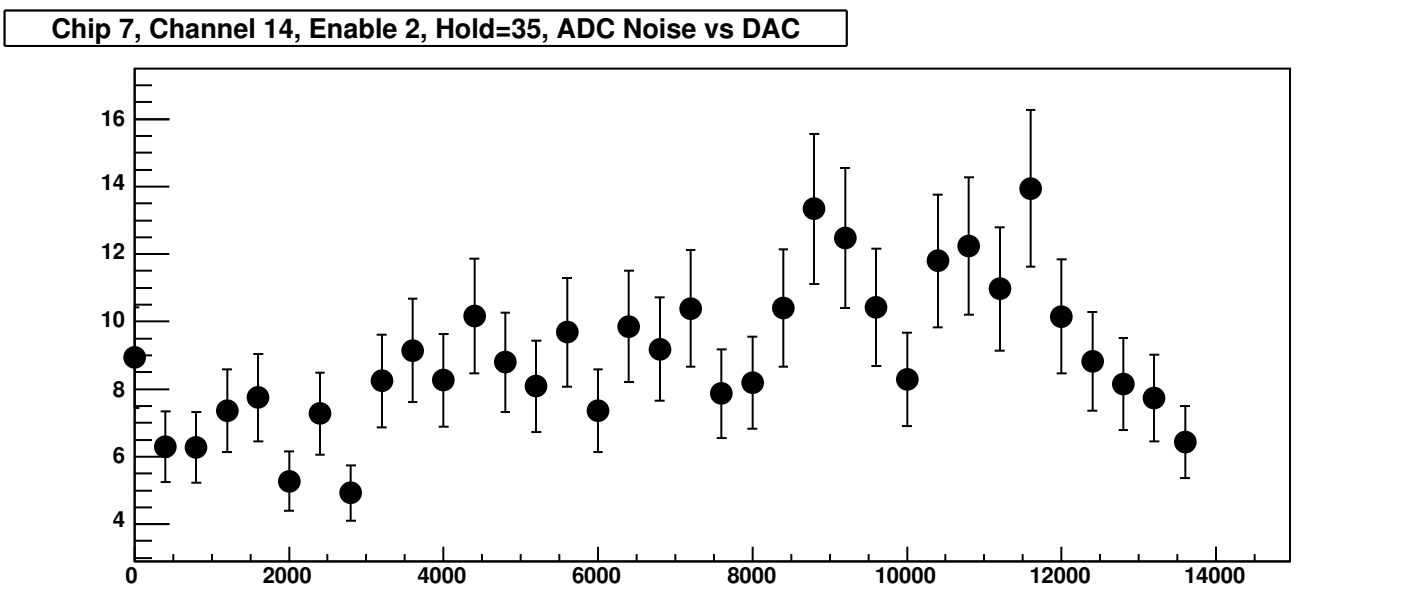

Chip 7, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

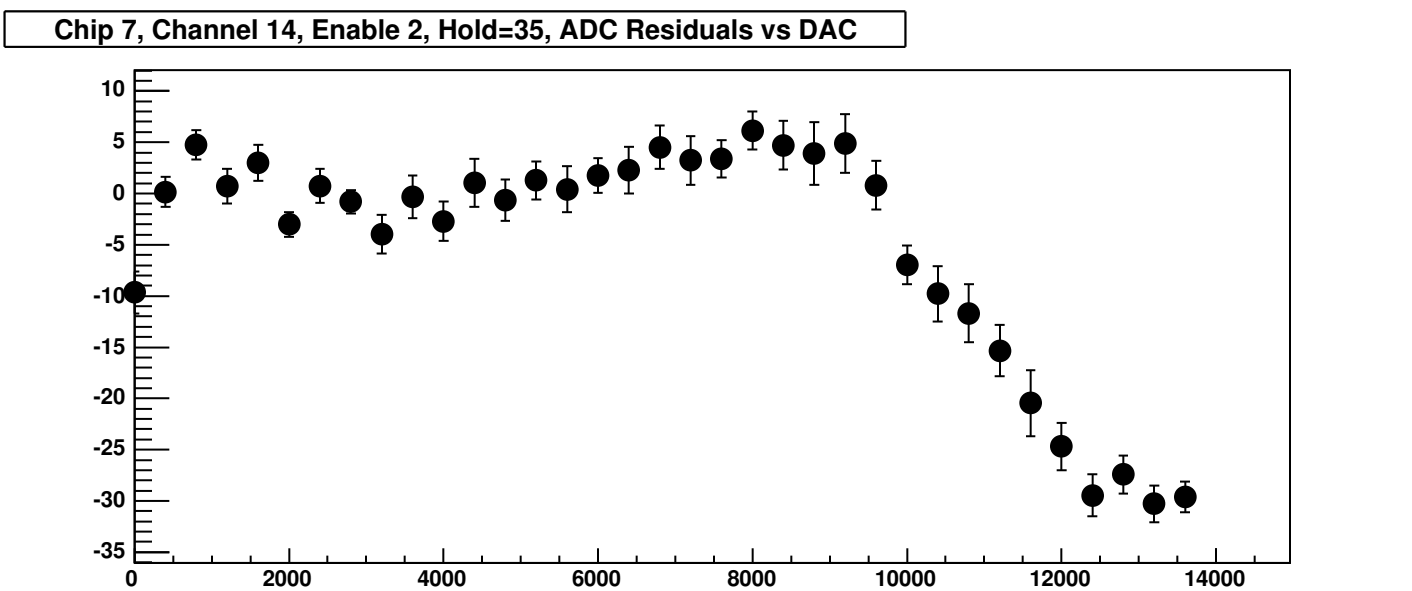

Chip 7, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 66.15 / 23
p0 -314 ± 0.7677
p1 0.01277 ± 0.0001238
-120
-140
-160
-180
-200
-220
-240
-260
-280
-300
0
2000
4000
6000
8000
10000
12000
14000

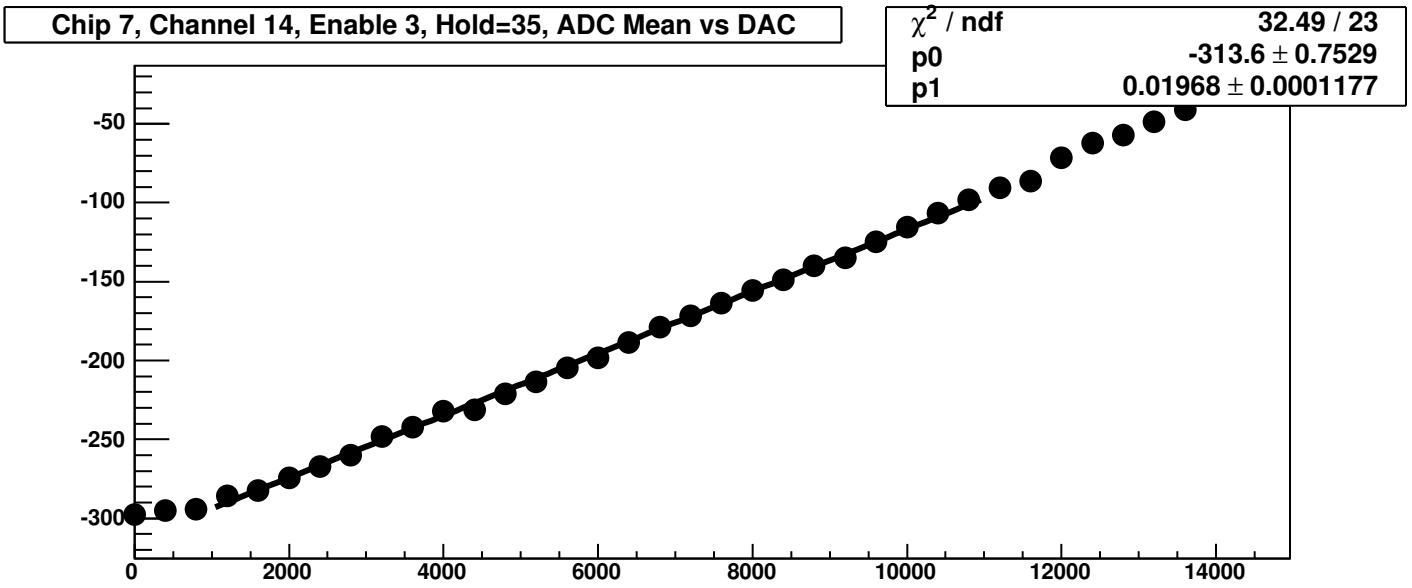

Chip 7, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

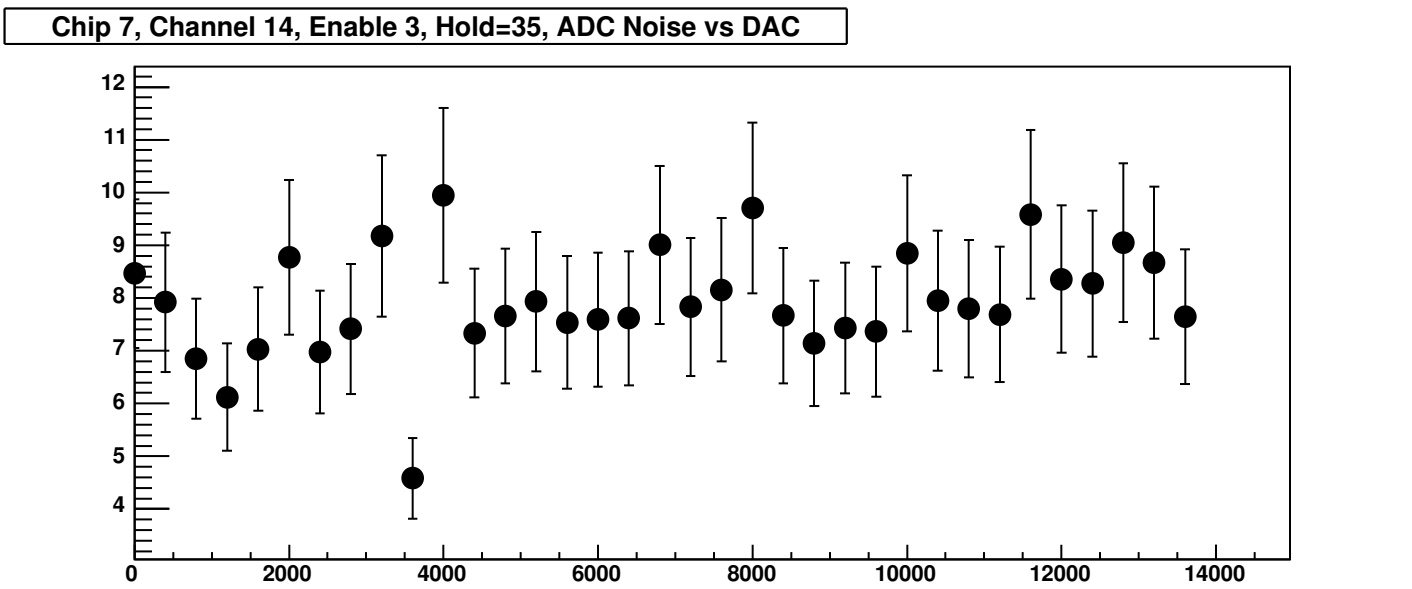

Chip 7, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

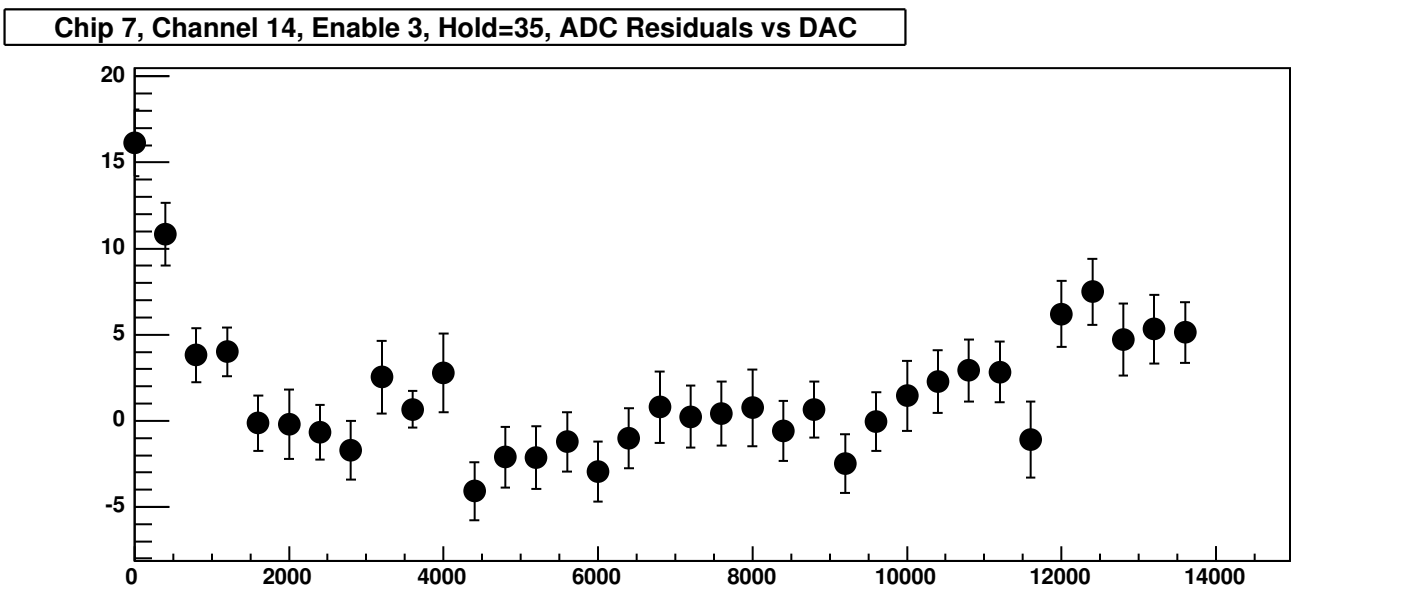

Chip 7, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 92.26 / 23
p0 -312.5 ± 0.7789
p1 -0.02469 ± 0.0001356
-300
-350
-400
-450
-500
-550
-600
-650
-700
0
2000
4000
6000
8000
10000
12000
14000

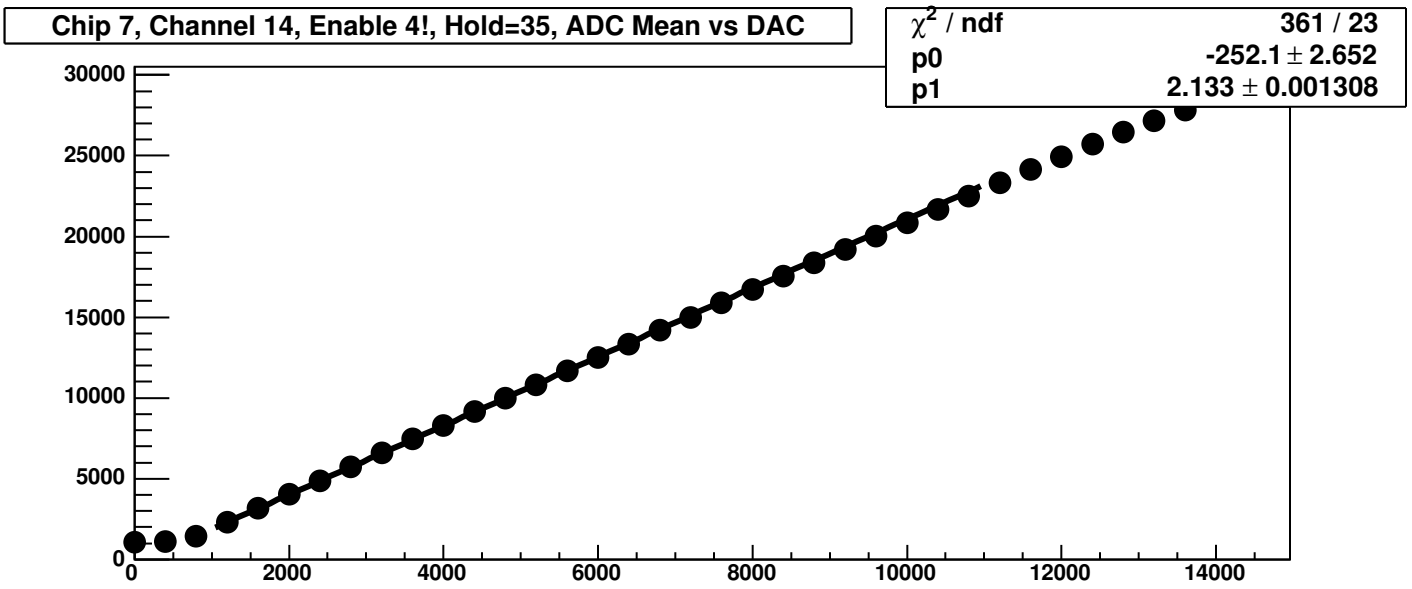

Chip 7, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

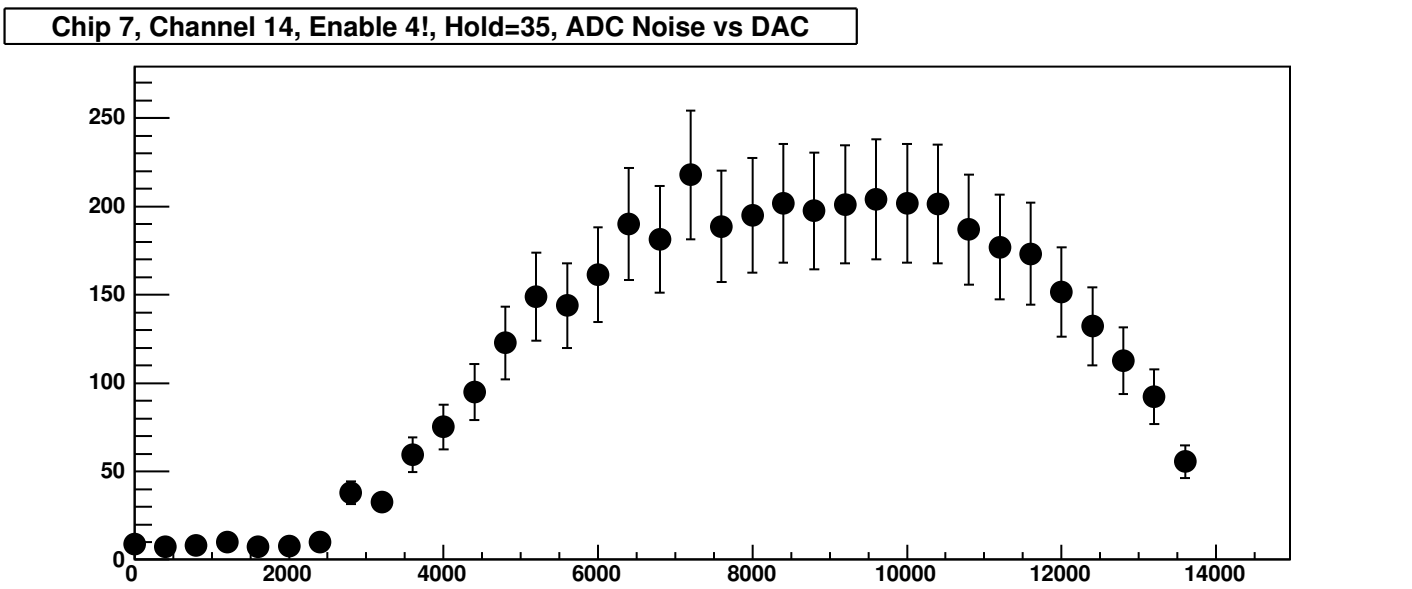

Chip 7, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
-35
0
2000
4000
6000
8000
10000
12000
14000

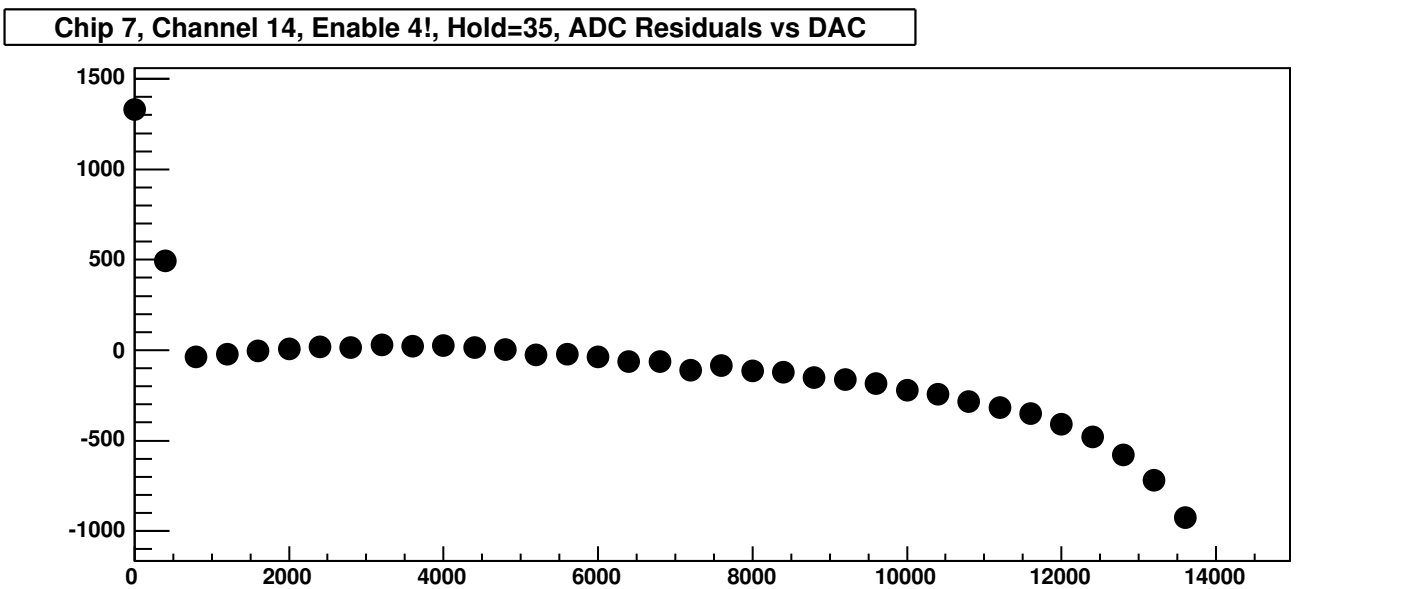

Chip 7, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 32.49 / 23
p0 -313.6 ± 0.7529
p1 0.01968 ± 0.0001177
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

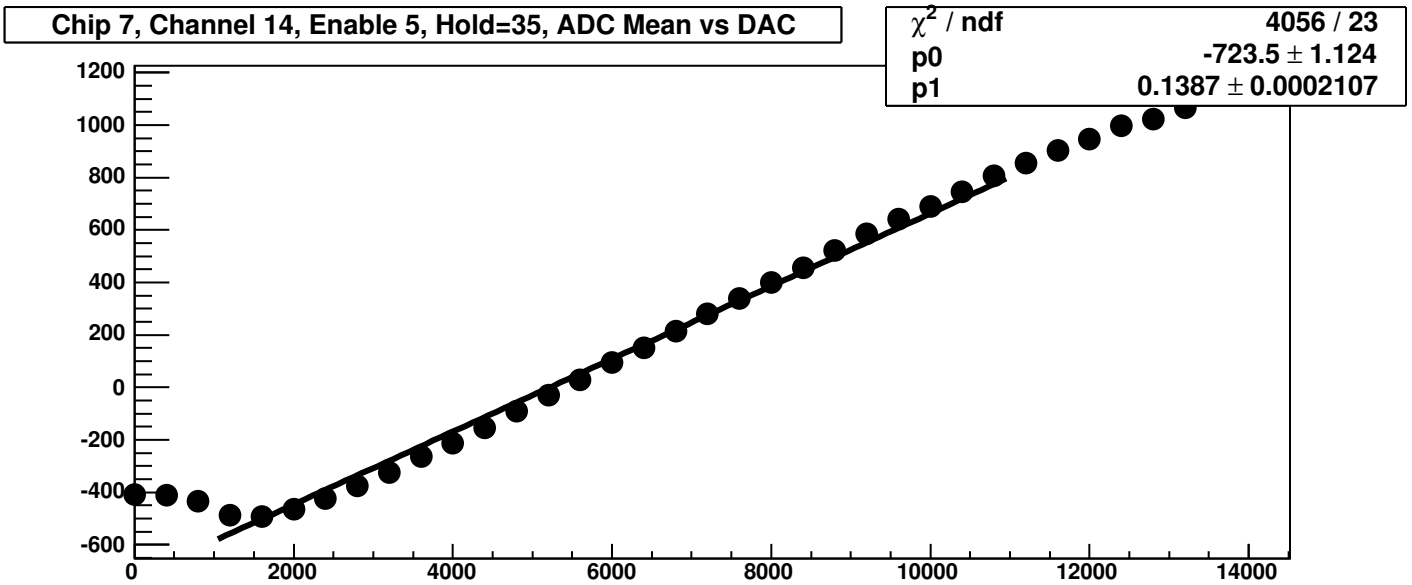

Chip 7, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

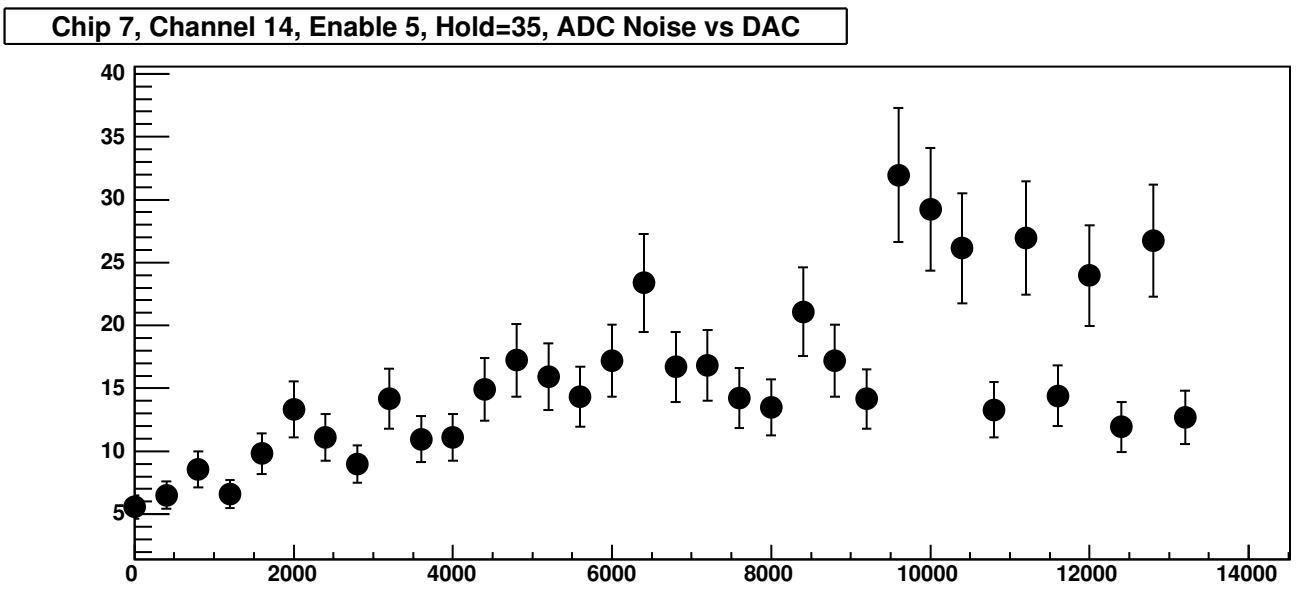

Chip 7, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

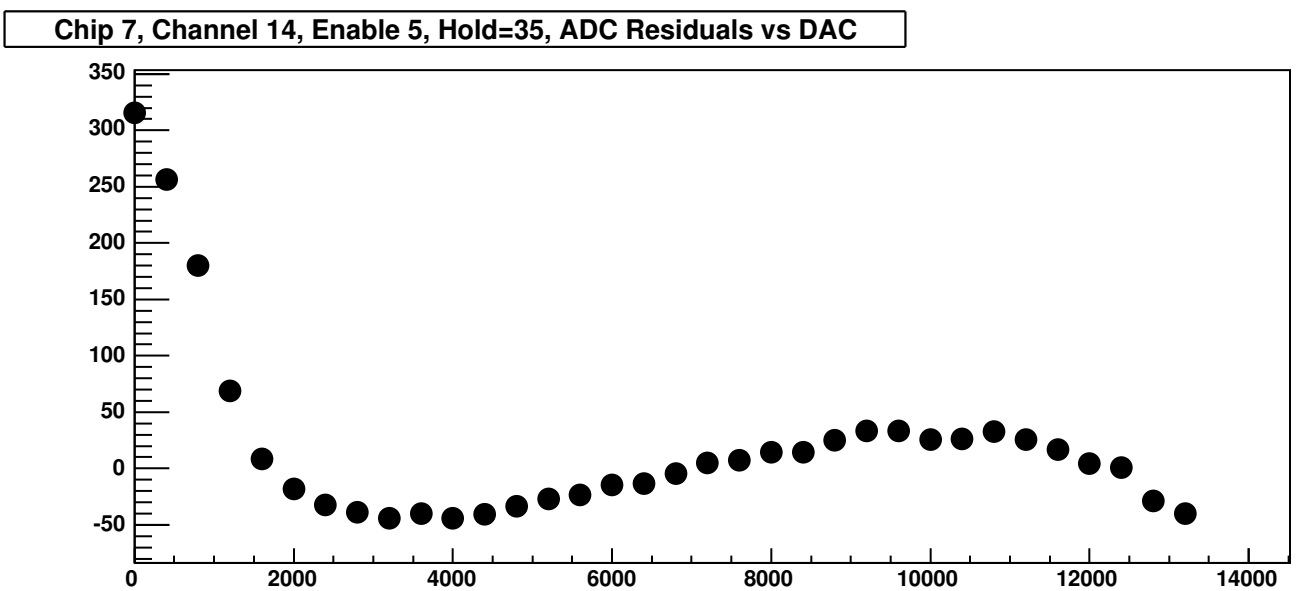

Chip 7, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 361 / 23
p0 -252.1 ± 2.652
p1 2.133 ± 0.001308
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

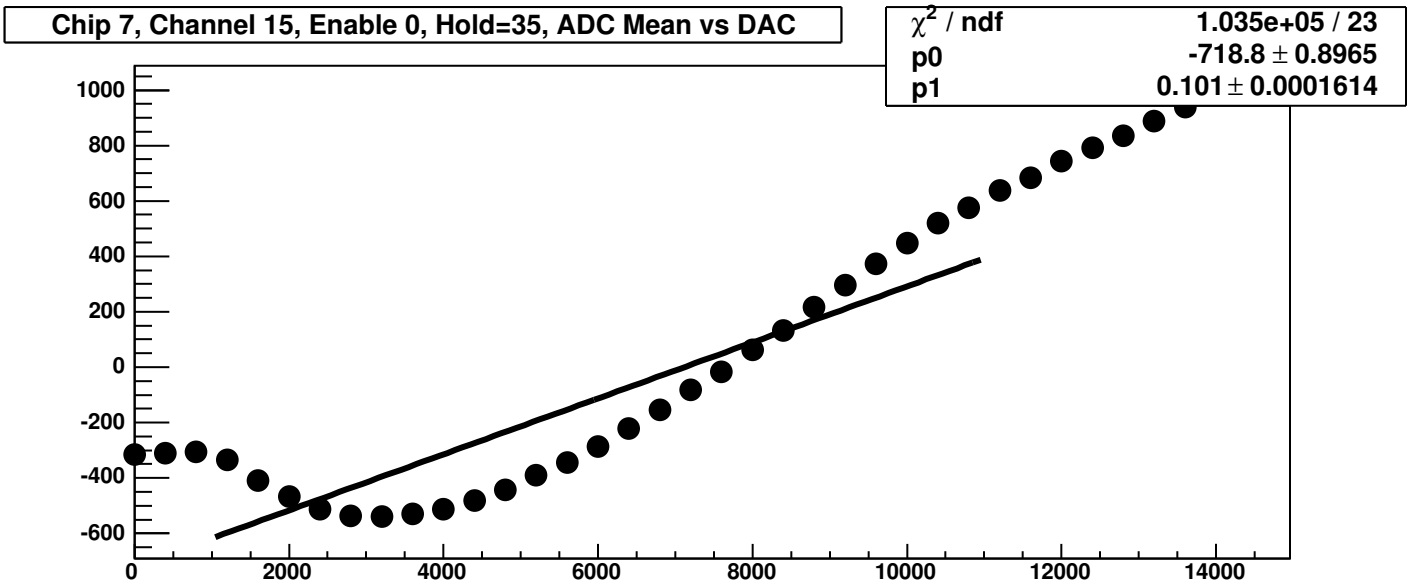

Chip 7, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

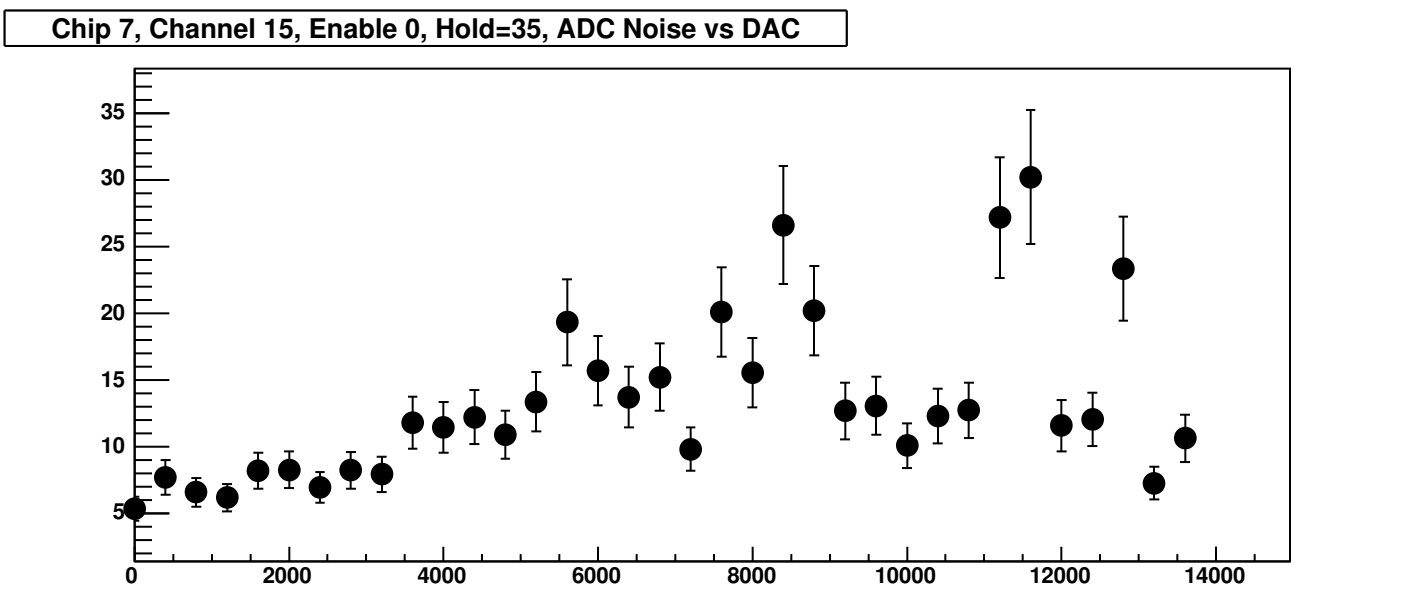

Chip 7, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

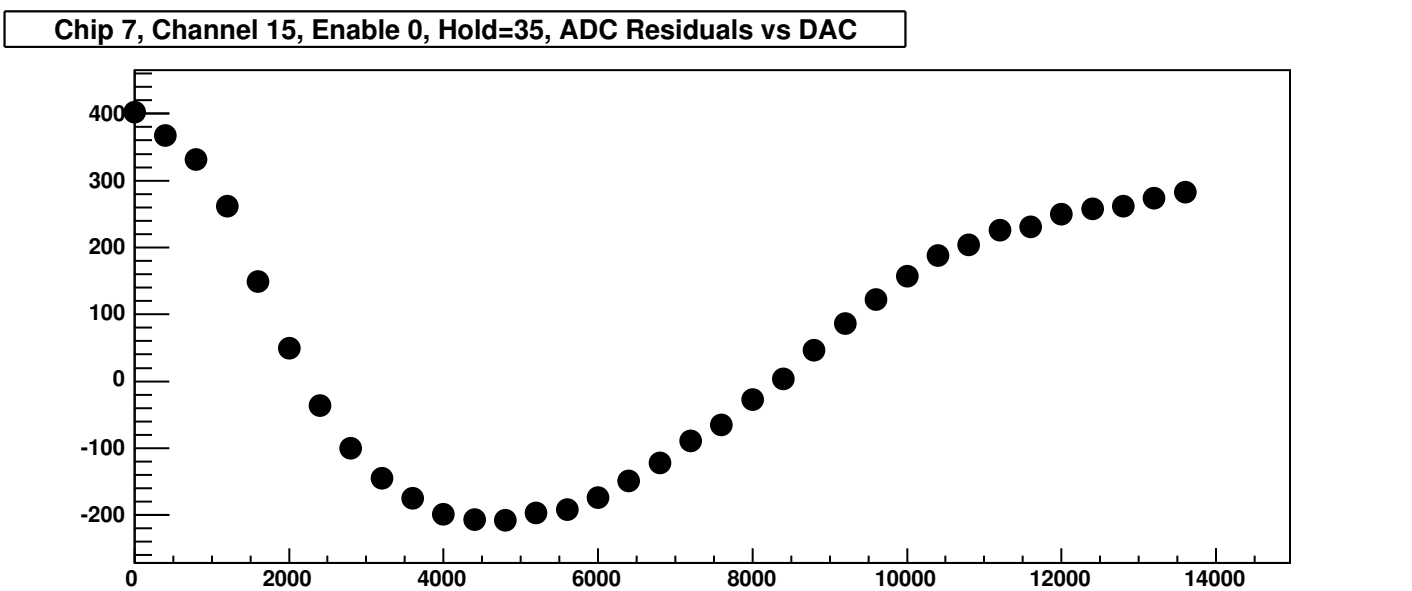

Chip 7, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4056 / 23
p0 -723.5 ± 1.124
p1 0.1387 ± 0.0002107
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

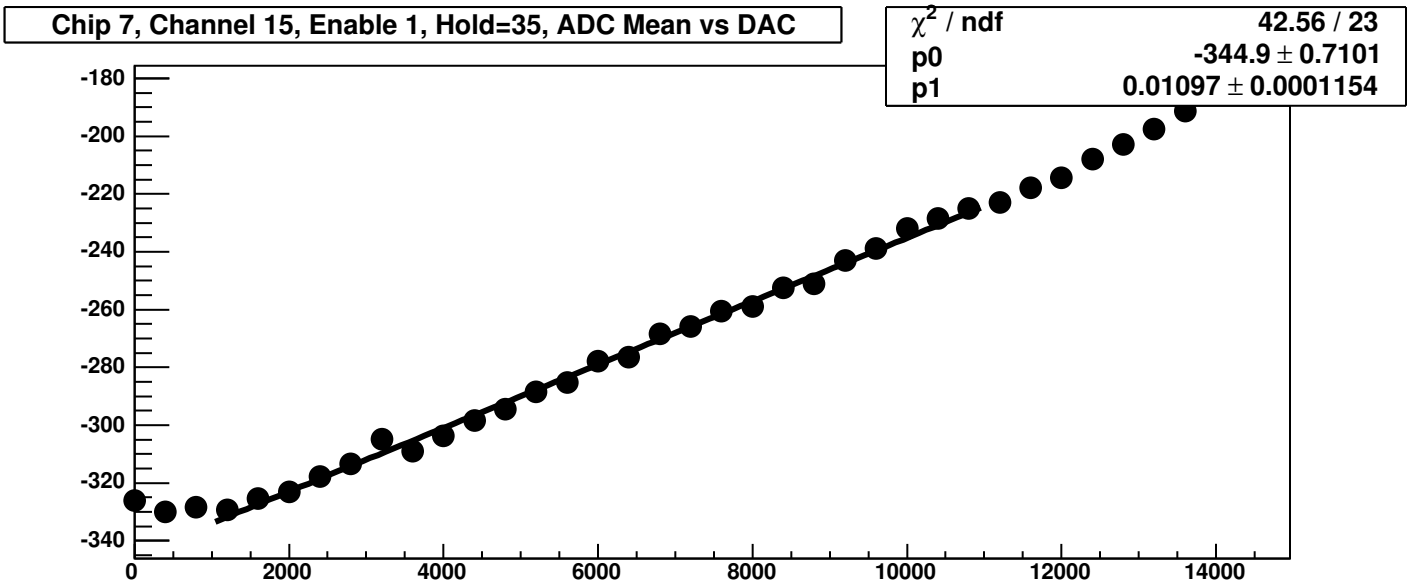

Chip 7, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

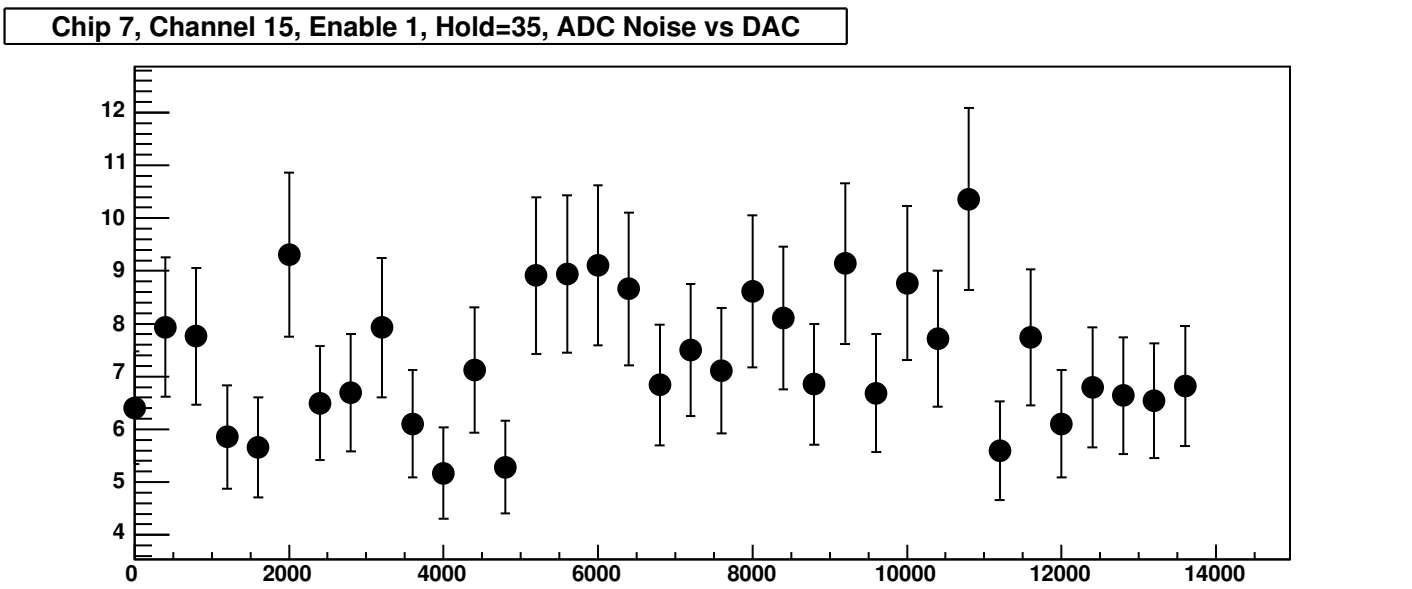

Chip 7, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

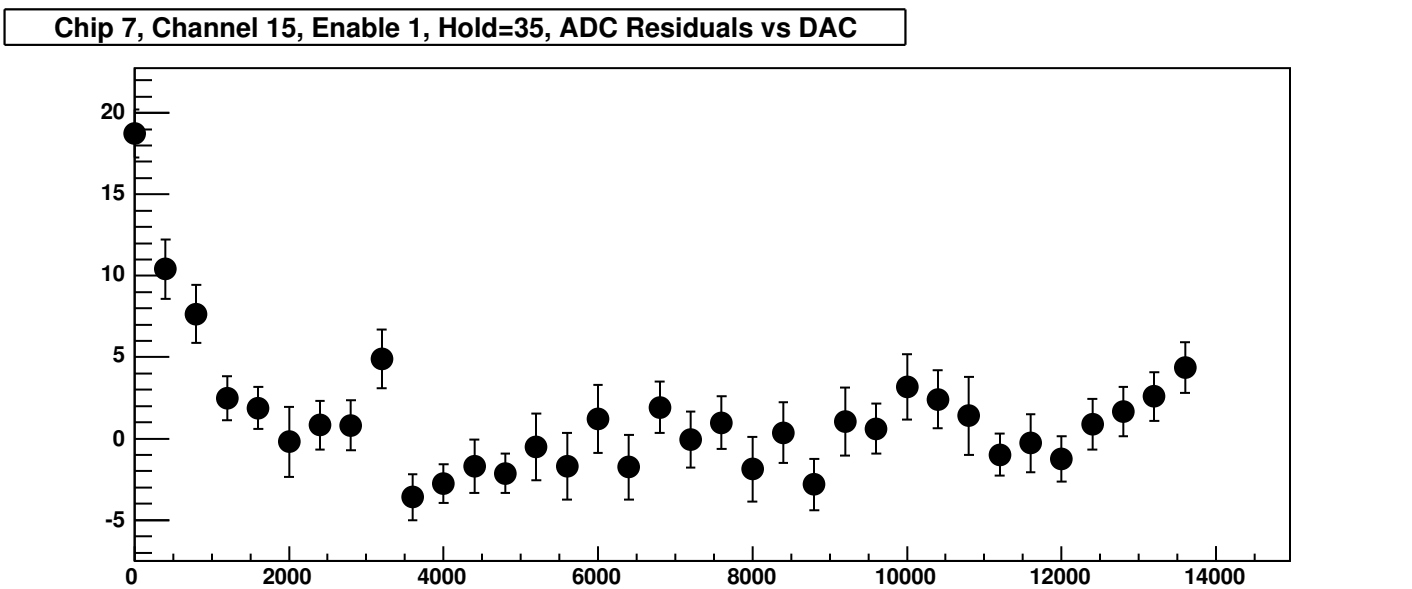

Chip 7, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.035e+05 / 23
p0 -718.8 ± 0.8965
p1 0.101 ± 0.0001614
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

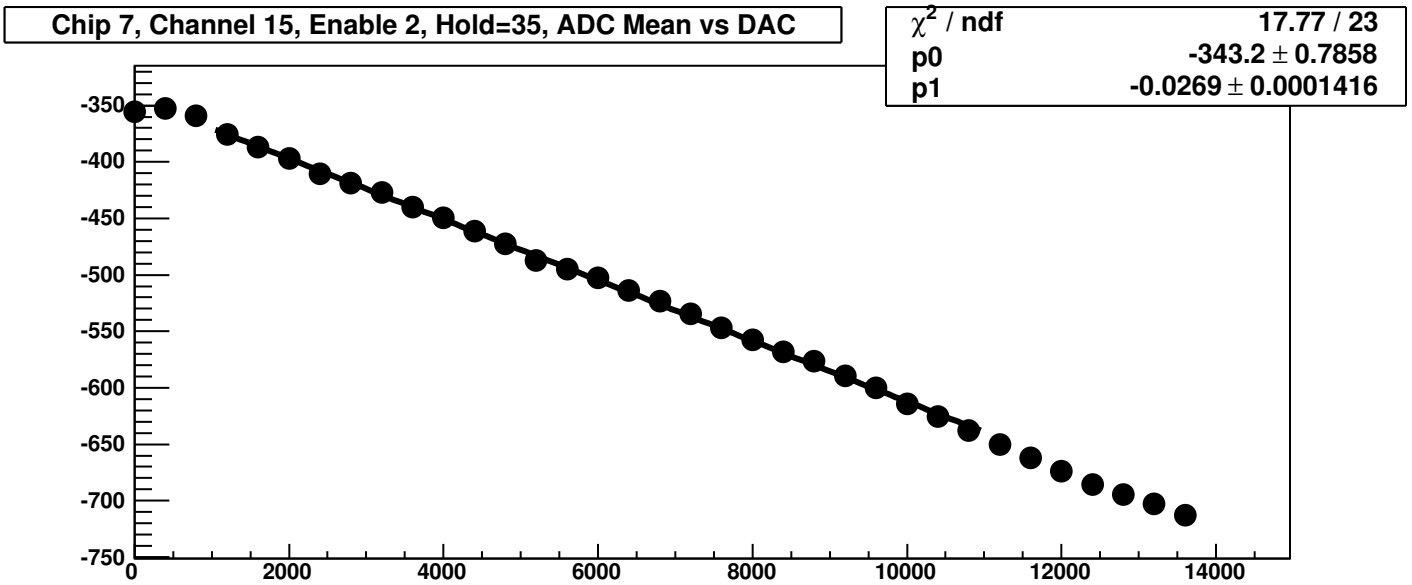

Chip 7, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

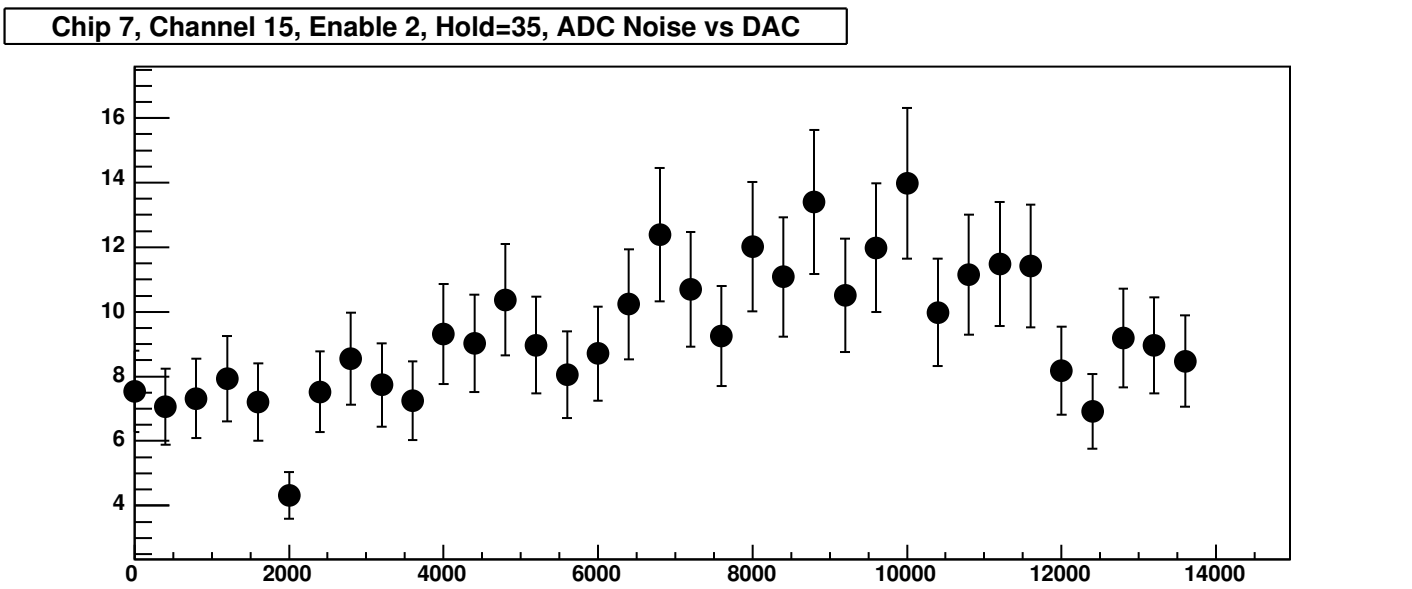

Chip 7, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

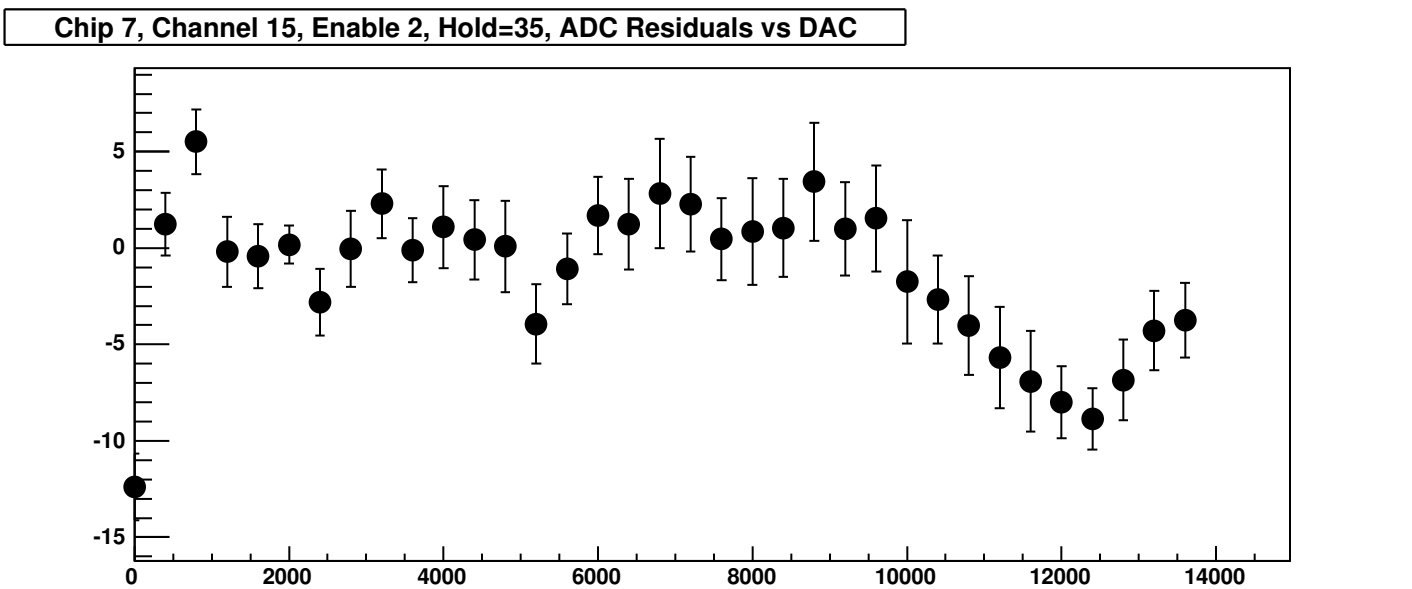

Chip 7, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 42.56 / 23
p0 -344.9 ± 0.7101
p1 0.01097 ± 0.0001154
-180
-200
-220
-240
-260
-280
-300
-320
-340
0
2000
4000
6000
8000
10000
12000
14000

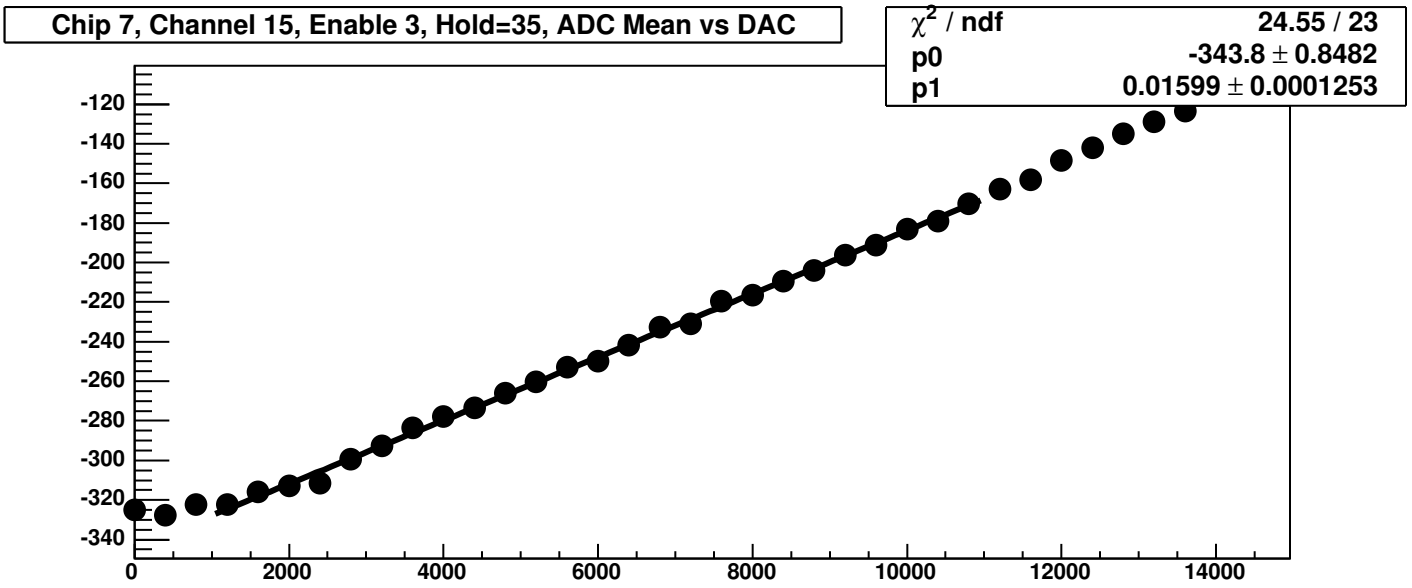

Chip 7, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

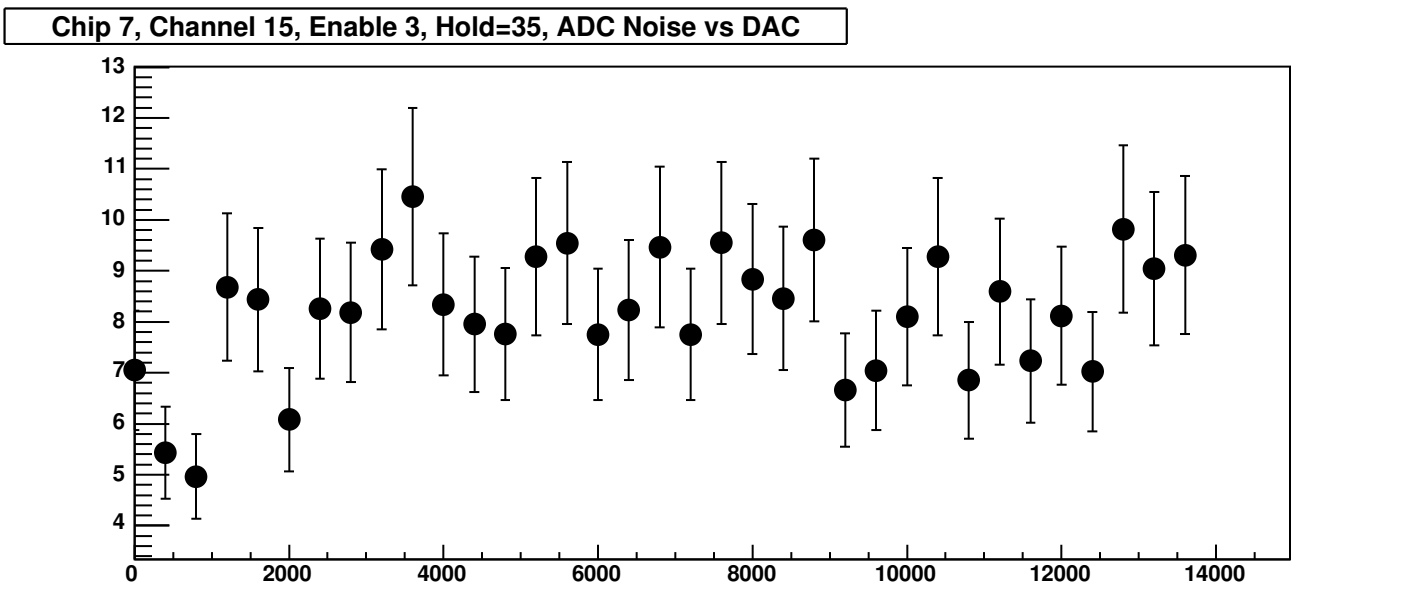

Chip 7, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

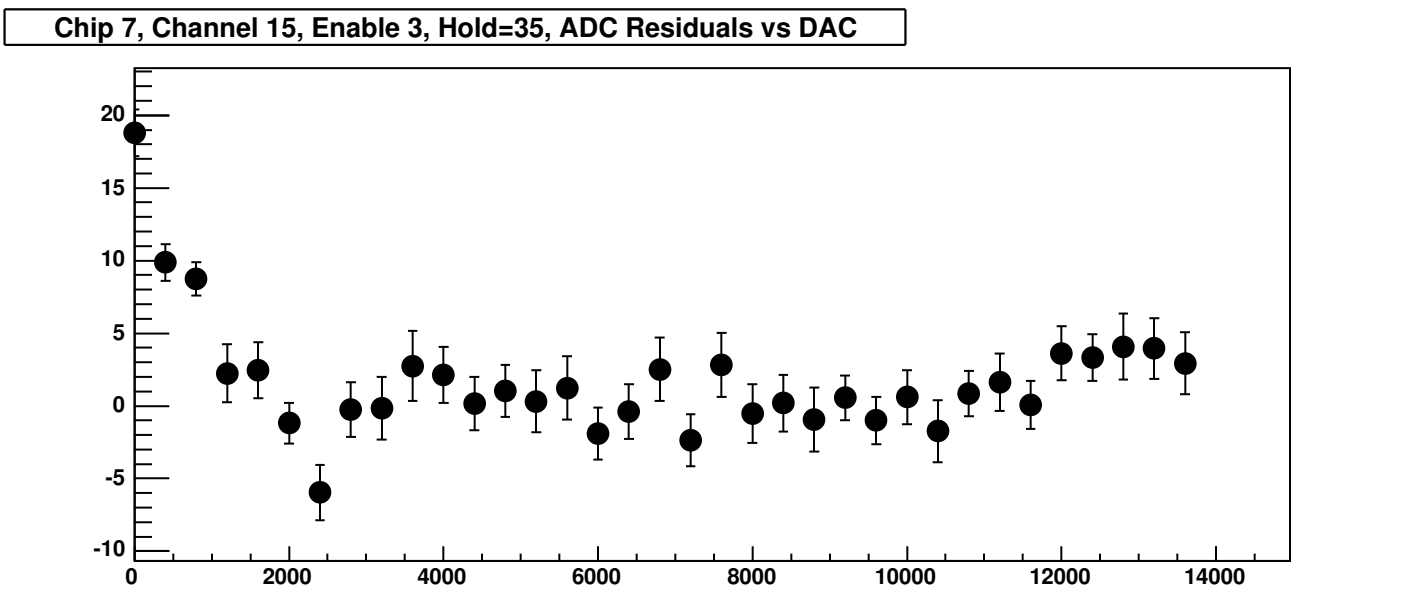

Chip 7, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 17.77 / 23
p0 -343.2 ± 0.7858
p1 -0.0269 ± 0.0001416
-350
-400
-450
-500
-550
-600
-650
-700
-750
0
2000
4000
6000
8000
10000
12000
14000

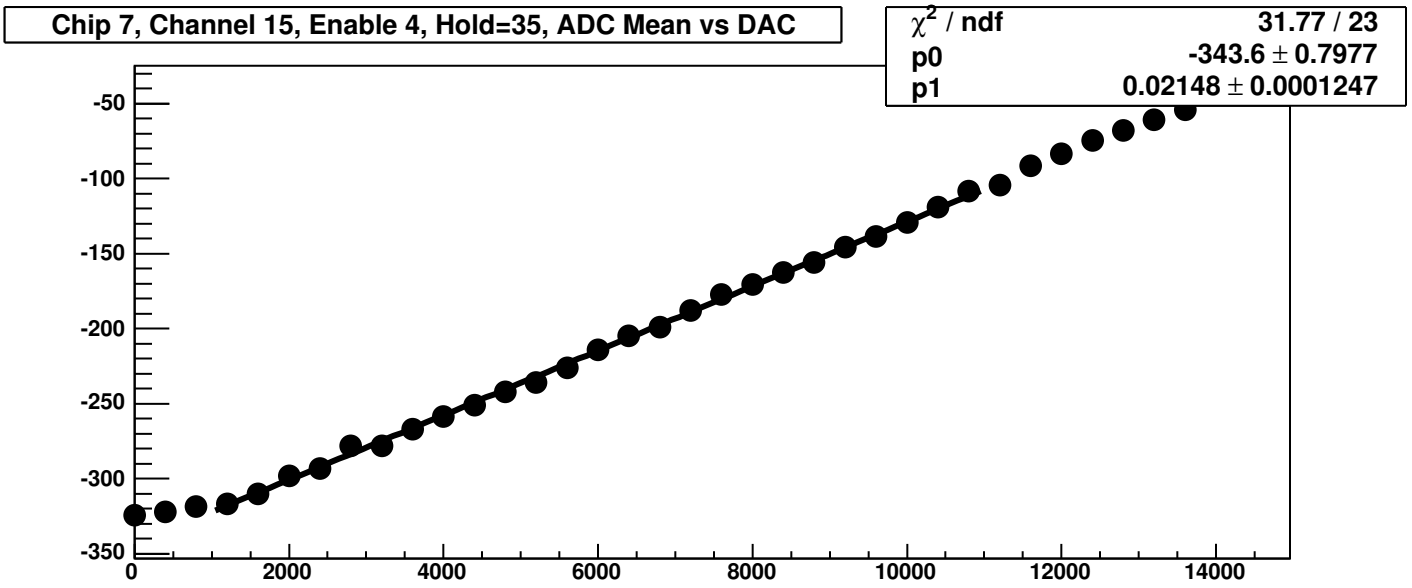

Chip 7, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

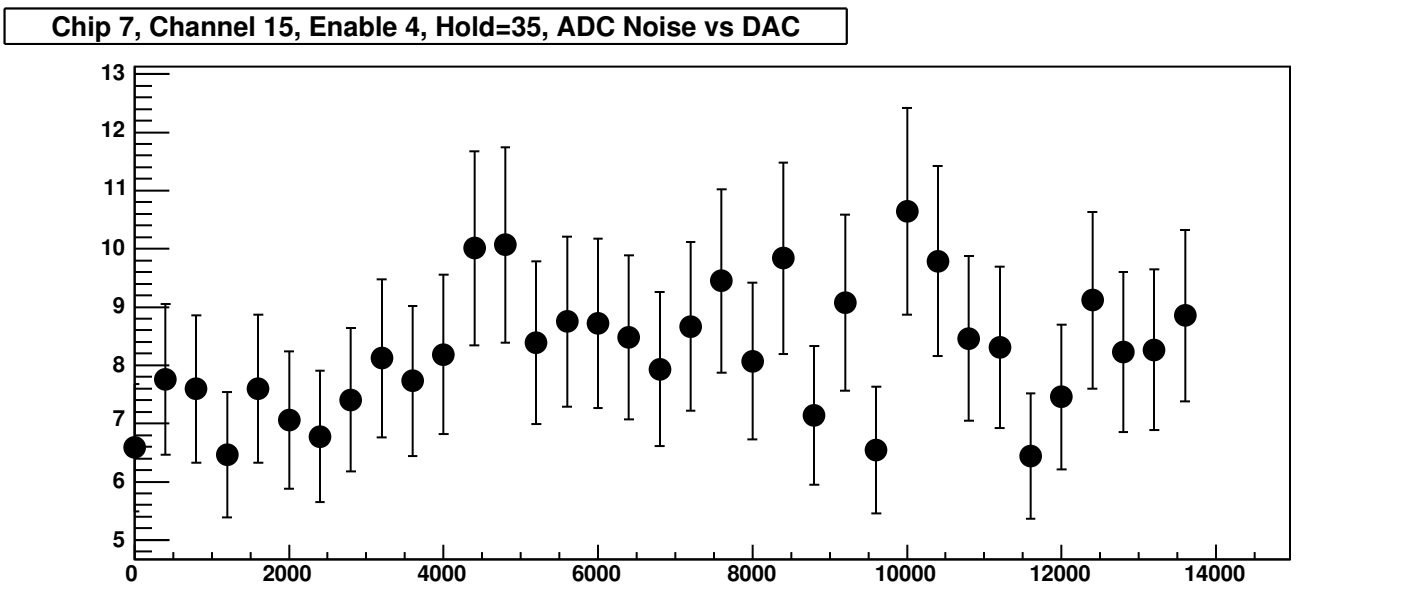

Chip 7, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

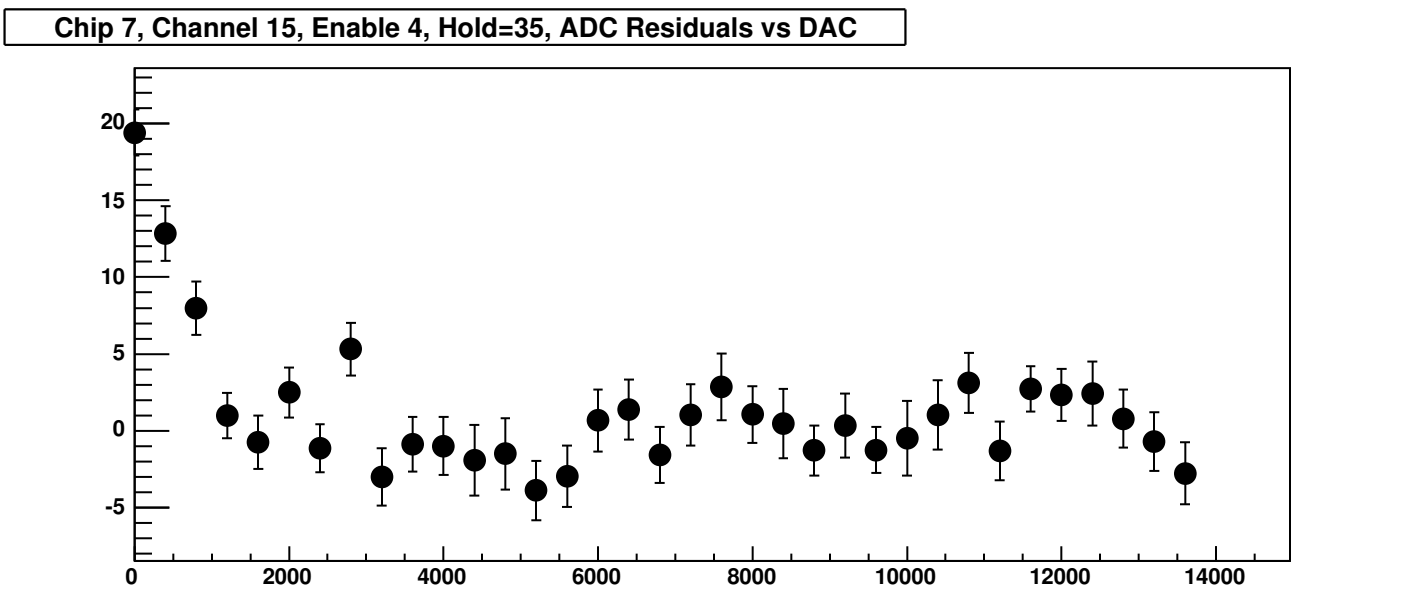

Chip 7, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.55 / 23
p0 -343.8 ± 0.8482
p1 0.01599 ± 0.0001253
-120
-140
-160
-180
-200
-220
-240
-260
-280
-300
-320
-340
0
2000
4000
6000
8000
10000
12000
14000

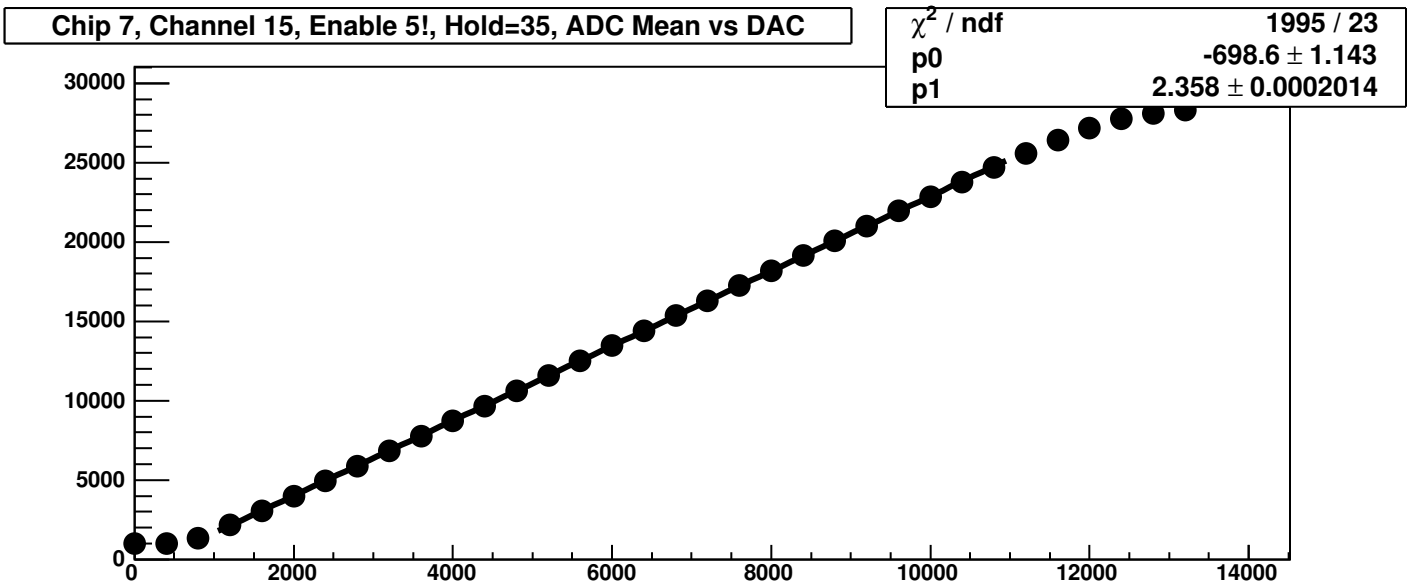

Chip 7, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

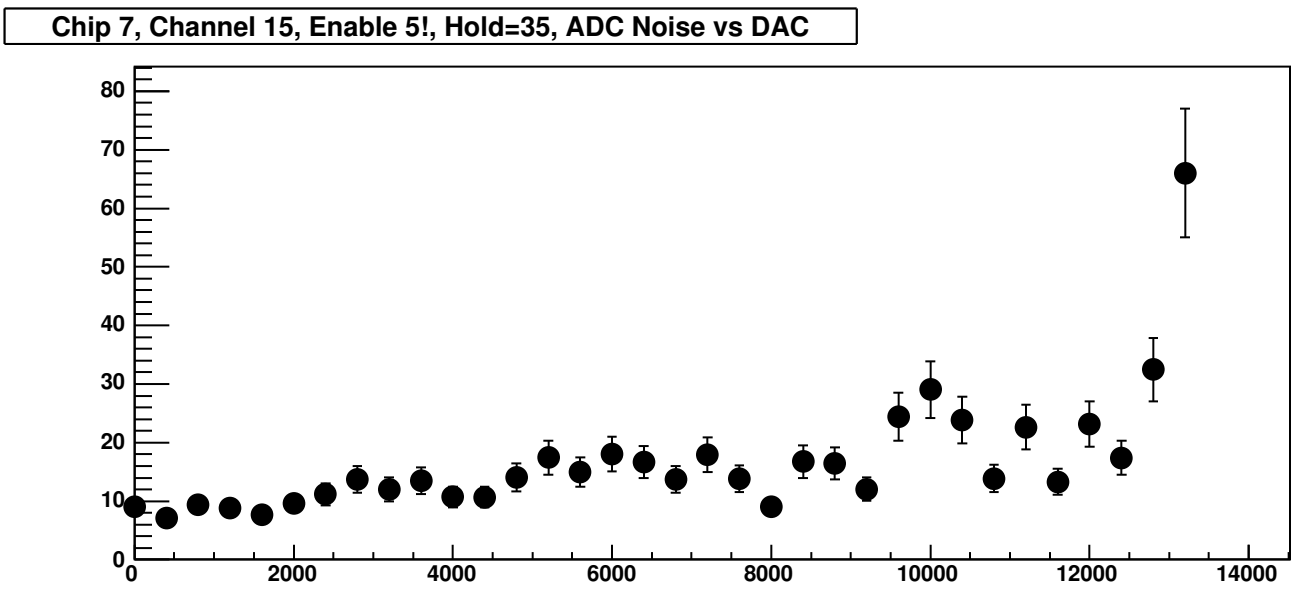

Chip 7, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

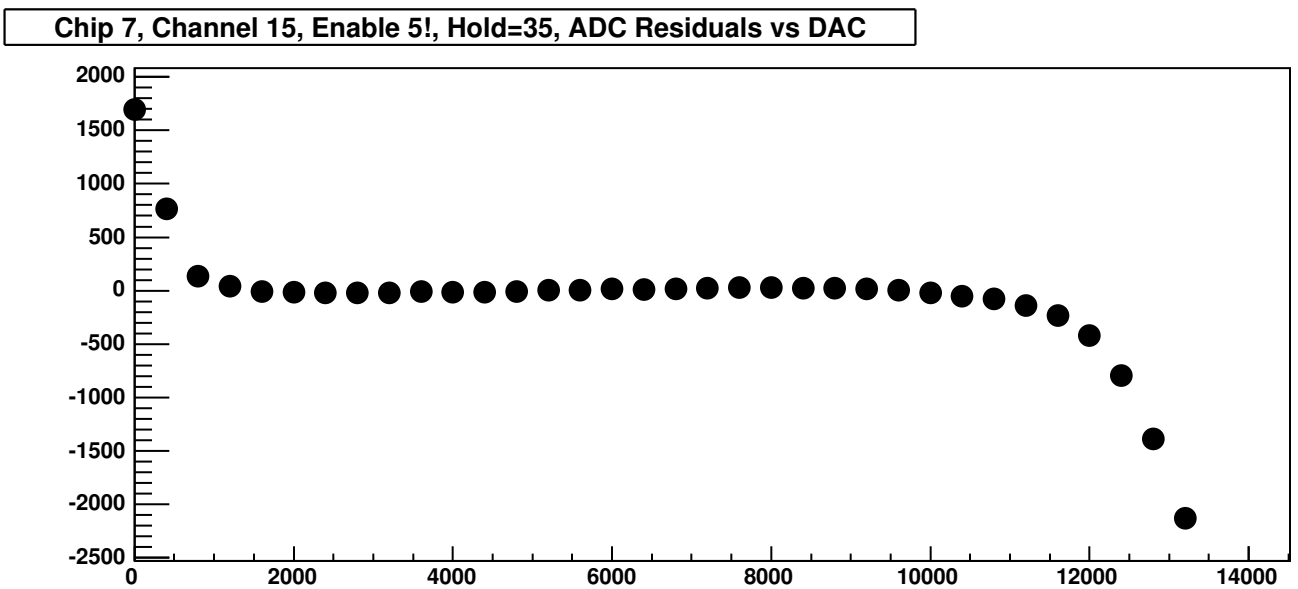

Chip 7, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 31.77 / 23
p0 -343.6 ± 0.7977
p1 0.02148 ± 0.0001247
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

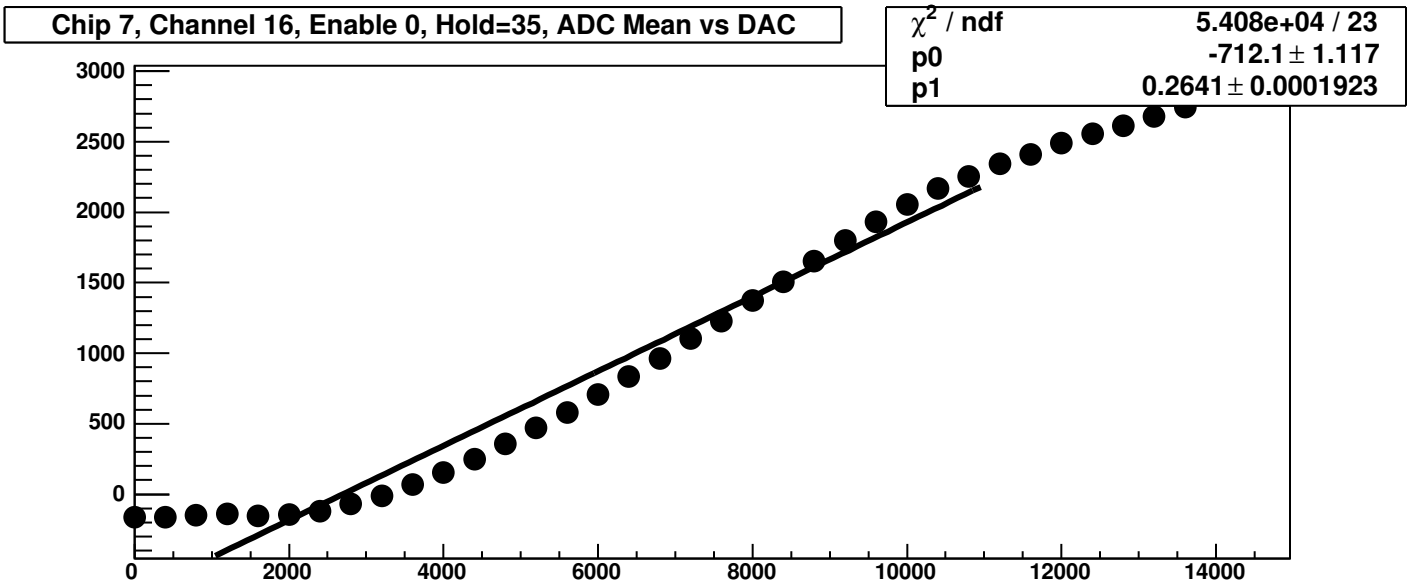

Chip 7, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

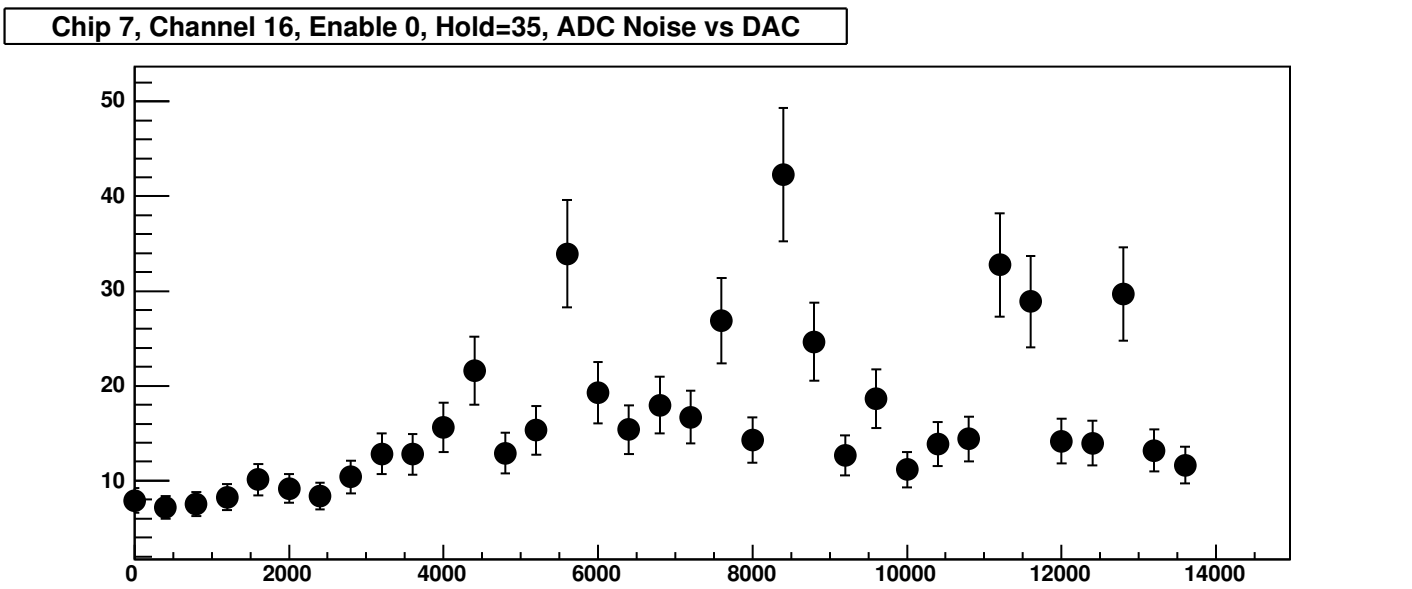

Chip 7, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

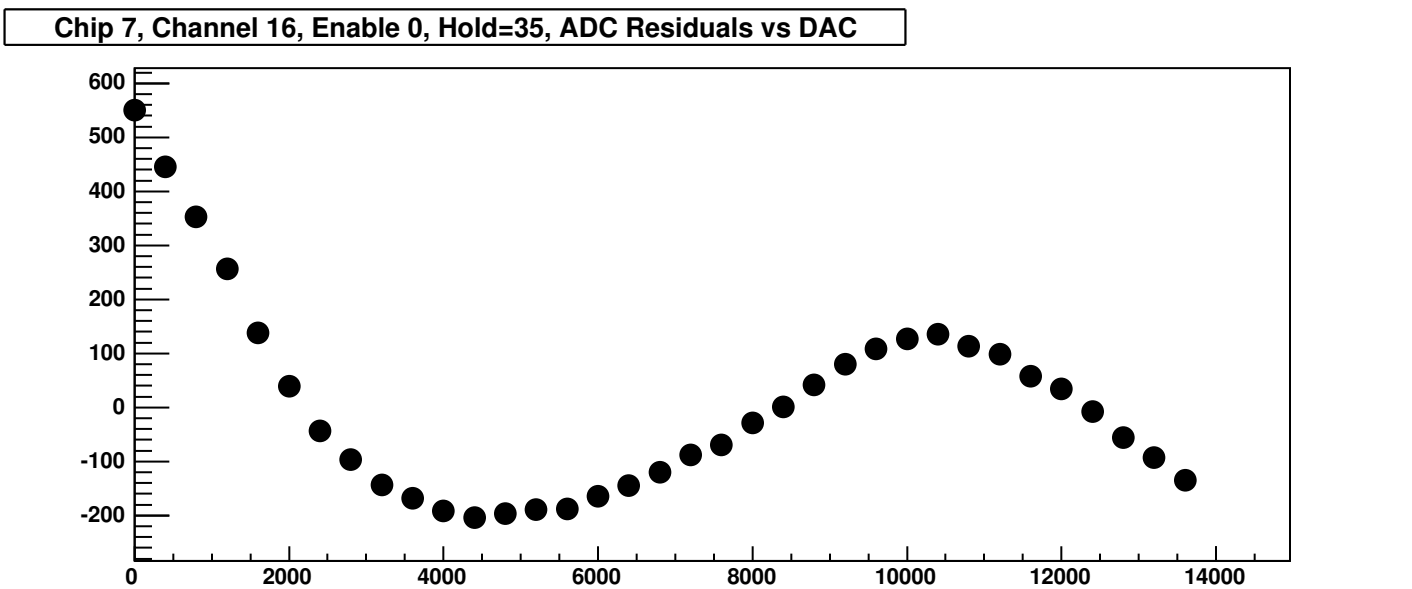

Chip 7, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1995 / 23
p0 -698.6 ± 1.143
p1 2.358 ± 0.0002014
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

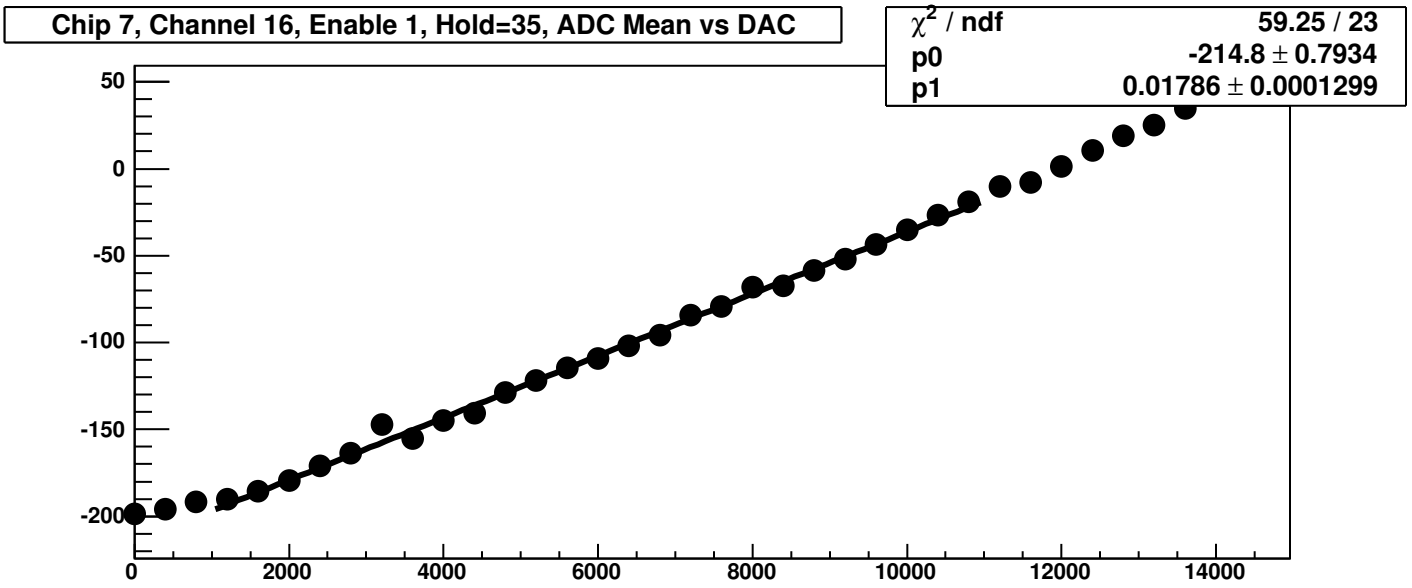

Chip 7, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

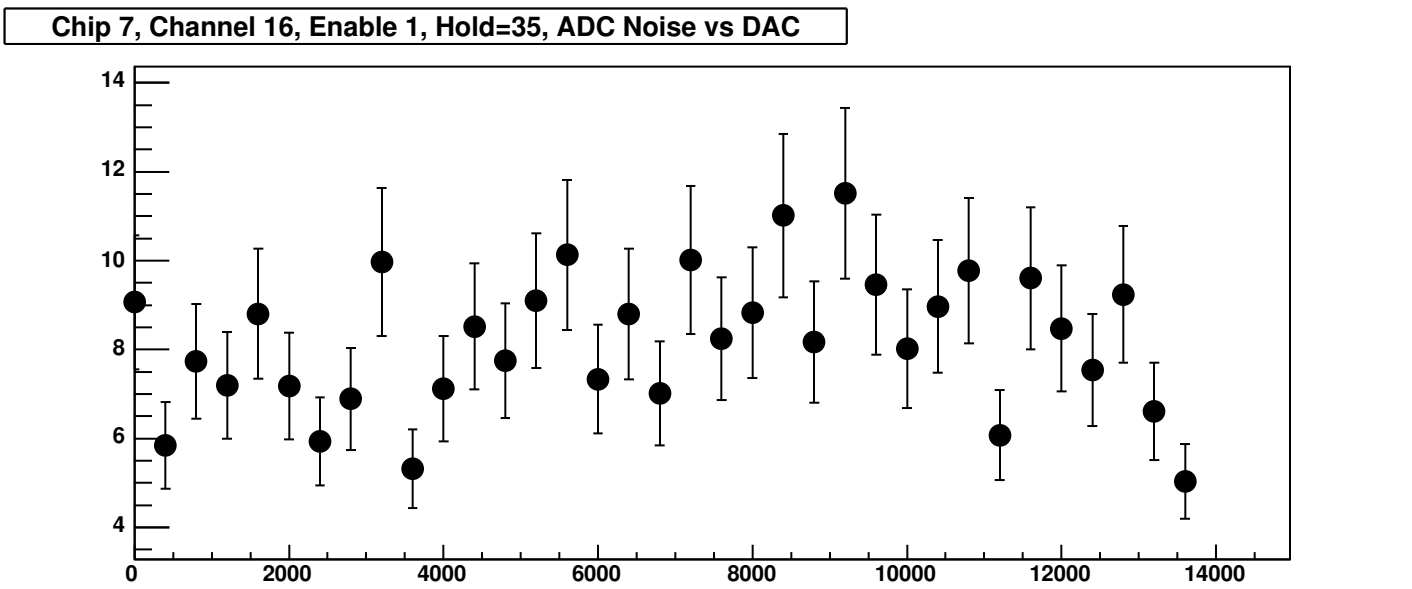

Chip 7, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

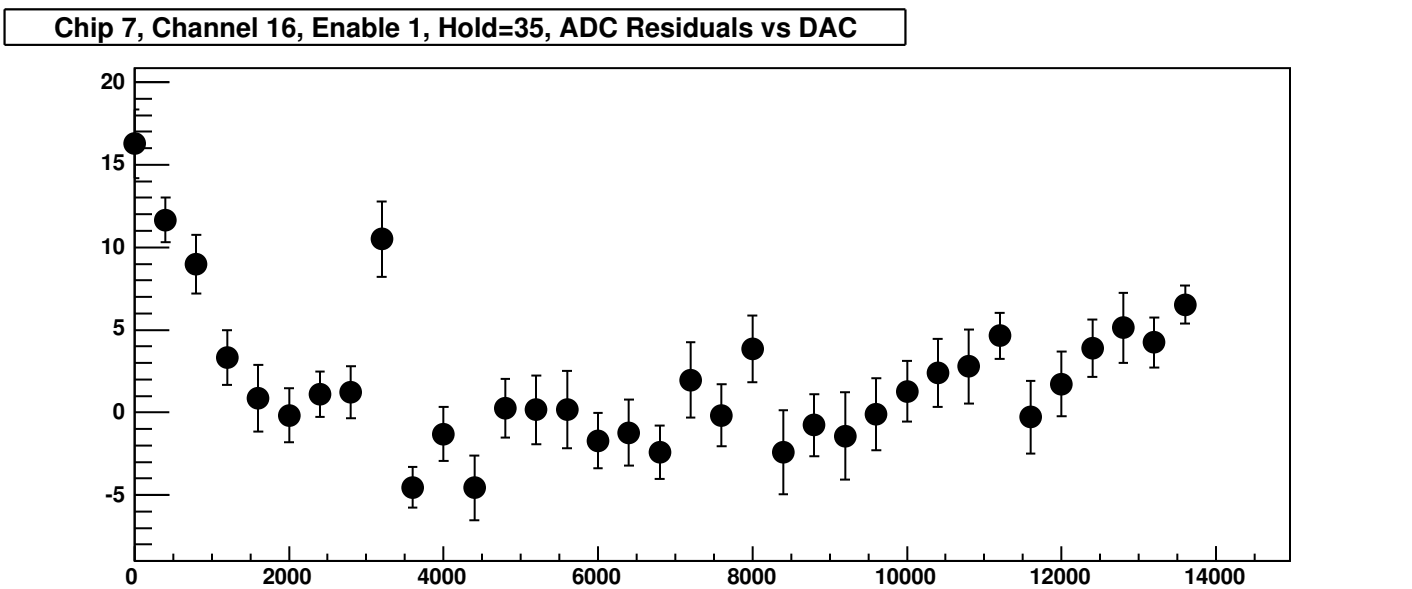

Chip 7, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.408e+04 / 23
p0 -712.1 ± 1.117
p1 0.2641 ± 0.0001923
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

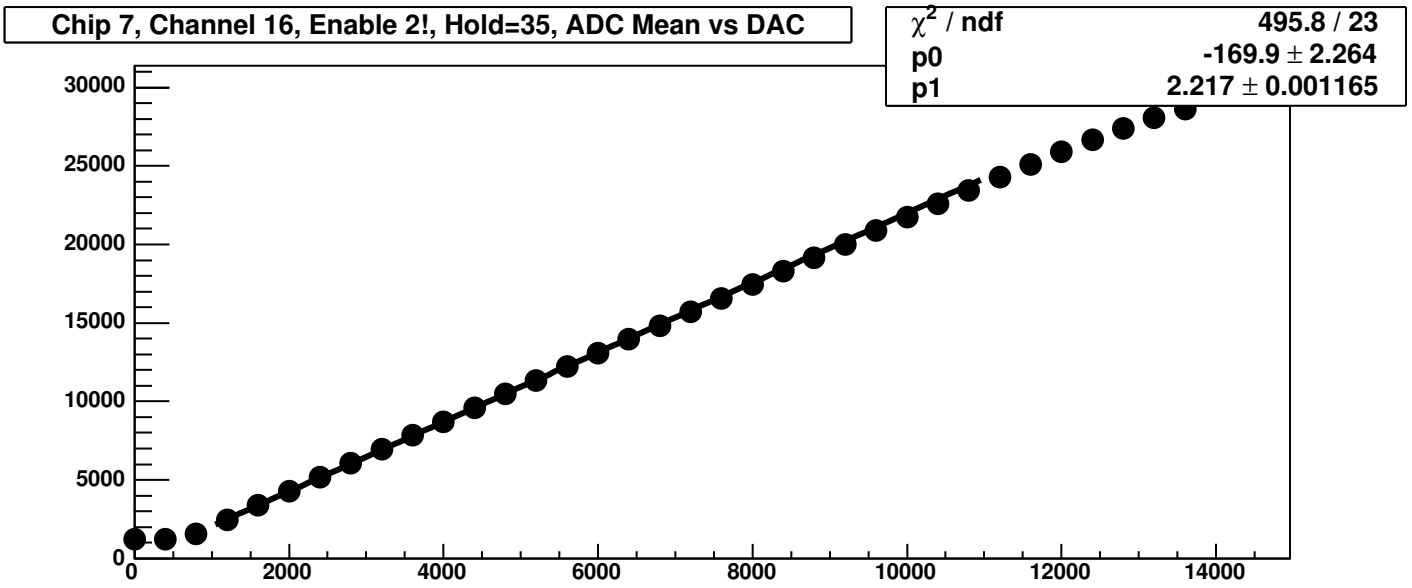

Chip 7, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

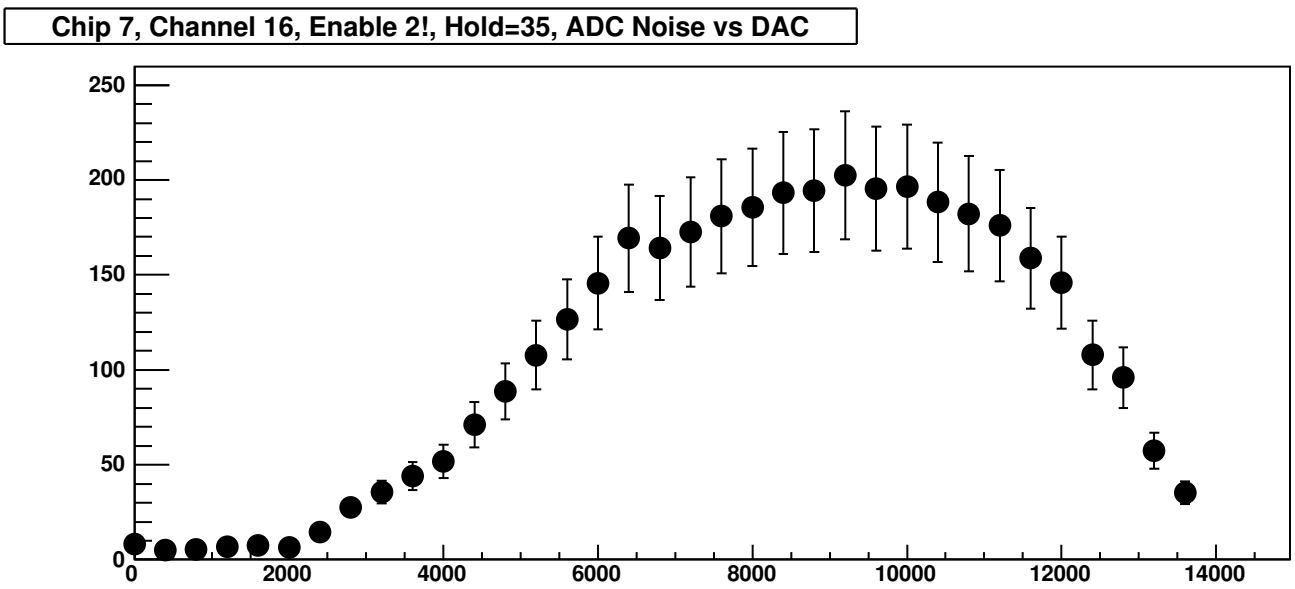

Chip 7, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

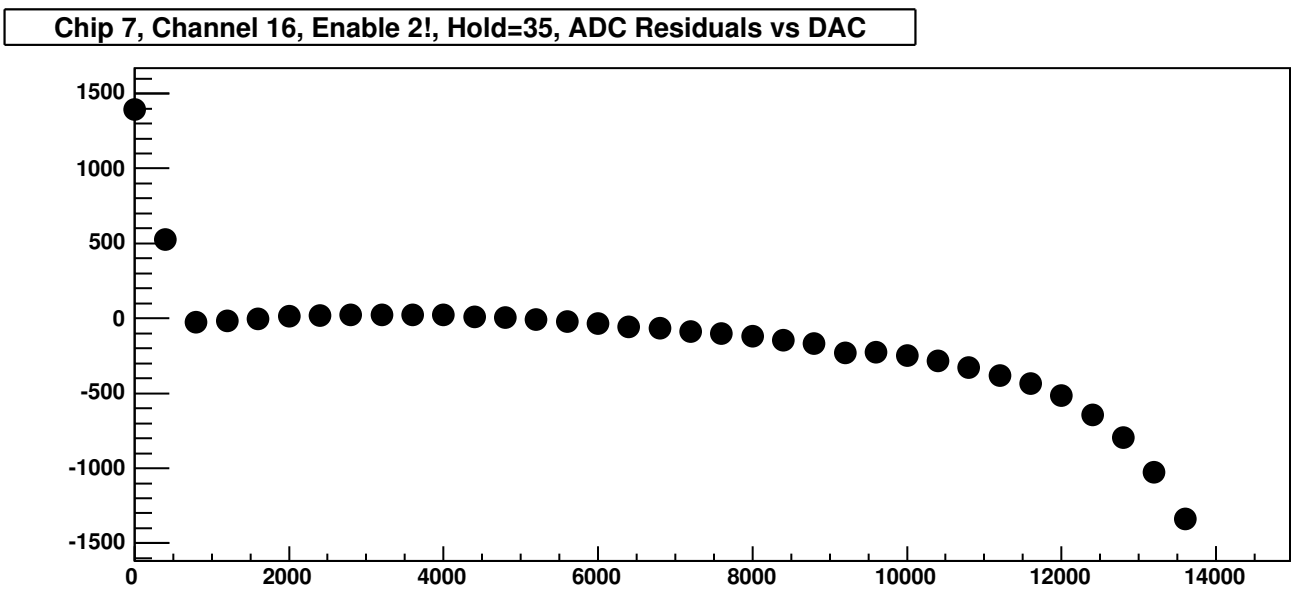

Chip 7, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 59.25 / 23
p0 -214.8 ± 0.7934
p1 0.01786 ± 0.0001299
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

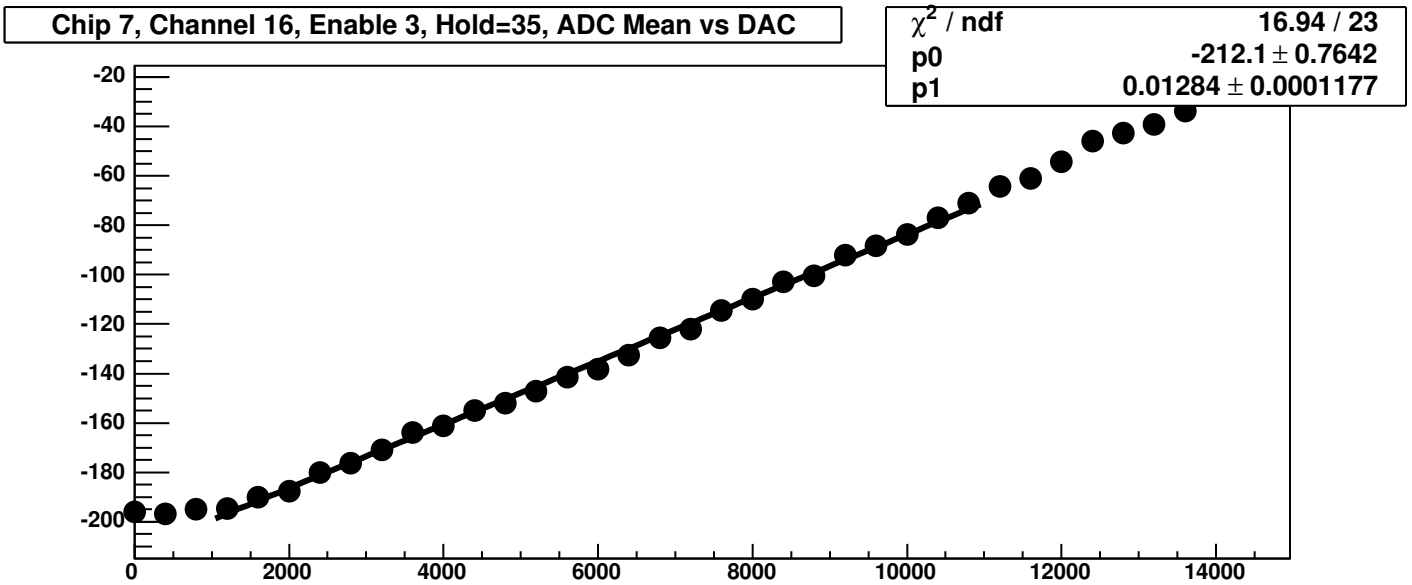

Chip 7, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

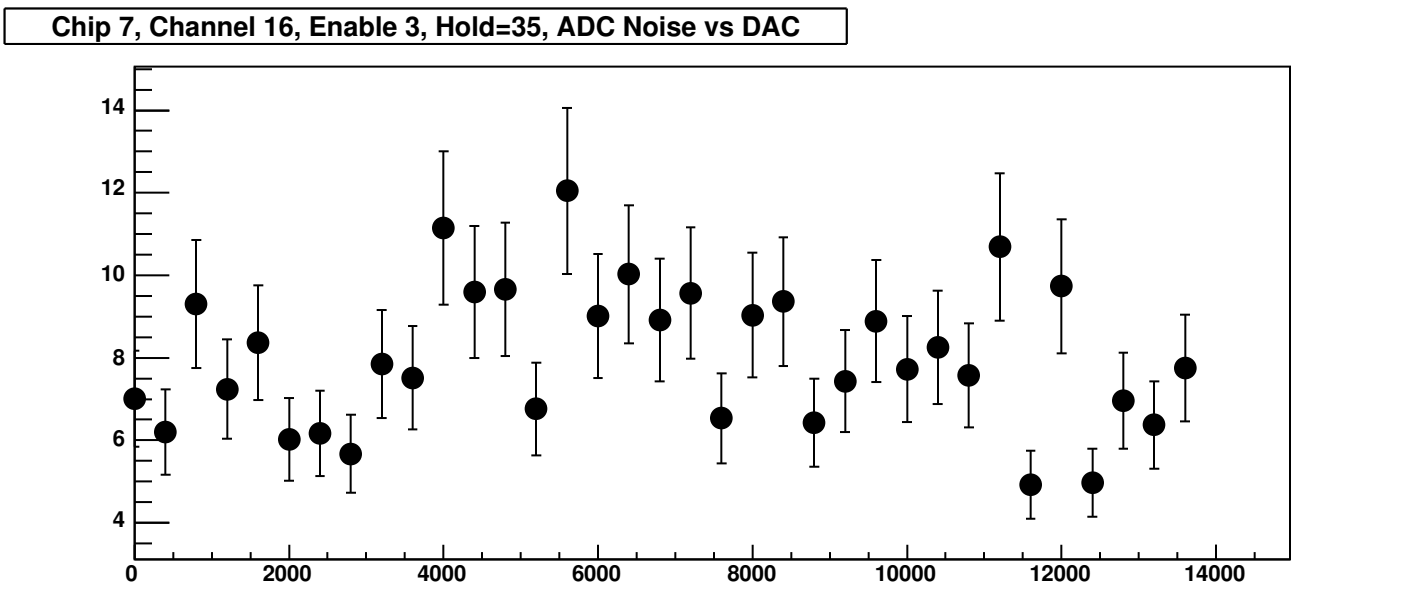

Chip 7, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

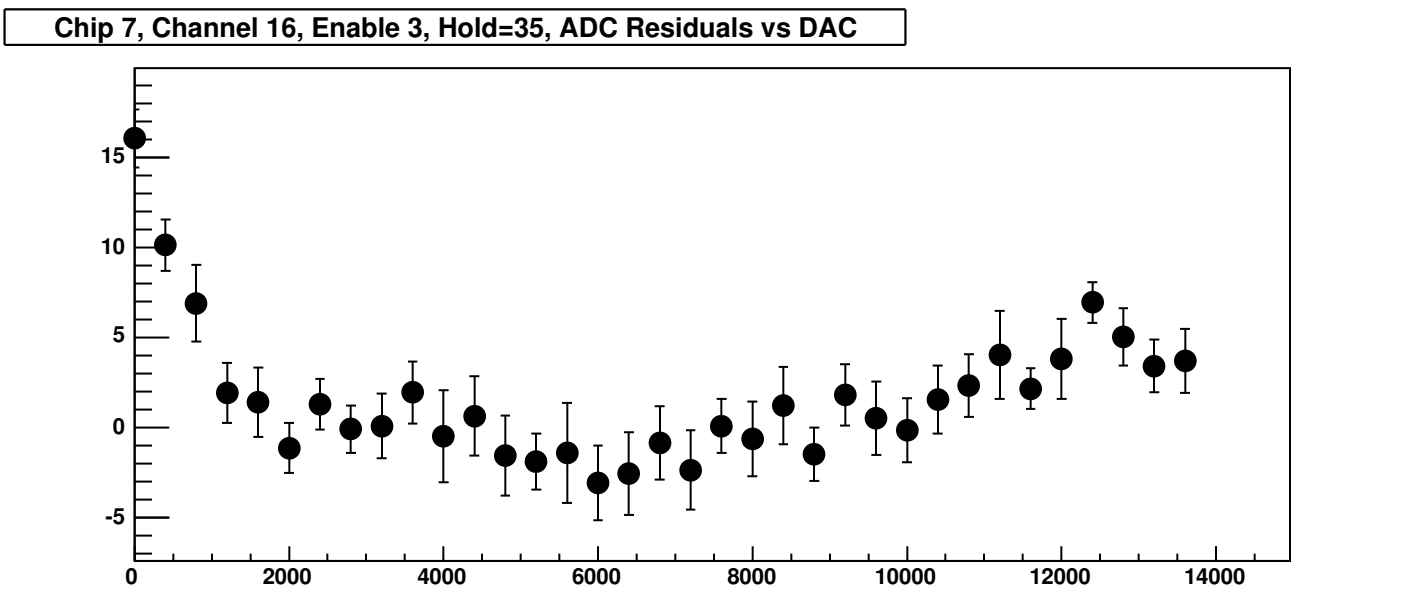

Chip 7, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 495.8 / 23
p0 -169.9 ± 2.264
p1 2.217 ± 0.001165
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

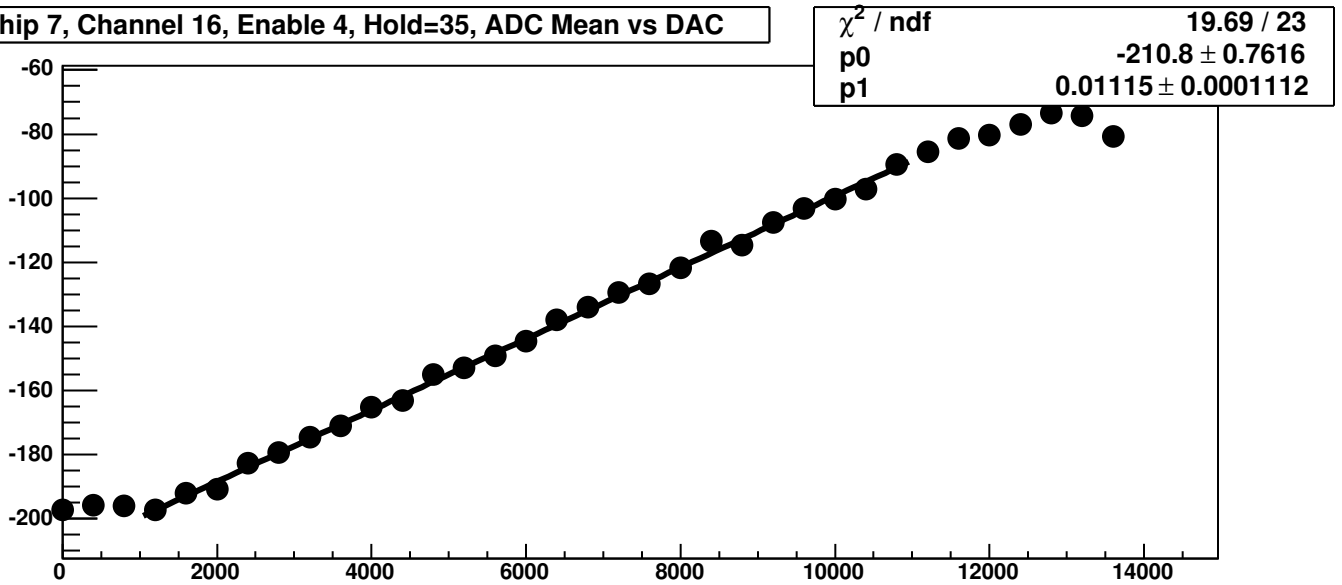

Chip 7, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 16.94 / 23
p0 -212.1 ± 0.7642
p1 0.01284 ± 0.0001177
-20
-40
-60
-80
-100
-120
-140
-160
-180
-200
0
2000
4000
6000
8000
10000
12000
14000

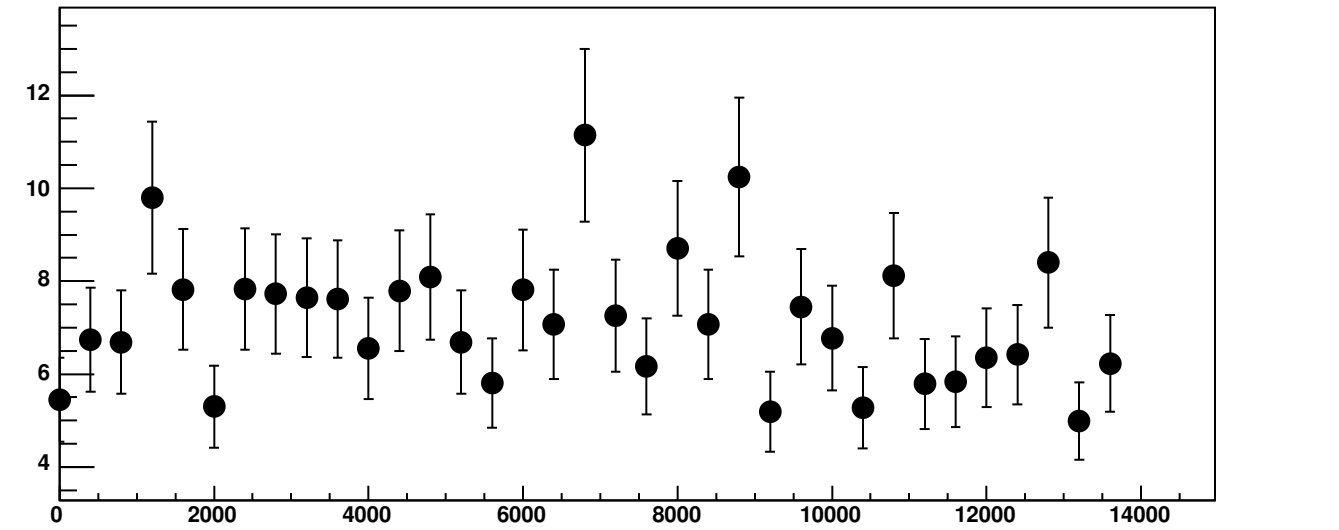

Chip 7, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

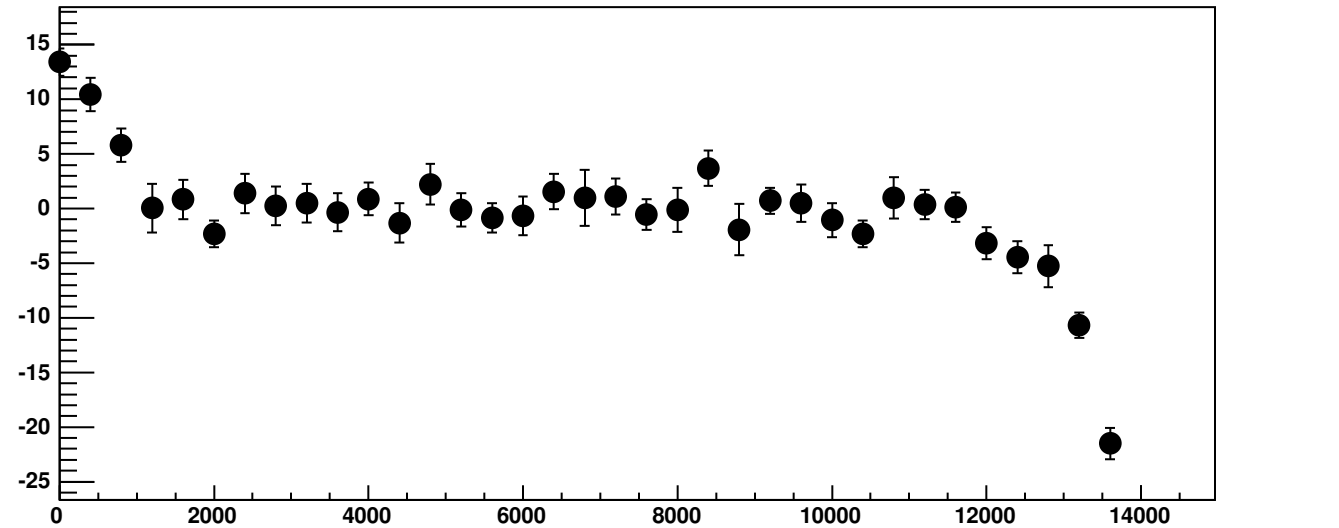
Chip 7, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

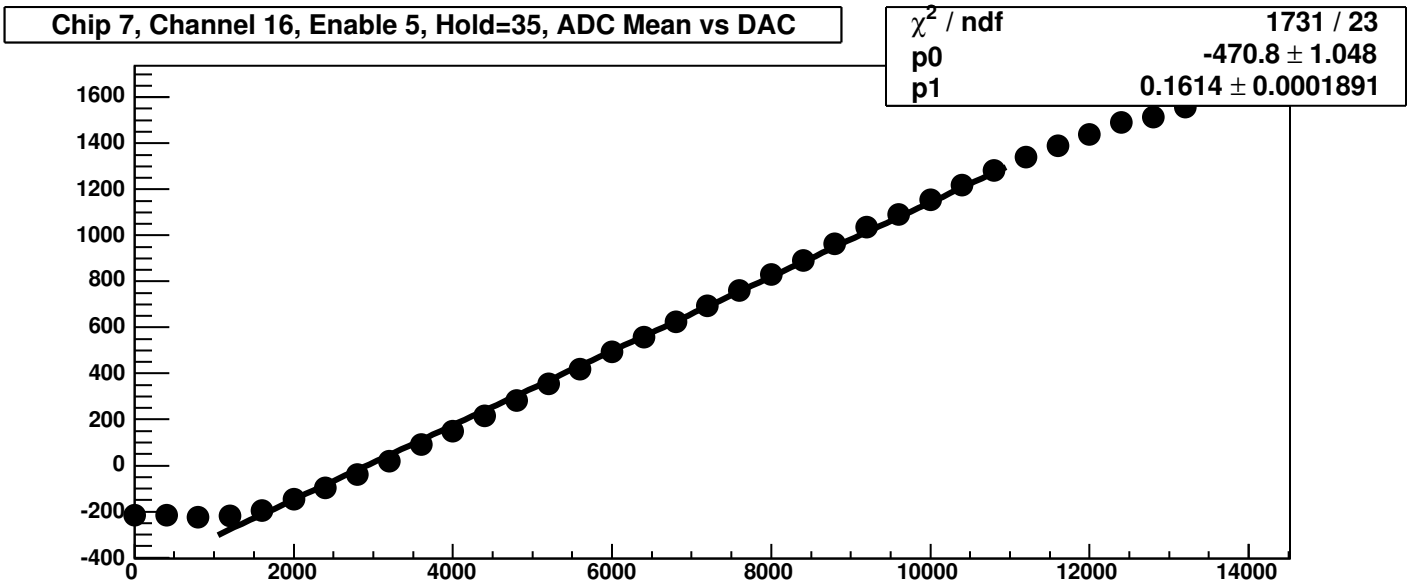


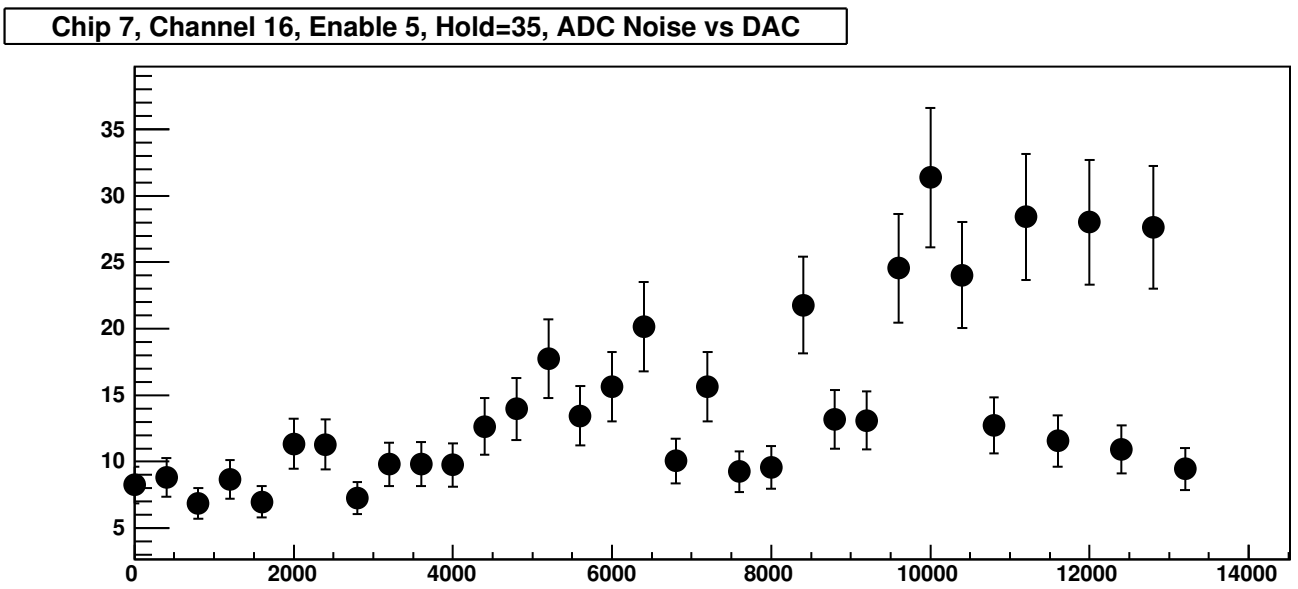
Chip 7, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC

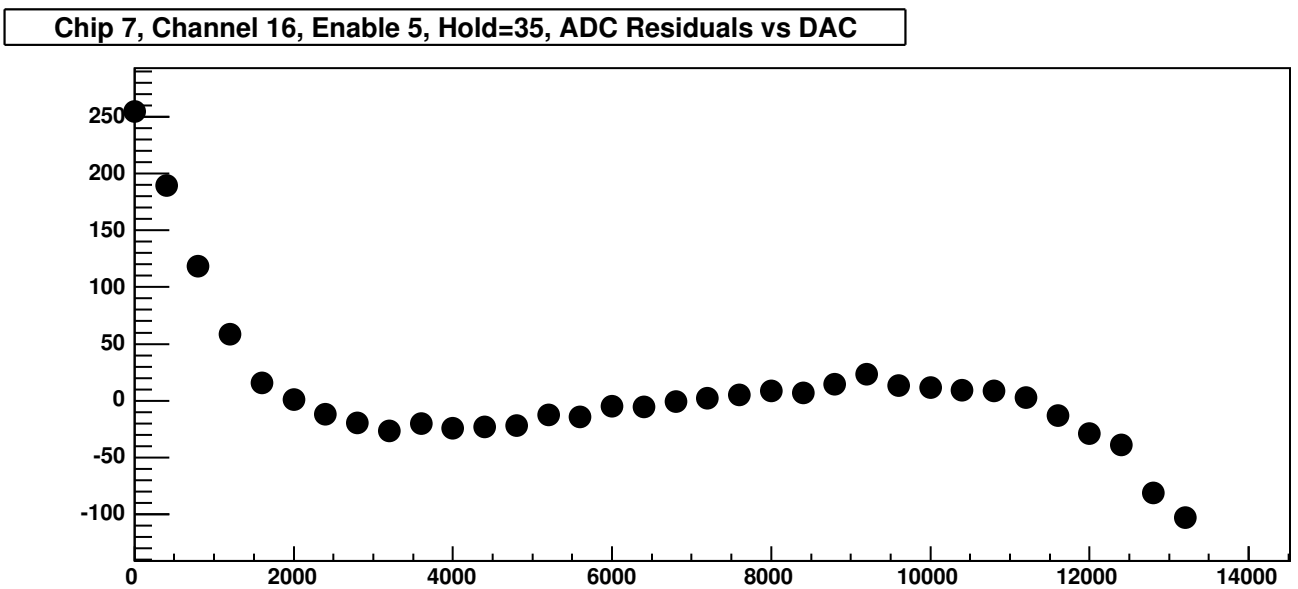


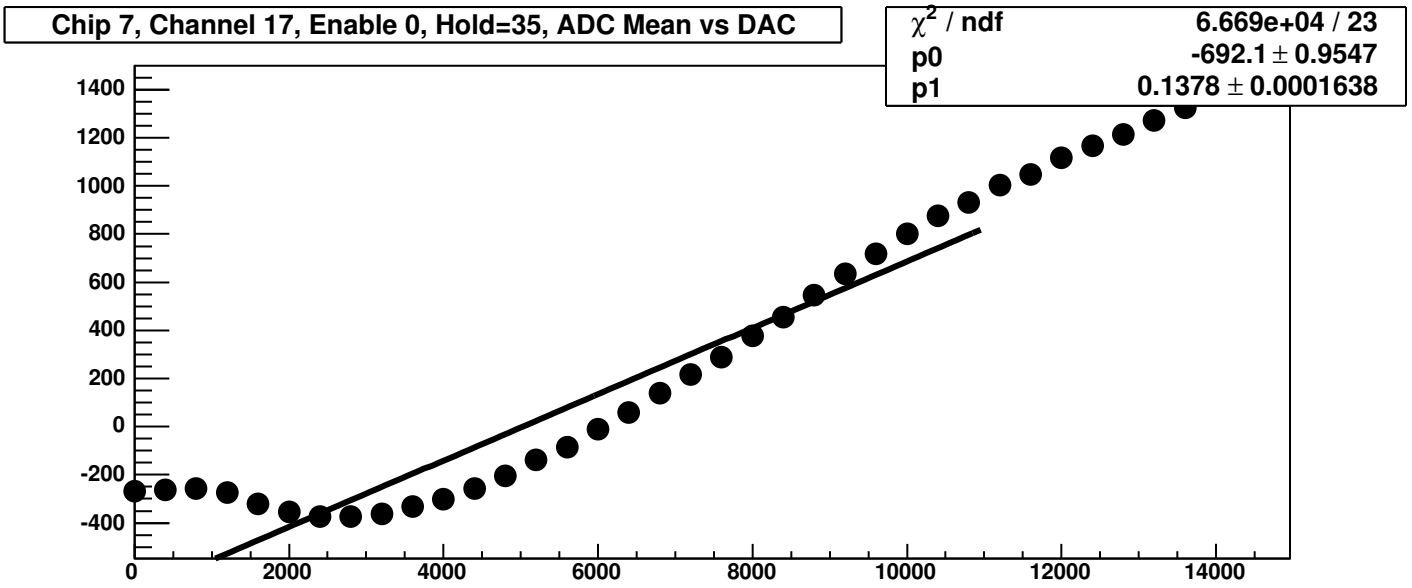
Chip 7, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

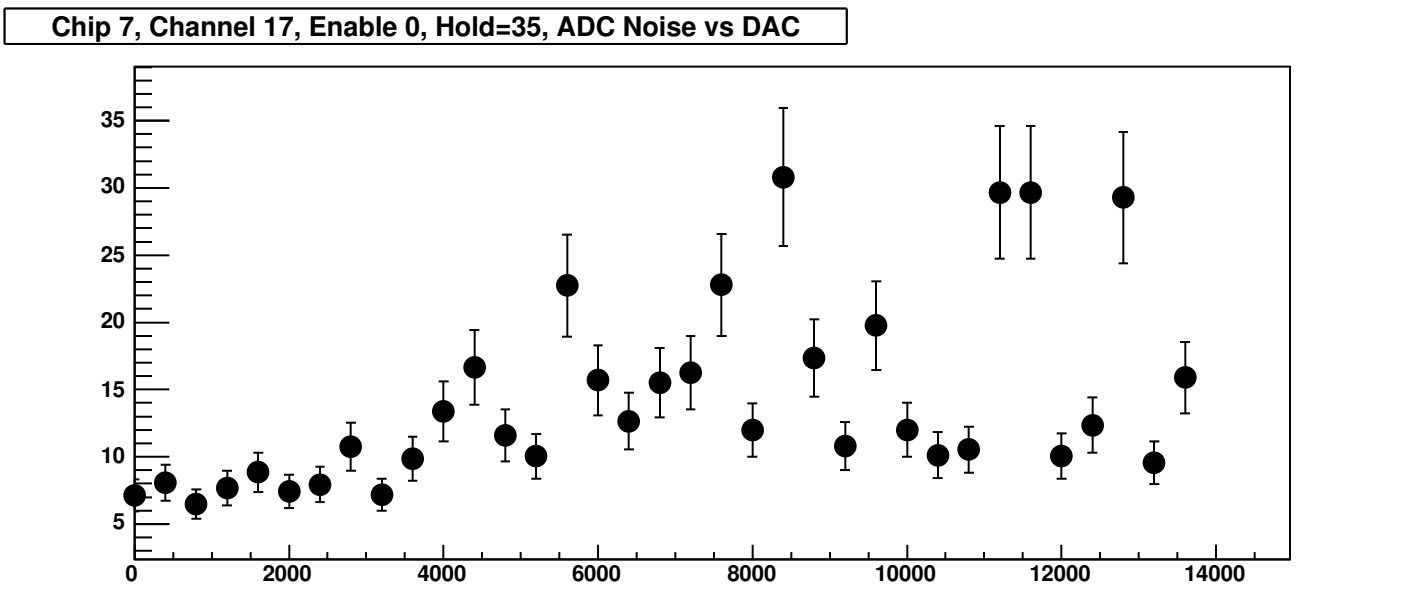


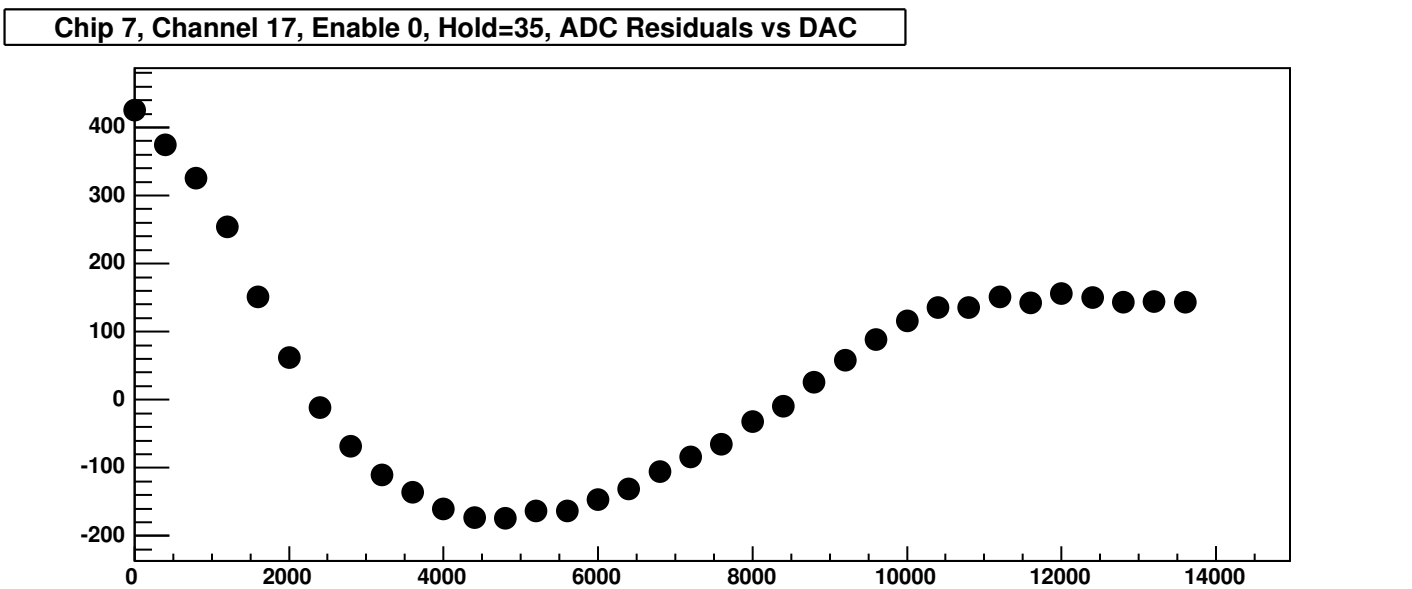

Chip 7, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1731 / 23
p0 -470.8 ± 1.048
p1 0.1614 ± 0.0001891
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

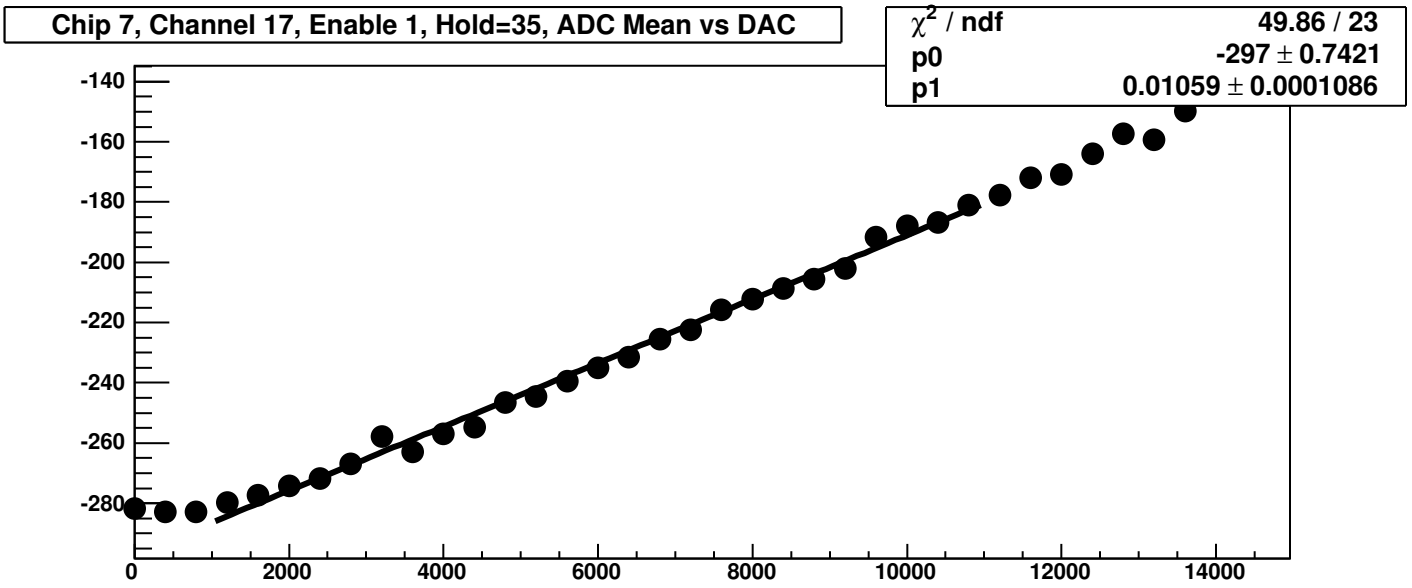

Chip 7, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

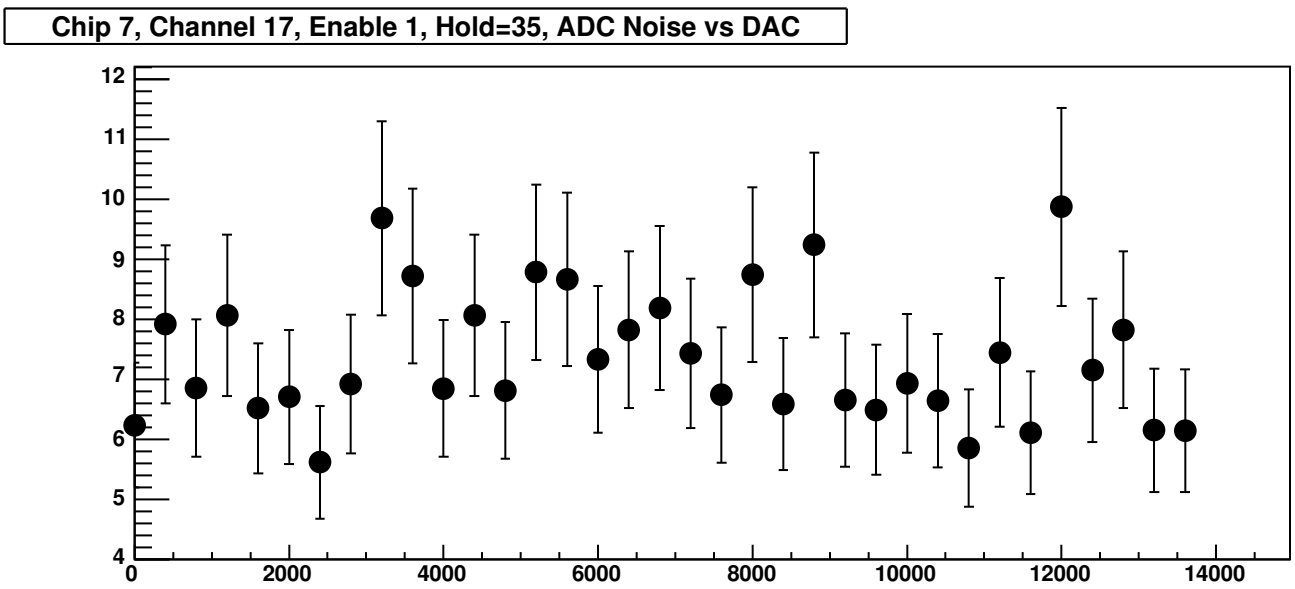

Chip 7, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

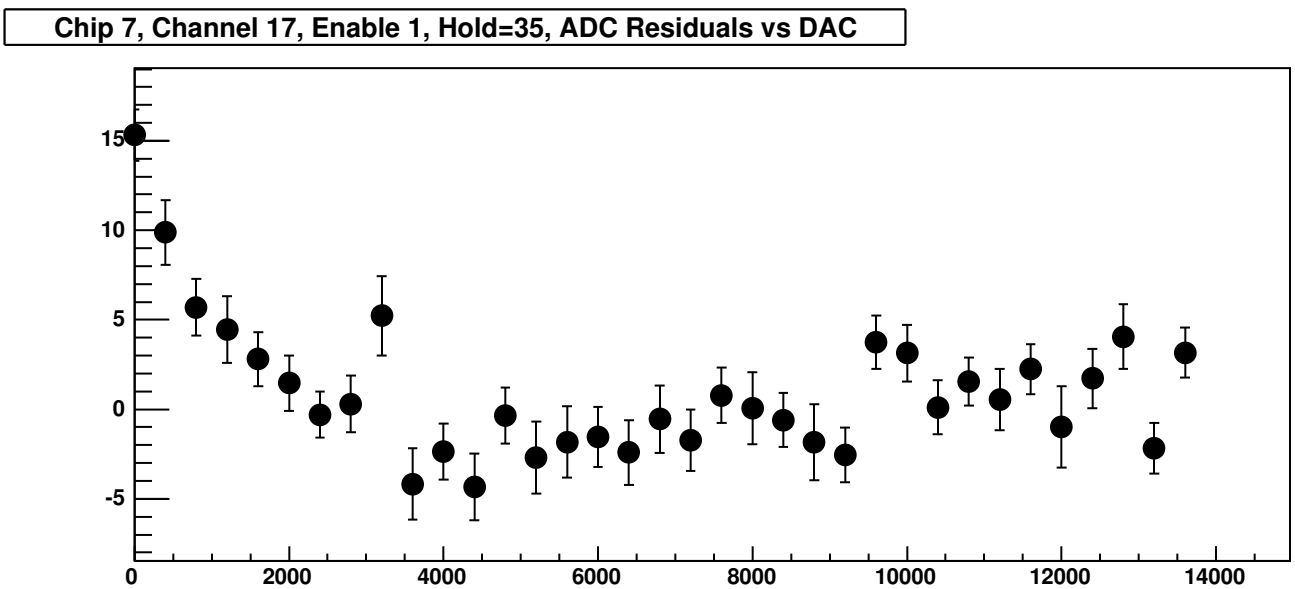

Chip 7, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 6.669e+04 / 23
p0 -692.1 ± 0.9547
p1 0.1378 ± 0.0001638
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

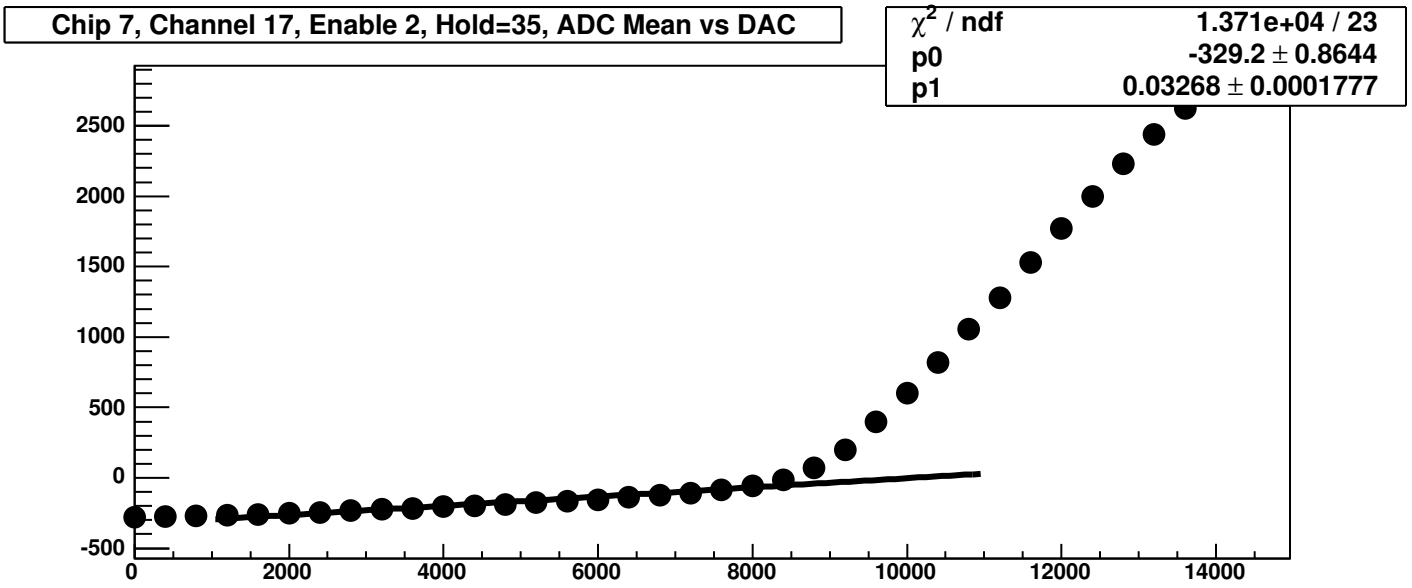

Chip 7, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

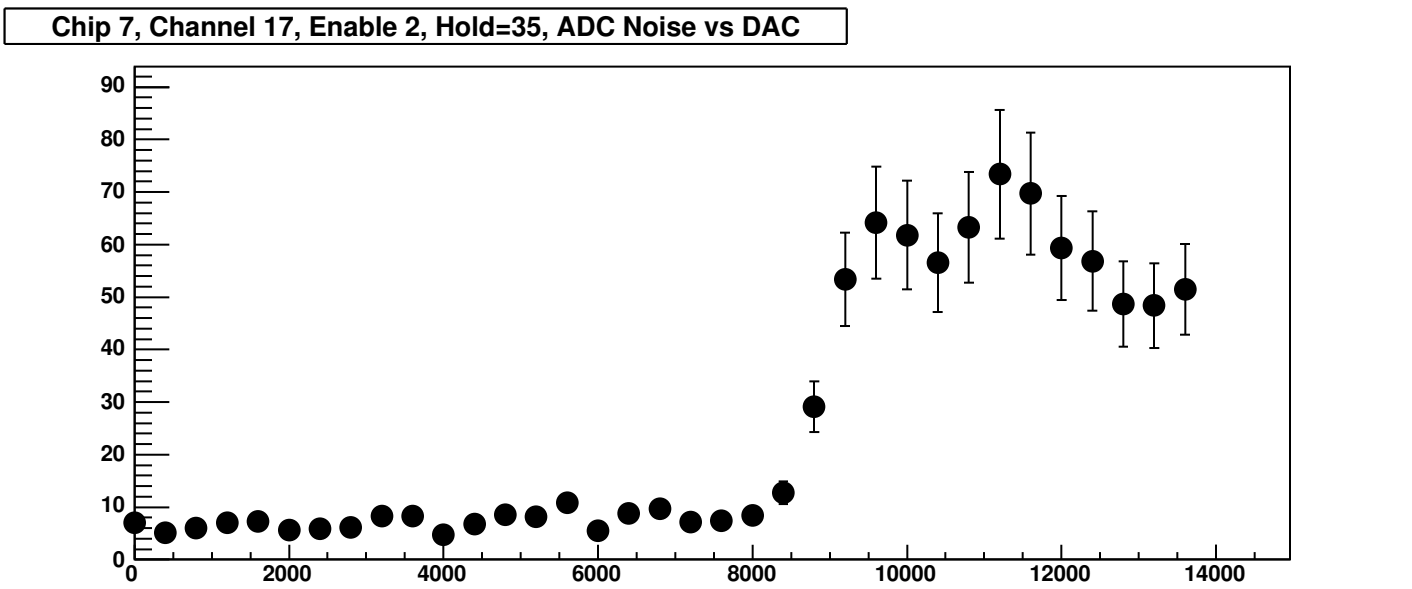

Chip 7, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

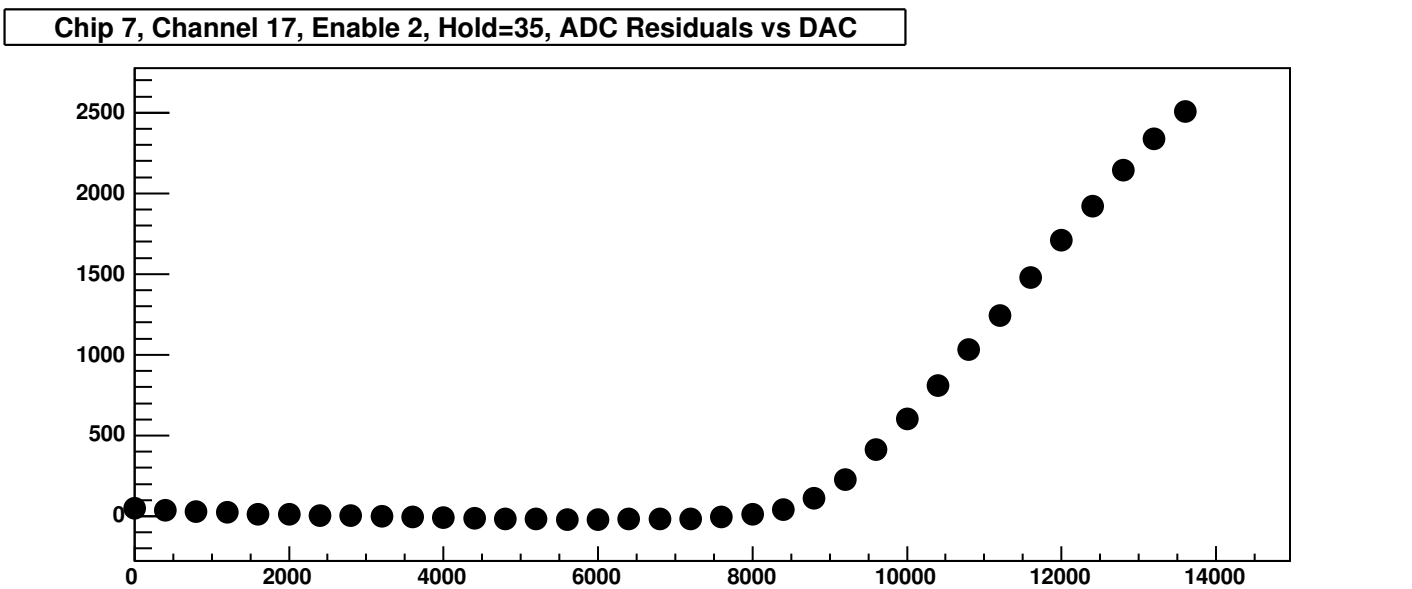

Chip 7, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 49.86 / 23
p0 -297 ± 0.7421
p1 0.01059 ± 0.0001086
-140
-160
-180
-200
-220
-240
-260
-280
0
2000
4000
6000
8000
10000
12000
14000

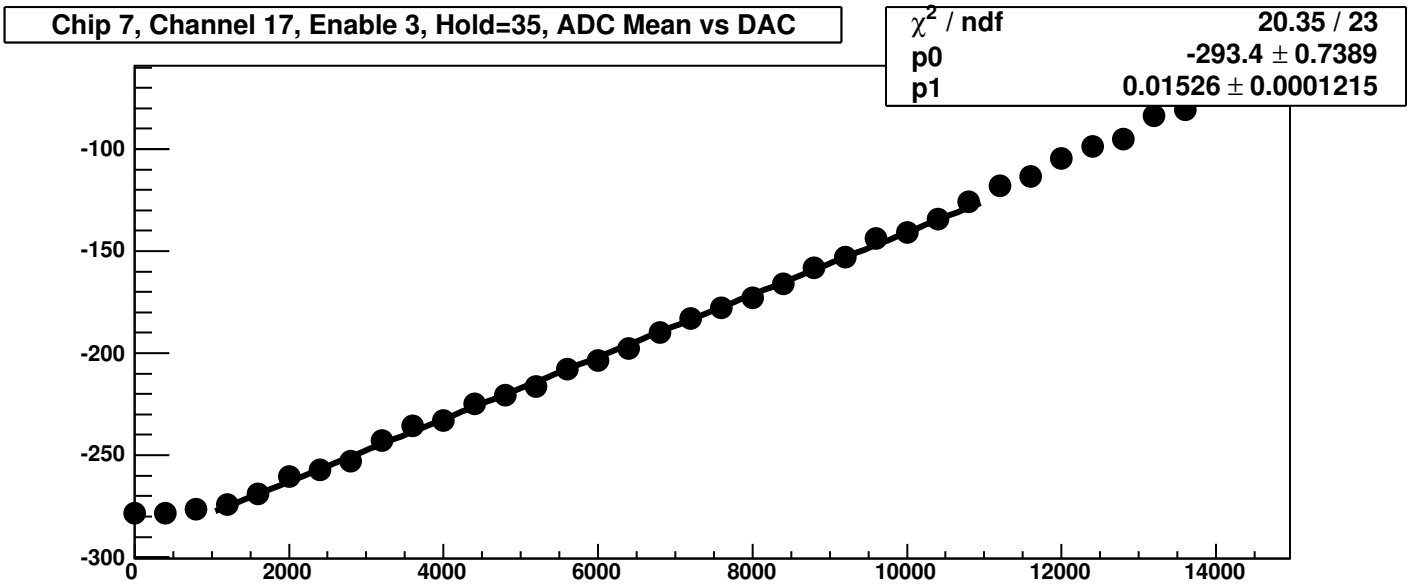

Chip 7, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

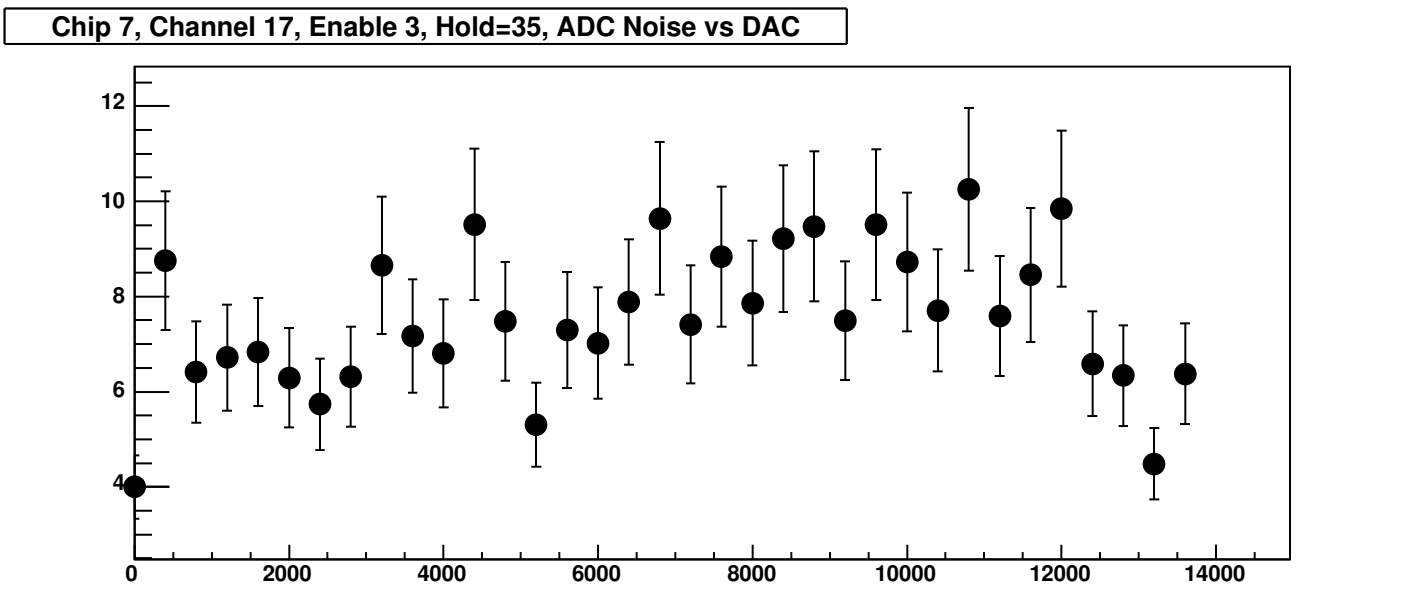

Chip 7, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

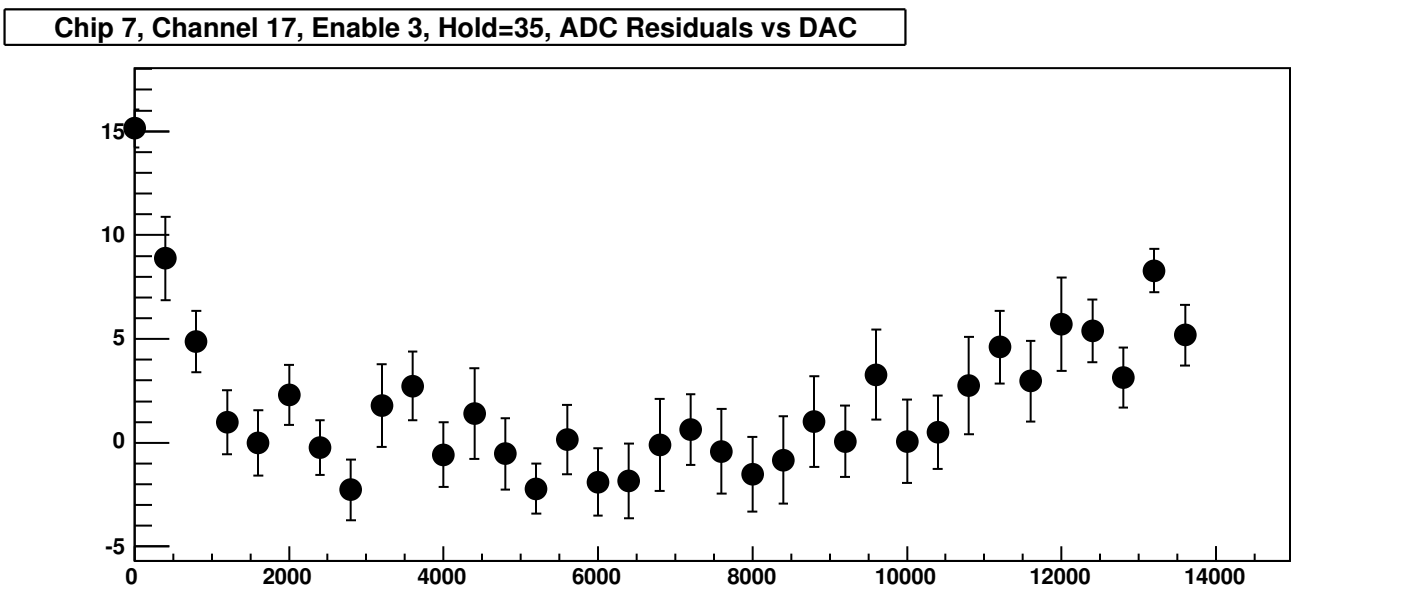

Chip 7, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1.371e+04 / 23
p0 -329.2 ± 0.8644
p1 0.03268 ± 0.0001777
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

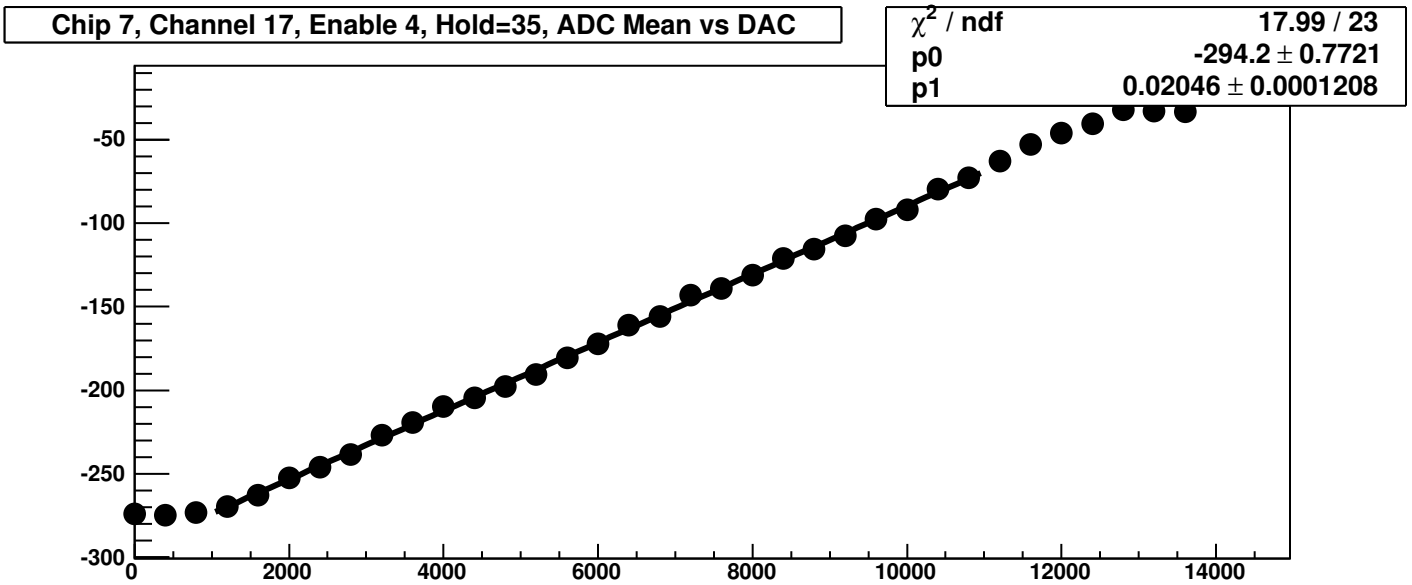

Chip 7, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

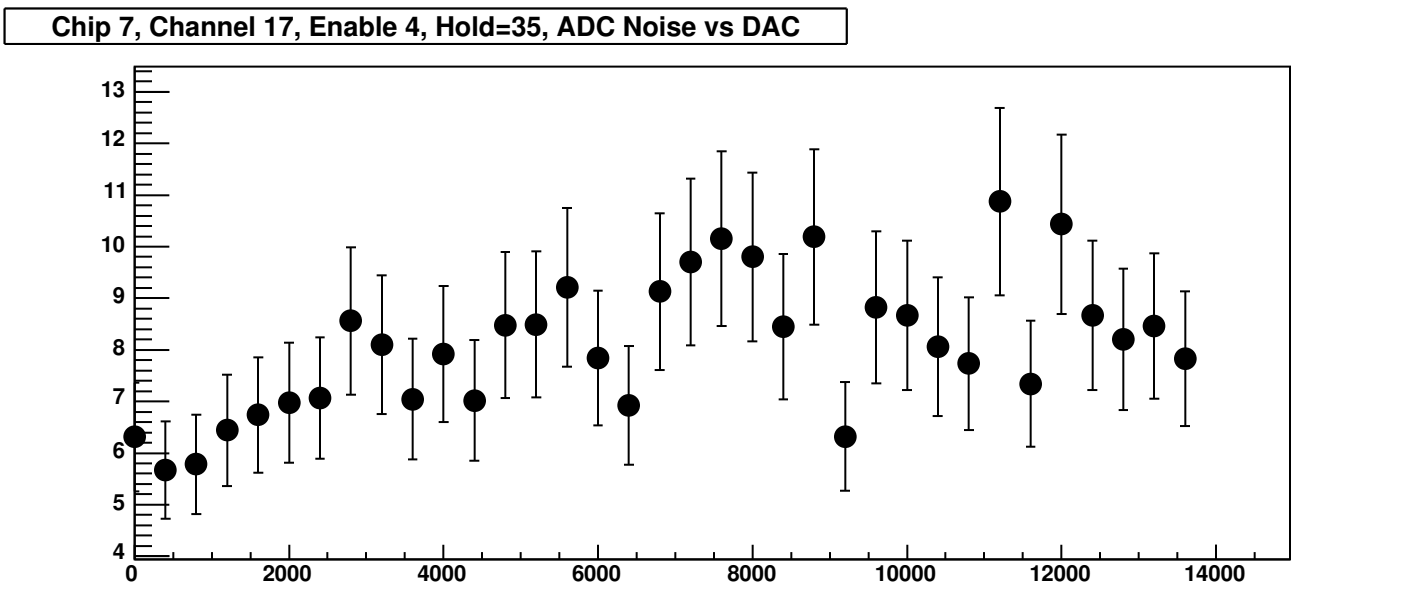

Chip 7, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

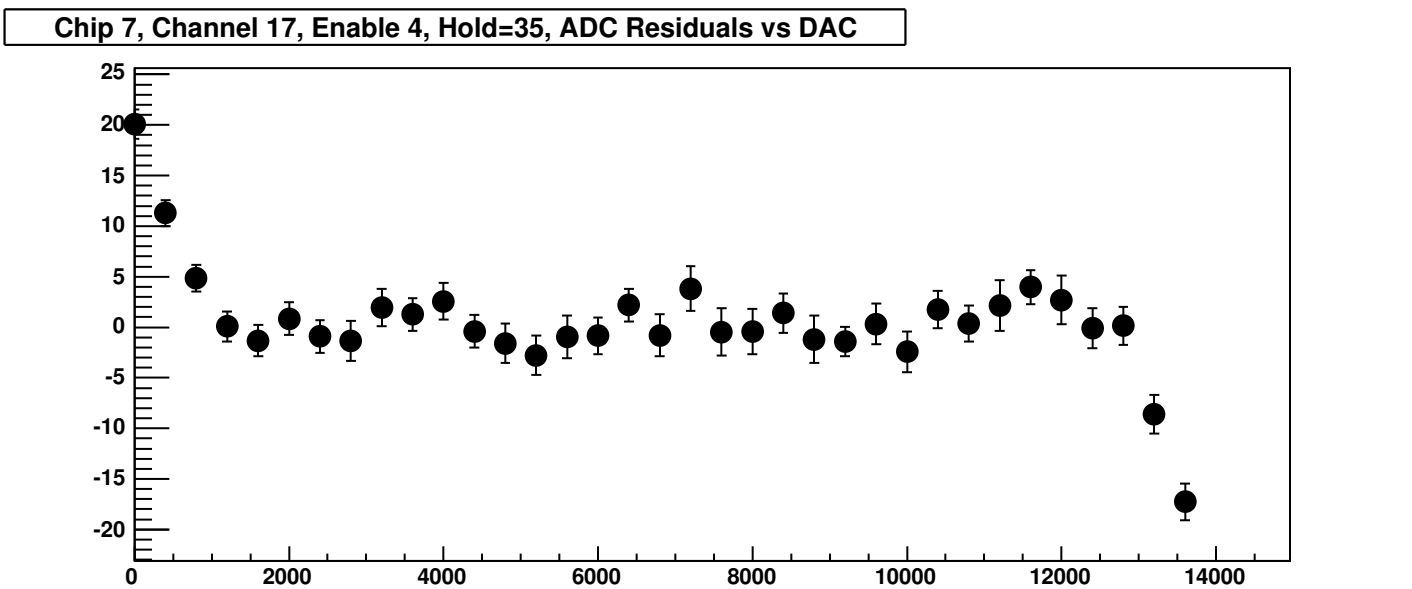

Chip 7, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 20.35 / 23
p0 -293.4 ± 0.7389
p1 0.01526 ± 0.0001215
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

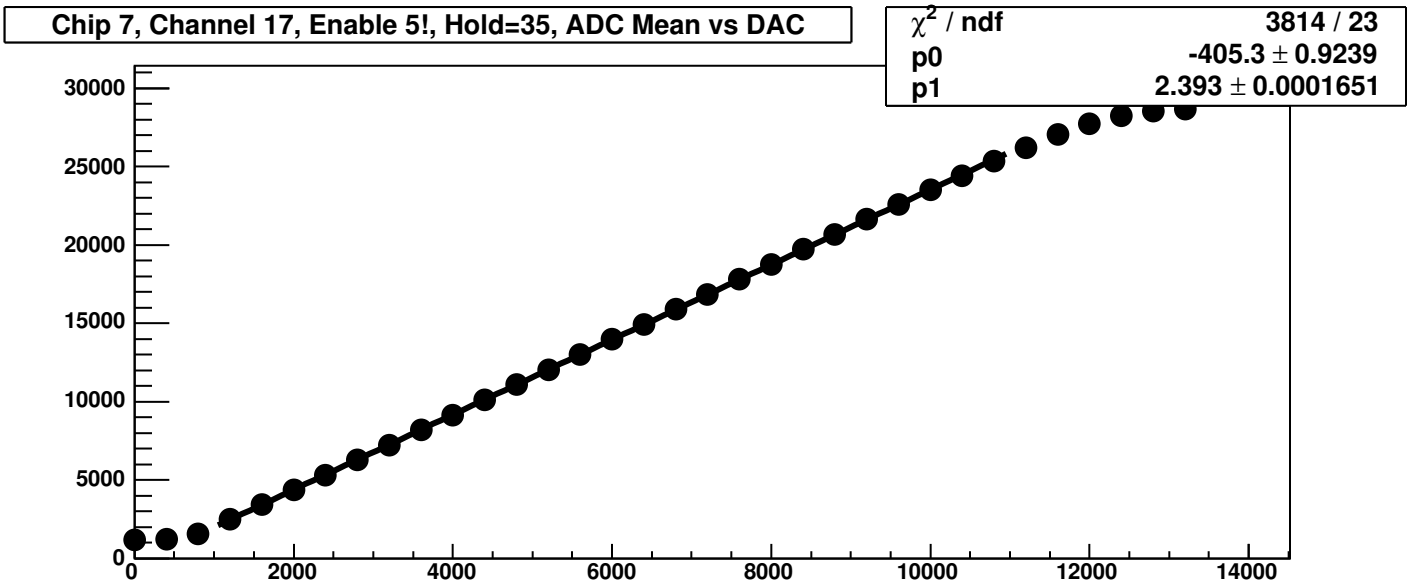

Chip 7, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

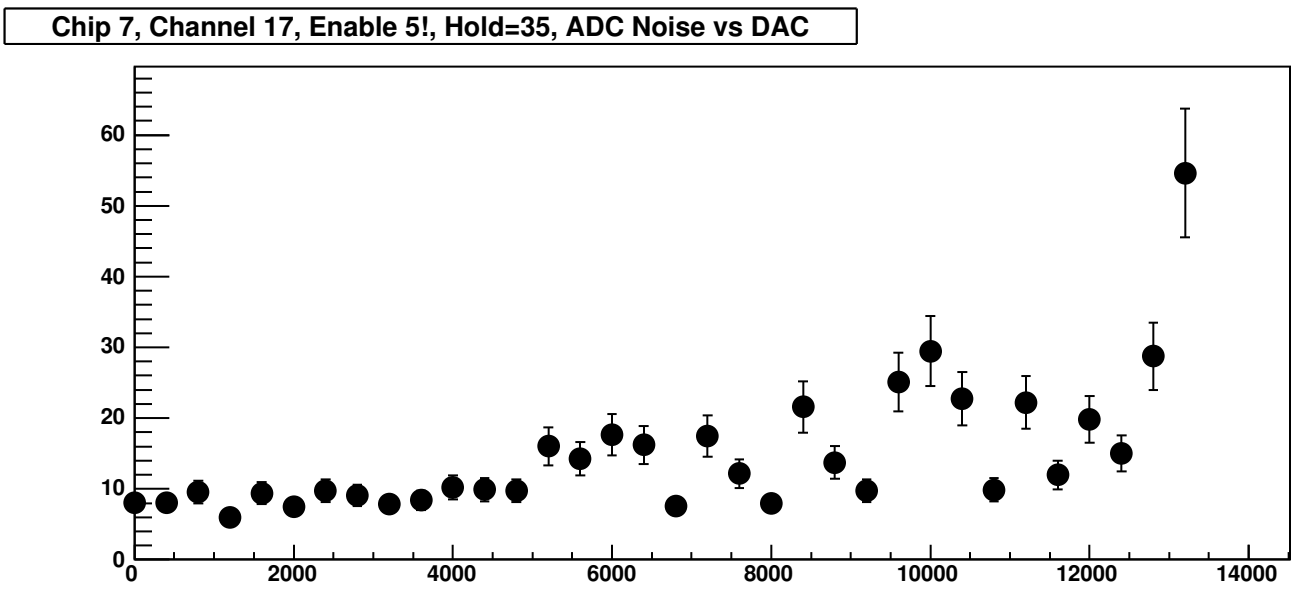

Chip 7, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

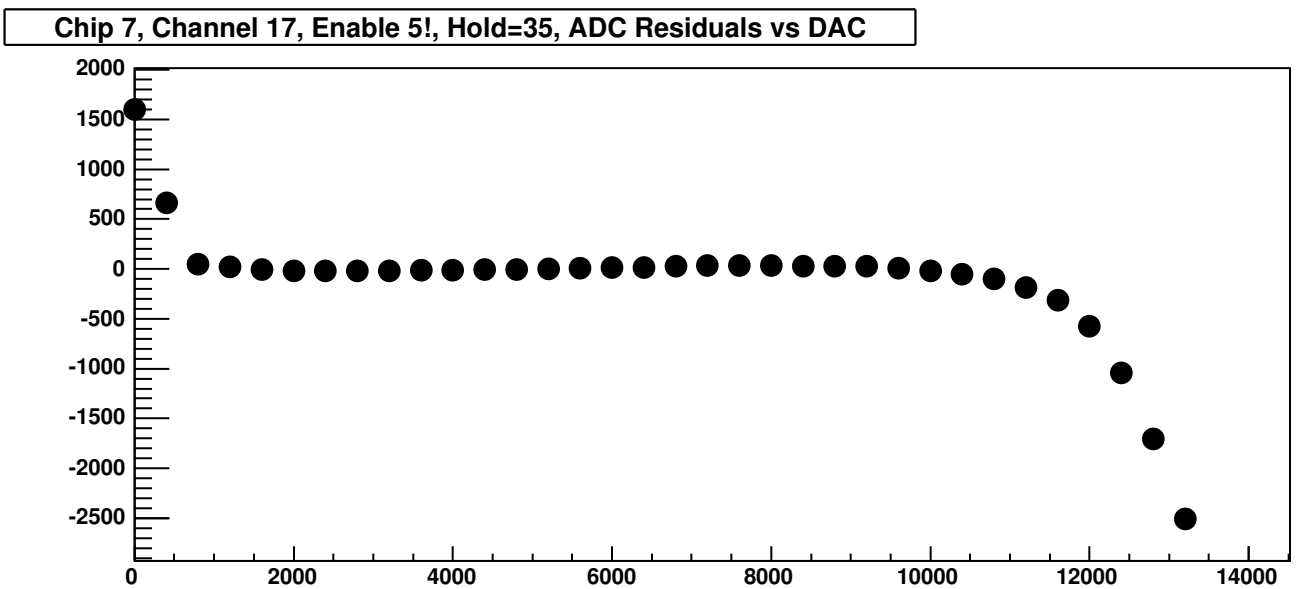

Chip 7, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 17.99 / 23
p0 -294.2 ± 0.7721
p1 0.02046 ± 0.0001208
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

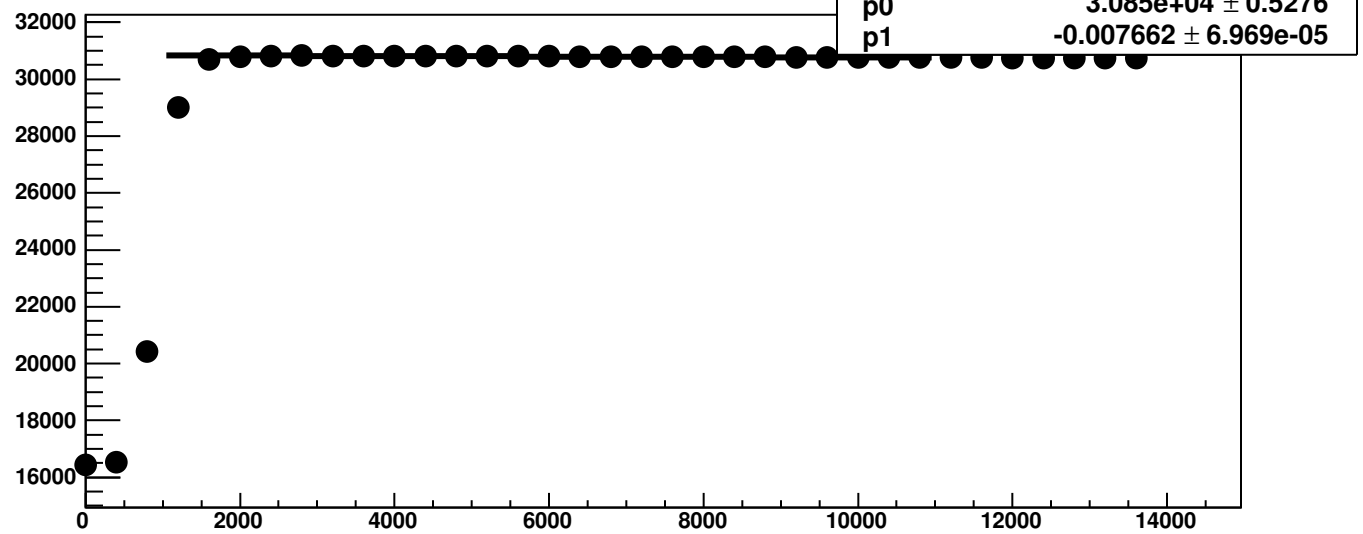

Chip 7, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3814 / 23
p0 -405.3 ± 0.9239
p1 2.393 ± 0.0001651
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

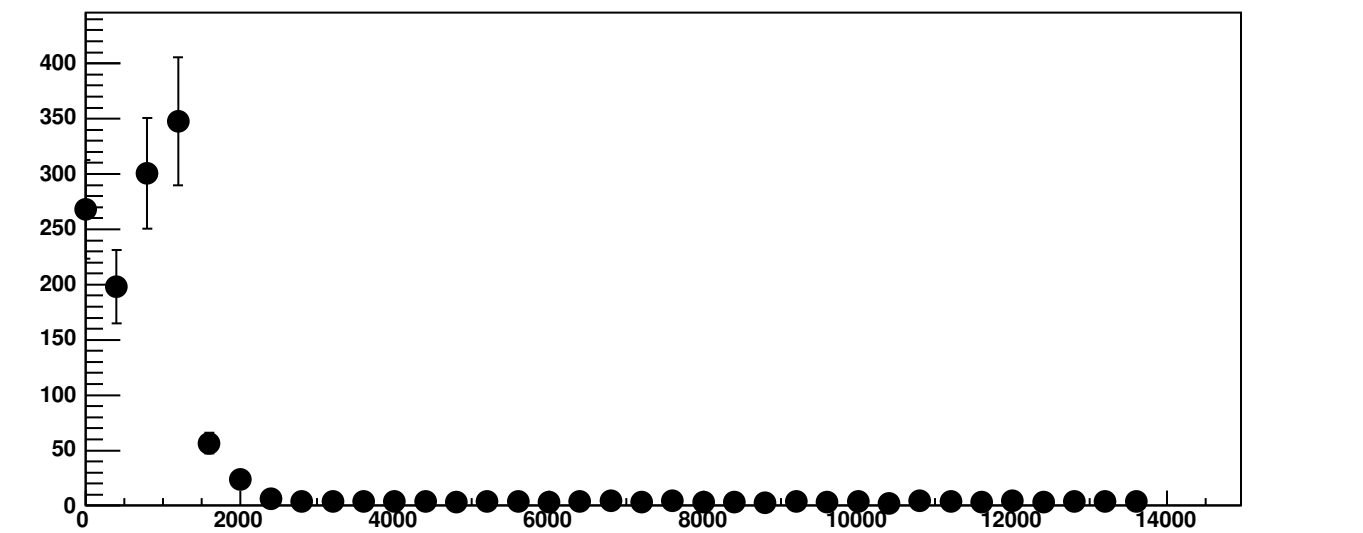

Chip 7, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

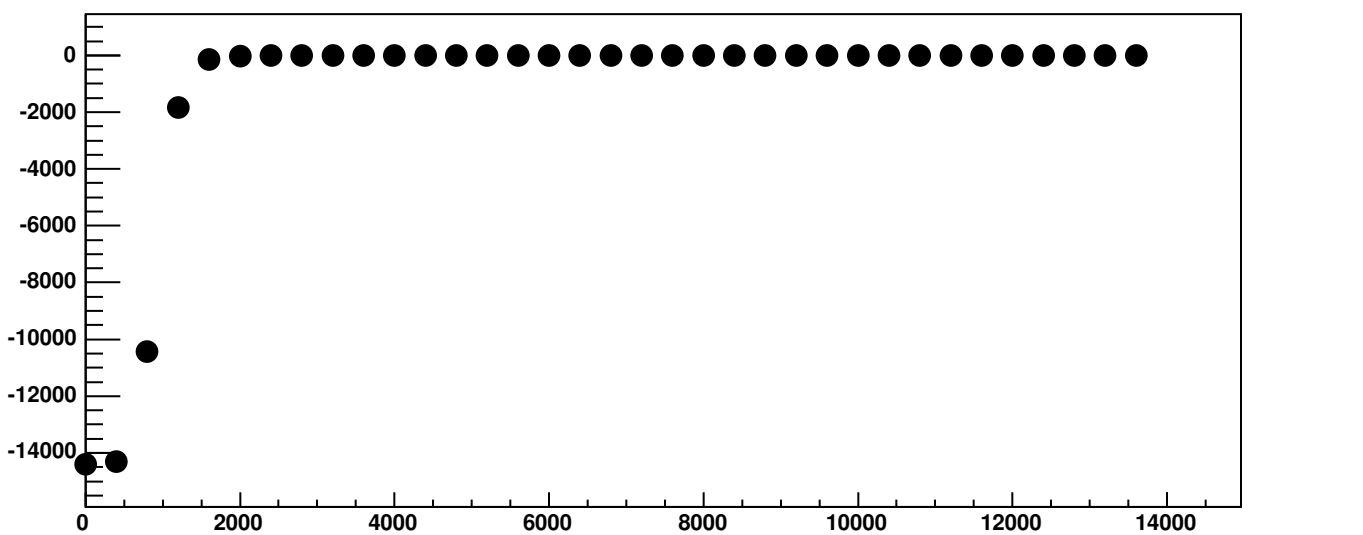
Chip 8, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC



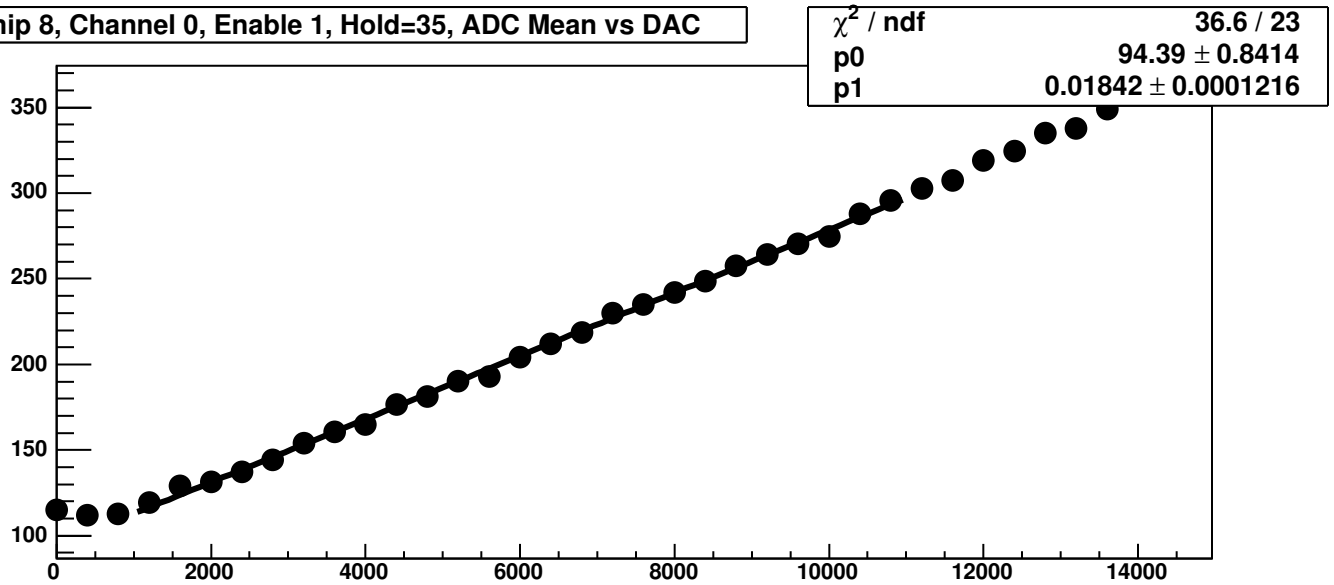
Chip 8, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC



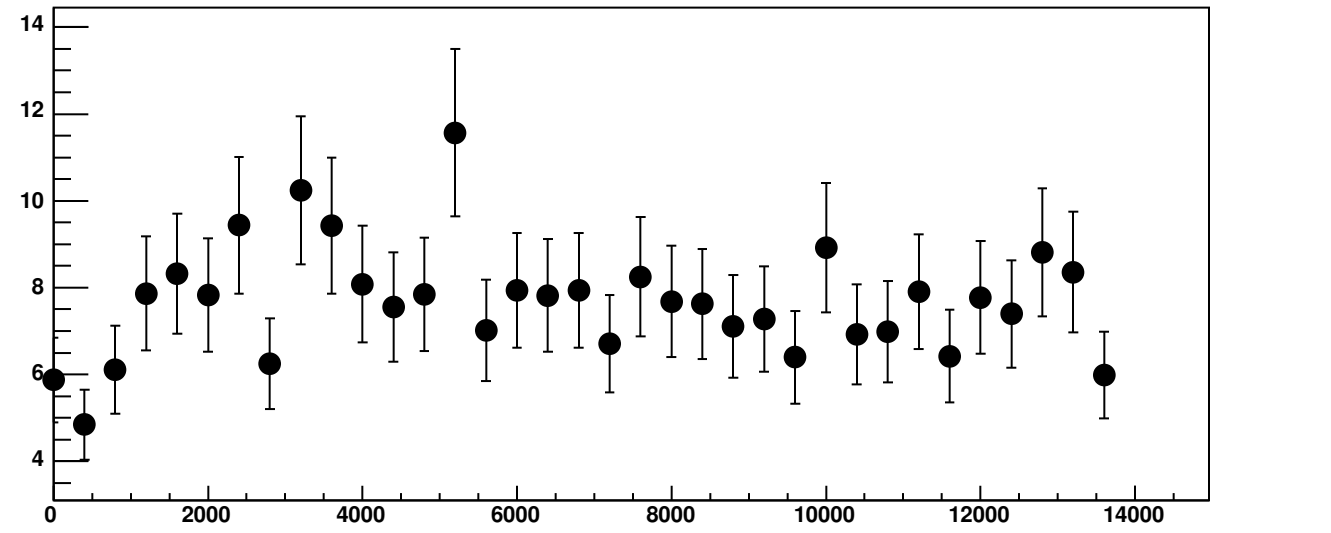
Chip 8, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC



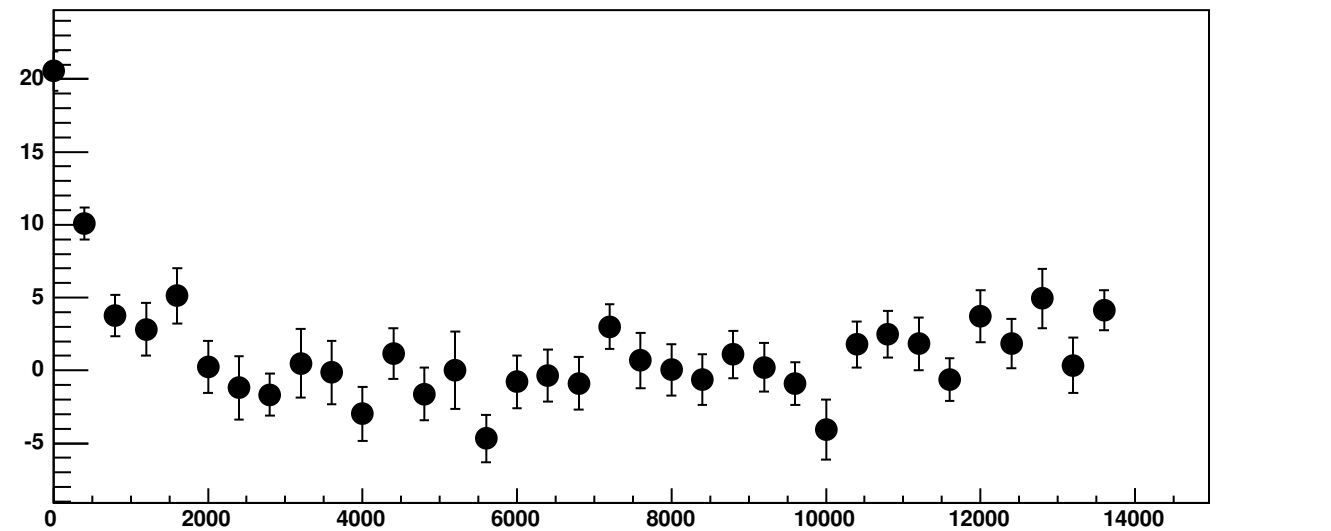
Chip 8, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

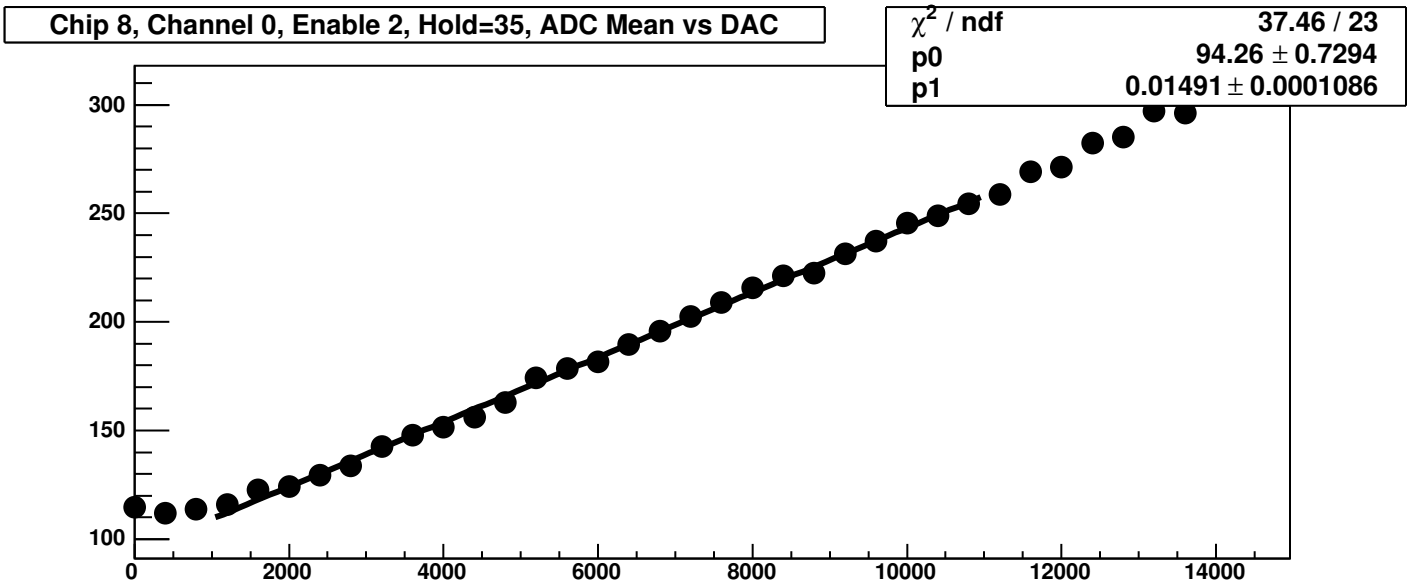


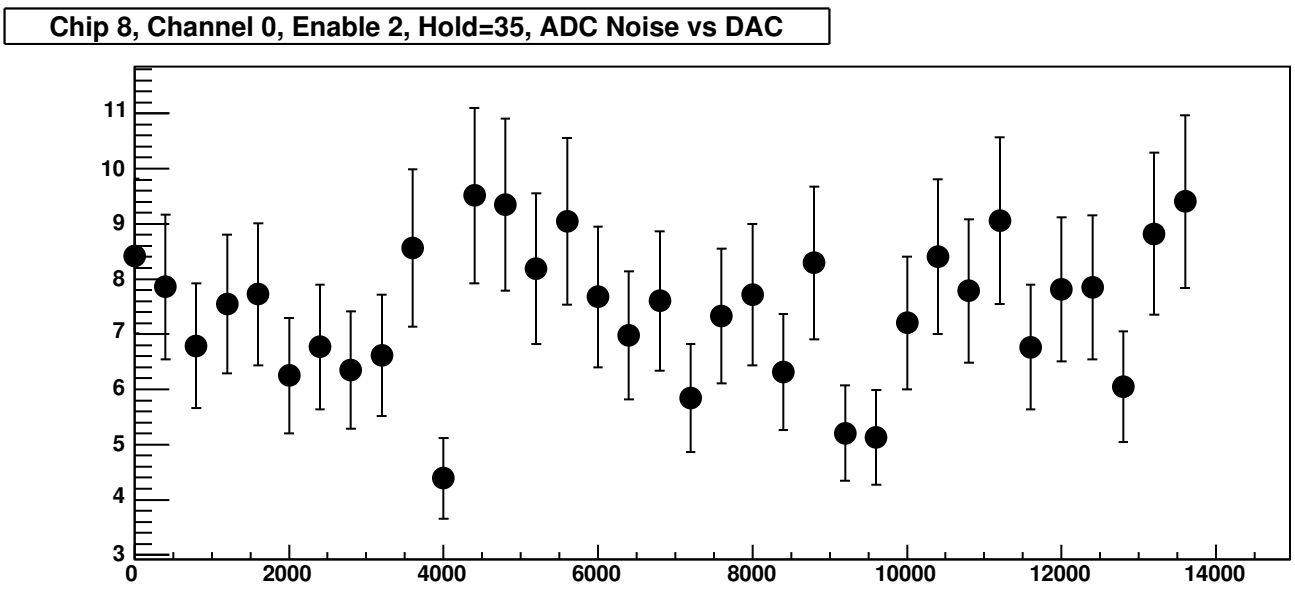
Chip 8, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC

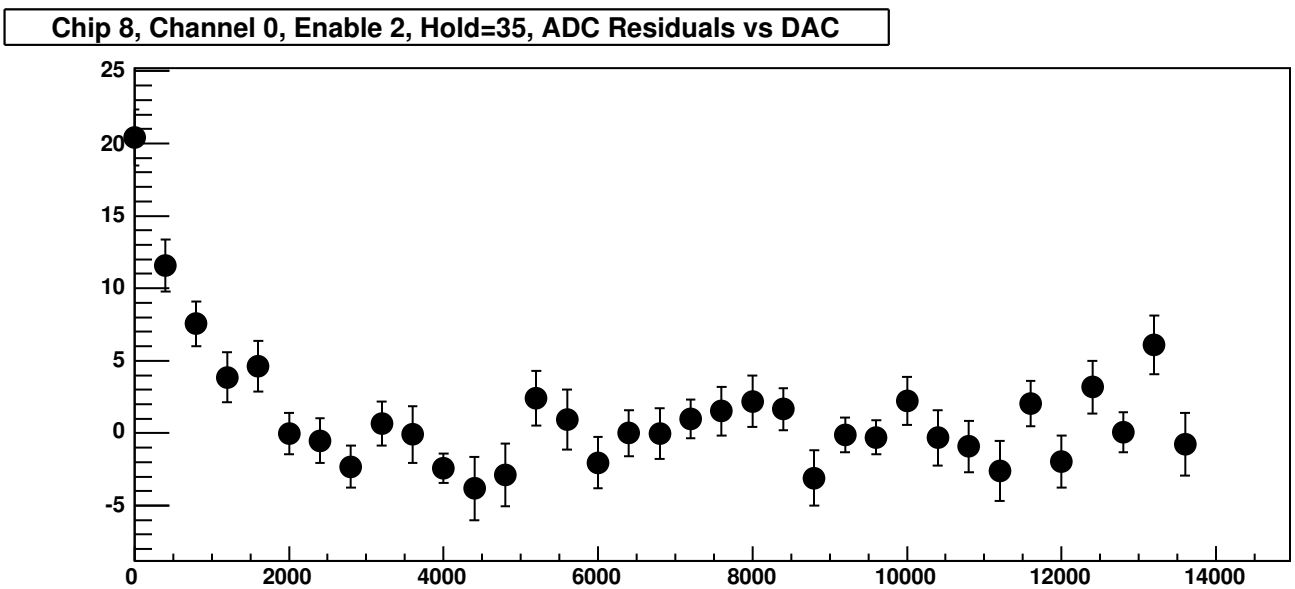


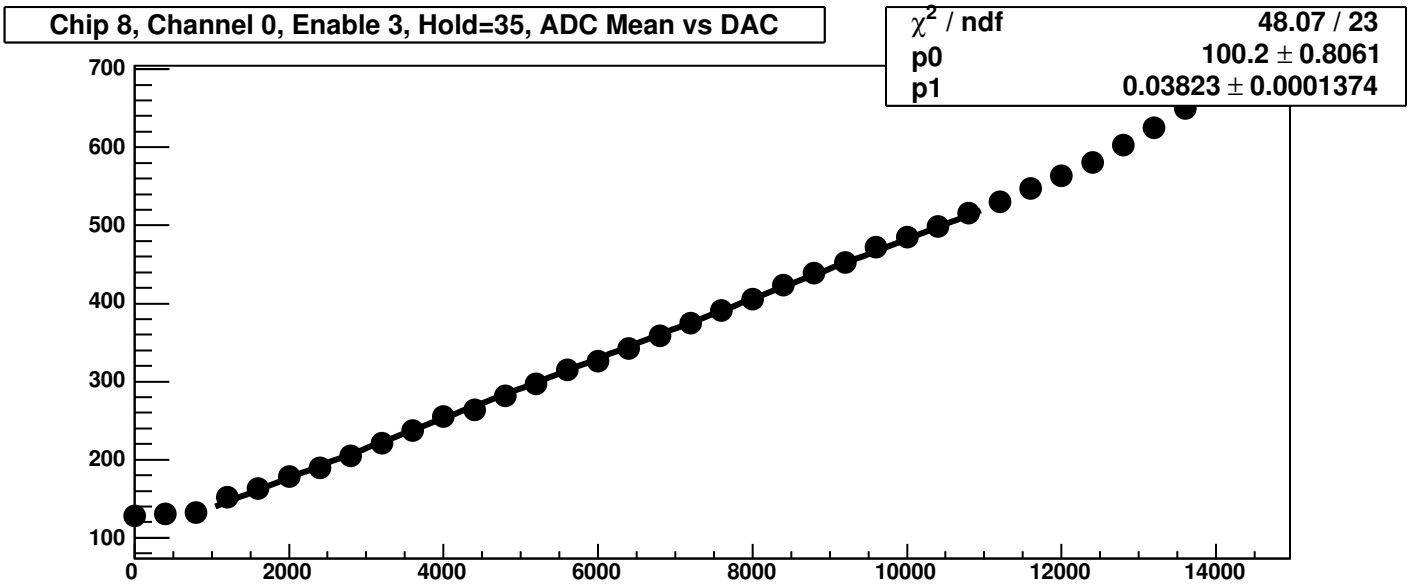
Chip 8, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

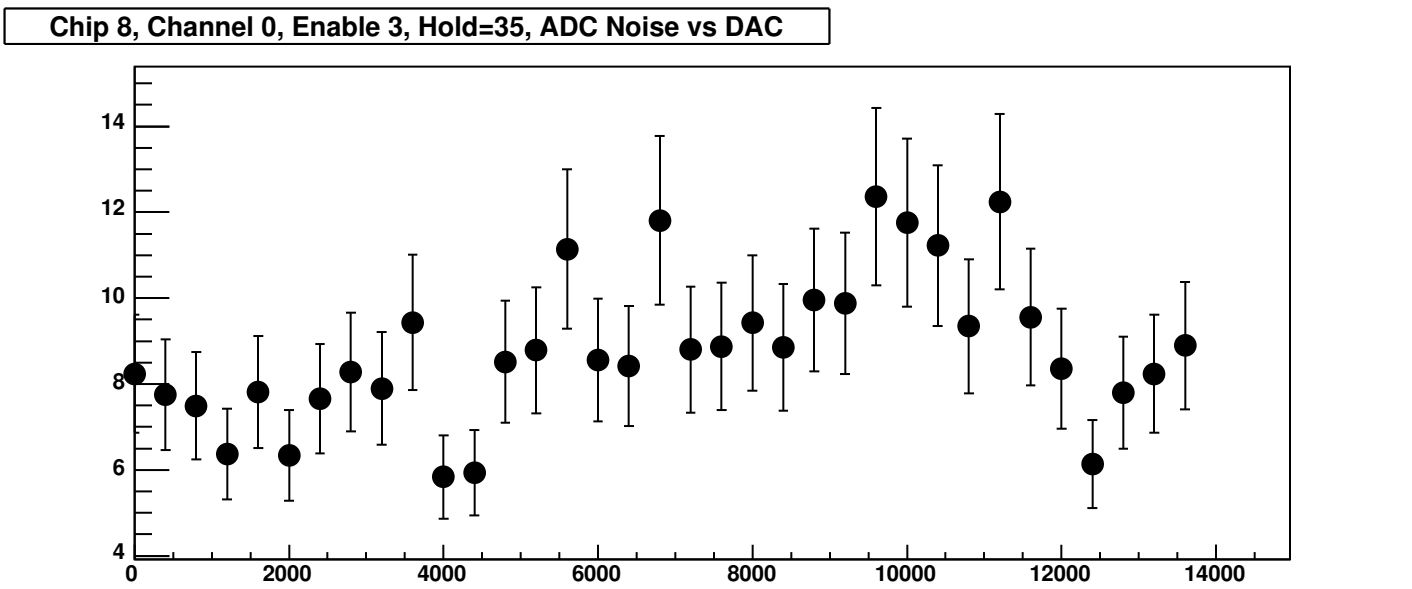


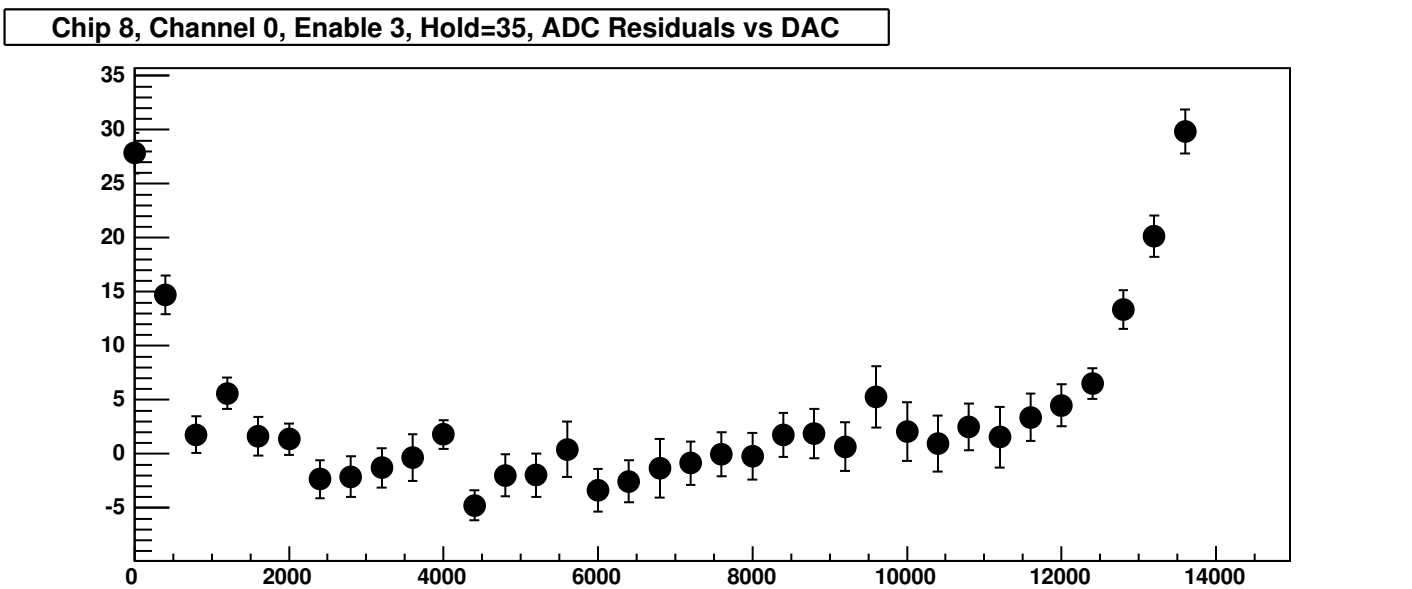

Chip 8, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 37.46 / 23
p0 94.26 ± 0.7294
p1 0.01491 ± 0.0001086
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

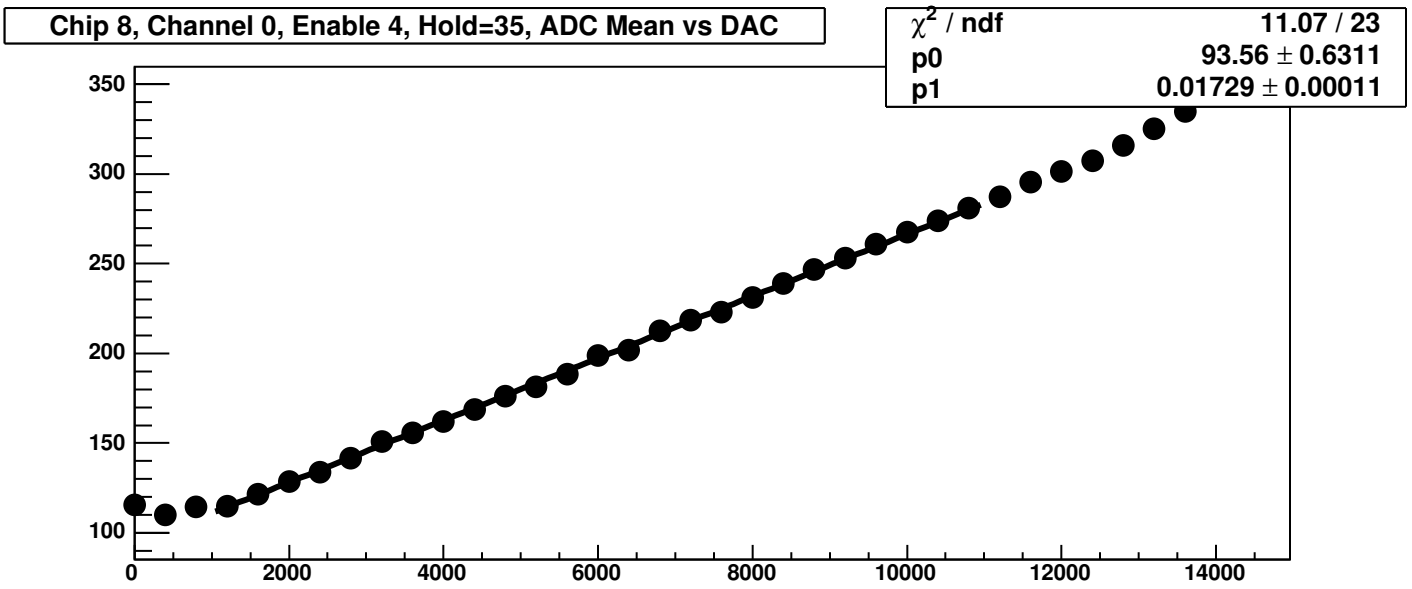

Chip 8, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

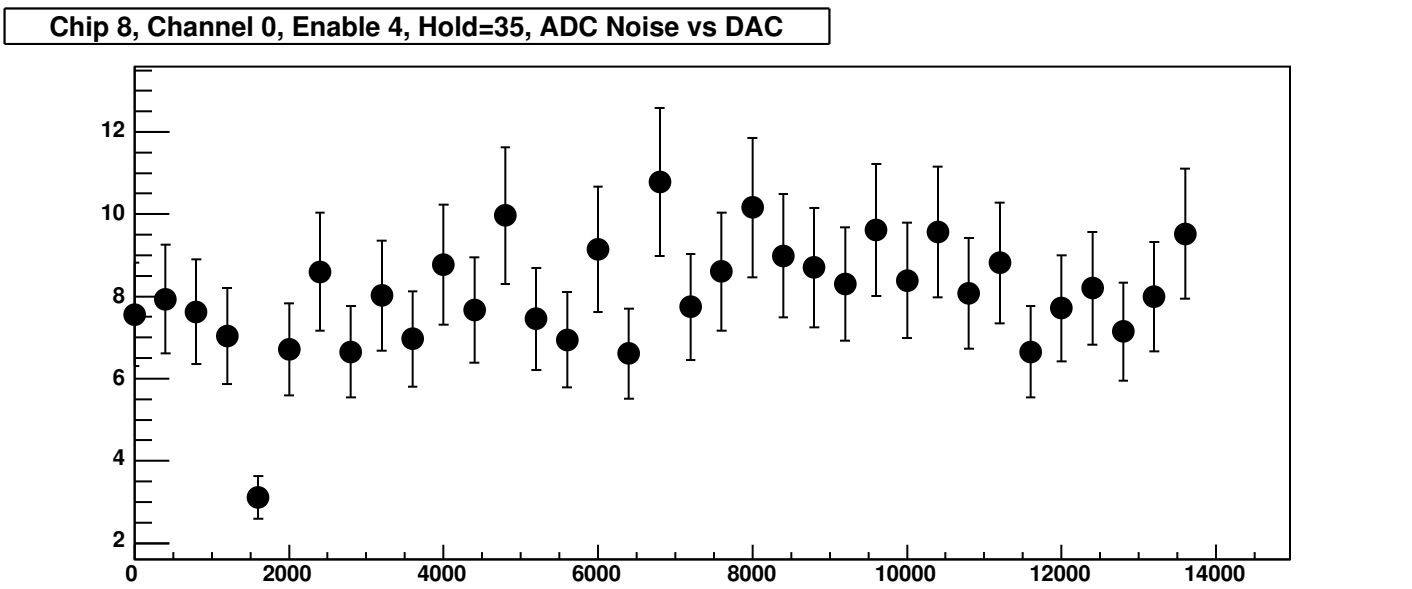

Chip 8, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

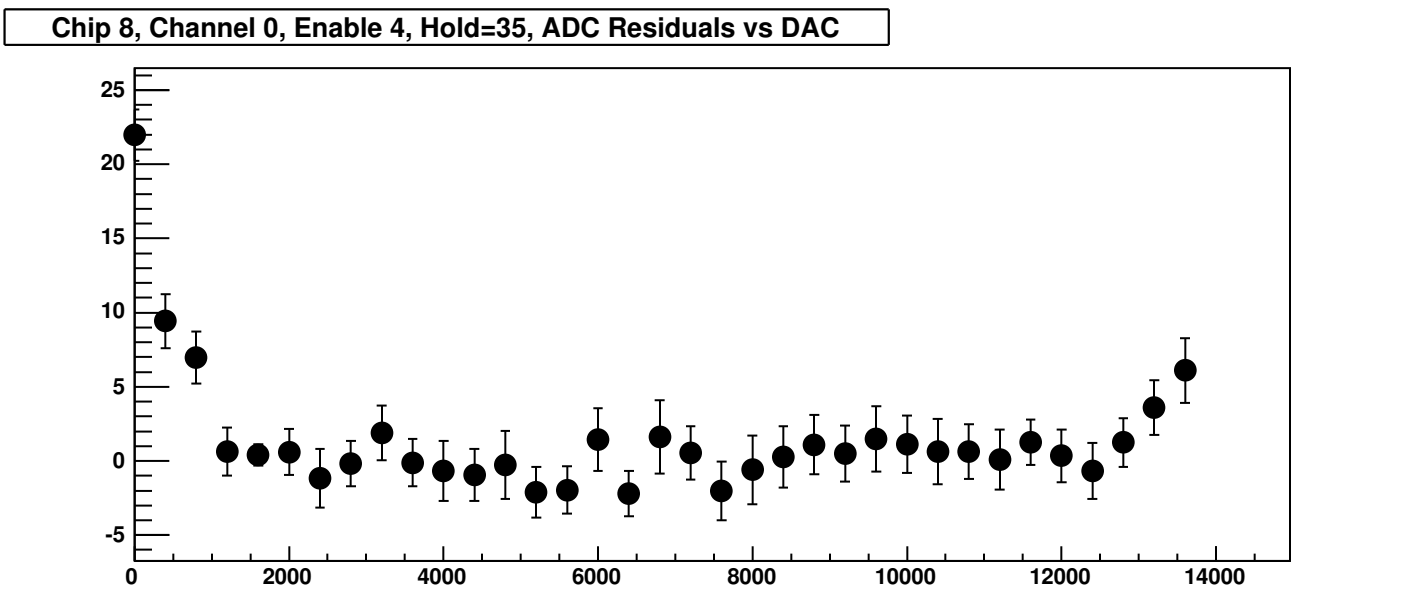

Chip 8, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 48.07 / 23
p0 100.2 ± 0.8061
p1 0.03823 ± 0.0001374
700
600
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000

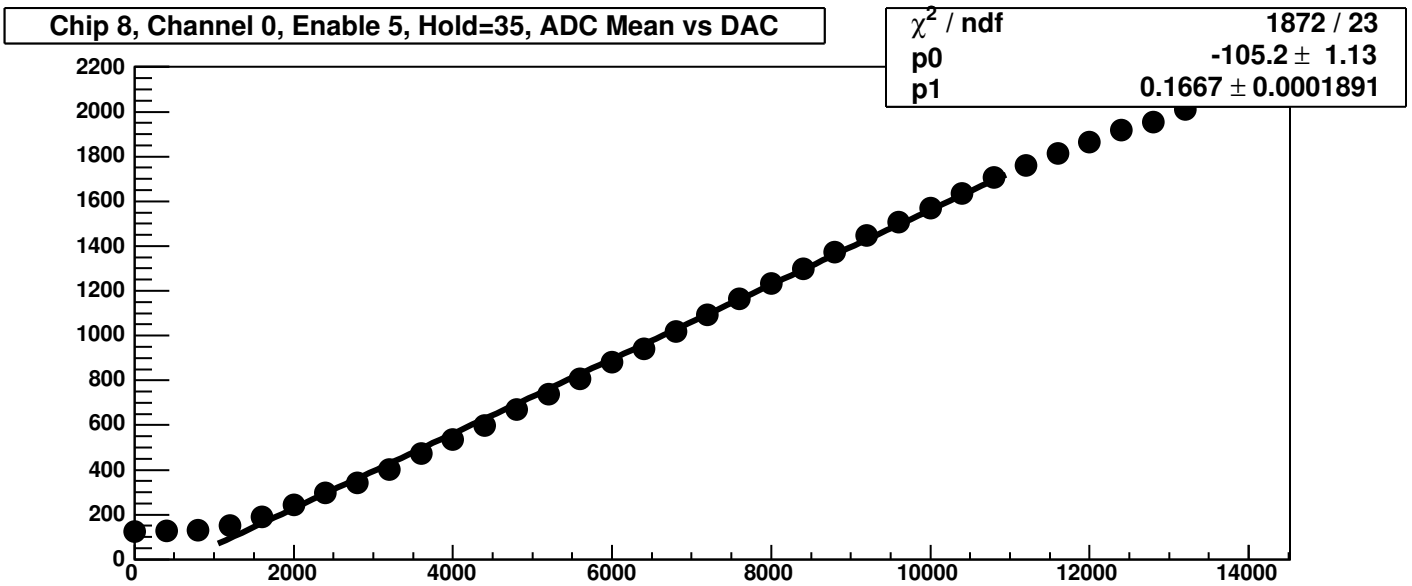

Chip 8, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

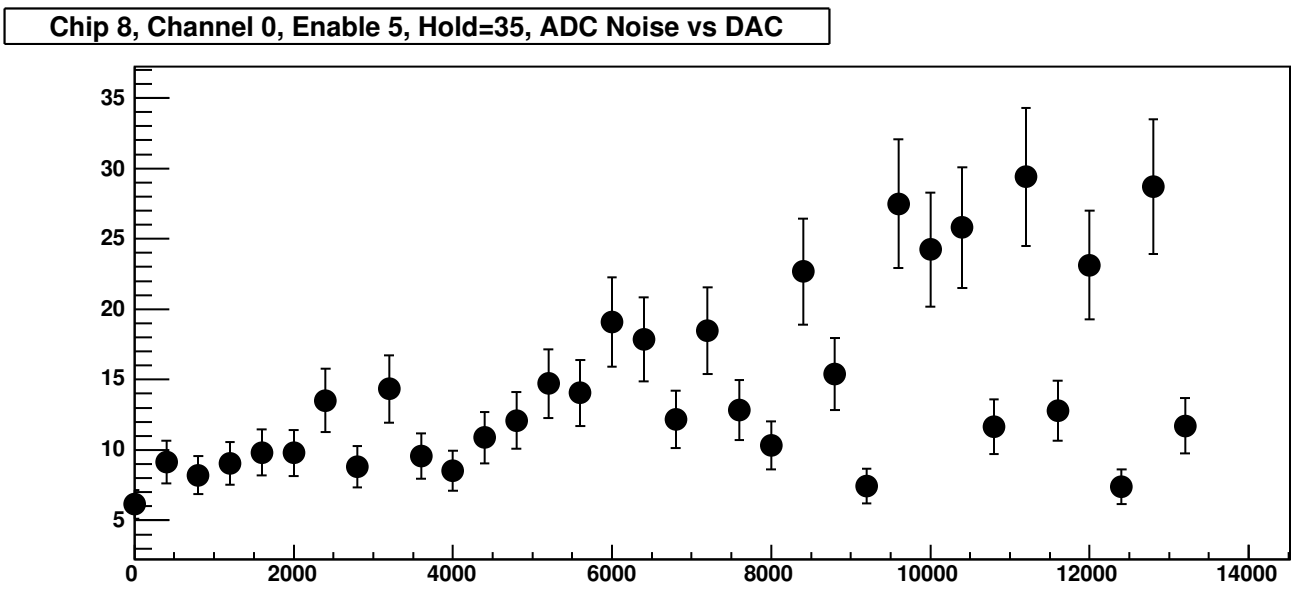

Chip 8, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

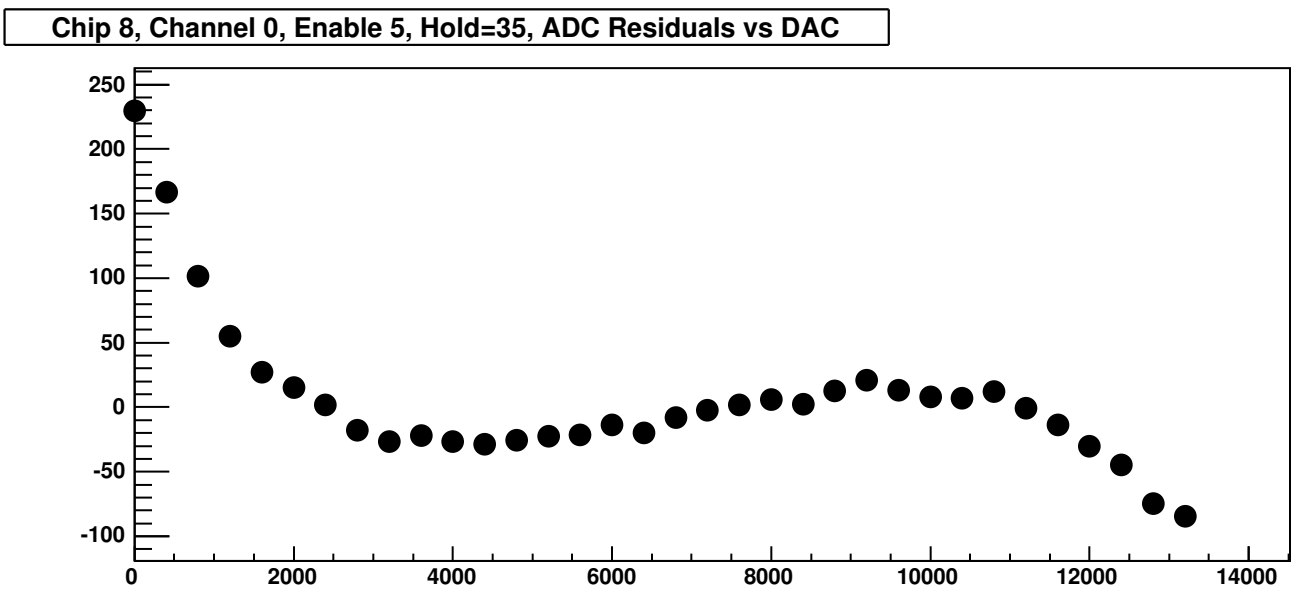

Chip 8, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 11.07 / 23
p0 93.56 ± 0.6311
p1 0.01729 ± 0.00011
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

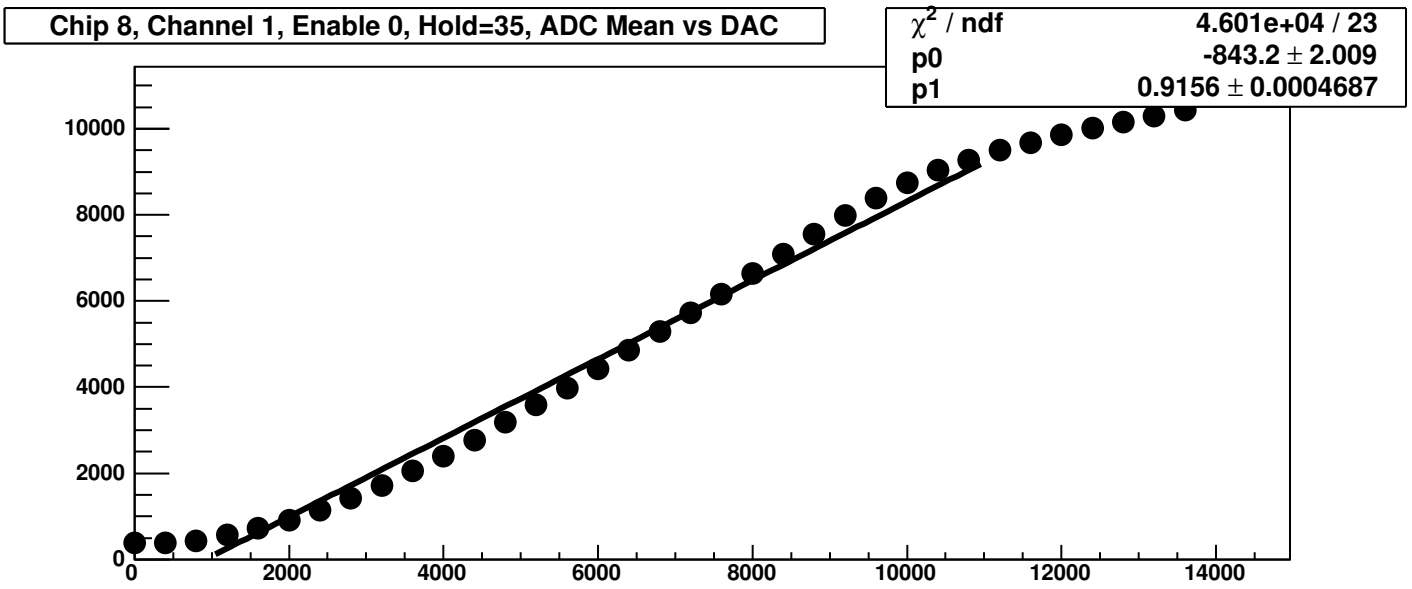

Chip 8, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

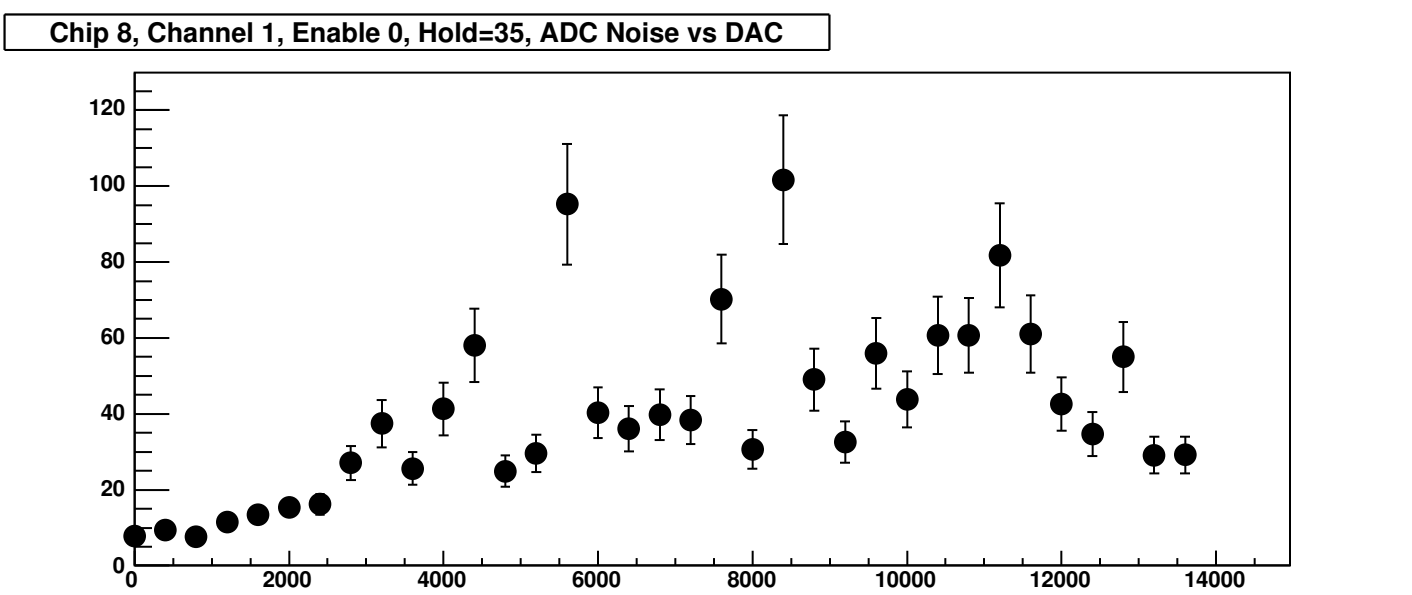

Chip 8, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

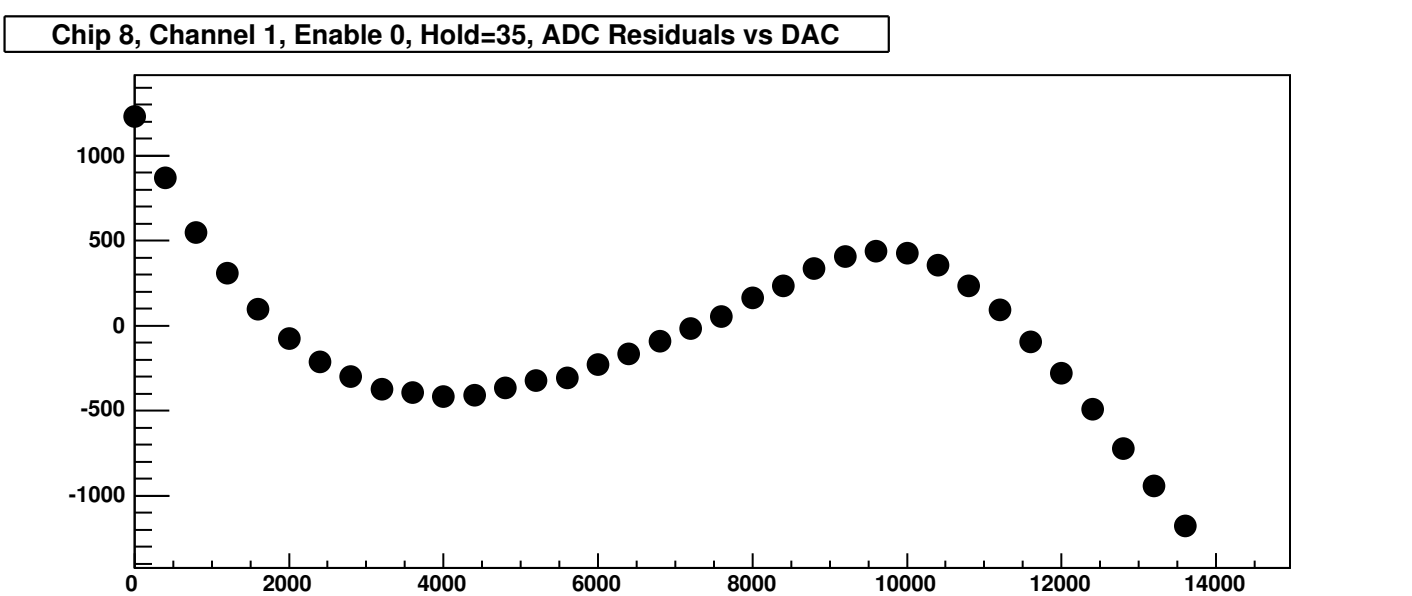

Chip 8, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1872 / 23
p0 -105.2 ± 1.13
p1 0.1667 ± 0.0001891
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

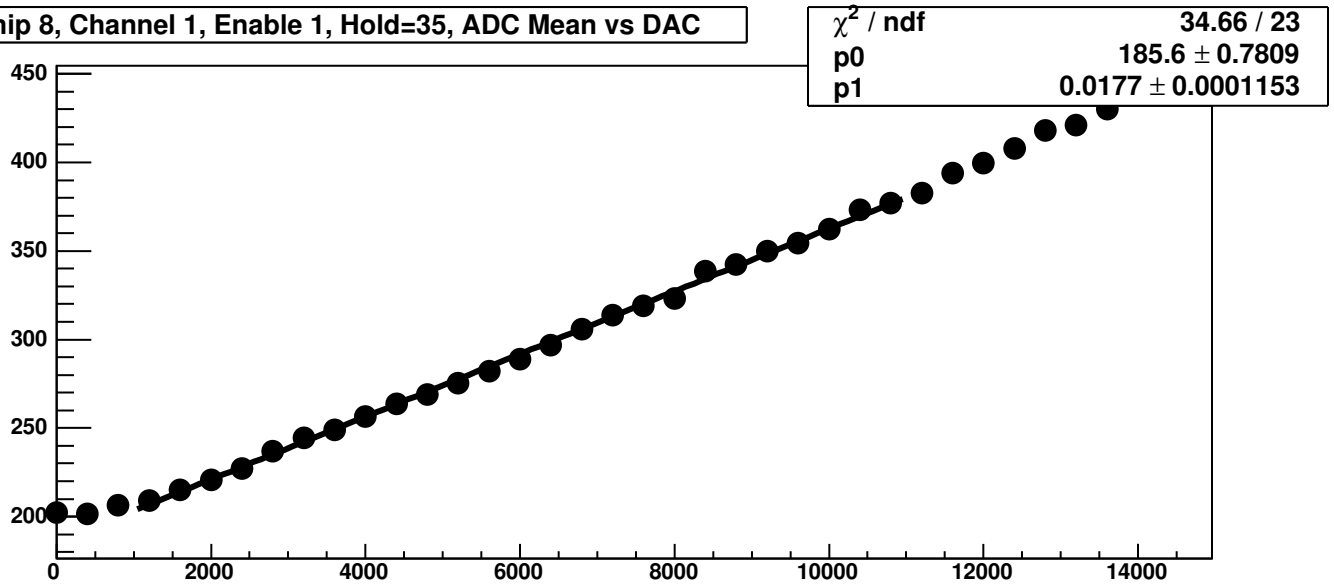

Chip 8, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.601e+04 / 23
p0 -843.2 ± 2.009
p1 0.9156 ± 0.0004687
0
2000
4000
6000
8000
10000
12000
14000

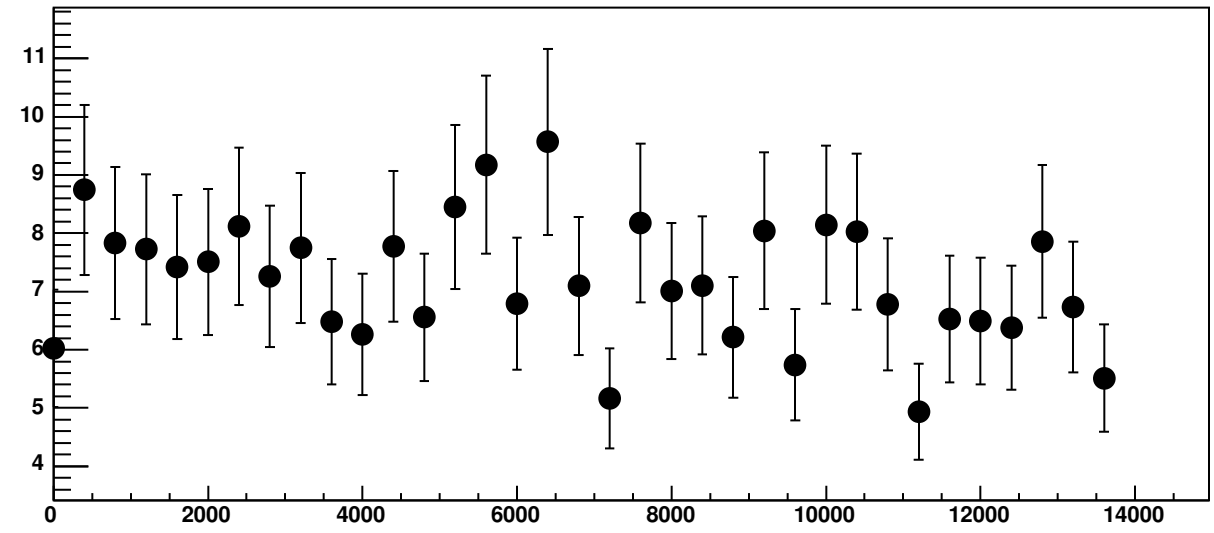

Chip 8, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
0
20
40
60
80
100
120
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
-1000
-500
0
500
1000
2000
4000
6000
8000
10000
12000
14000

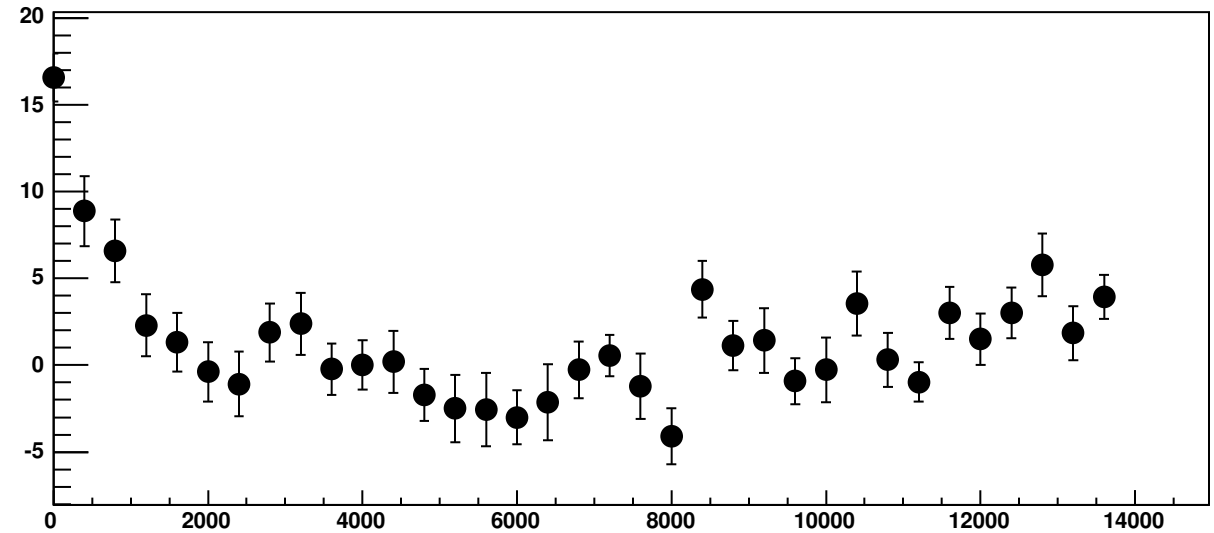
Chip 8, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

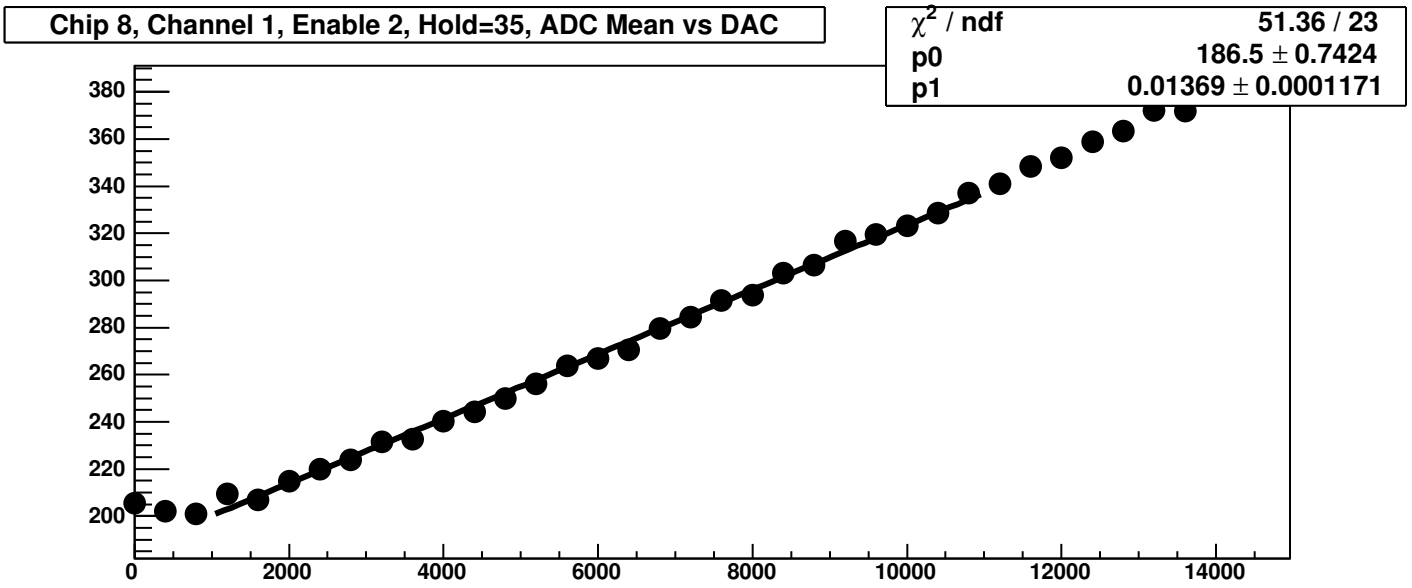


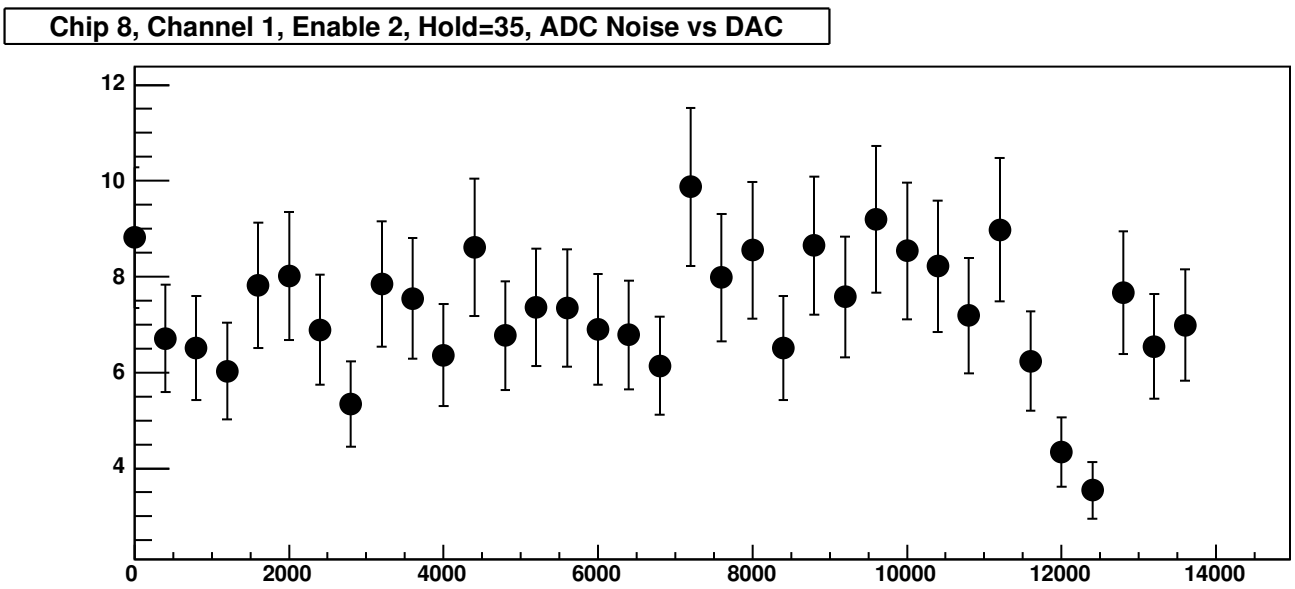
Chip 8, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC

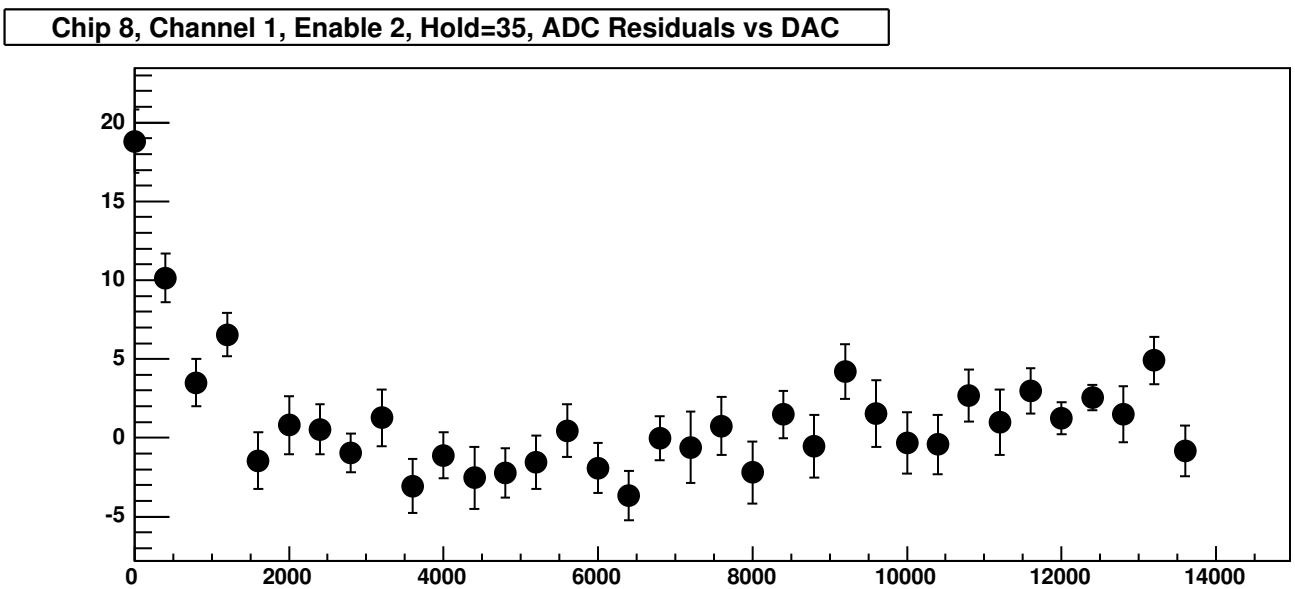


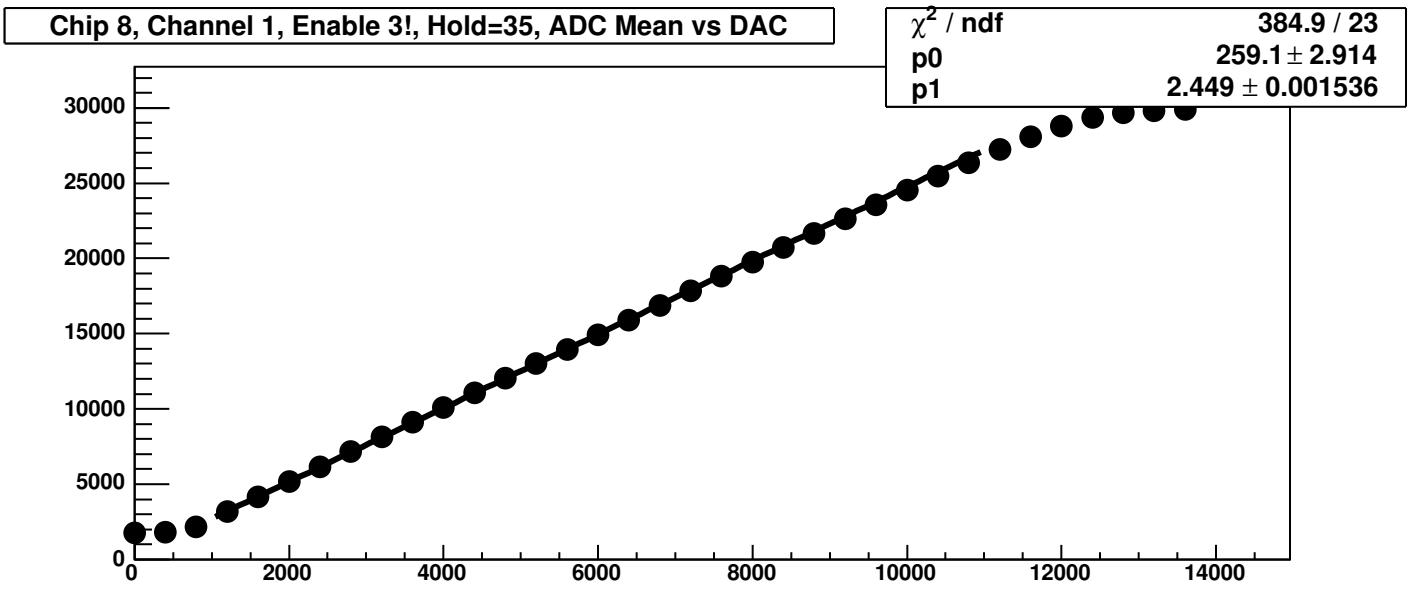
Chip 8, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

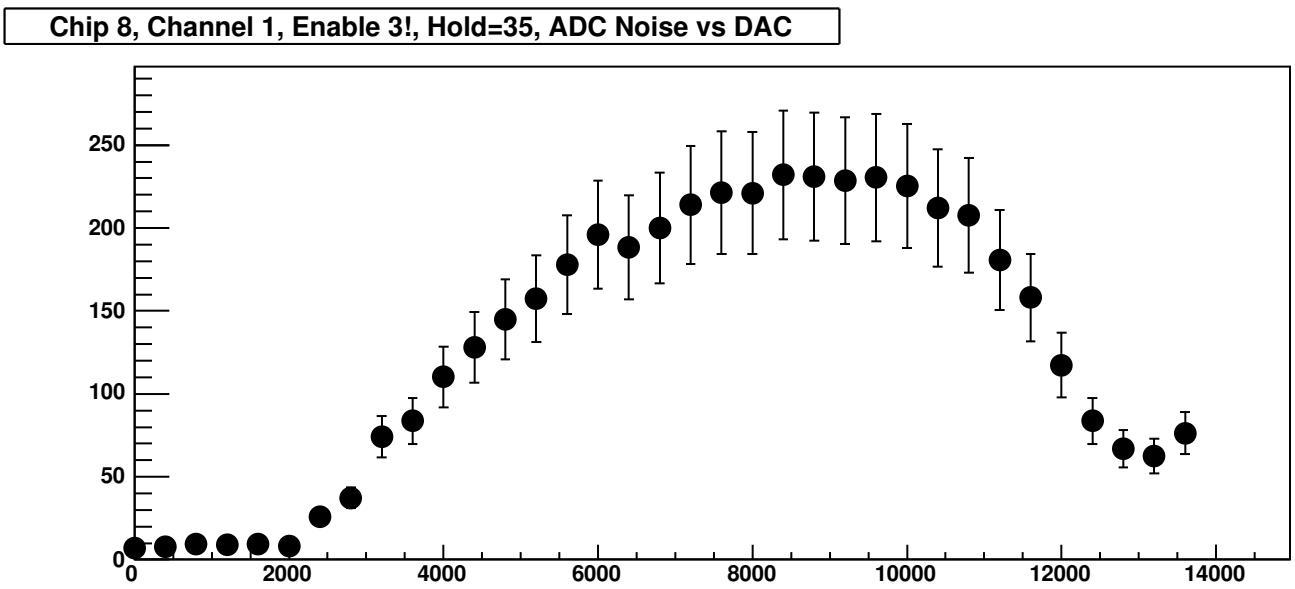


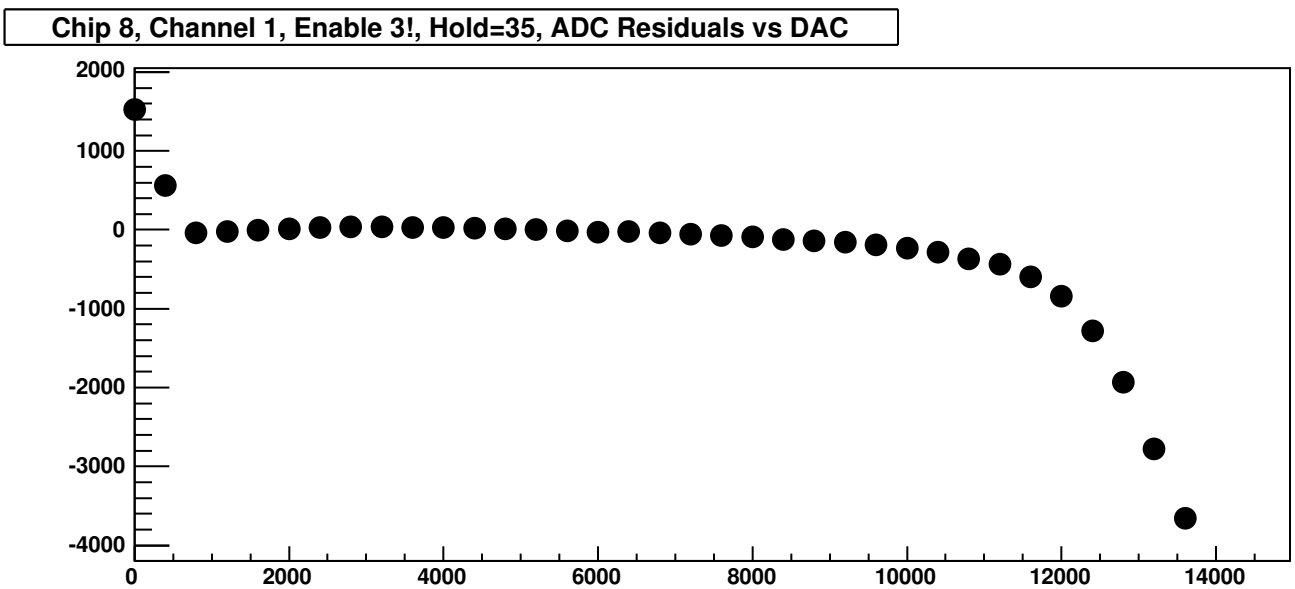

Chip 8, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 51.36 / 23
p0 186.5 ± 0.7424
p1 0.01369 ± 0.0001171
380
360
340
320
300
280
260
240
220
200
0
2000
4000
6000
8000
10000
12000
14000

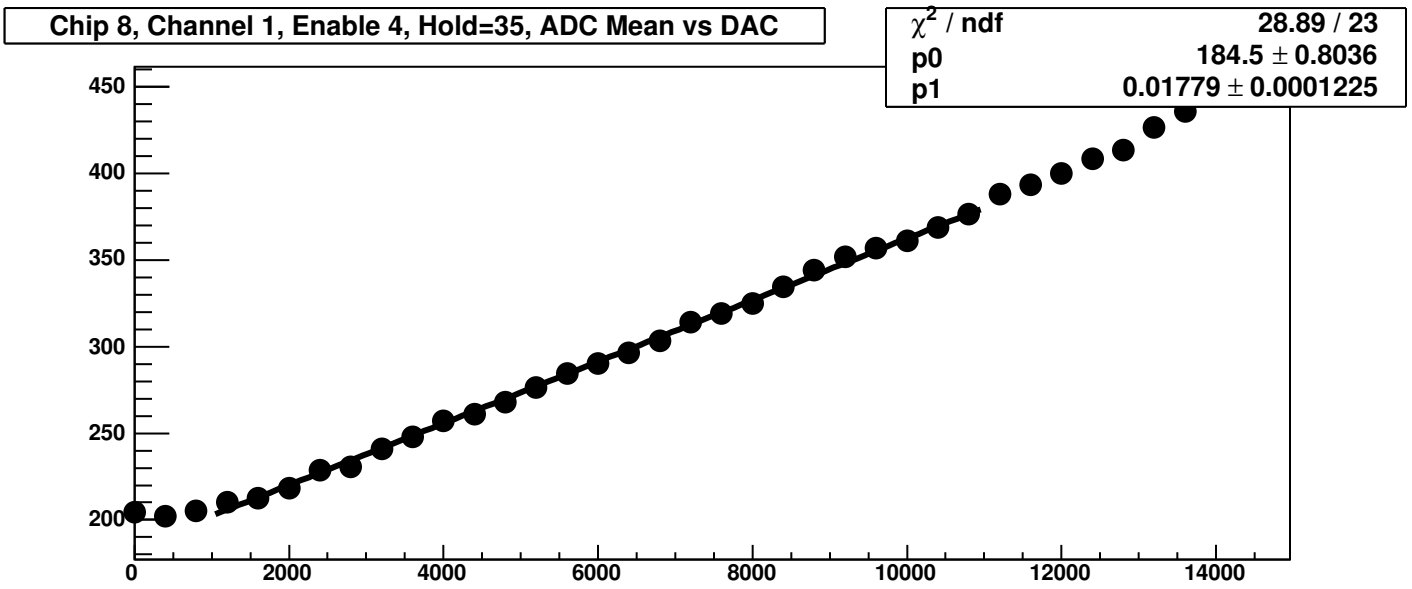

Chip 8, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

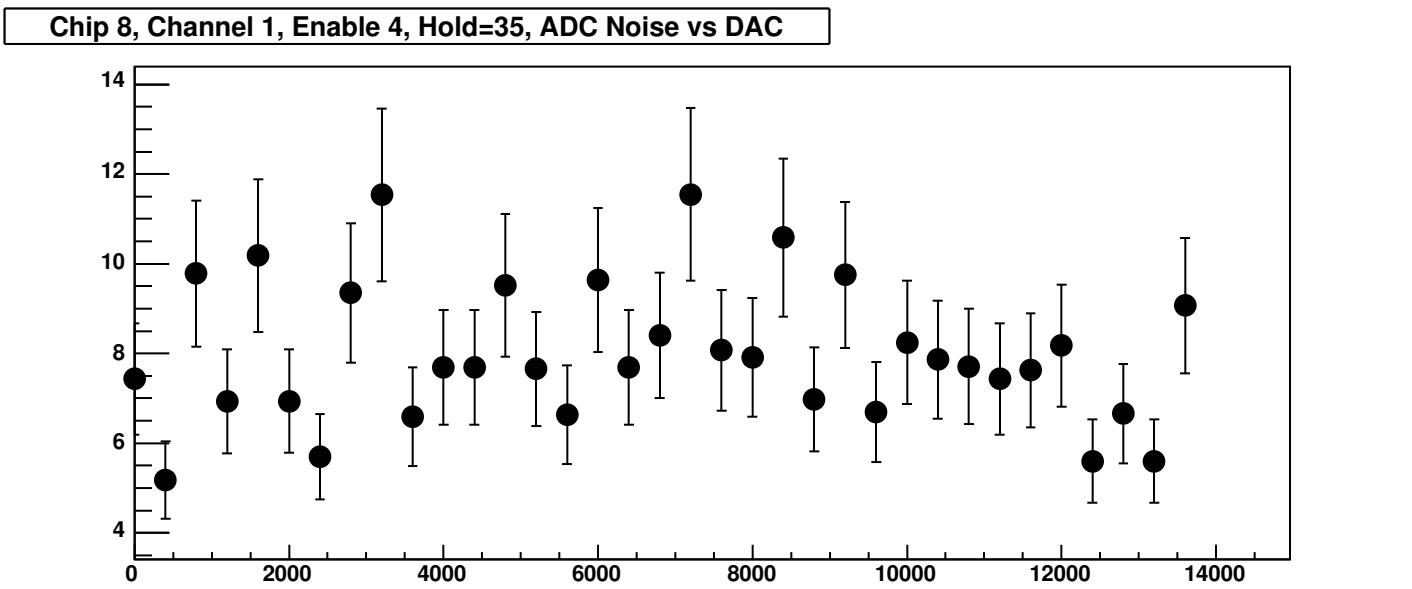

Chip 8, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

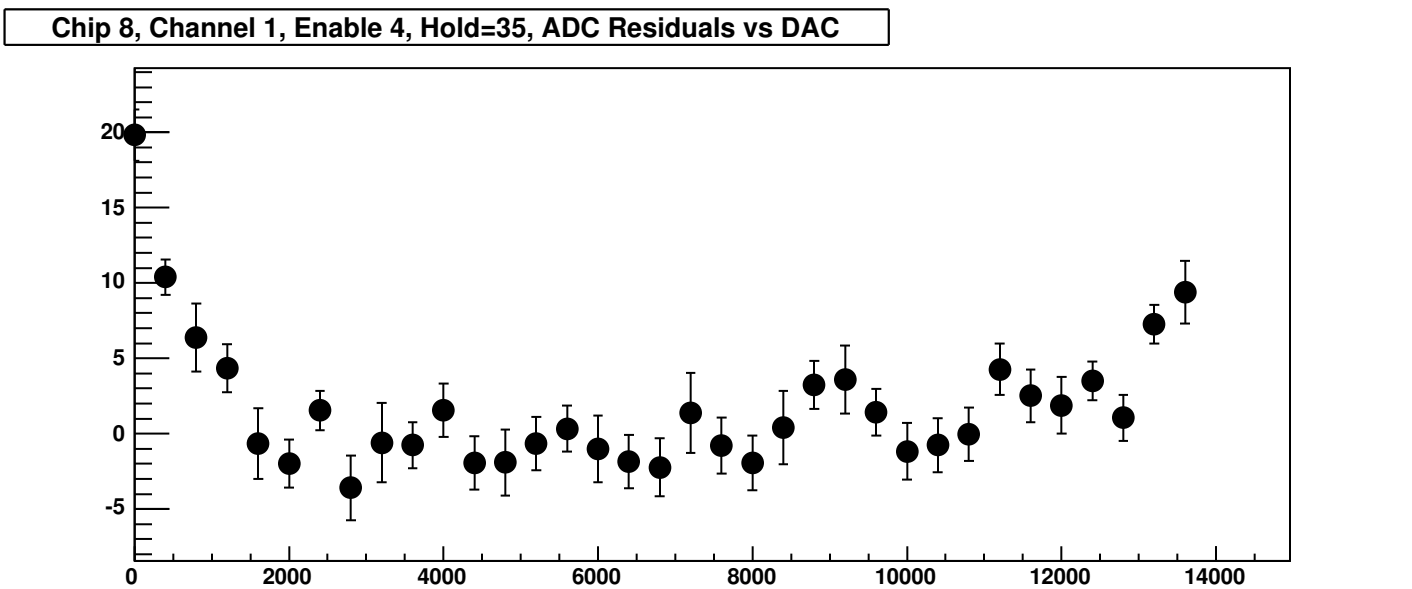

Chip 8, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 384.9 / 23
p0 259.1 ± 2.914
p1 2.449 ± 0.001536
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

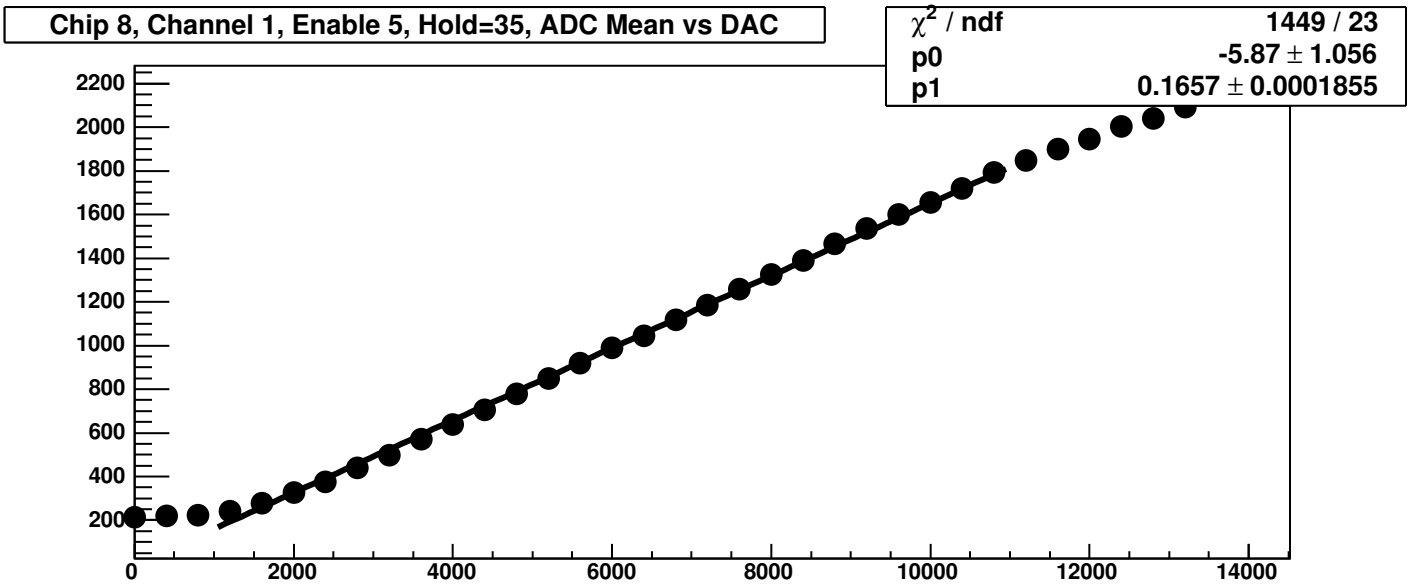

Chip 8, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

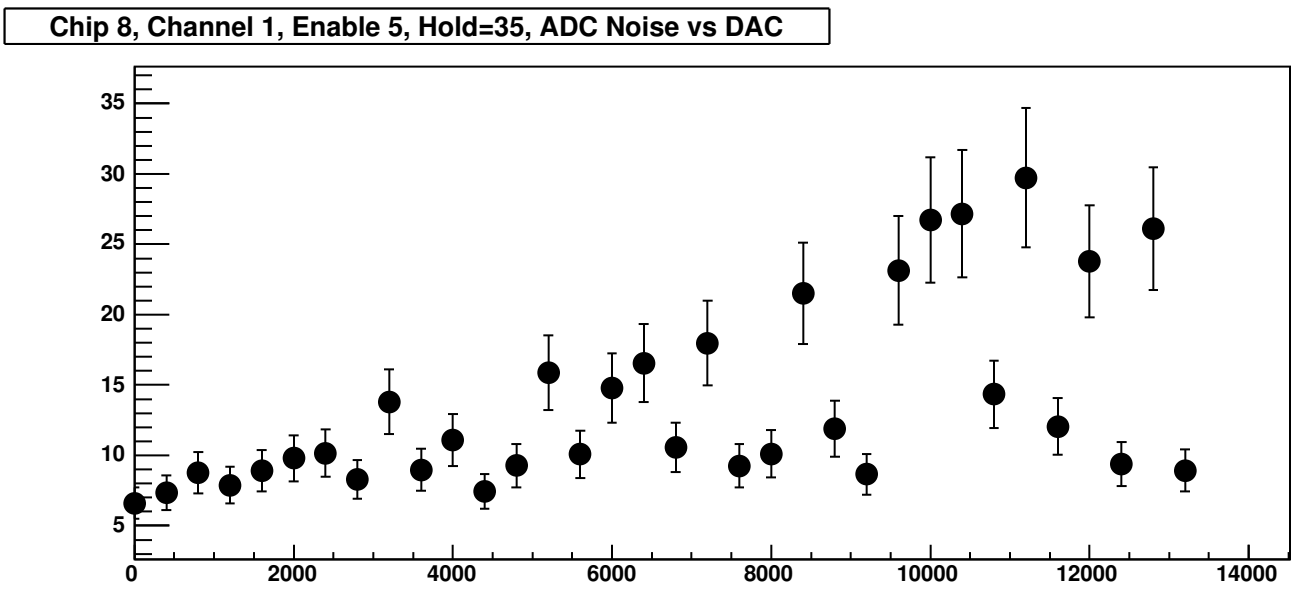

Chip 8, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

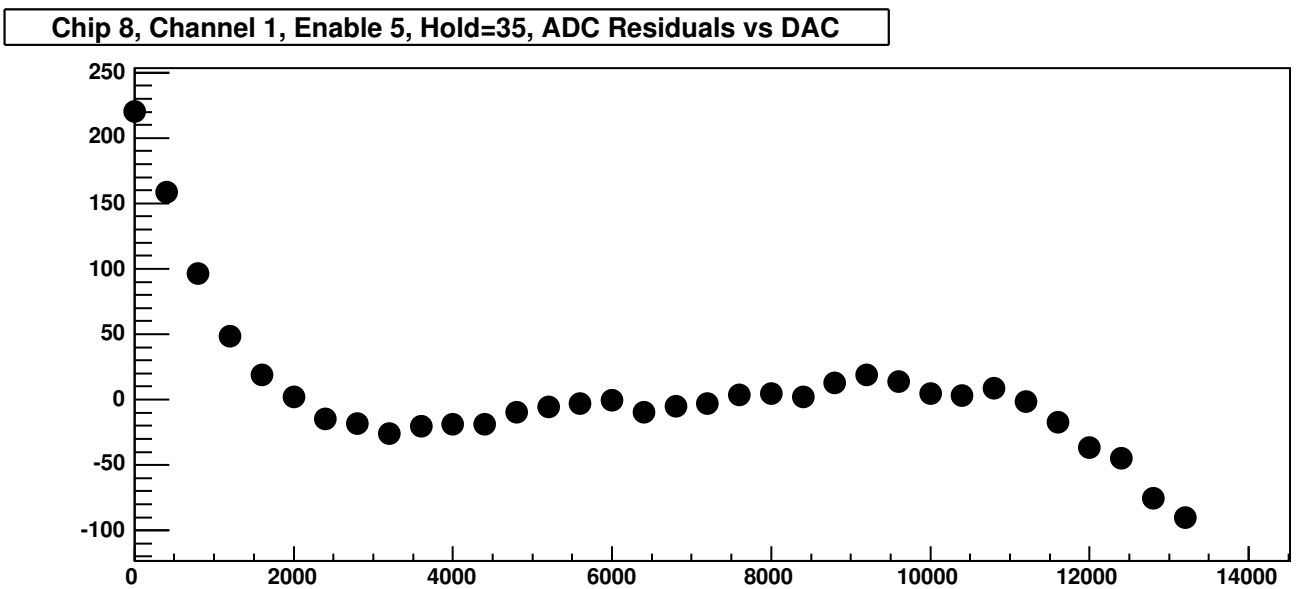

Chip 8, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 28.89 / 23
p0 184.5 ± 0.8036
p1 0.01779 ± 0.0001225
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

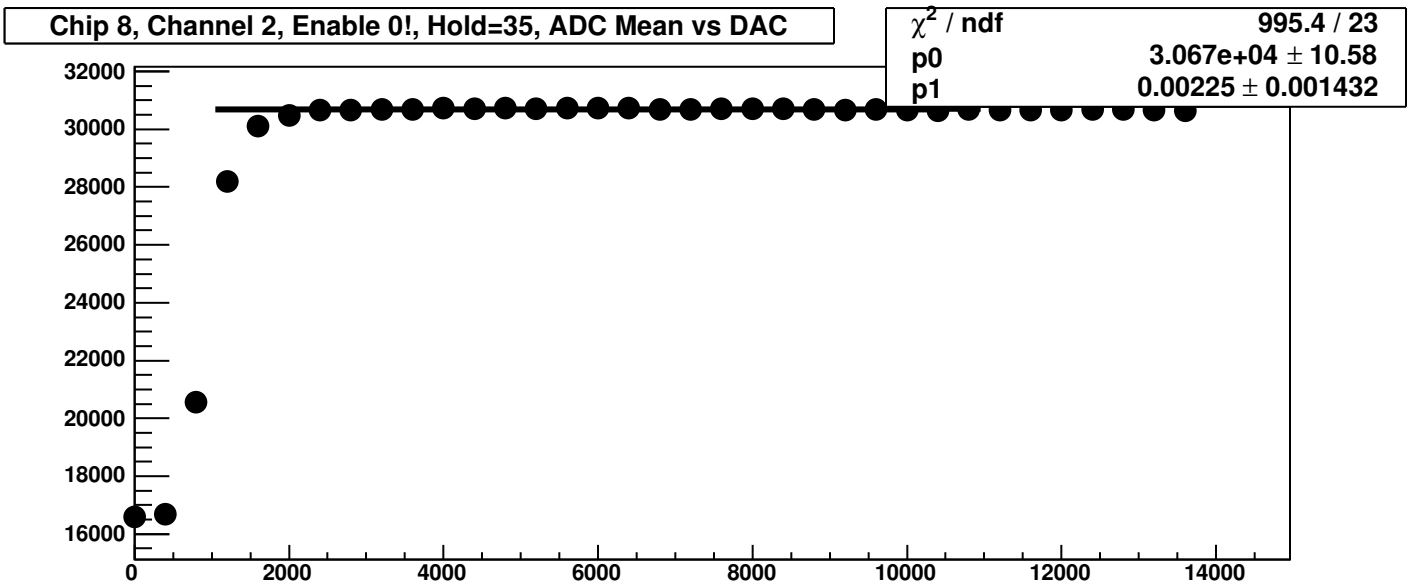

Chip 8, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

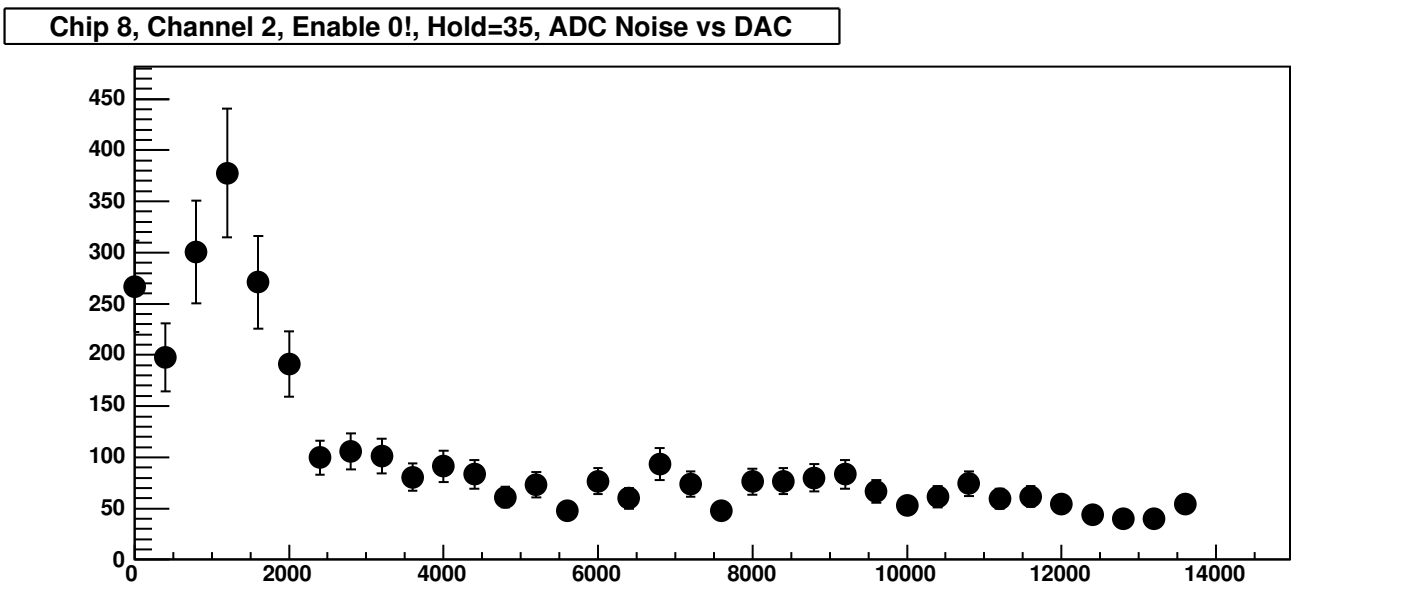

Chip 8, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

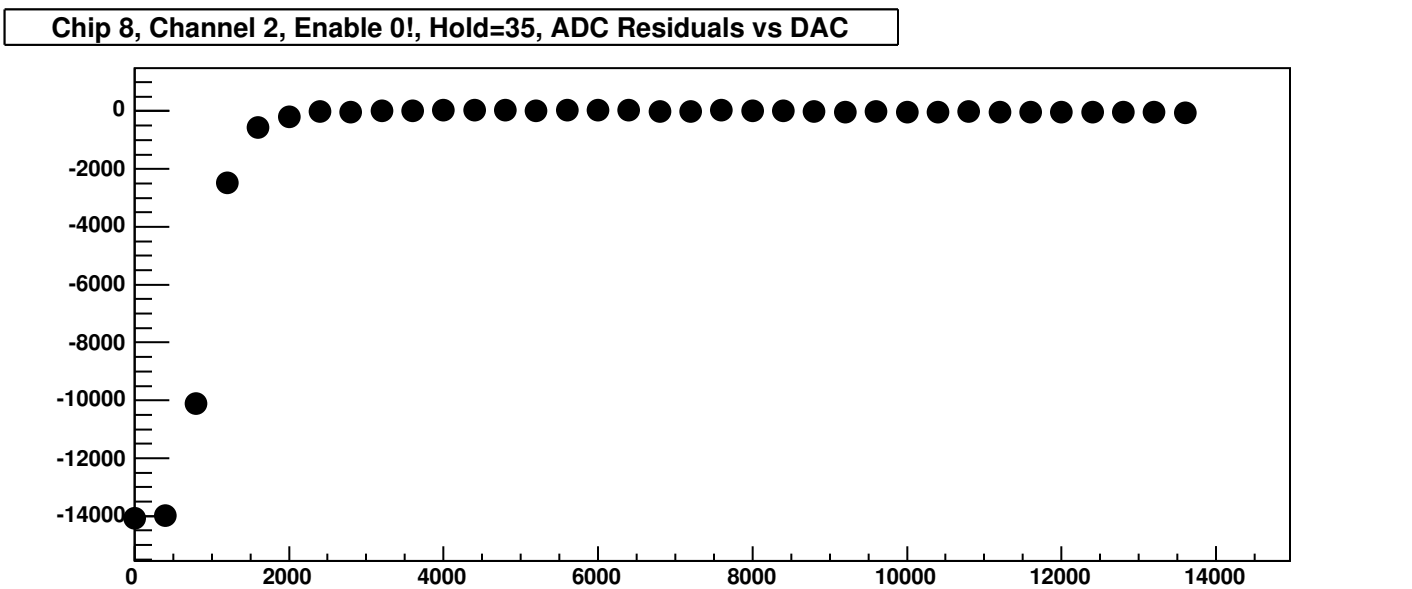

Chip 8, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1449 / 23
p0 -5.87 ± 1.056
p1 0.1657 ± 0.0001855
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

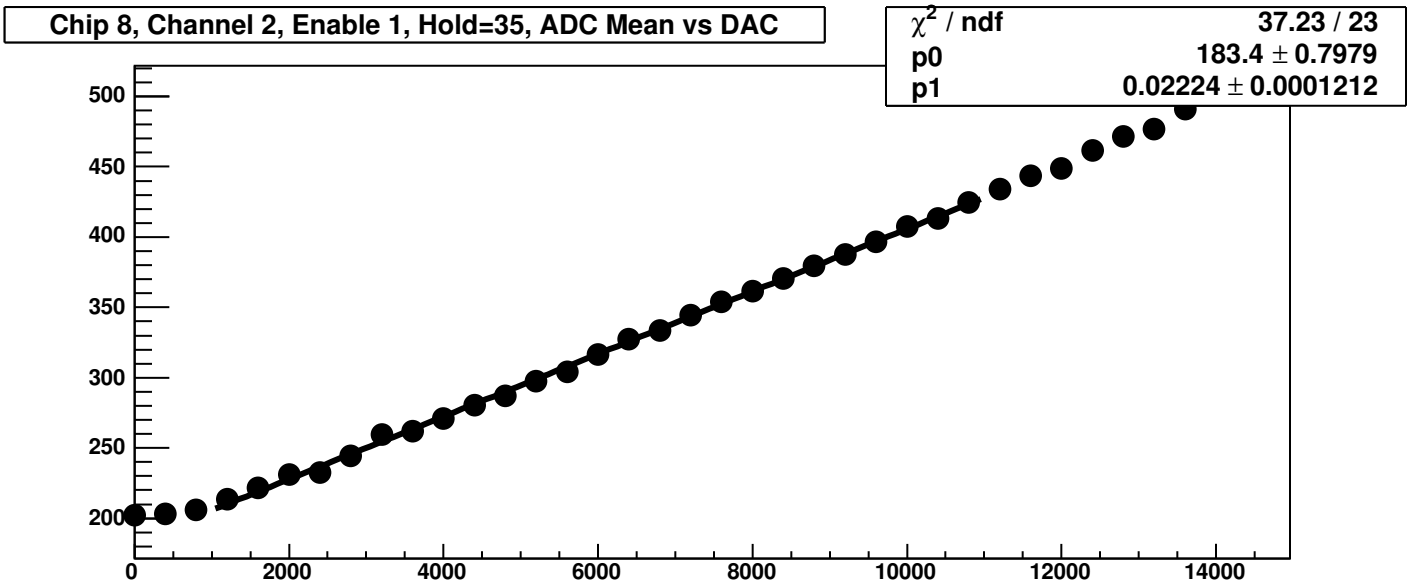

Chip 8, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

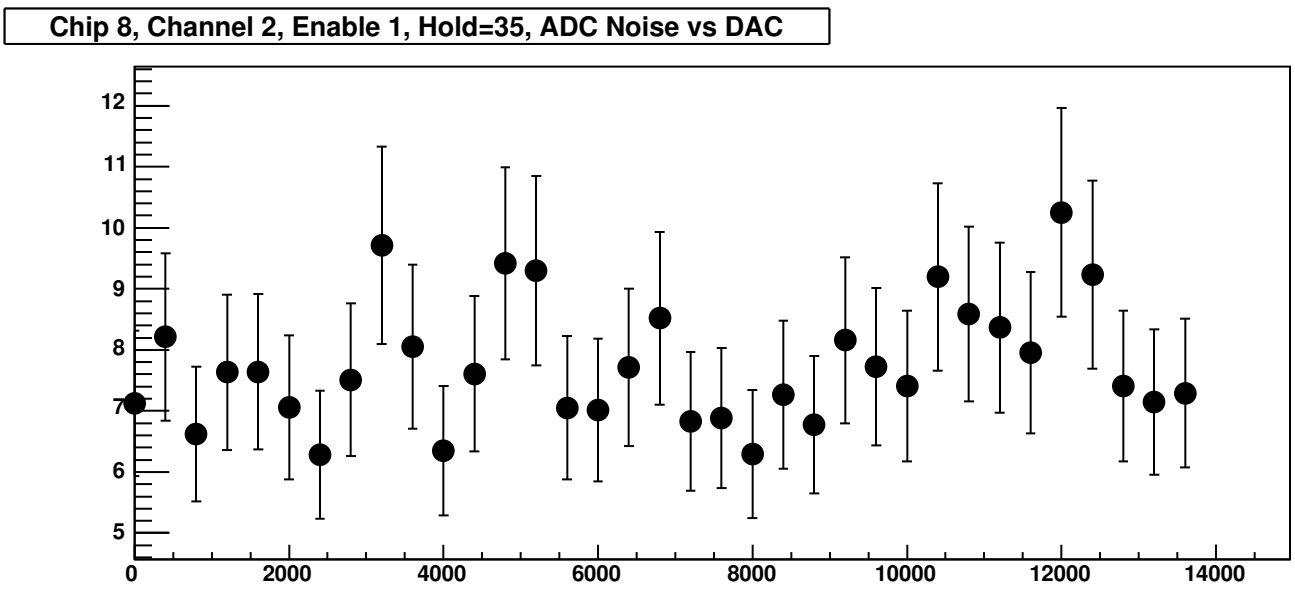

Chip 8, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

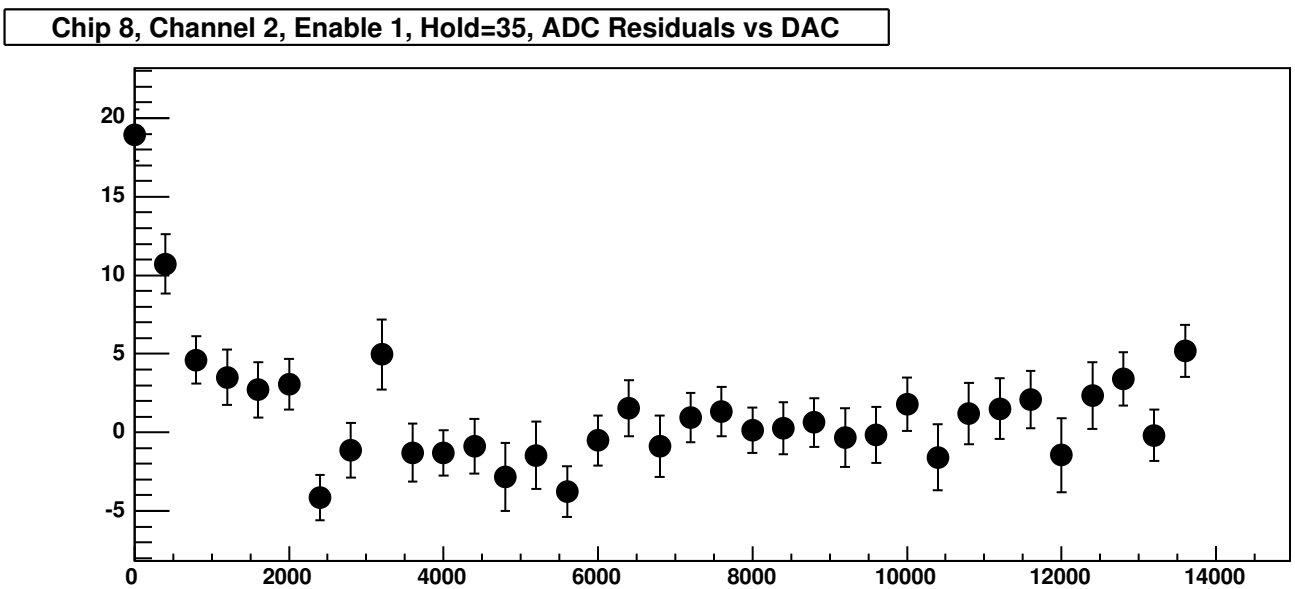

Chip 8, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 995.4 / 23
p0 3.067e+04 ± 10.58
p1 0.00225 ± 0.001432
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

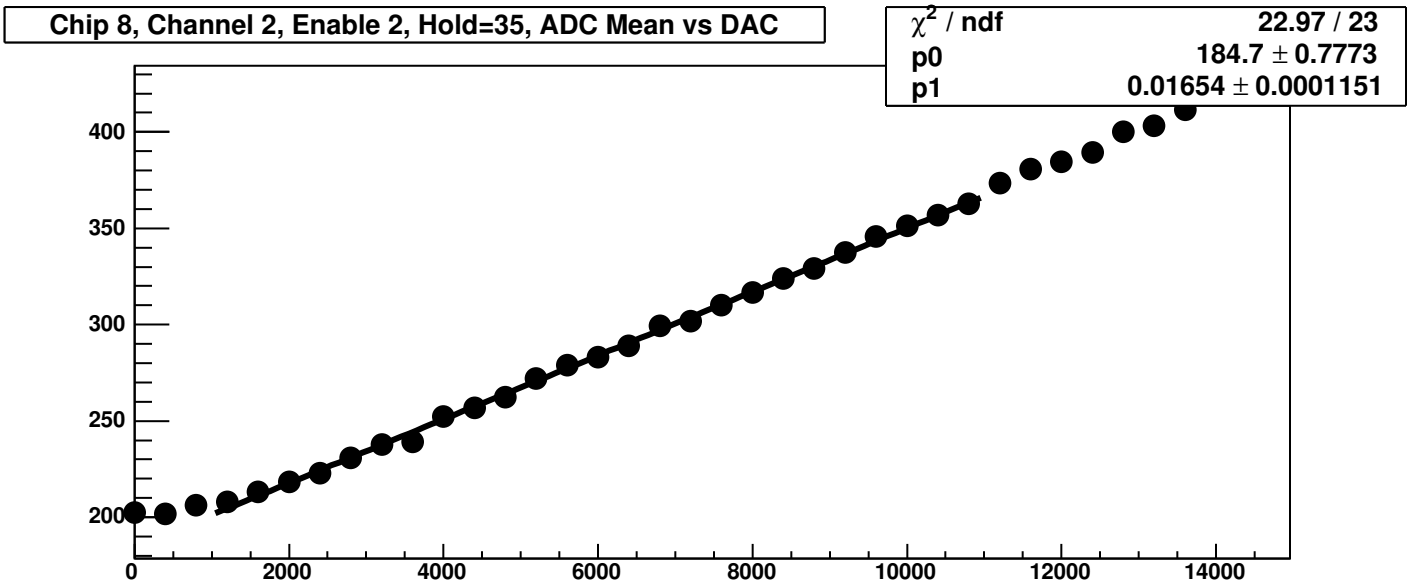

Chip 8, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

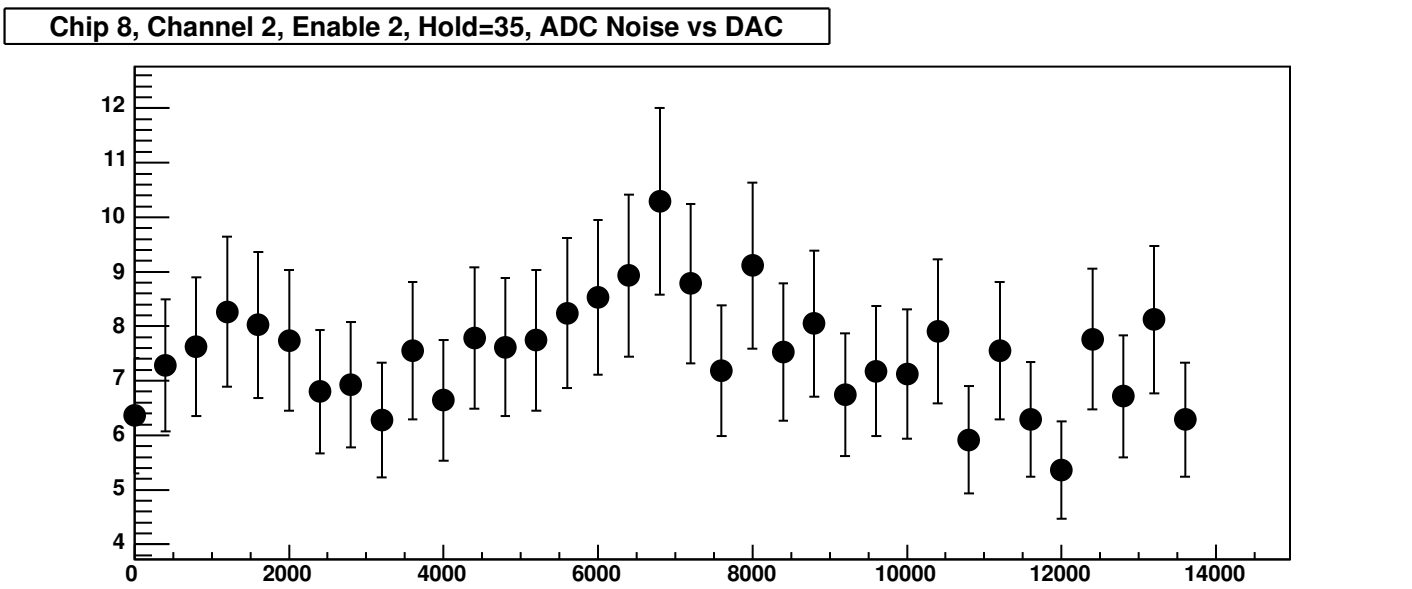

Chip 8, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

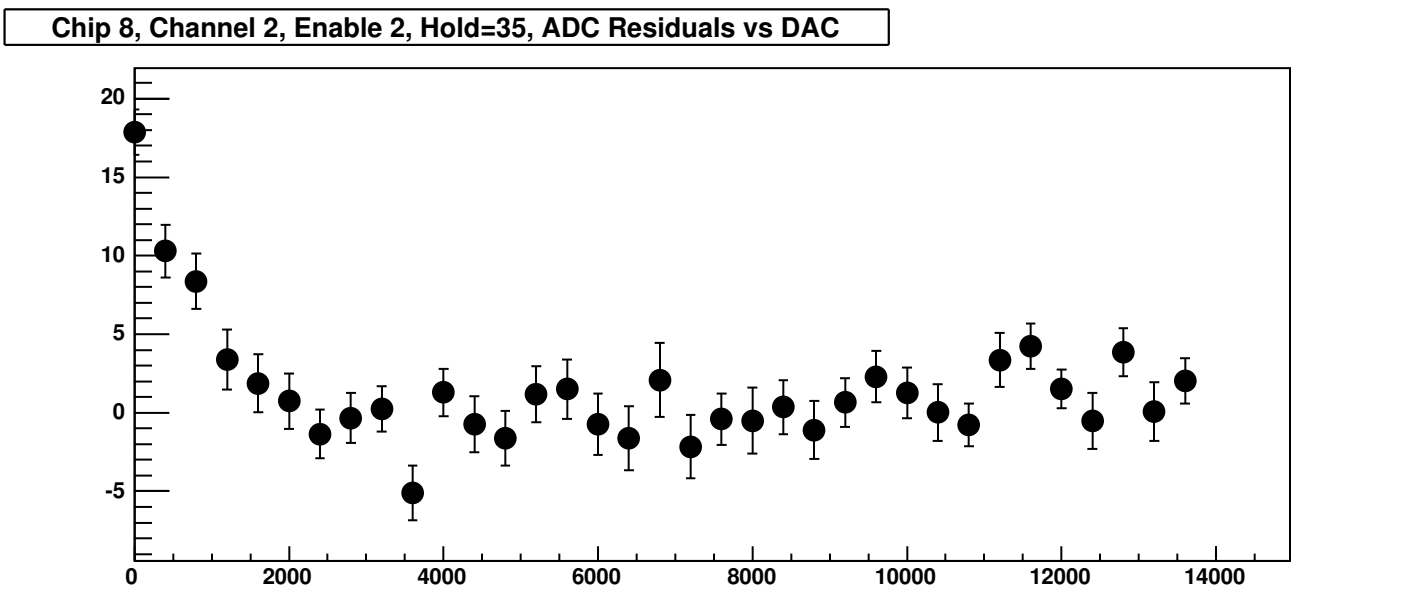

Chip 8, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 37.23 / 23
p0 183.4 ± 0.7979
p1 0.02224 ± 0.0001212
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

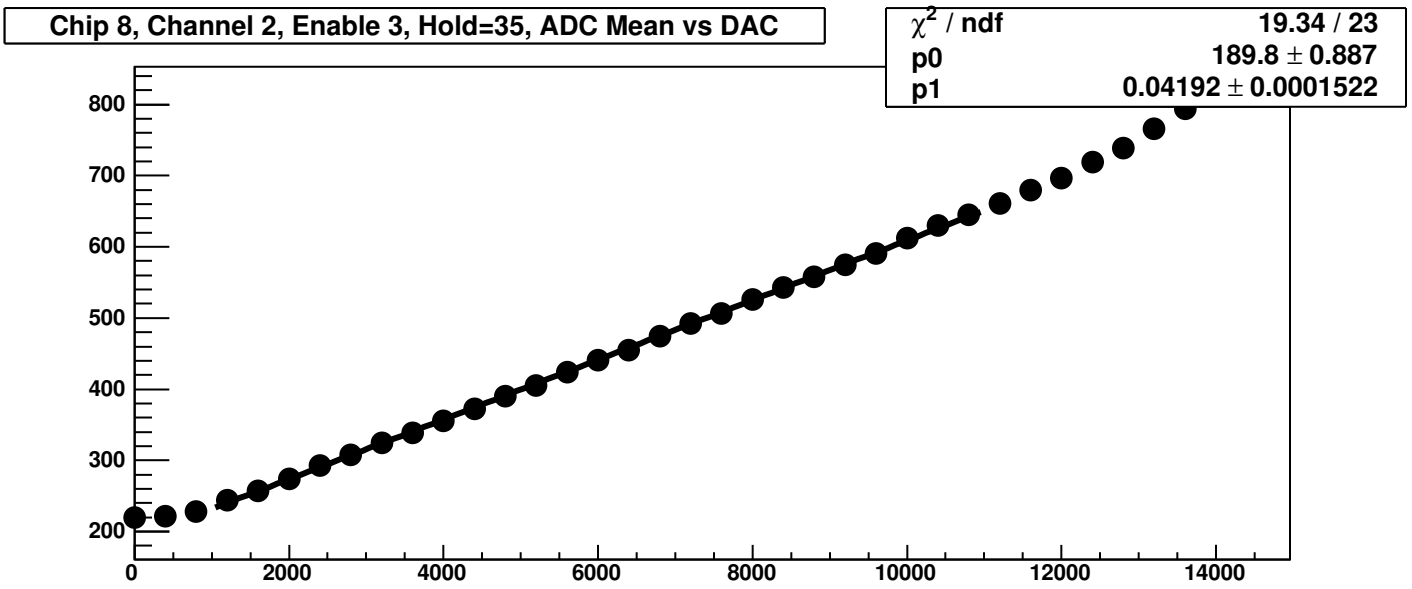

Chip 8, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

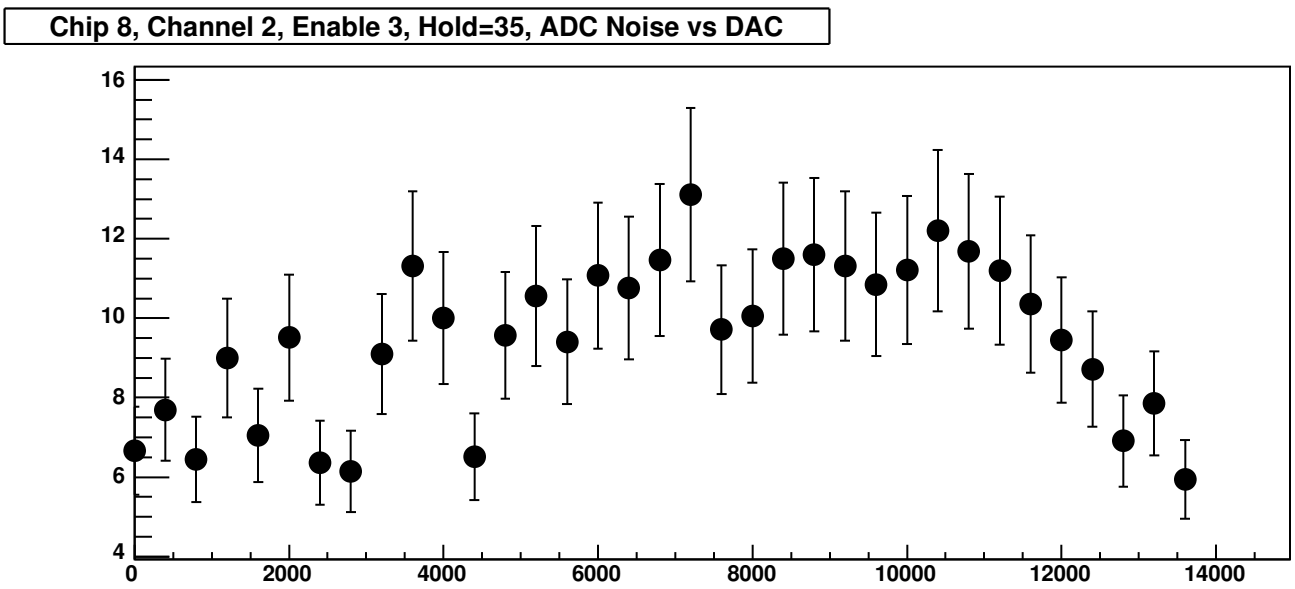

Chip 8, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

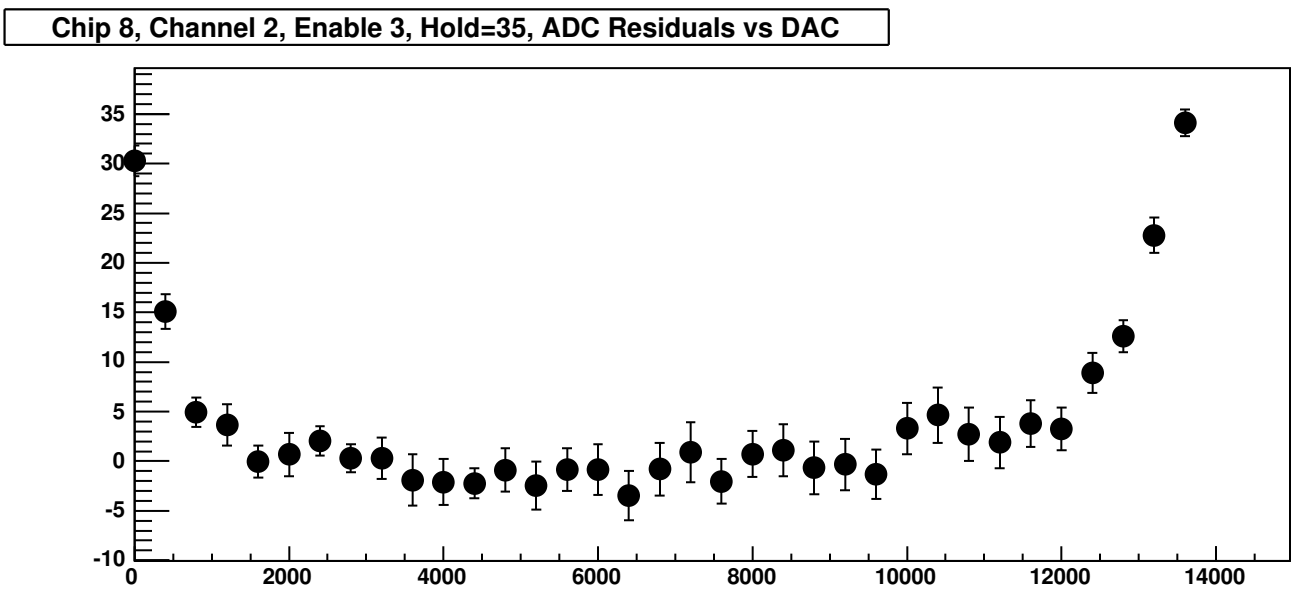

Chip 8, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 22.97 / 23
p0 184.7 ± 0.7773
p1 0.01654 ± 0.0001151
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

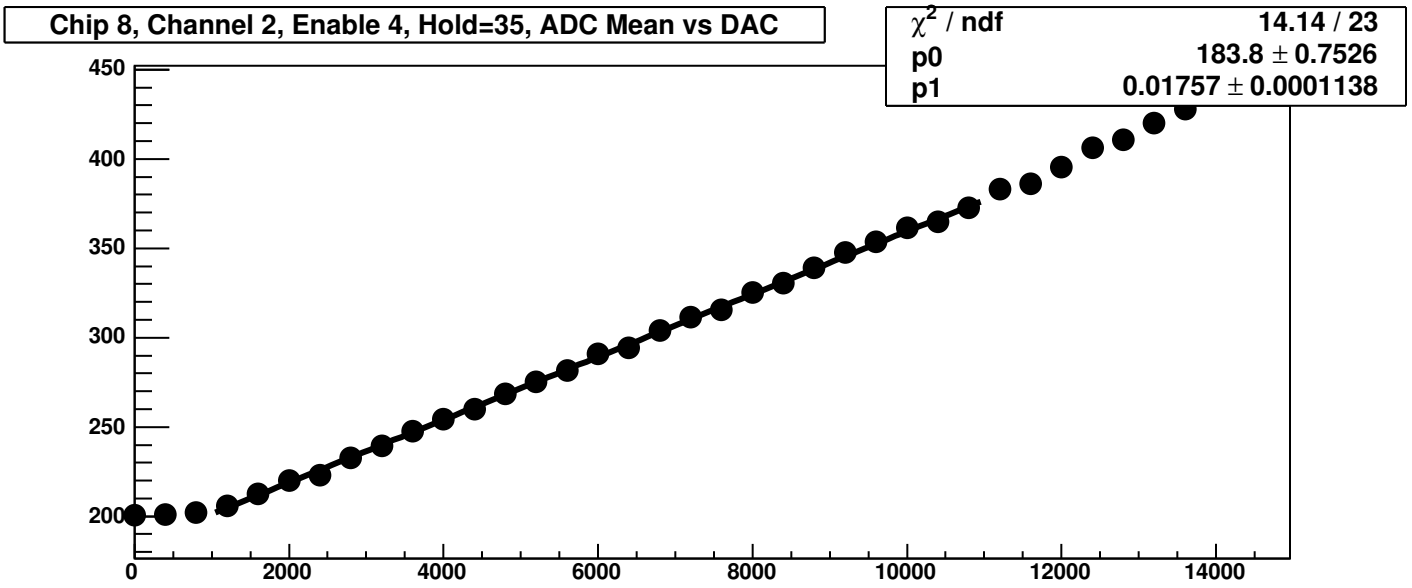

Chip 8, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

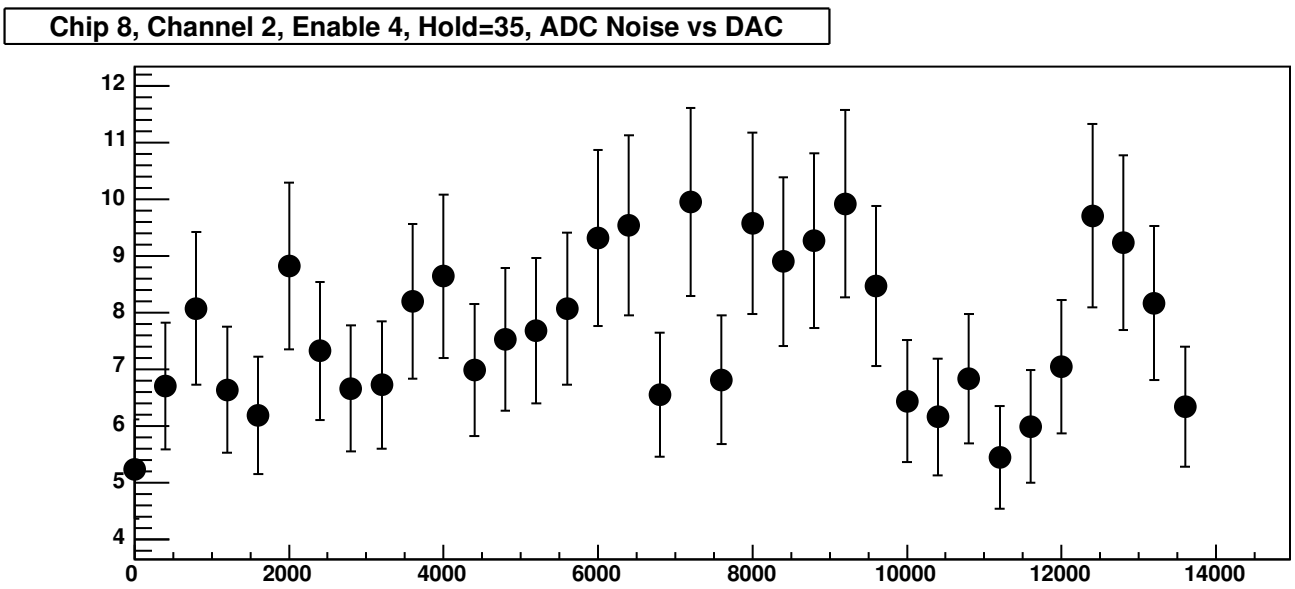

Chip 8, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

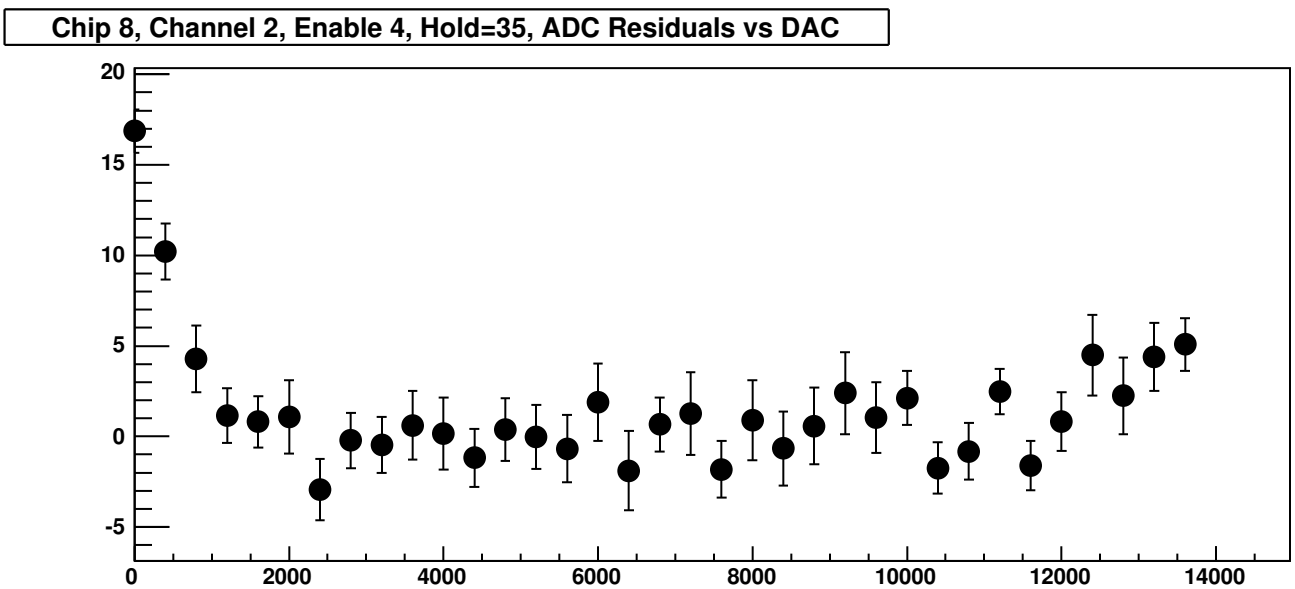

Chip 8, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 19.34 / 23
p0 189.8 ± 0.887
p1 0.04192 ± 0.0001522
800
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000

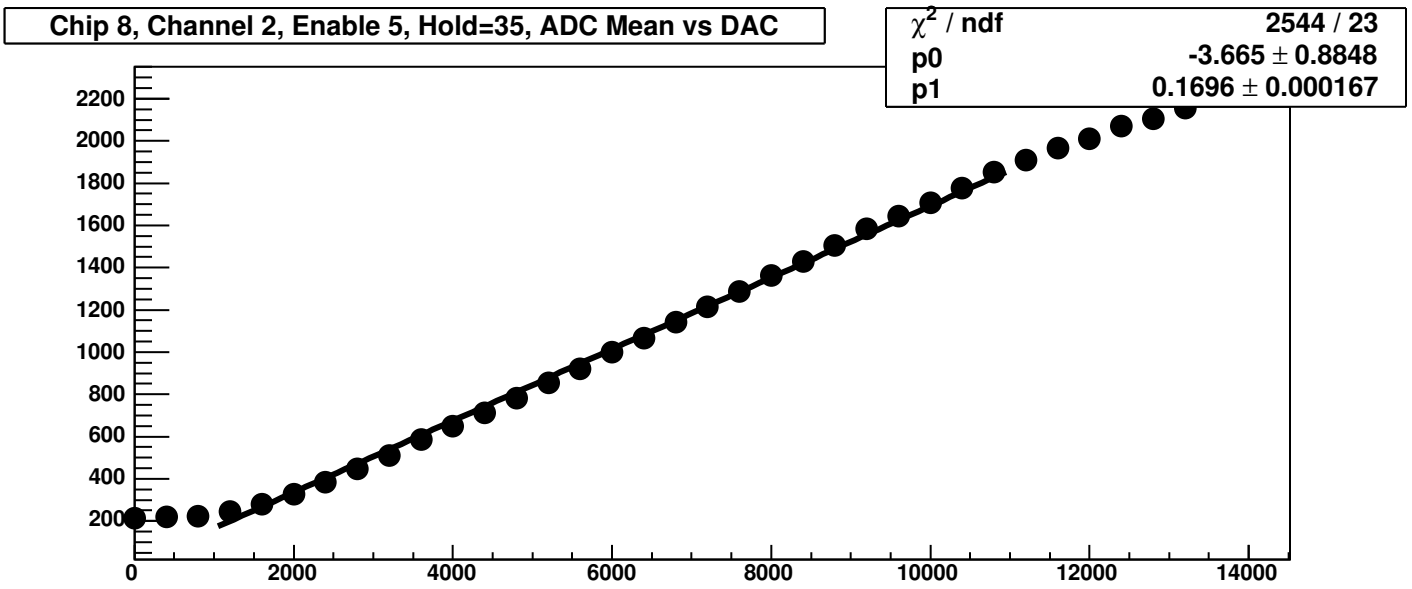

Chip 8, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

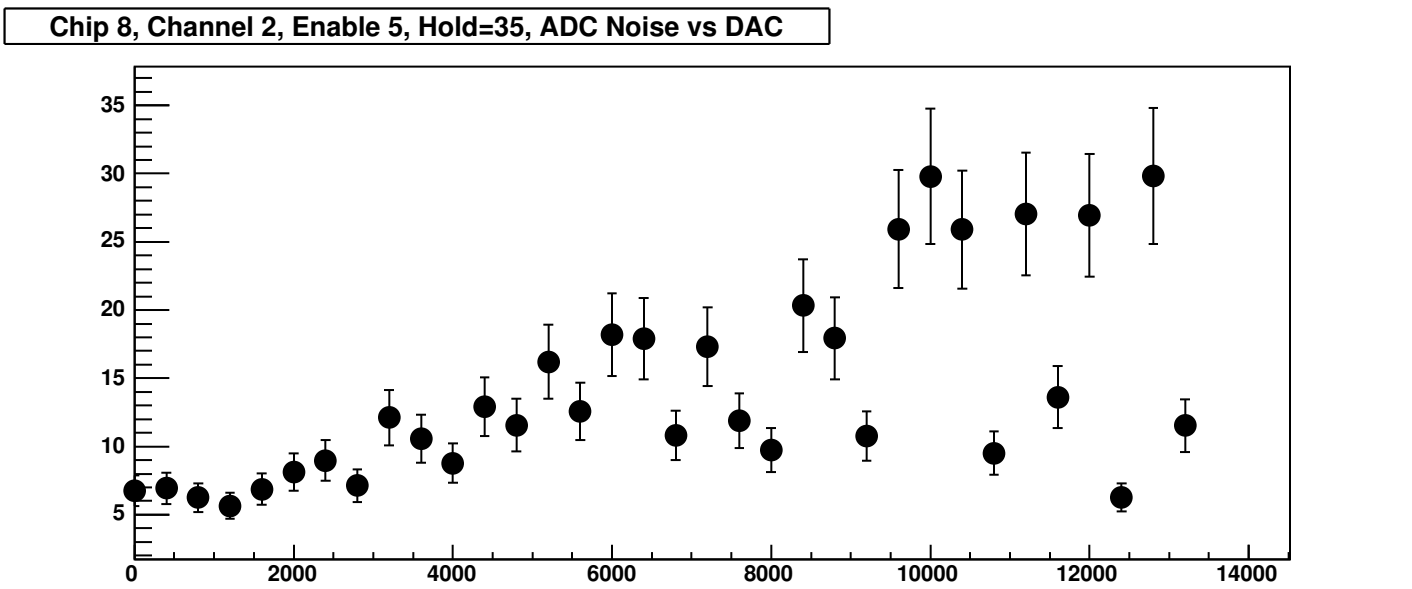

Chip 8, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

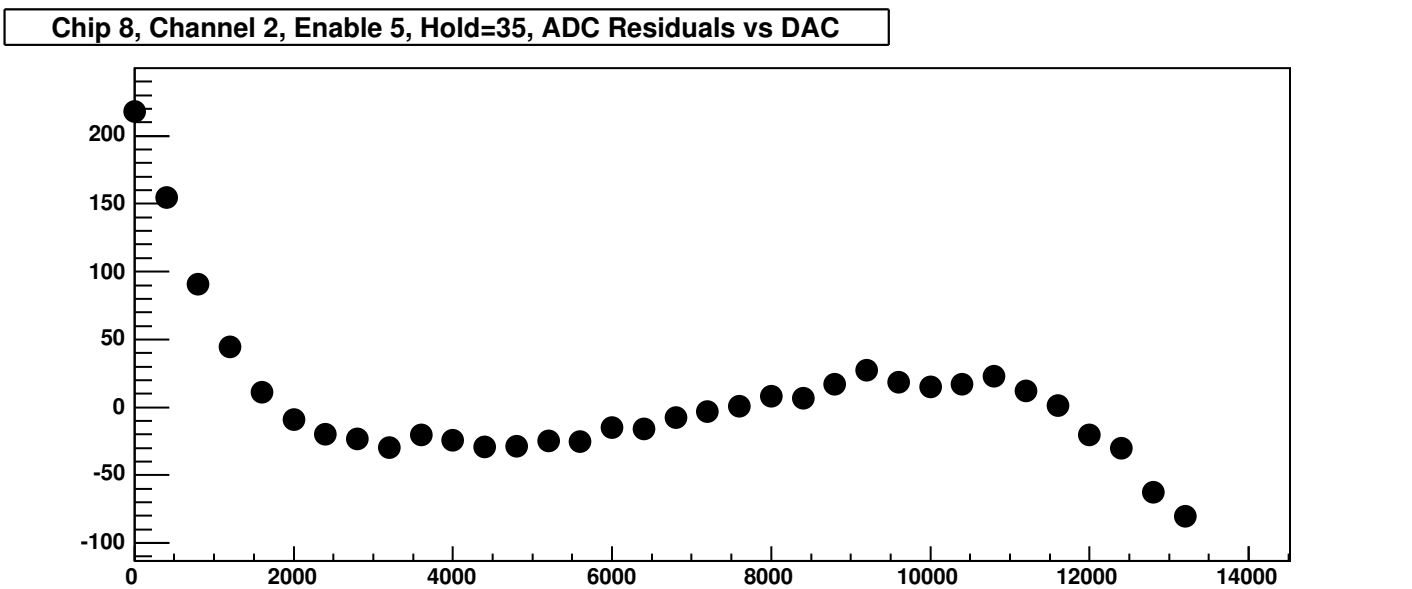

Chip 8, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 14.14 / 23
p0 183.8 ± 0.7526
p1 0.01757 ± 0.0001138
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

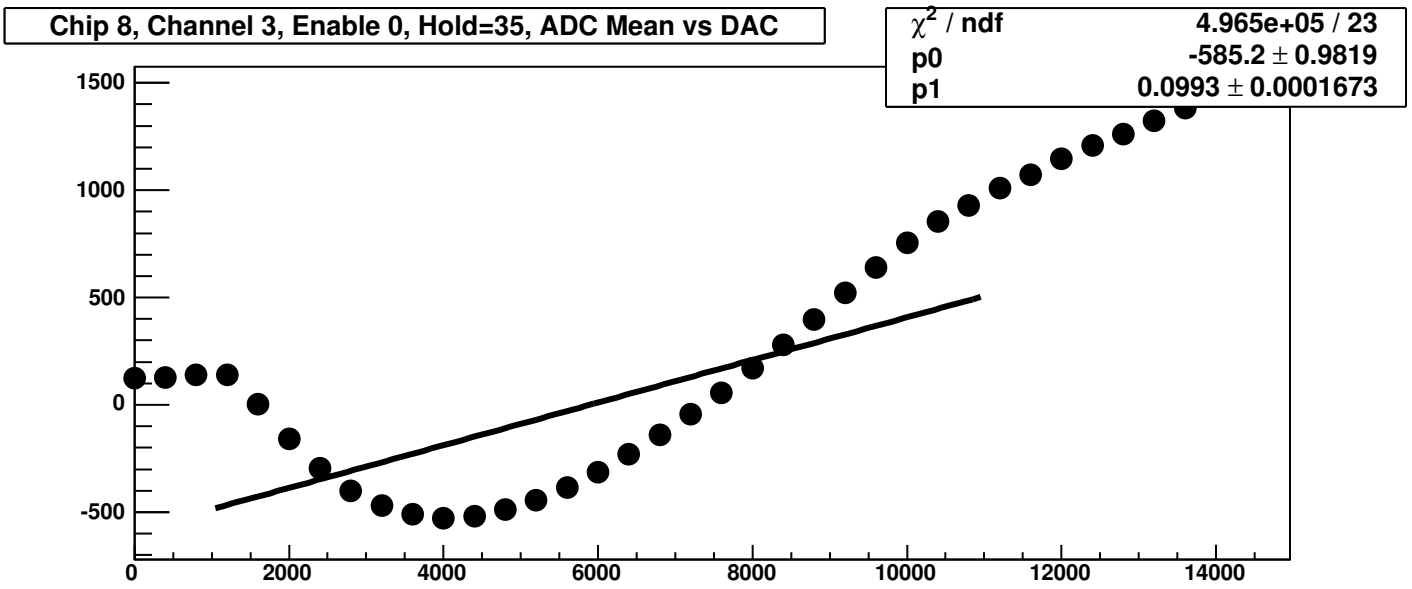

Chip 8, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

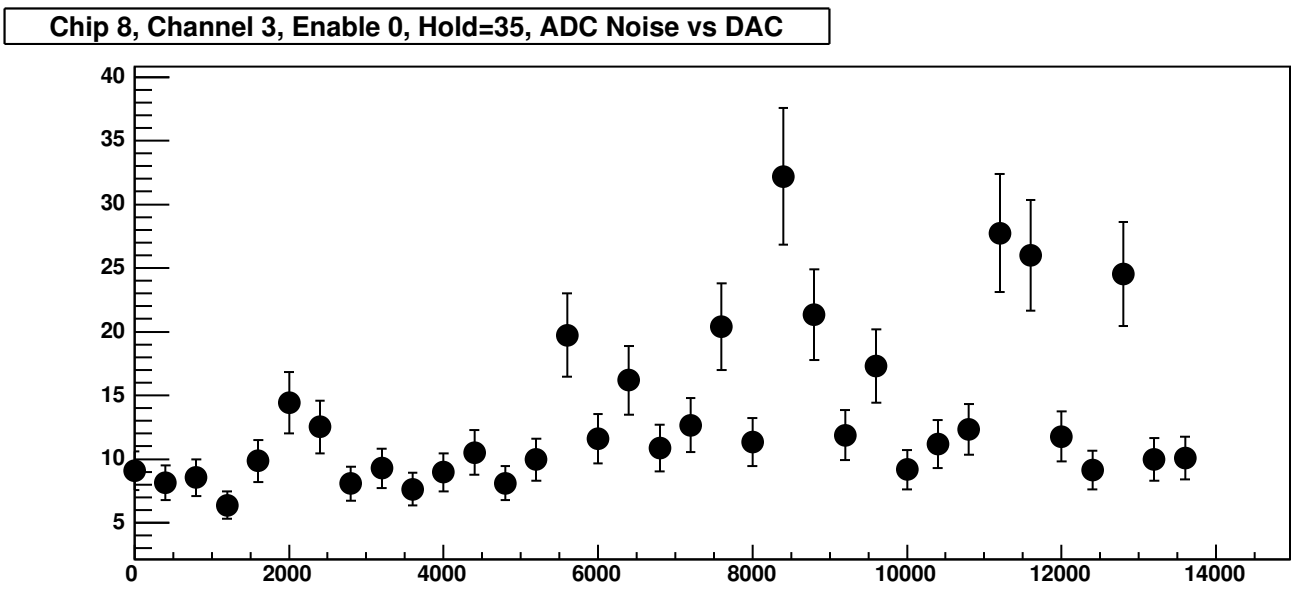

Chip 8, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

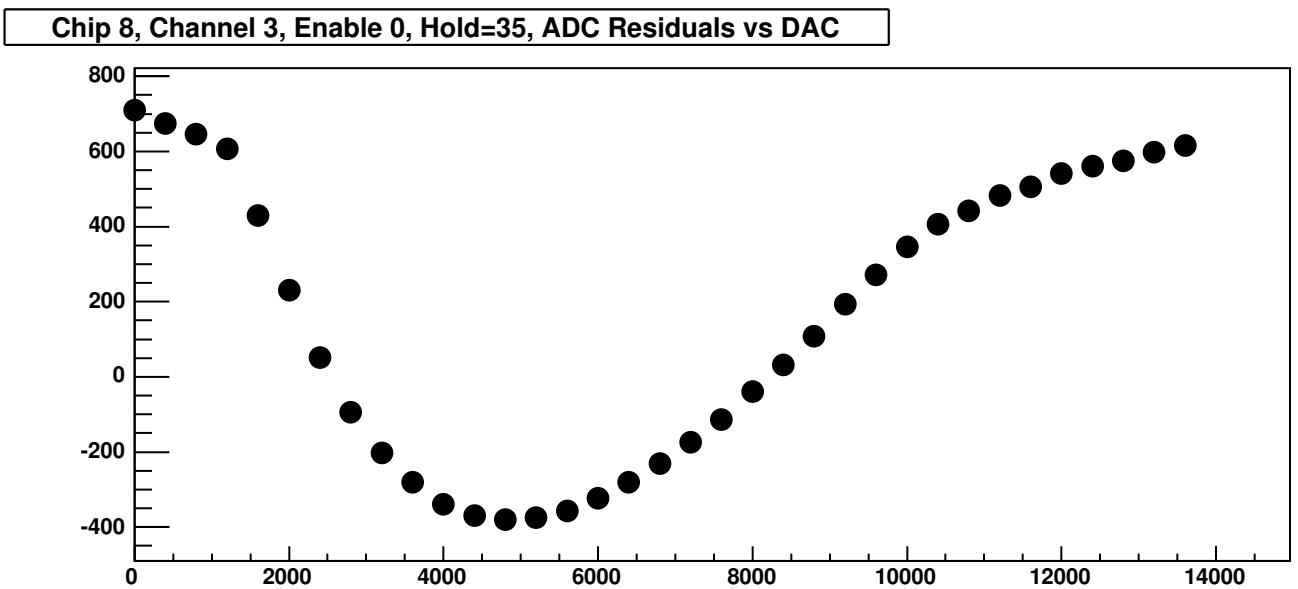

Chip 8, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2544 / 23
p0 -3.665 ± 0.8848
p1 0.1696 ± 0.000167
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

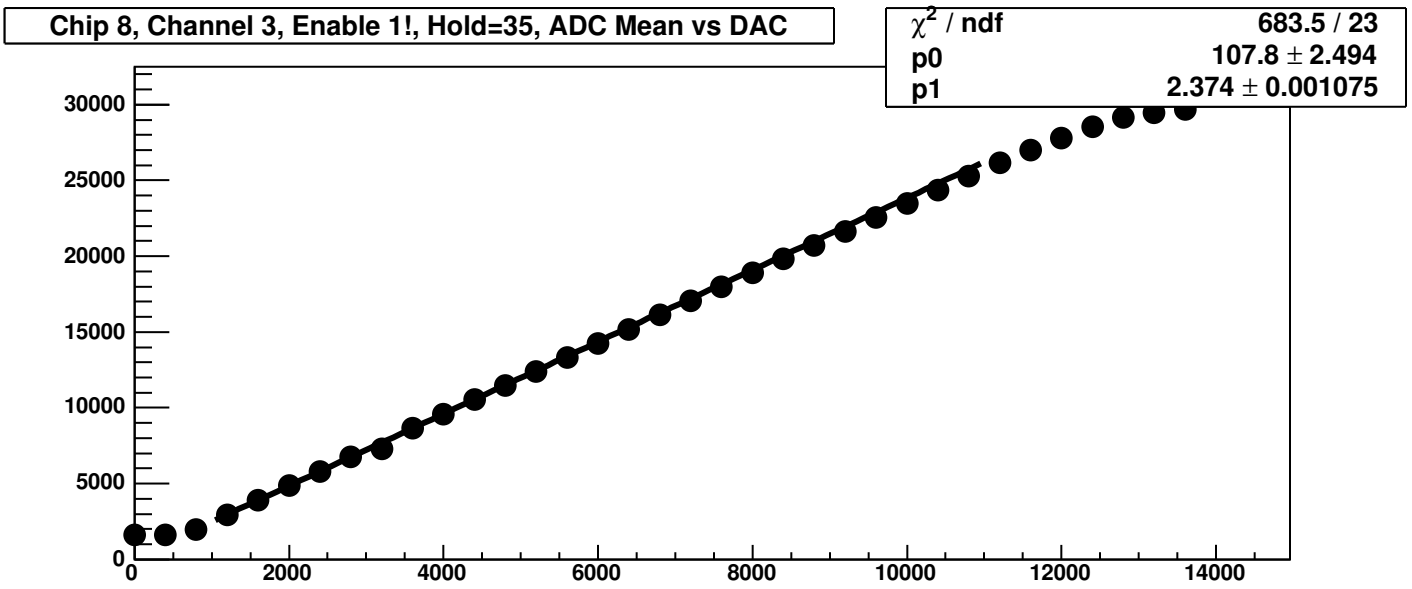

Chip 8, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

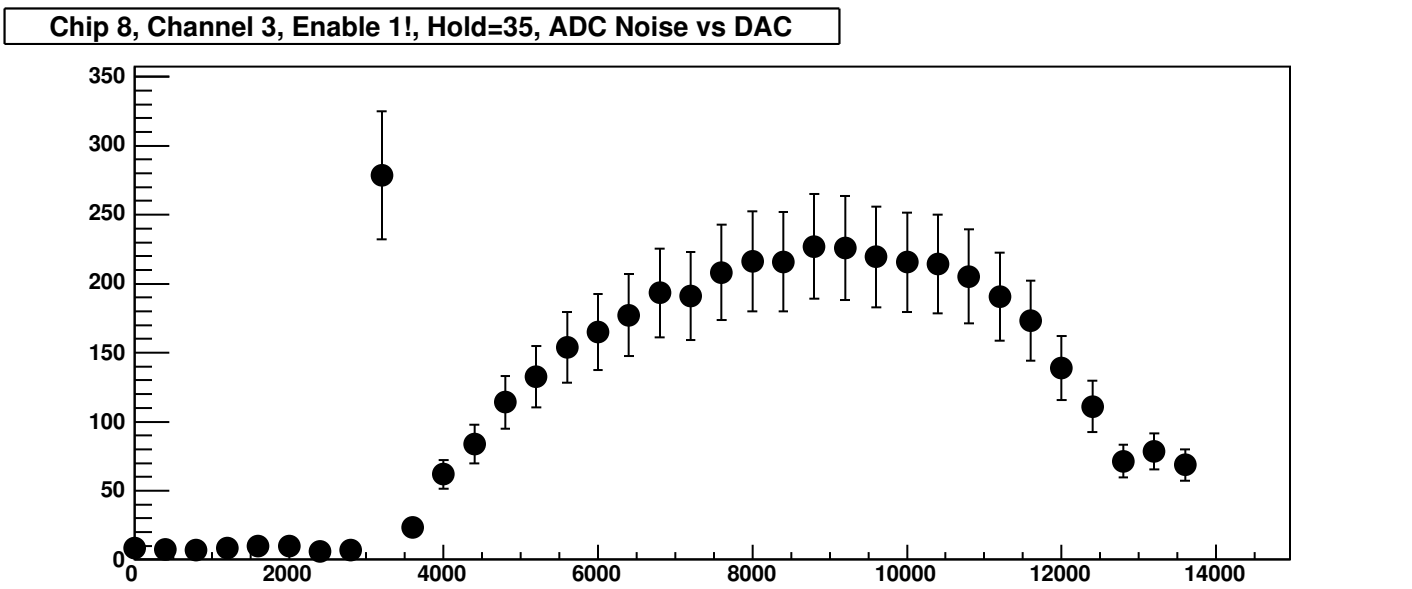

Chip 8, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

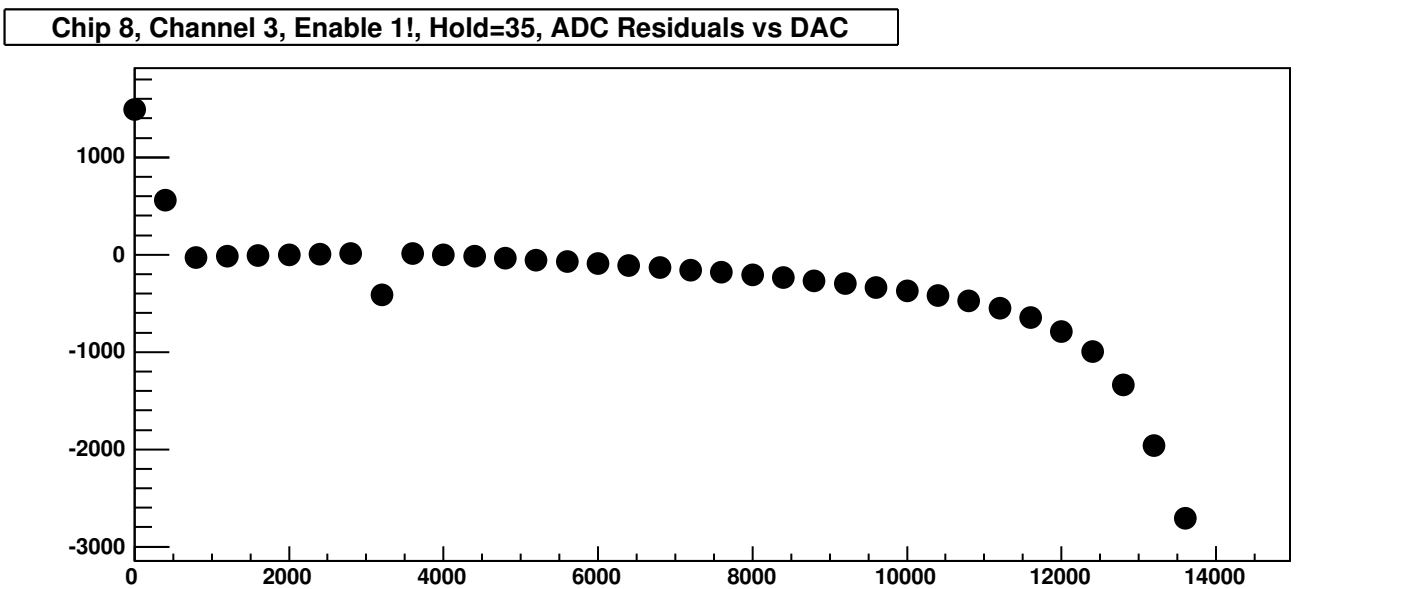

Chip 8, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.965e+05 / 23
p0 -585.2 ± 0.9819
p1 0.0993 ± 0.0001673
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

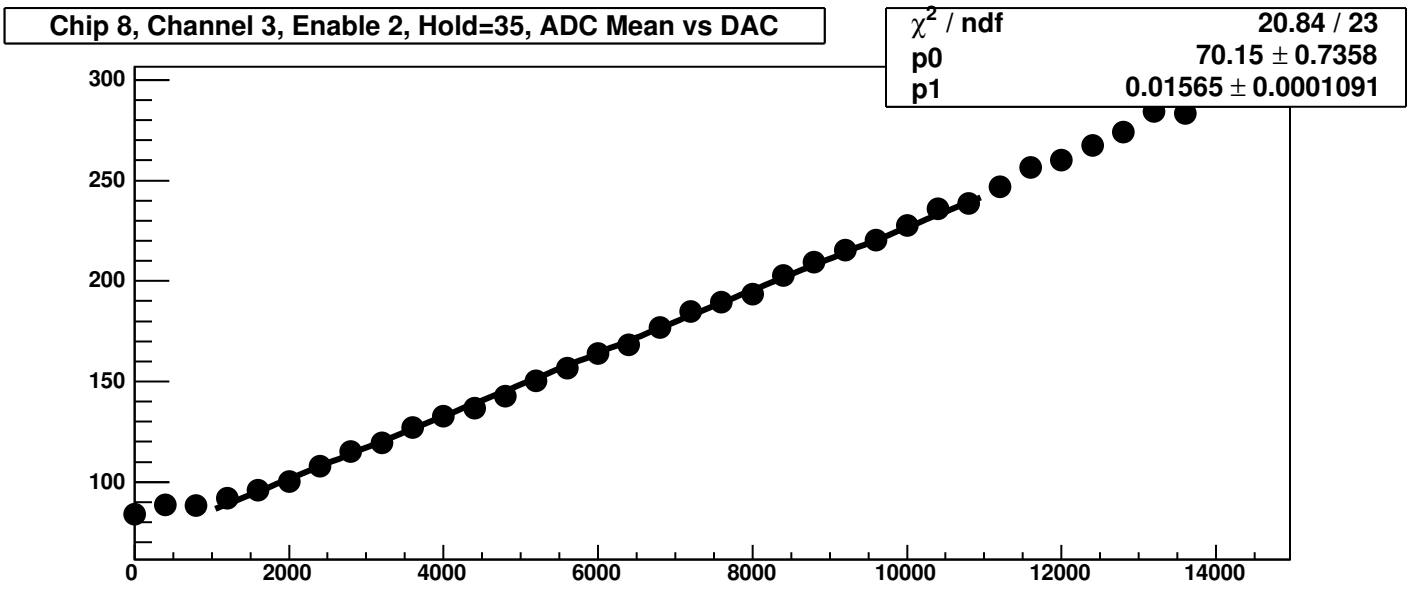

Chip 8, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

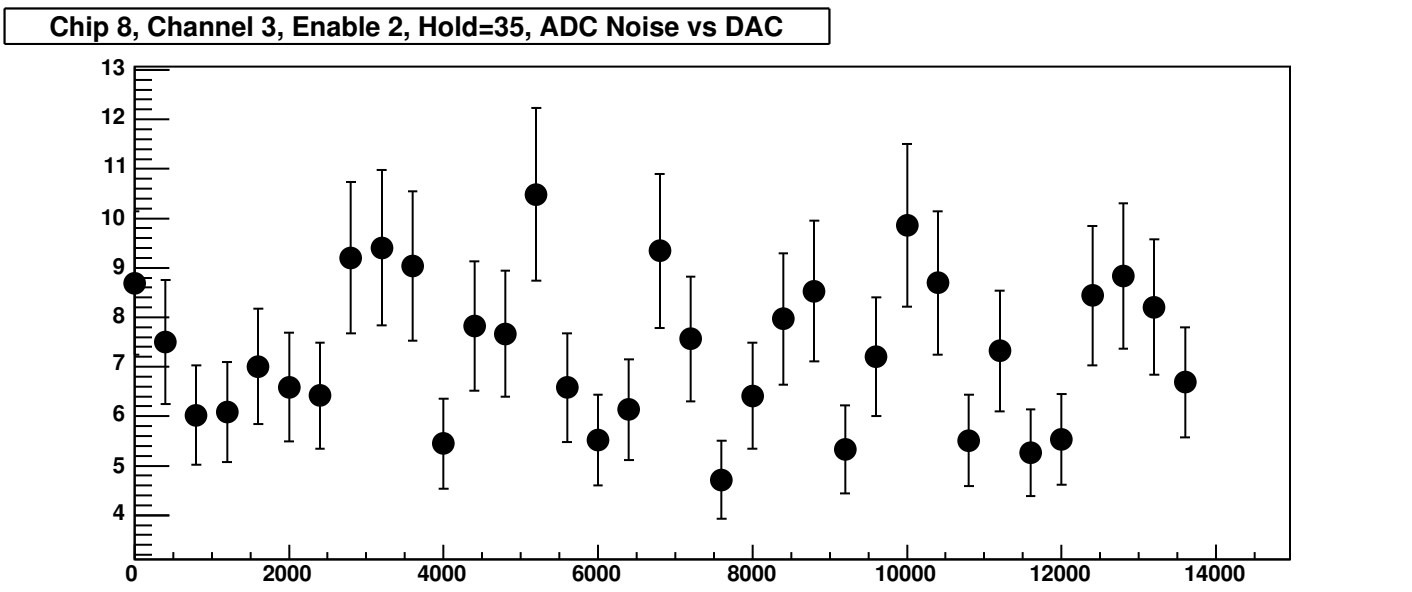

Chip 8, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

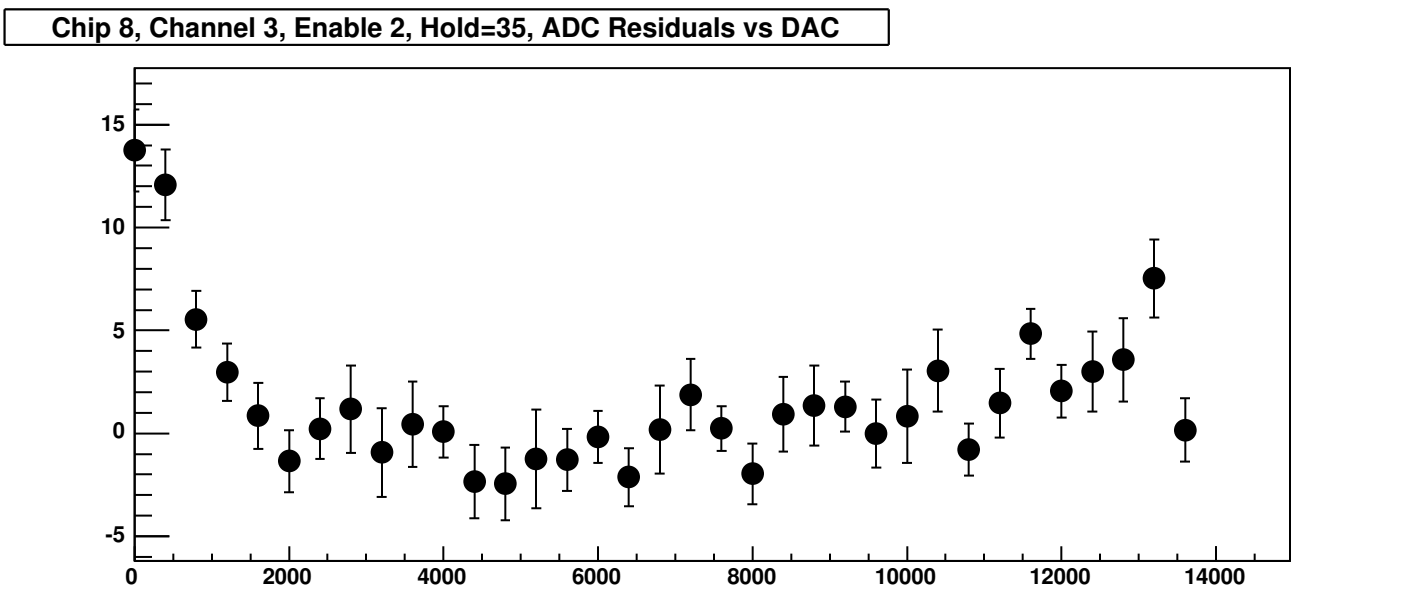

Chip 8, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 683.5 / 23
p0 107.8 ± 2.494
p1 2.374 ± 0.001075
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

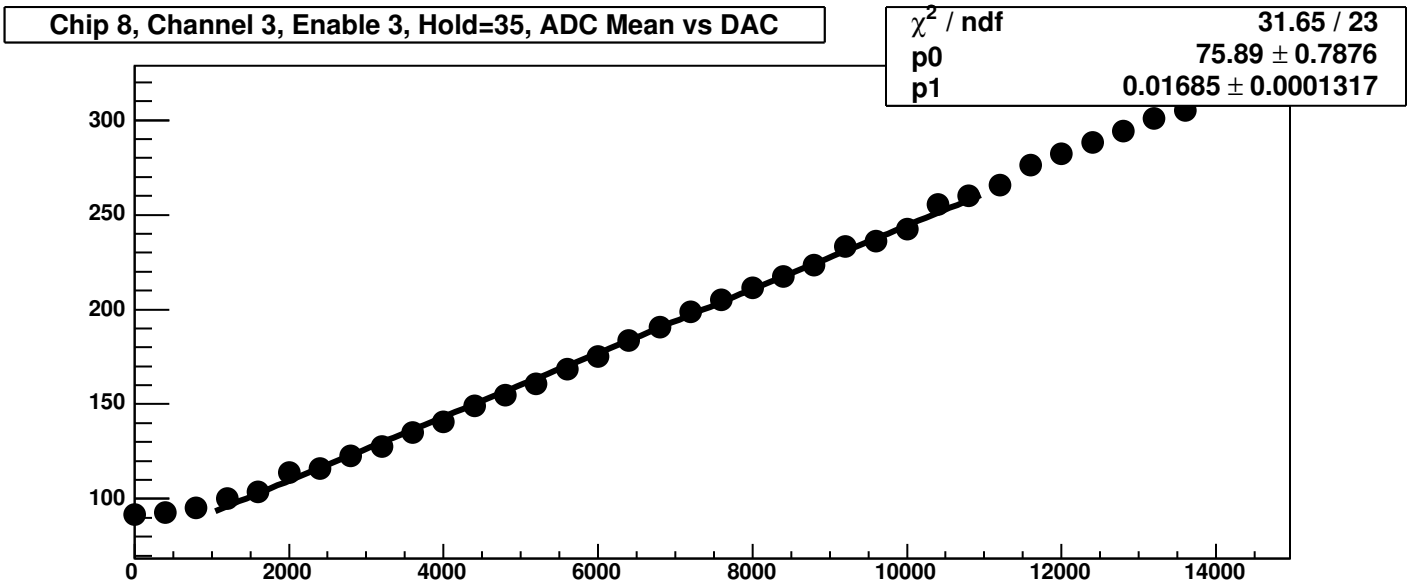

Chip 8, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

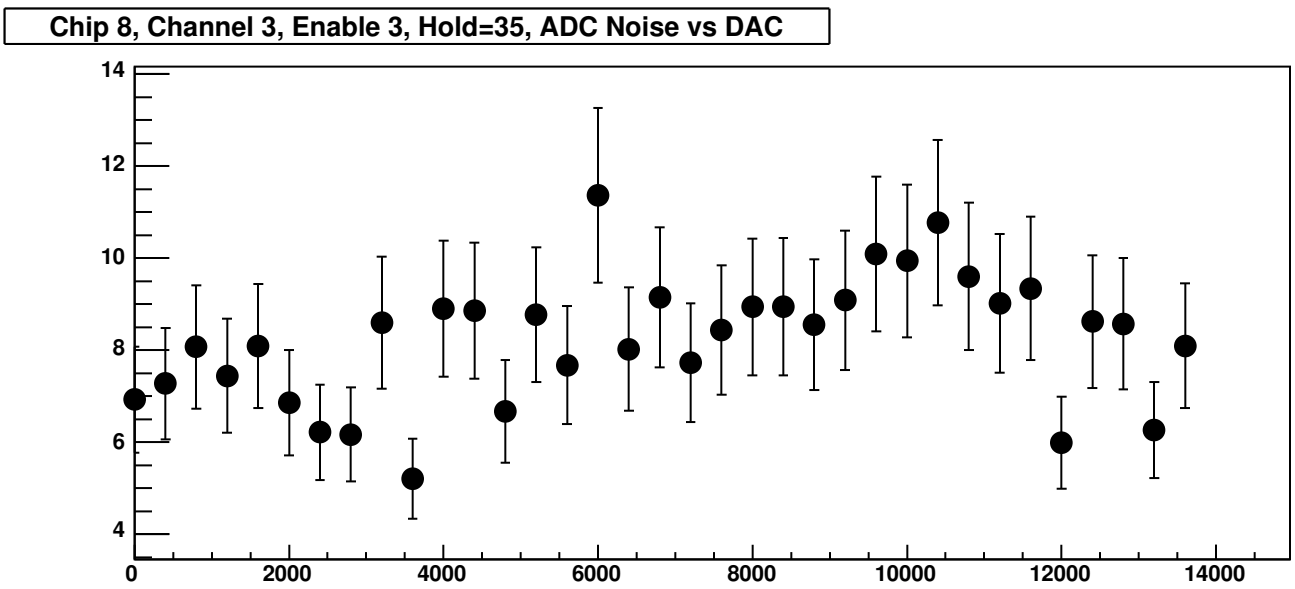

Chip 8, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

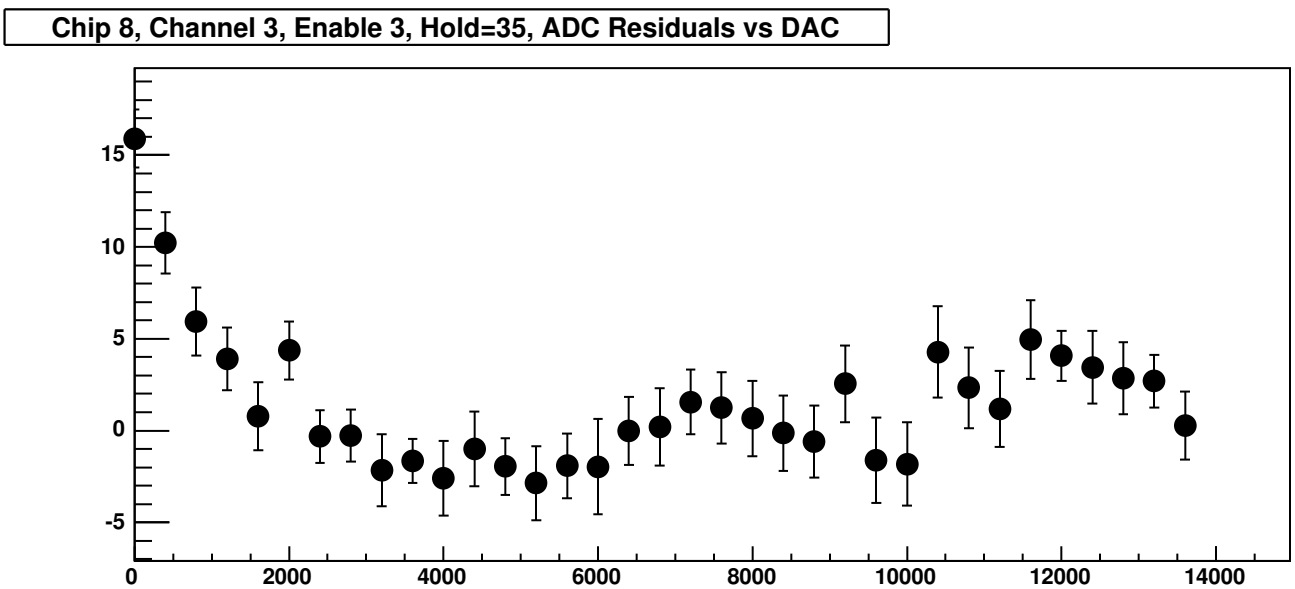

Chip 8, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 20.84 / 23
p0 70.15 ± 0.7358
p1 0.01565 ± 0.0001091
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

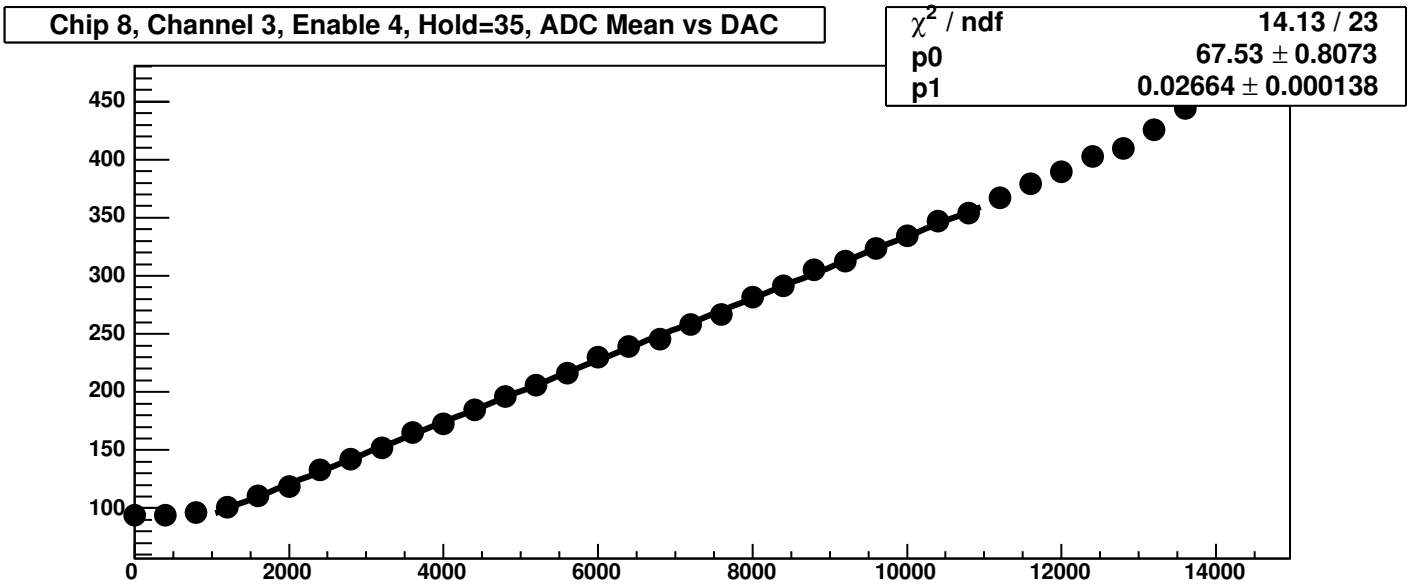

Chip 8, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

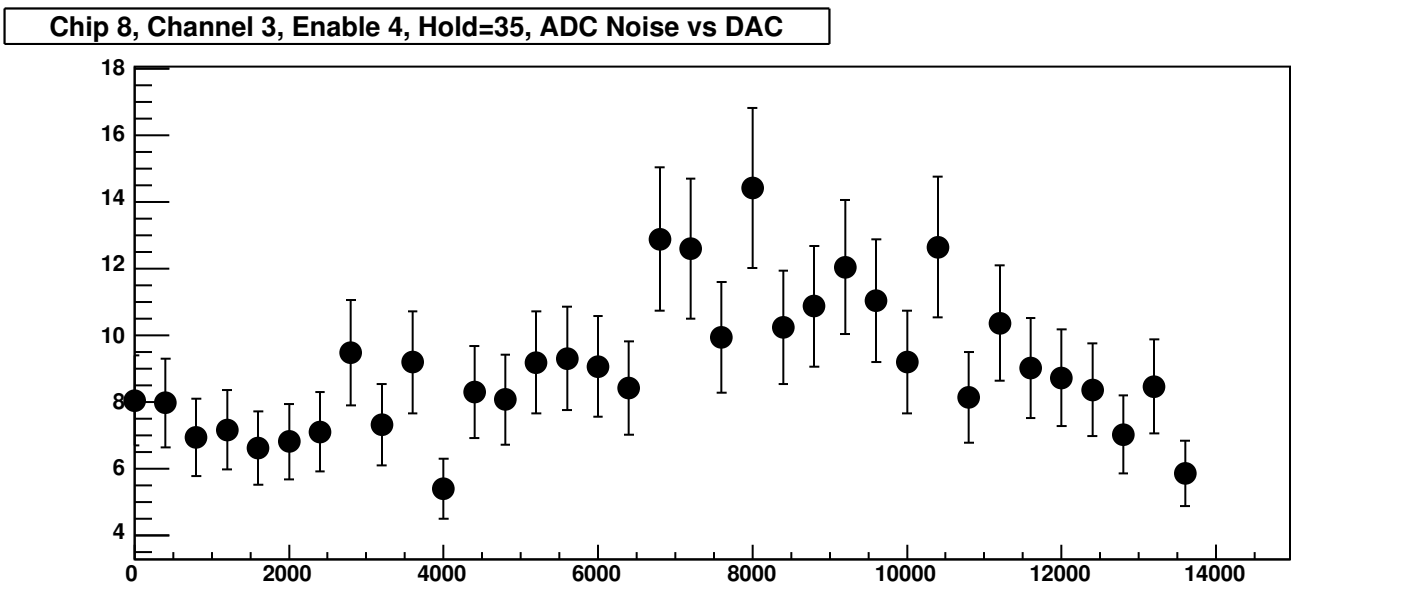

Chip 8, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

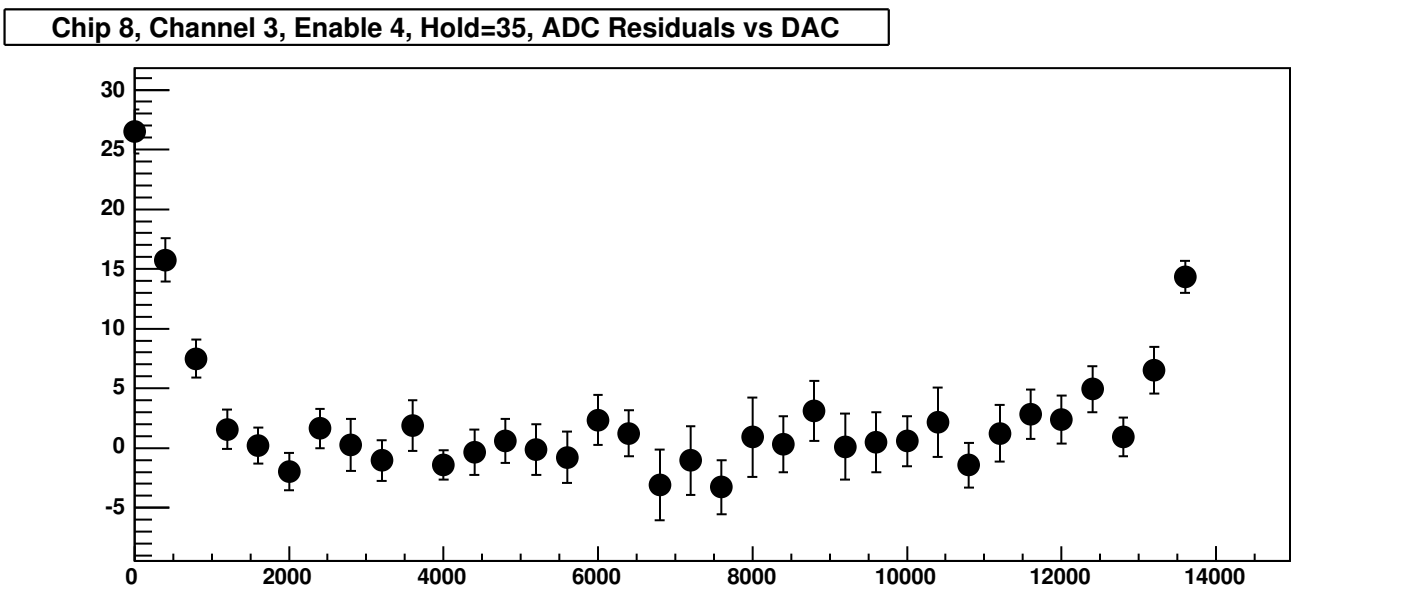

Chip 8, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.65 / 23
p0 75.89 ± 0.7876
p1 0.01685 ± 0.0001317
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

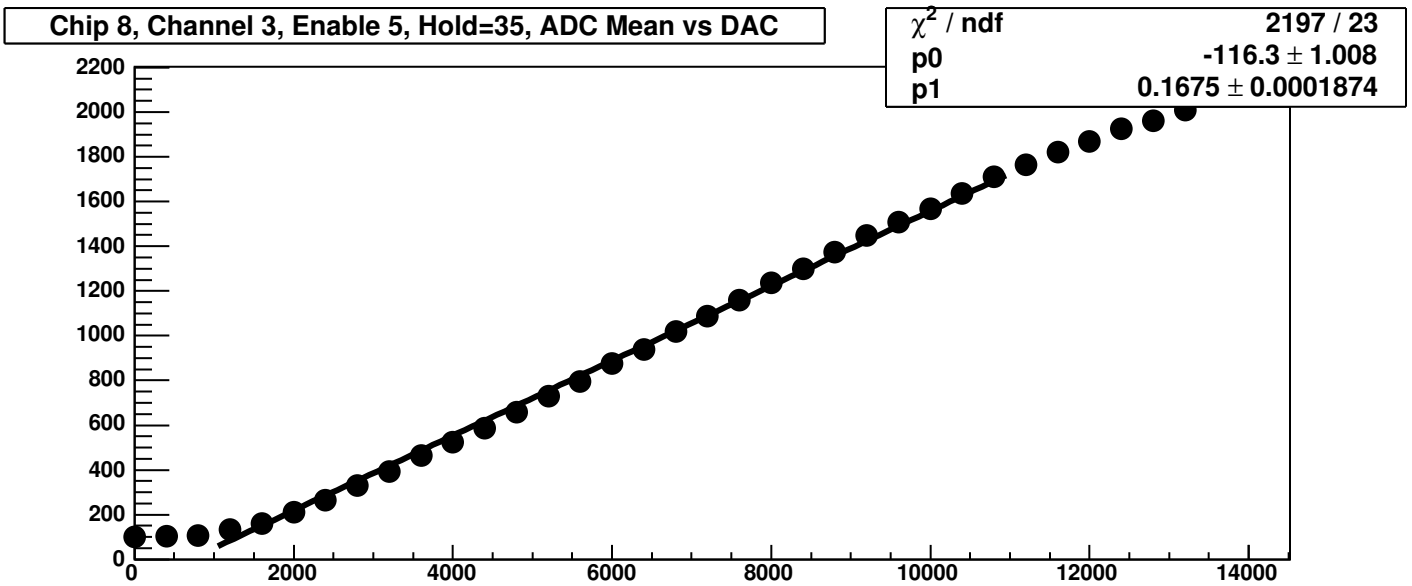

Chip 8, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

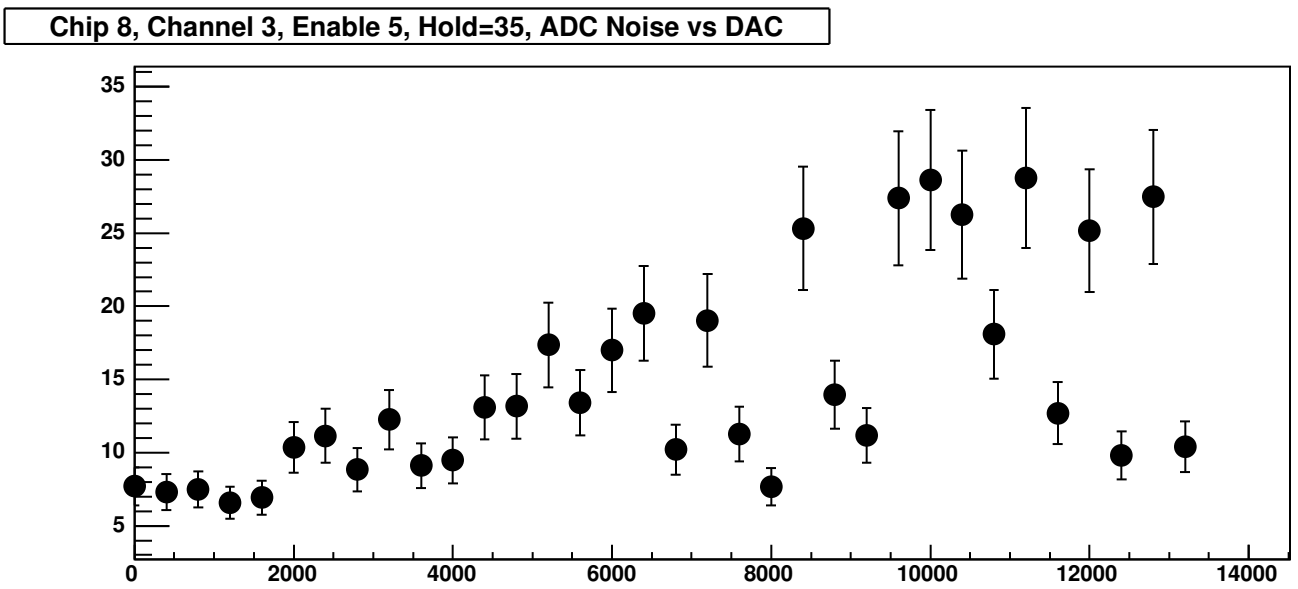

Chip 8, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

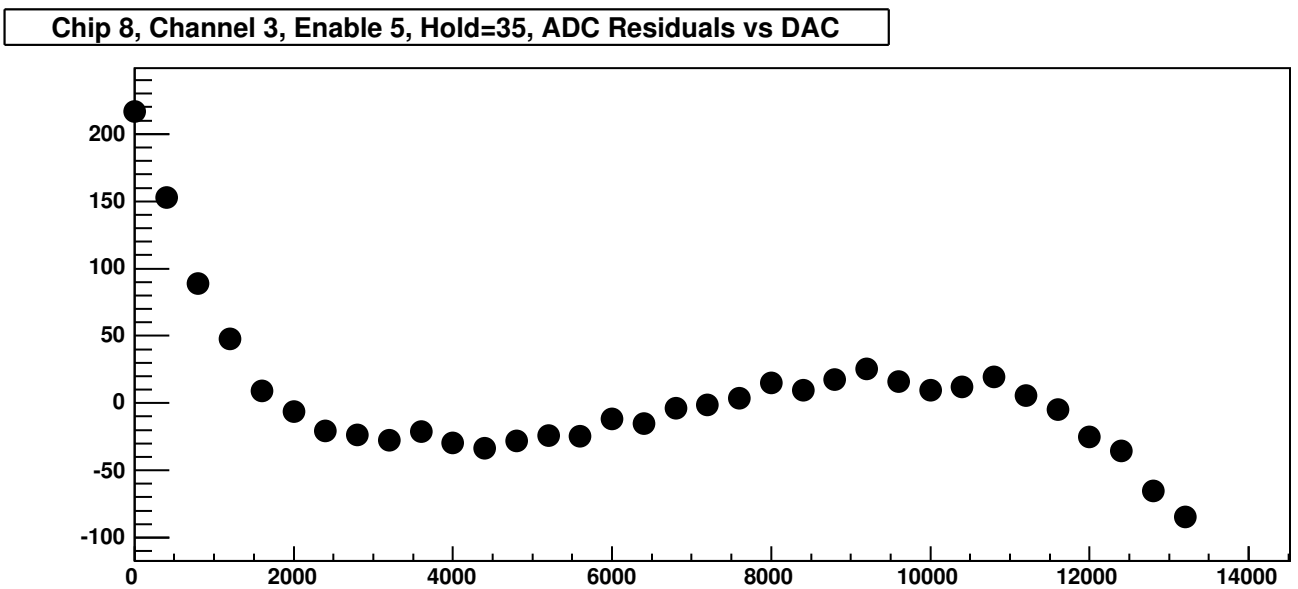

Chip 8, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 14.13 / 23
p0 67.53 ± 0.8073
p1 0.02664 ± 0.000138
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

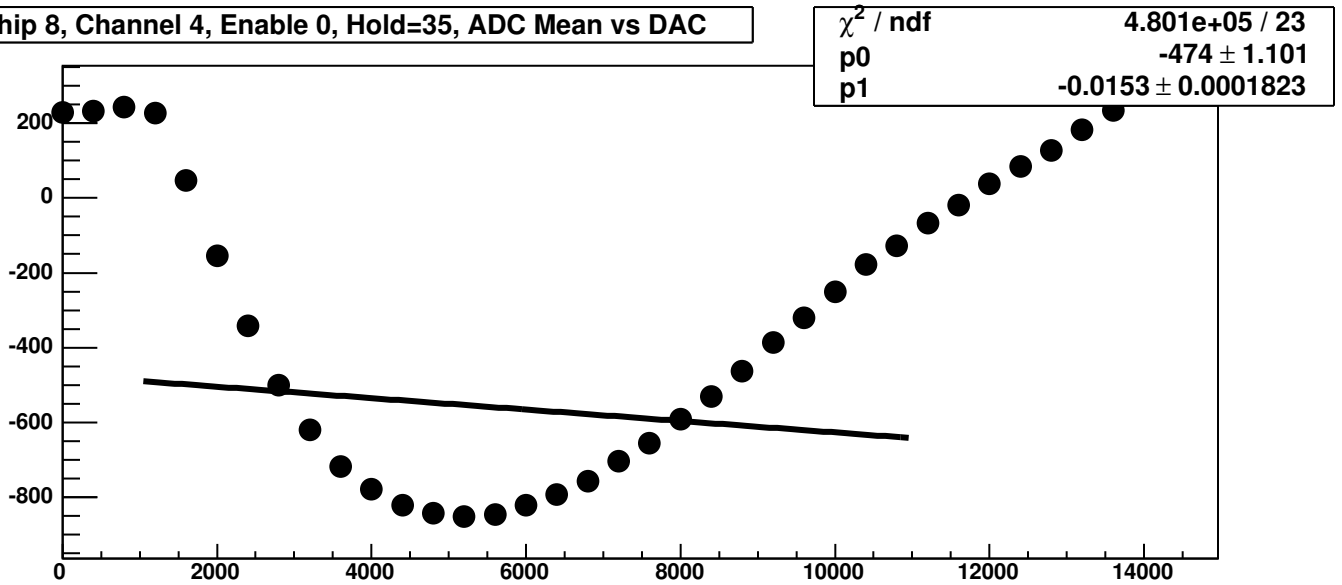

Chip 8, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2197 / 23
p0 -116.3 ± 1.008
p1 0.1675 ± 0.0001874
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

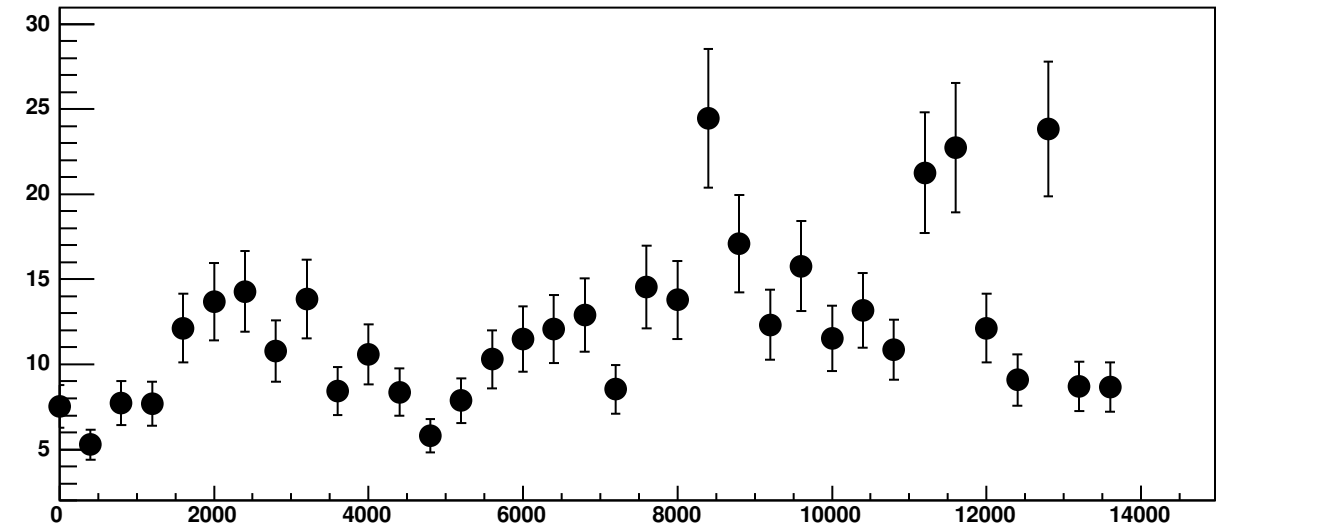

Chip 8, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

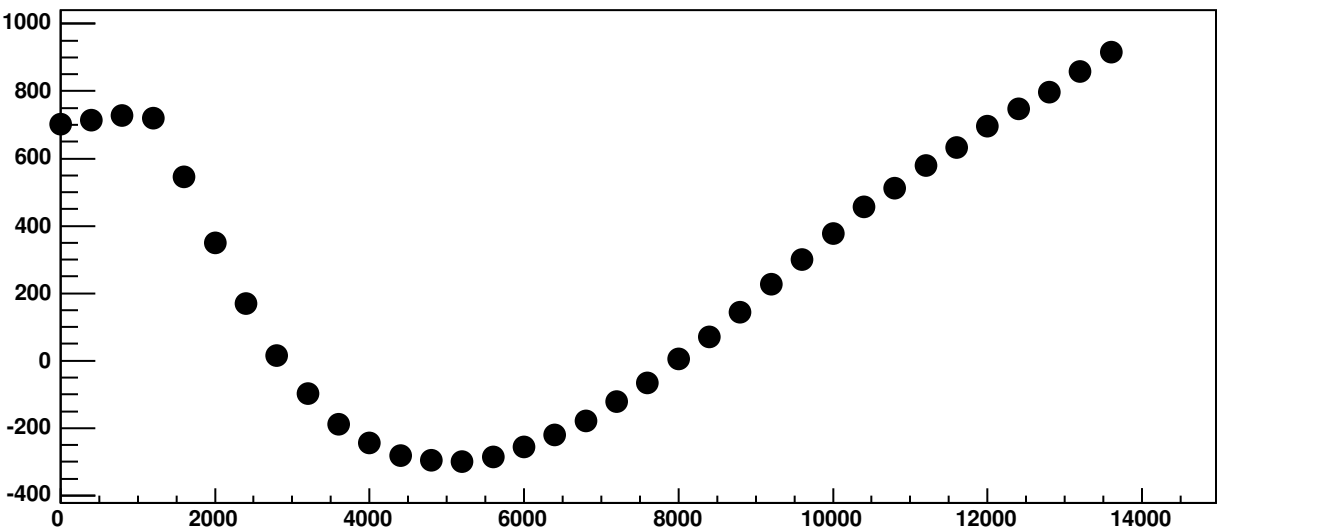
Chip 8, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

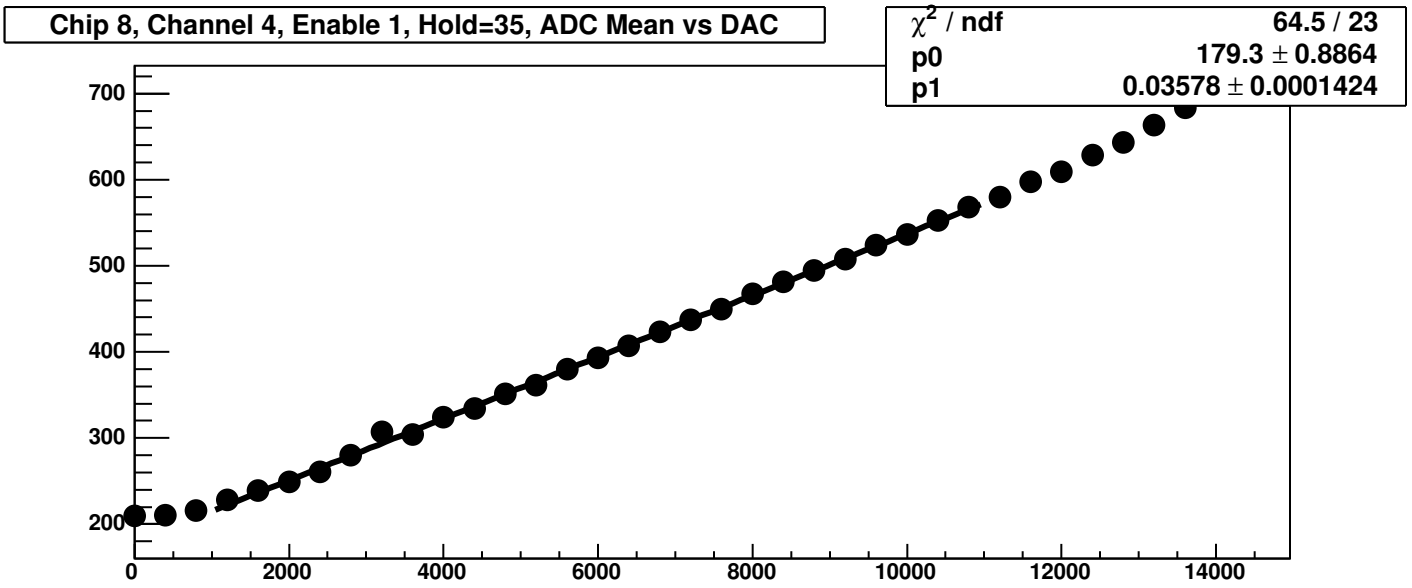


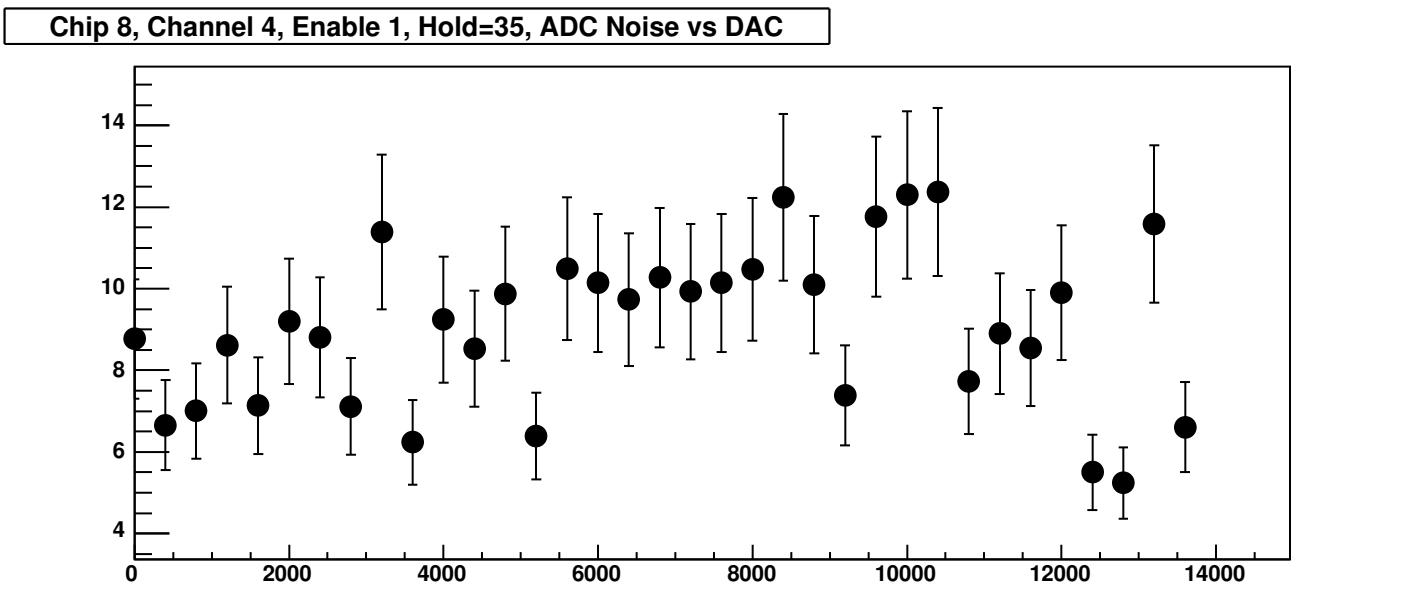
Chip 8, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC

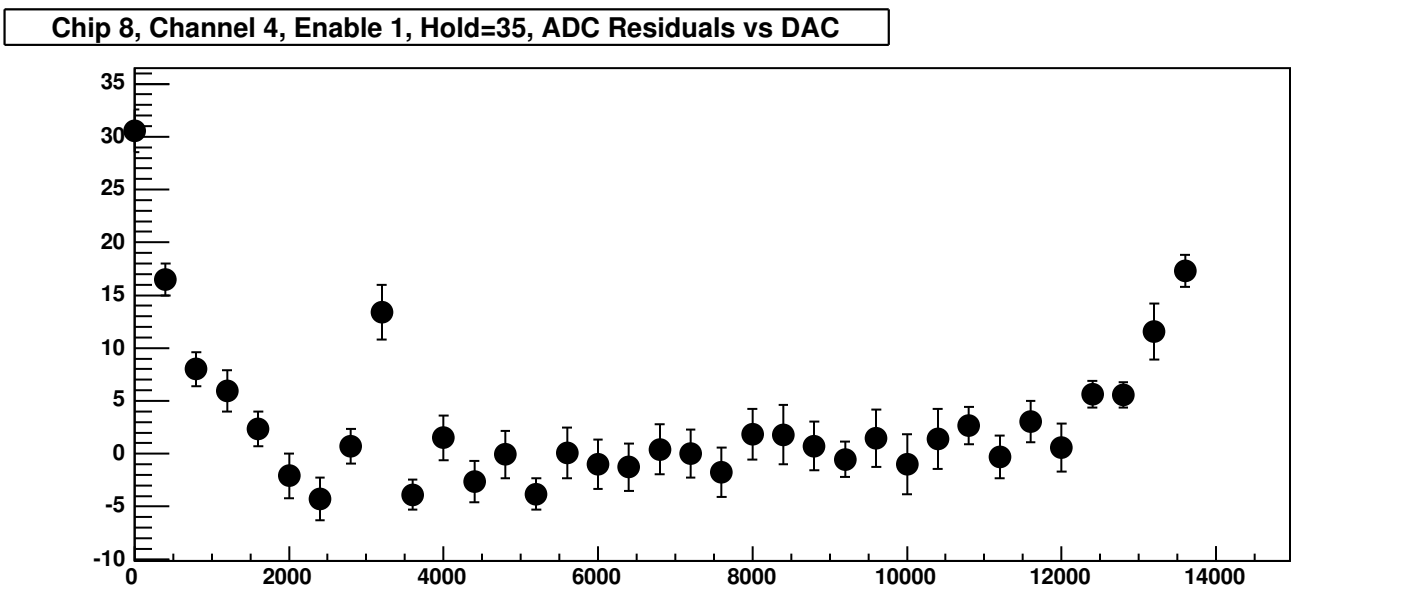


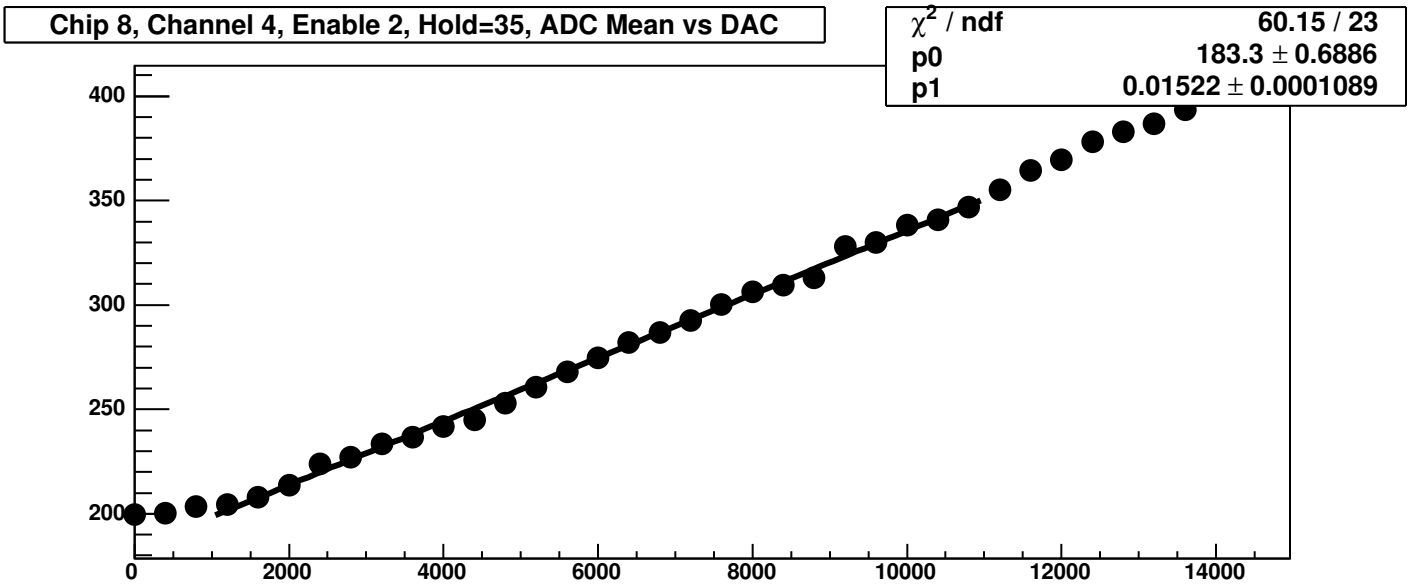
Chip 8, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

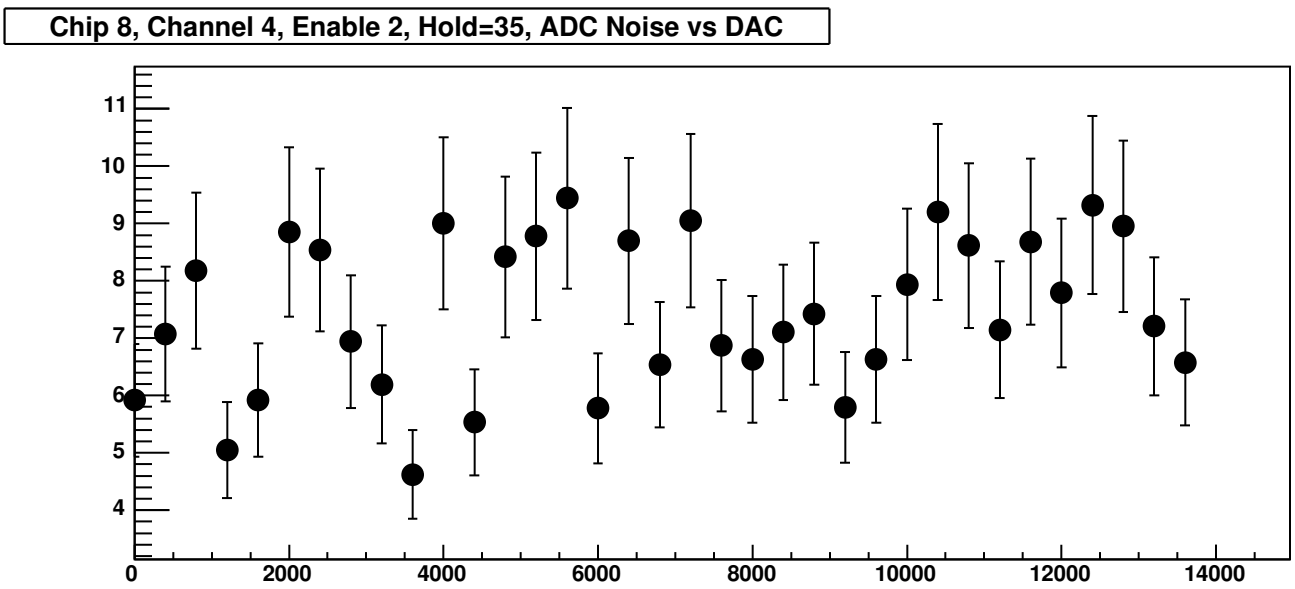


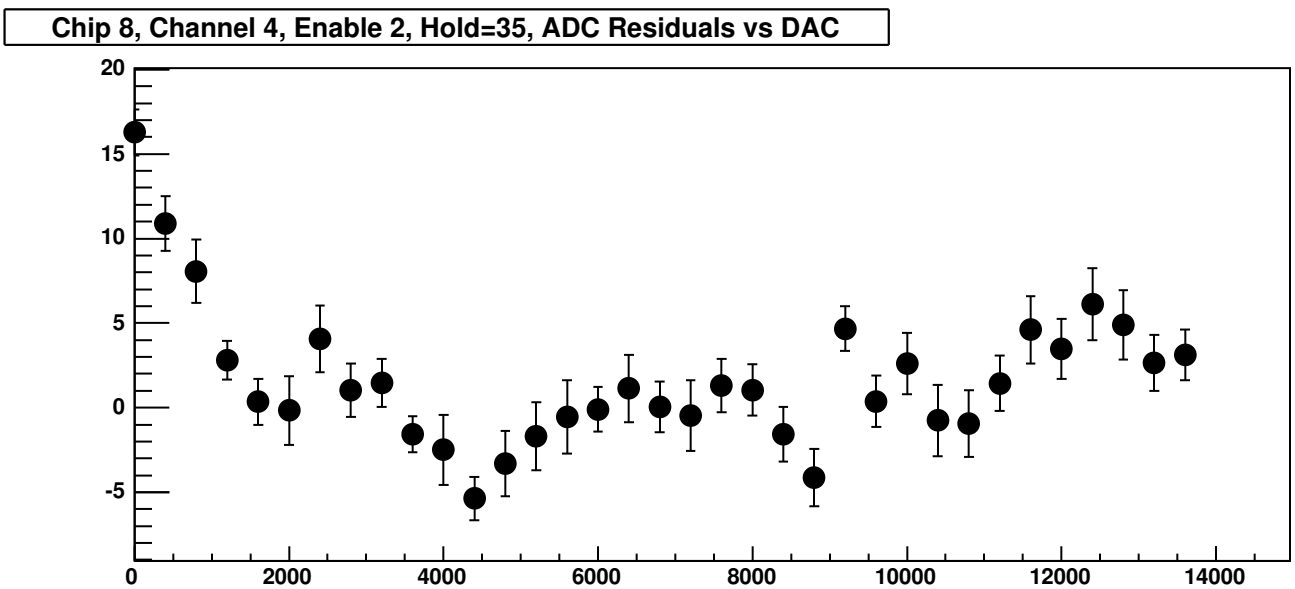

Chip 8, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 64.5 / 23
p0 179.3 ± 0.8864
p1 0.03578 ± 0.0001424
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

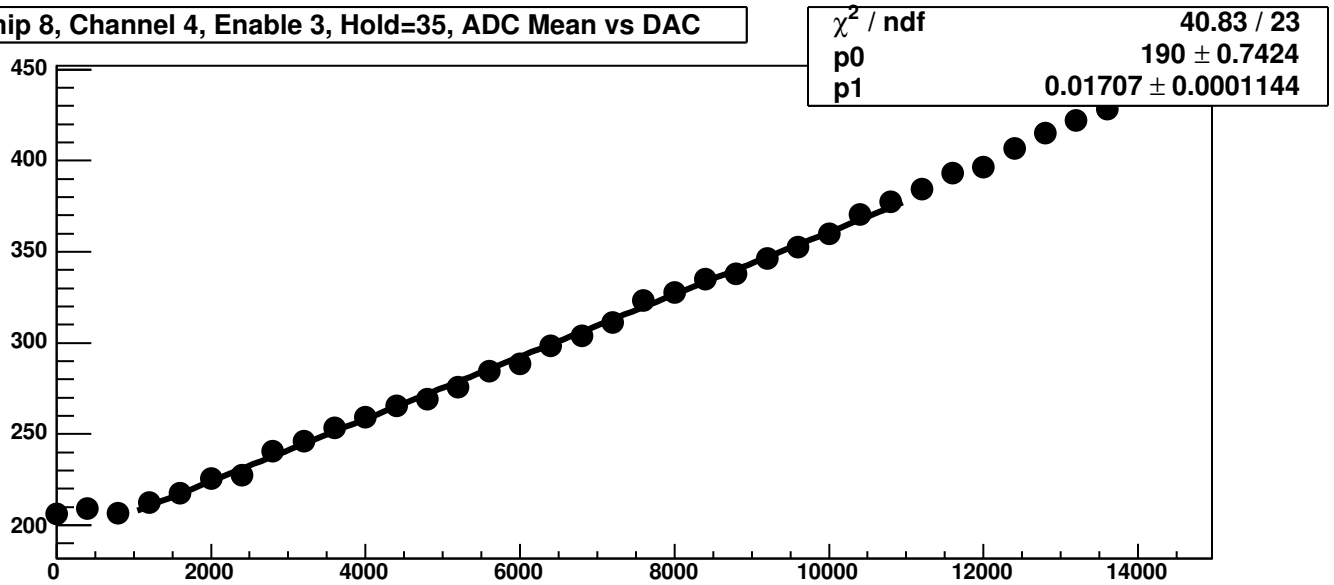

Chip 8, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 60.15 / 23
p0 183.3 ± 0.6886
p1 0.01522 ± 0.0001089
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

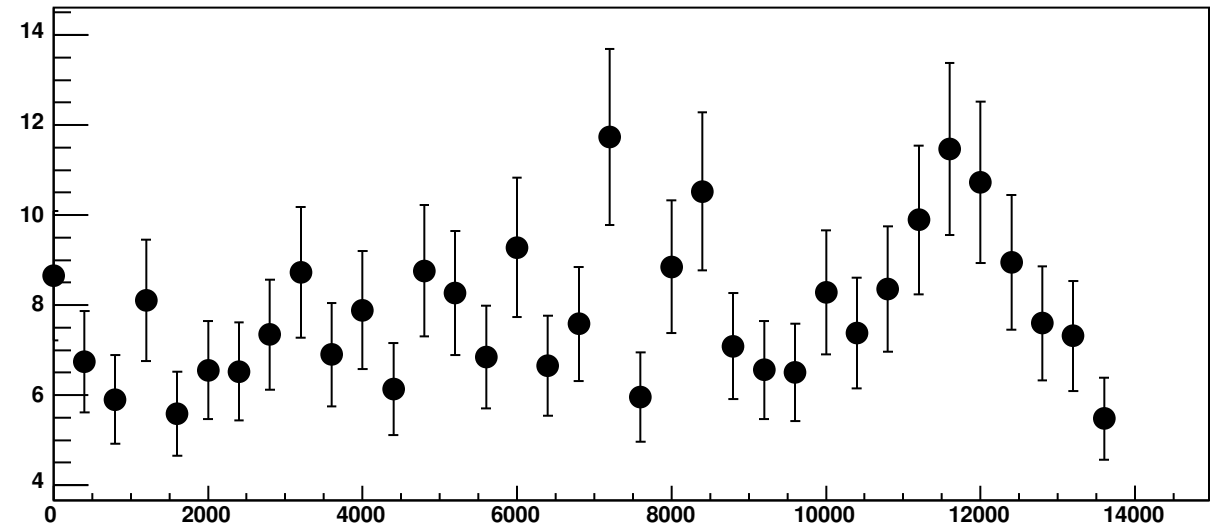

Chip 8, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

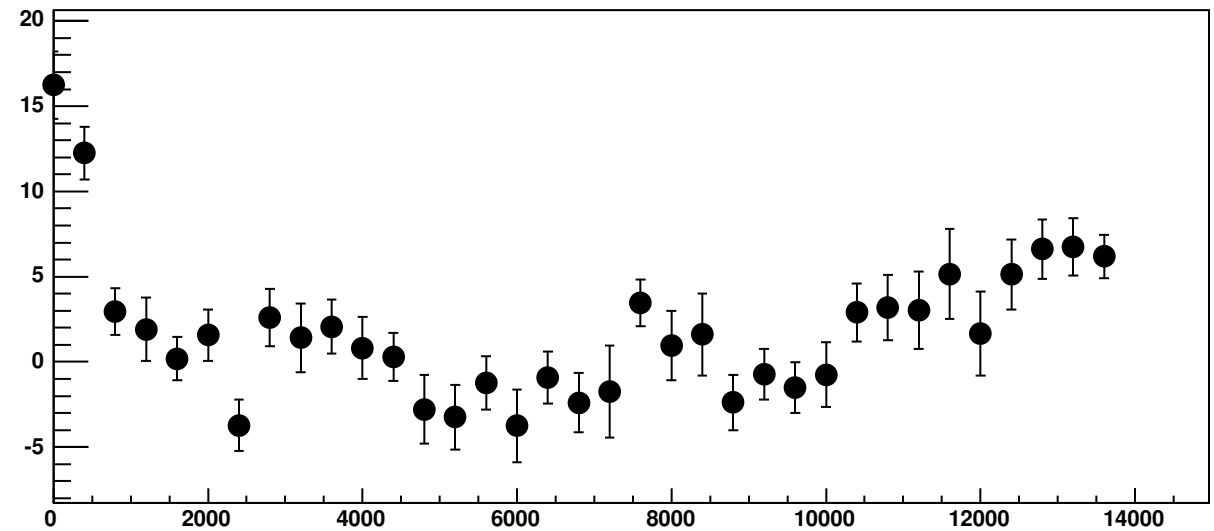
Chip 8, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

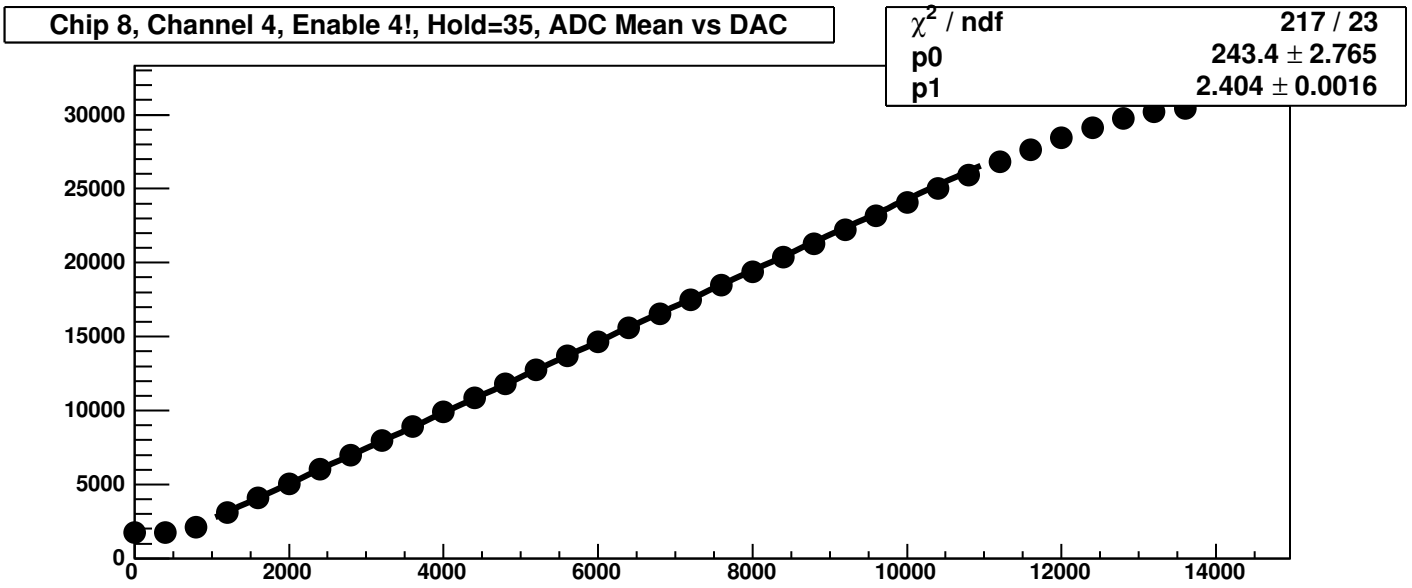


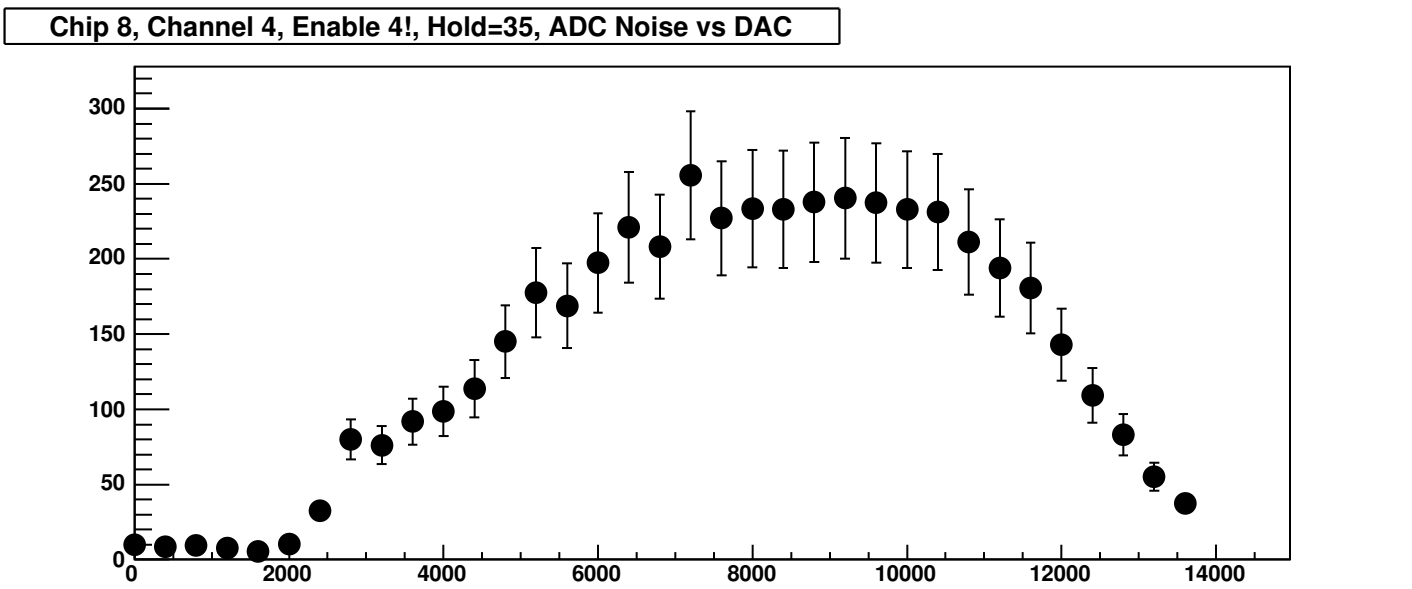
Chip 8, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC

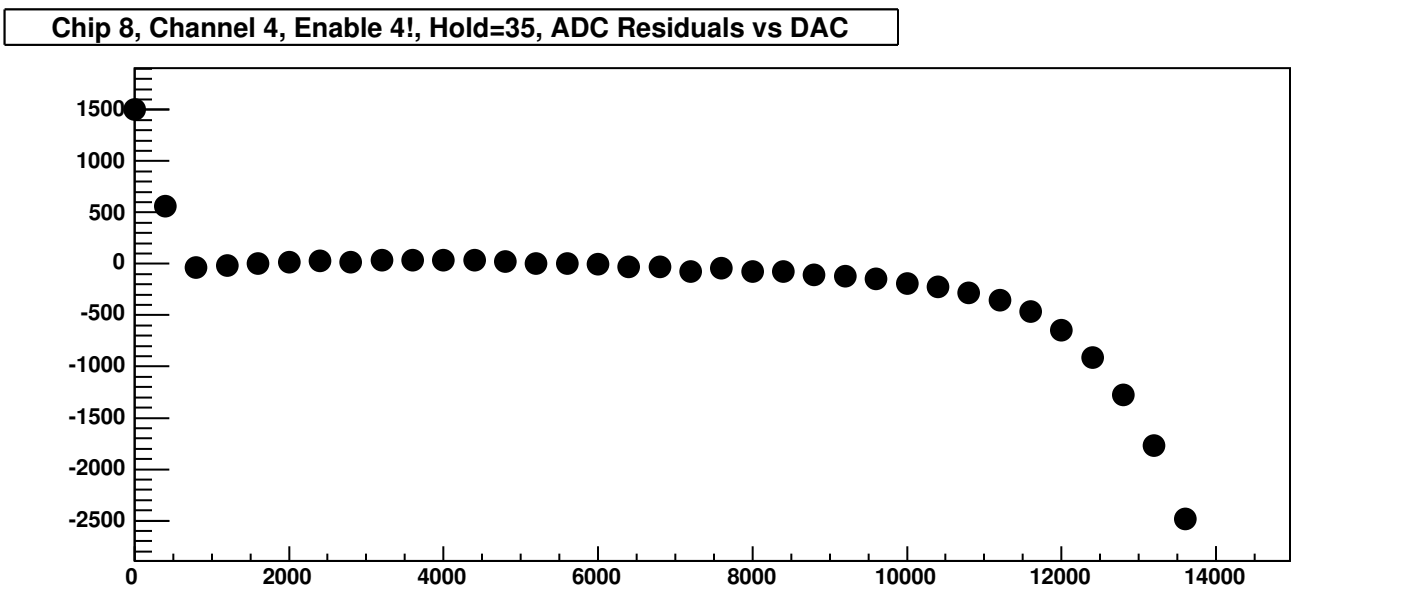


Chip 8, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

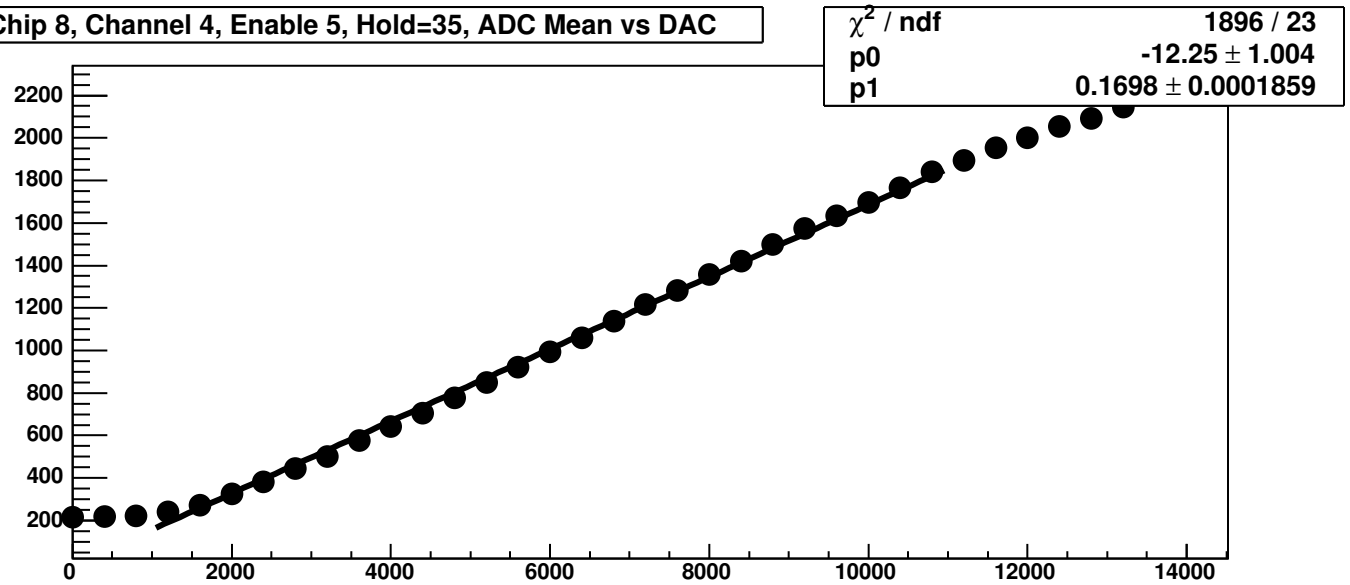



Chip 8, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 217 / 23
p0 243.4 ± 2.765
p1 2.404 ± 0.0016
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

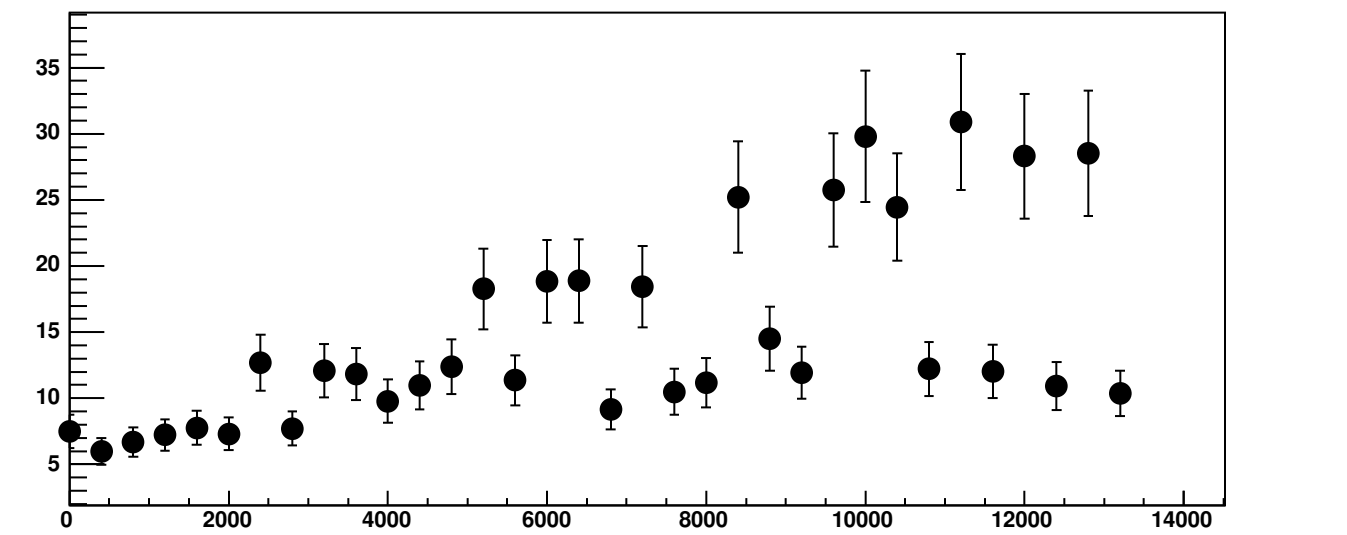

Chip 8, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

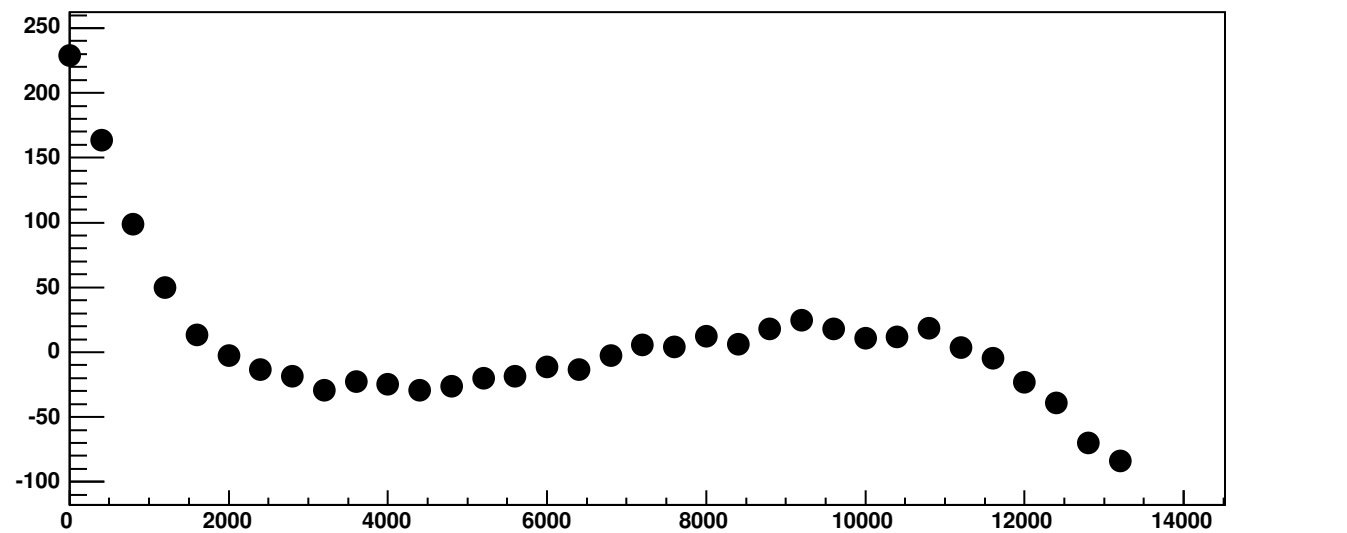
Chip 8, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



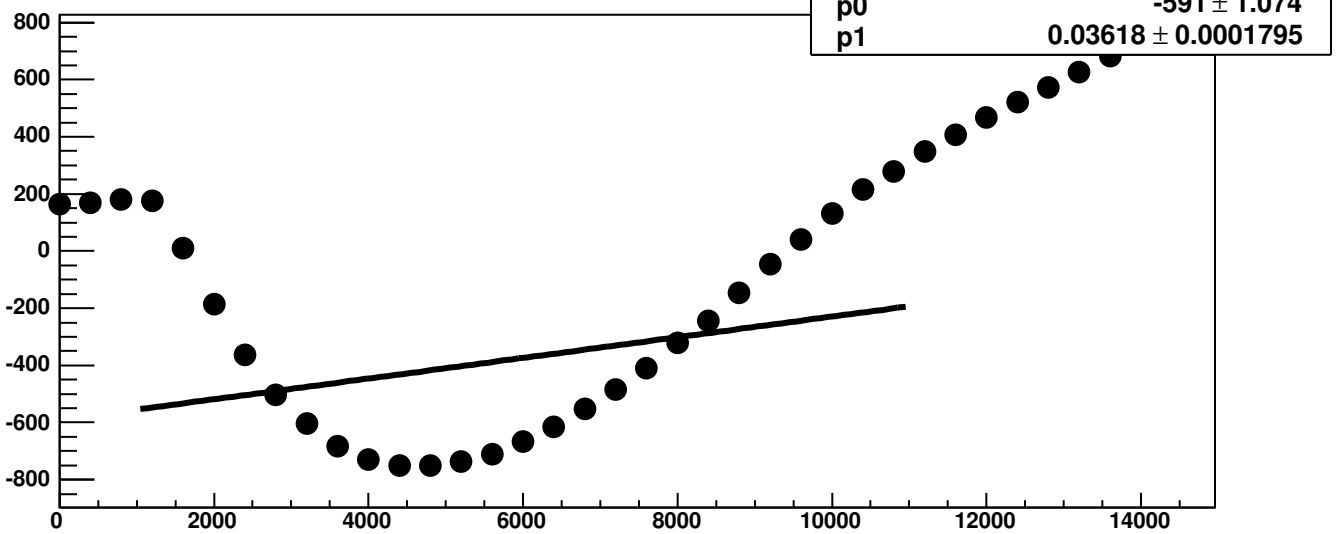
Chip 8, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



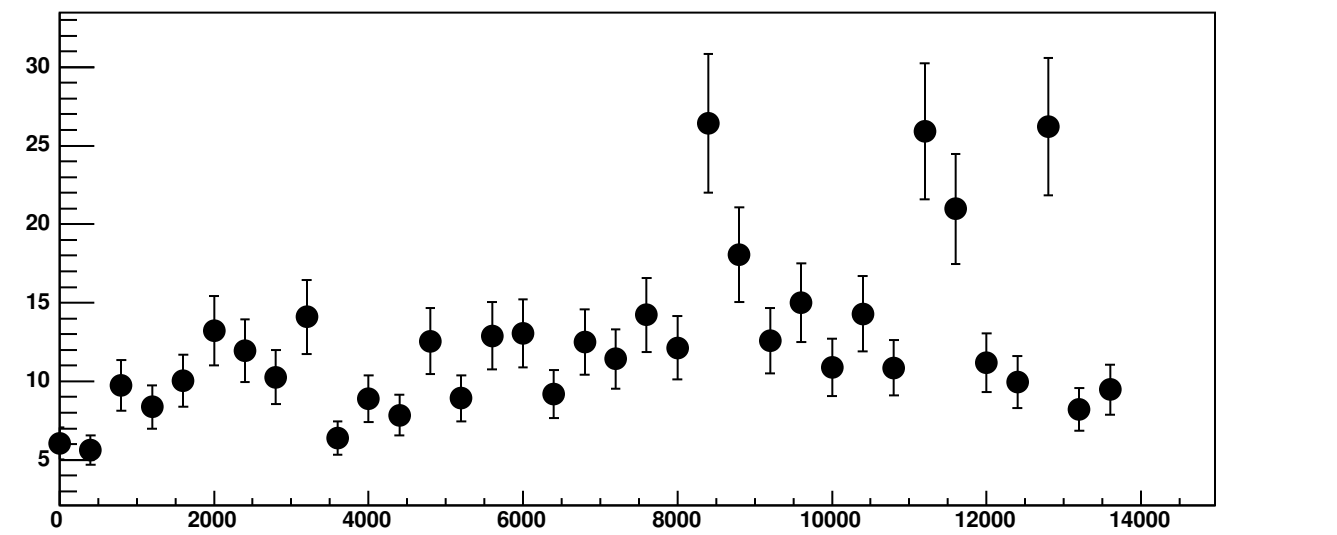
Chip 8, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



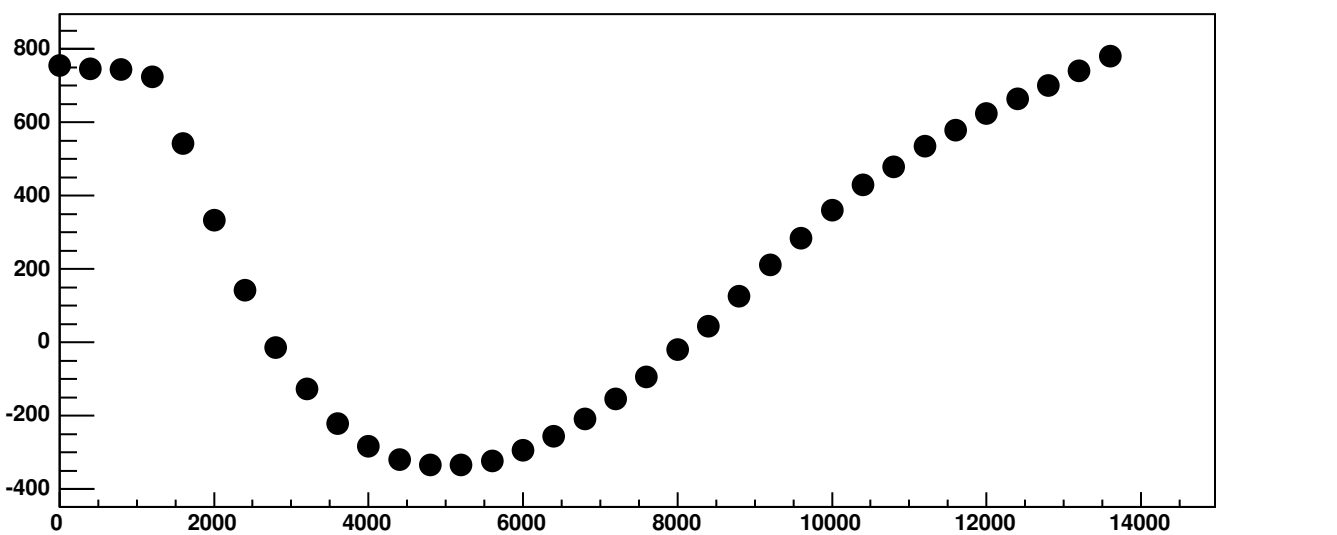
Chip 8, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

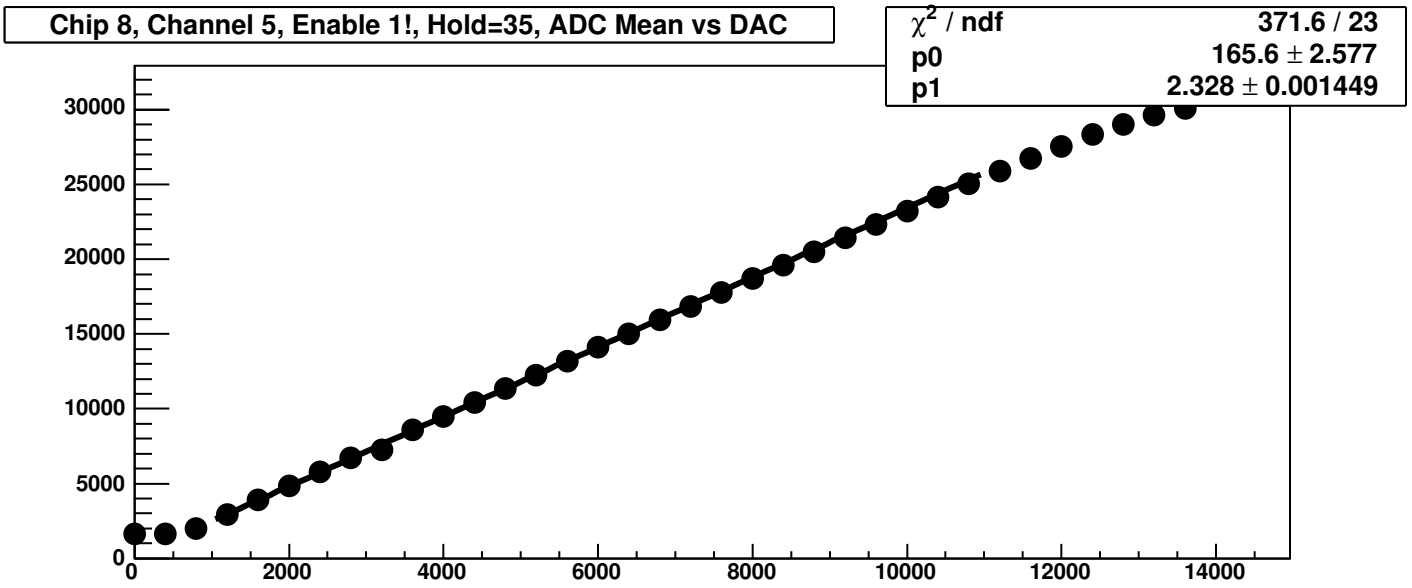


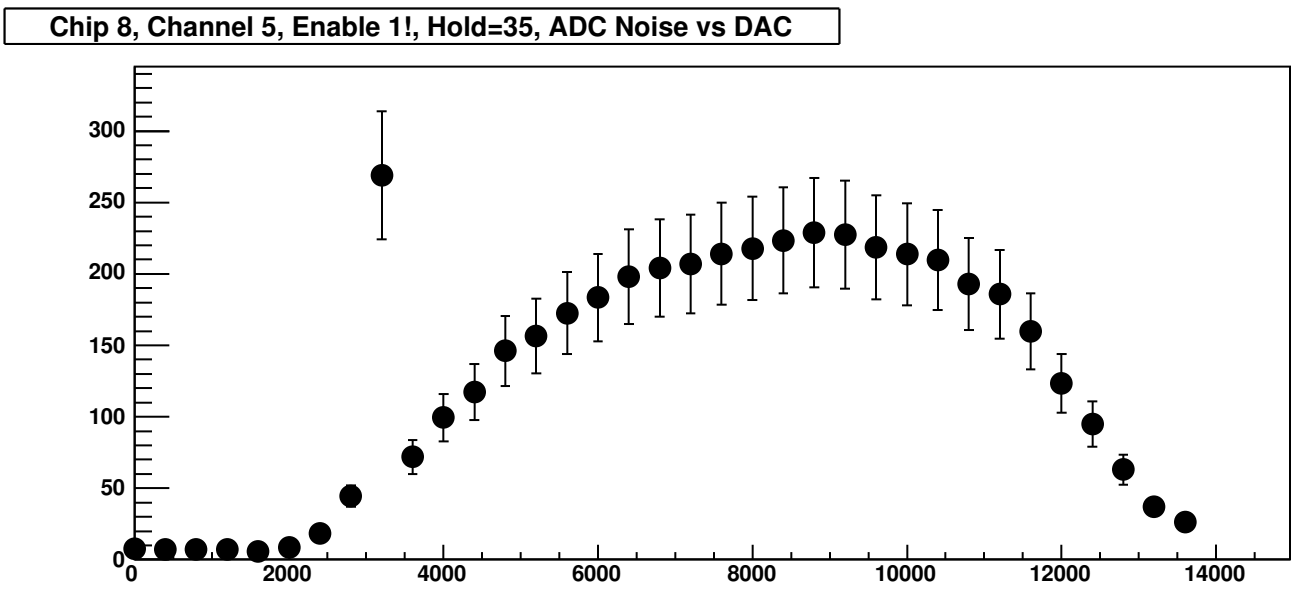
Chip 8, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC

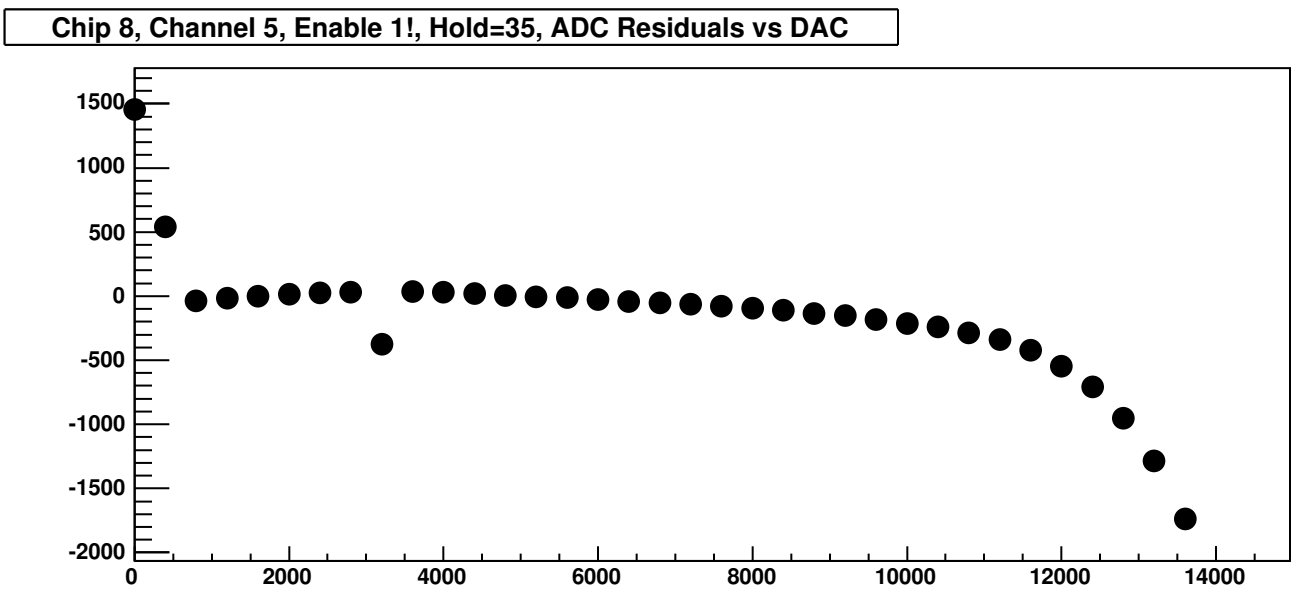


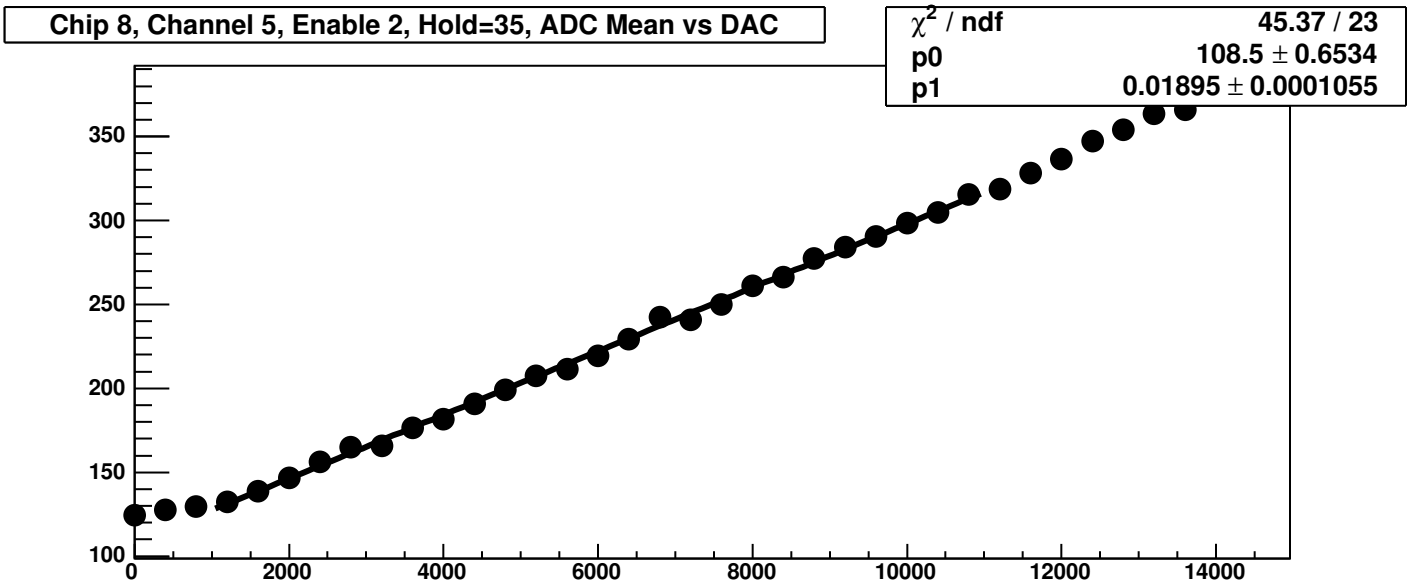
Chip 8, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

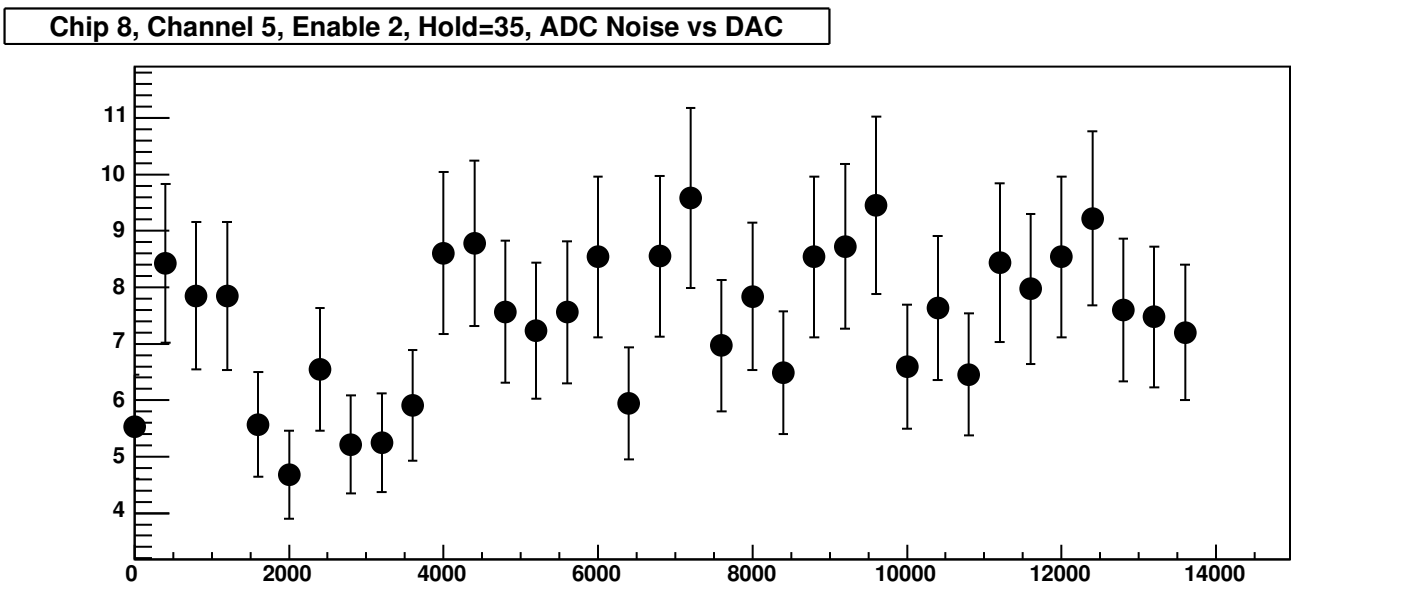


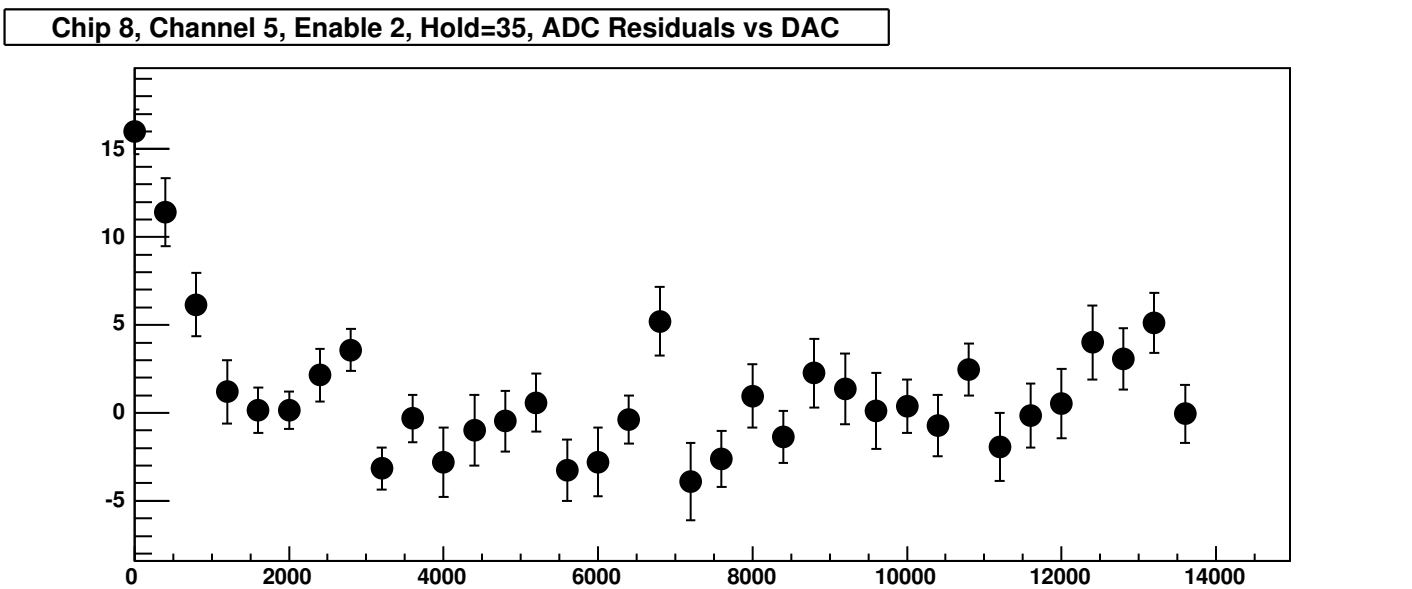

Chip 8, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 371.6 / 23
p0 165.6 ± 2.577
p1 2.328 ± 0.001449
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

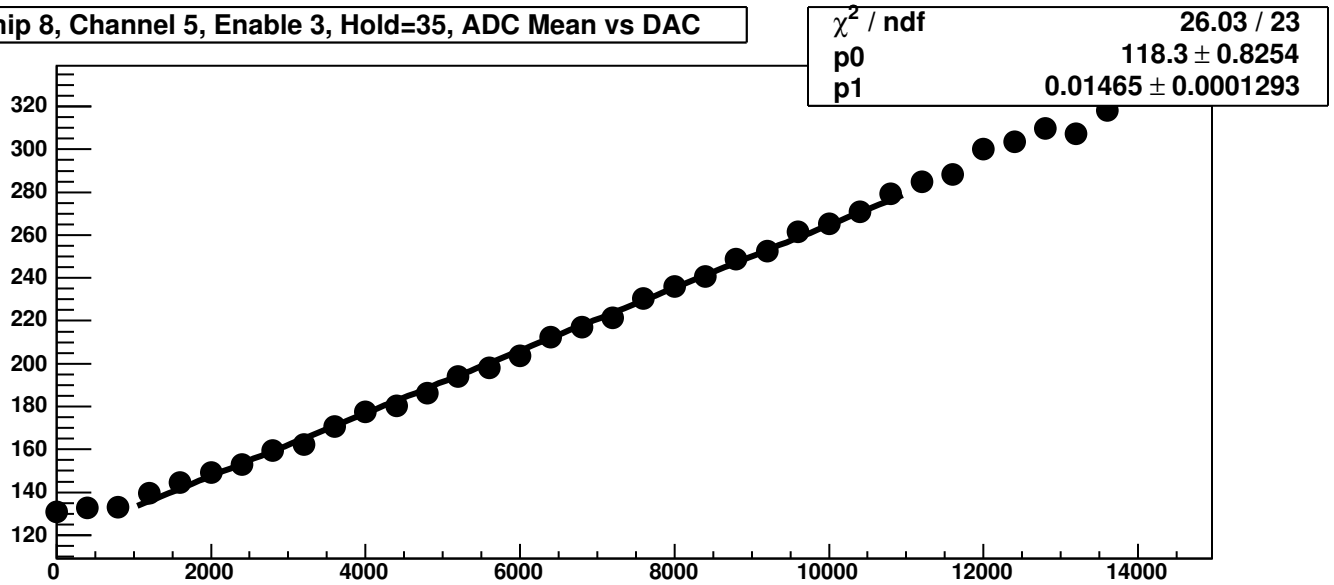

Chip 8, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 45.37 / 23
p0 108.5 ± 0.6534
p1 0.01895 ± 0.0001055
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

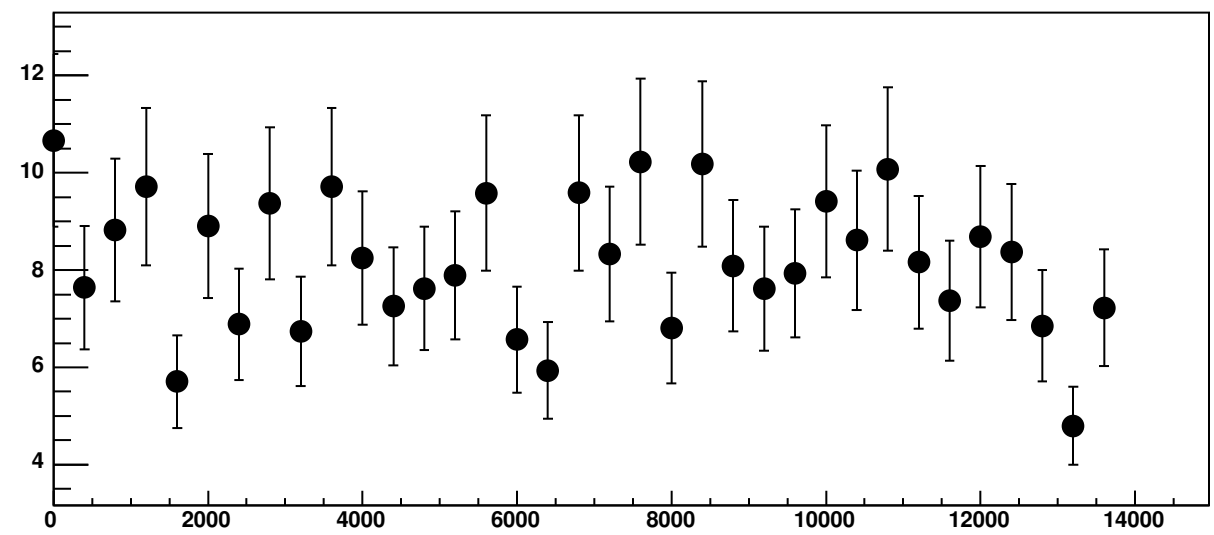

Chip 8, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

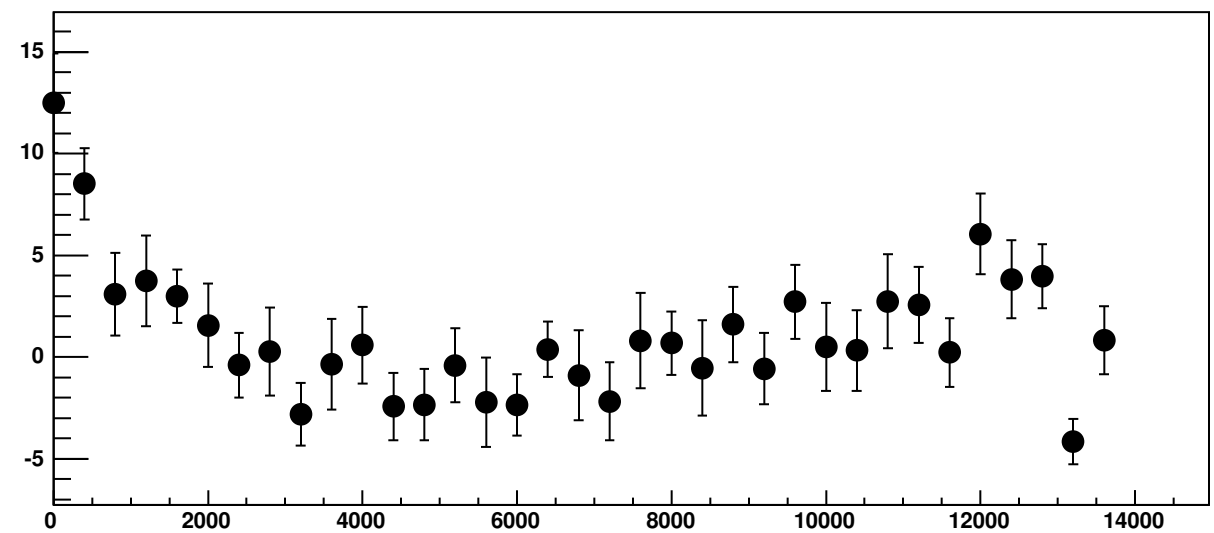
Chip 8, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

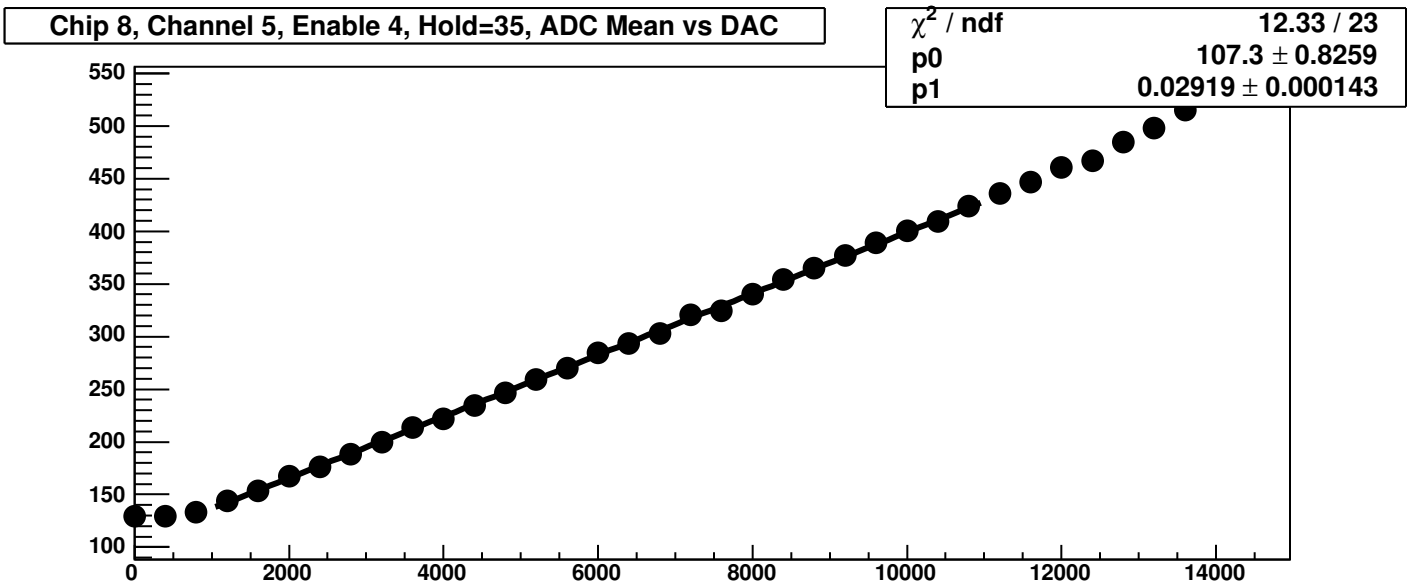


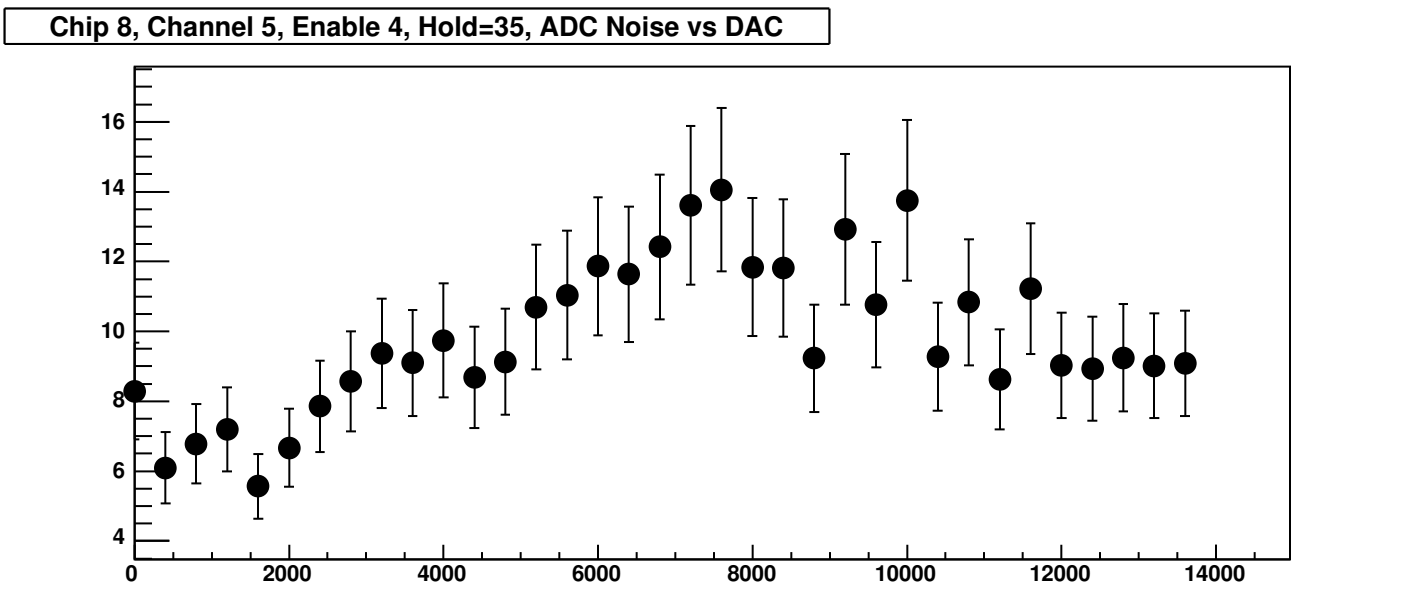
Chip 8, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC

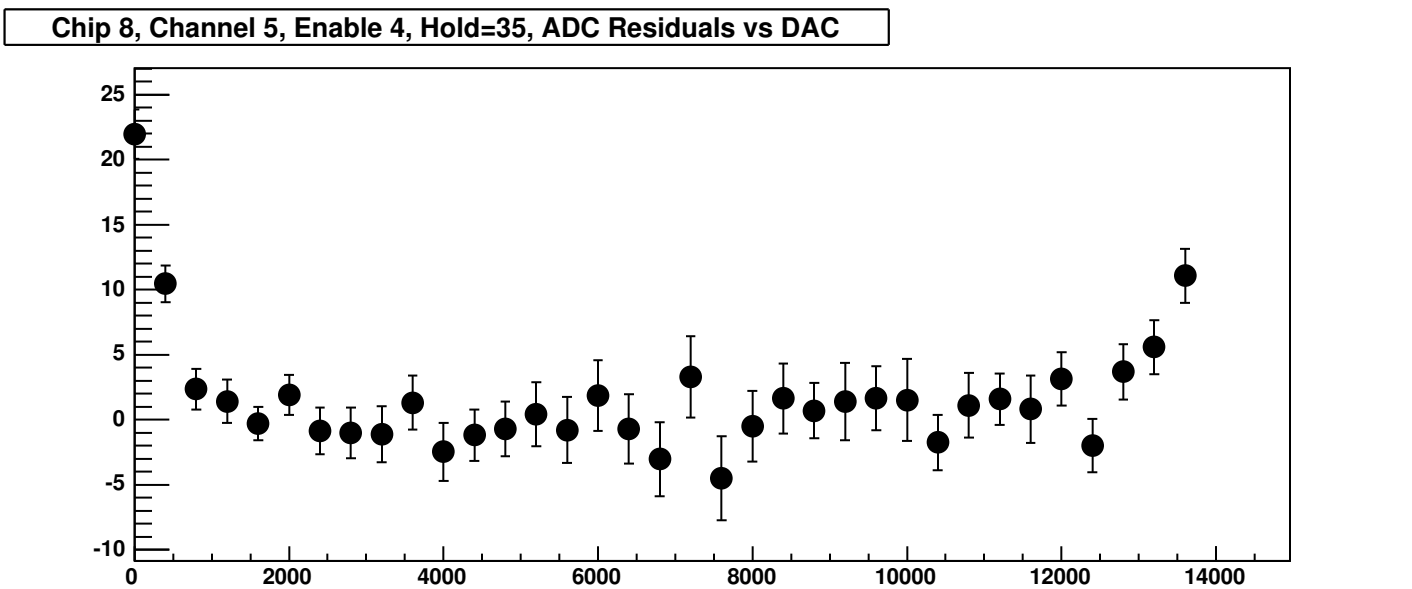


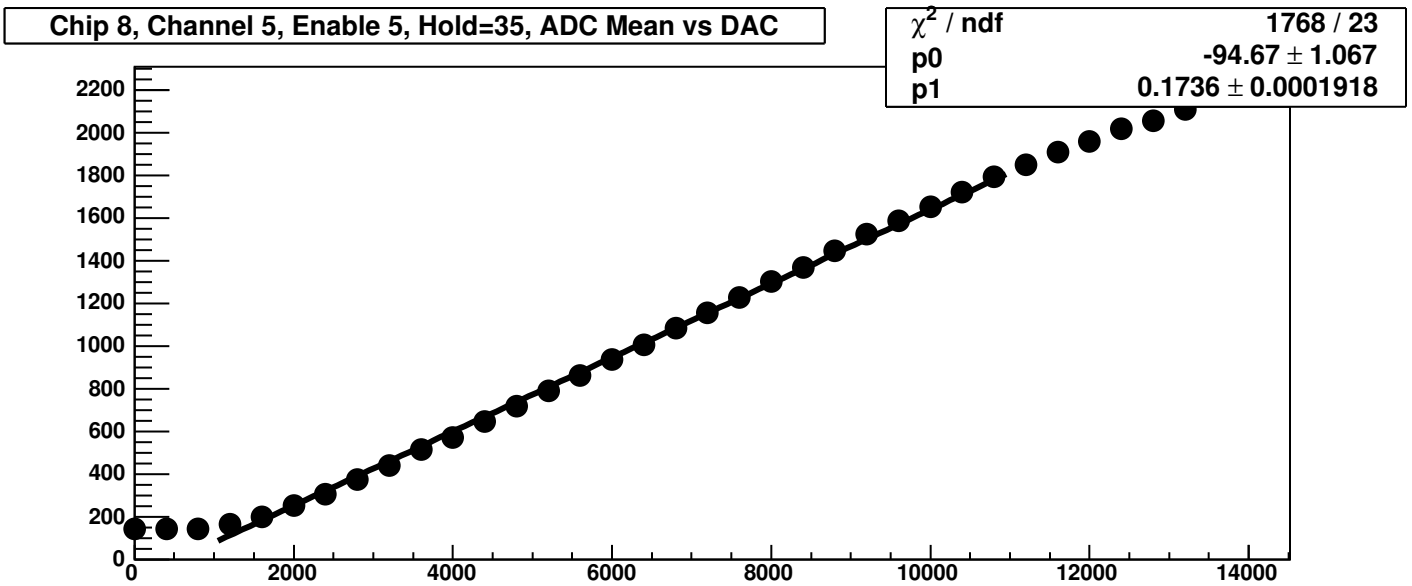
Chip 8, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

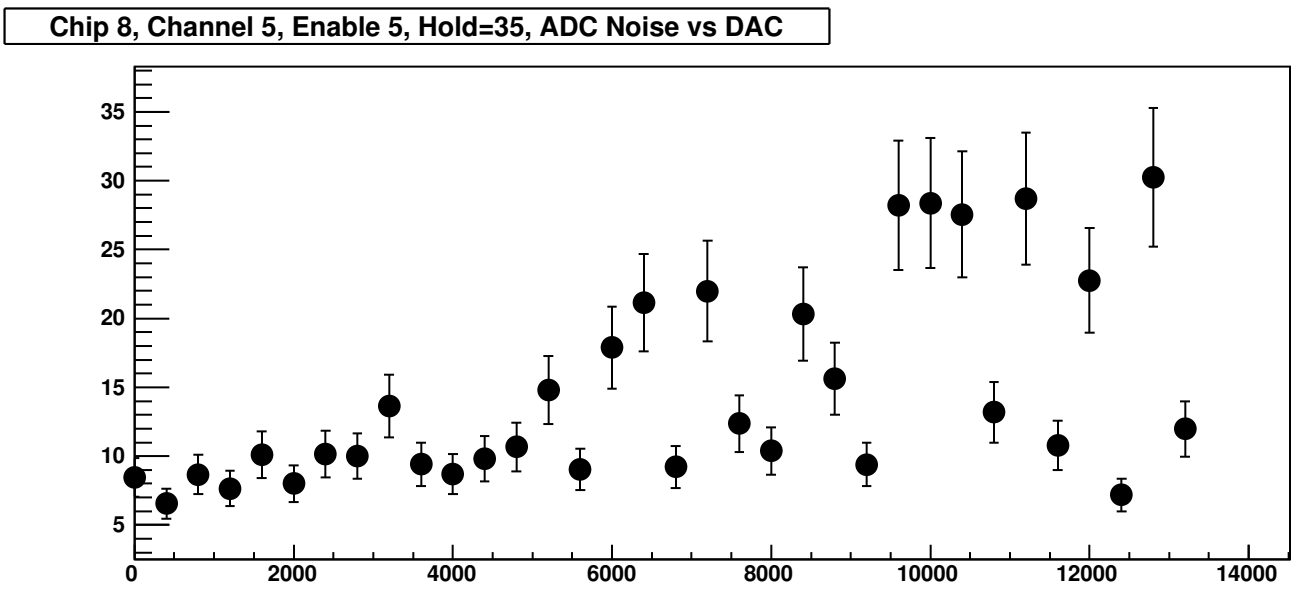


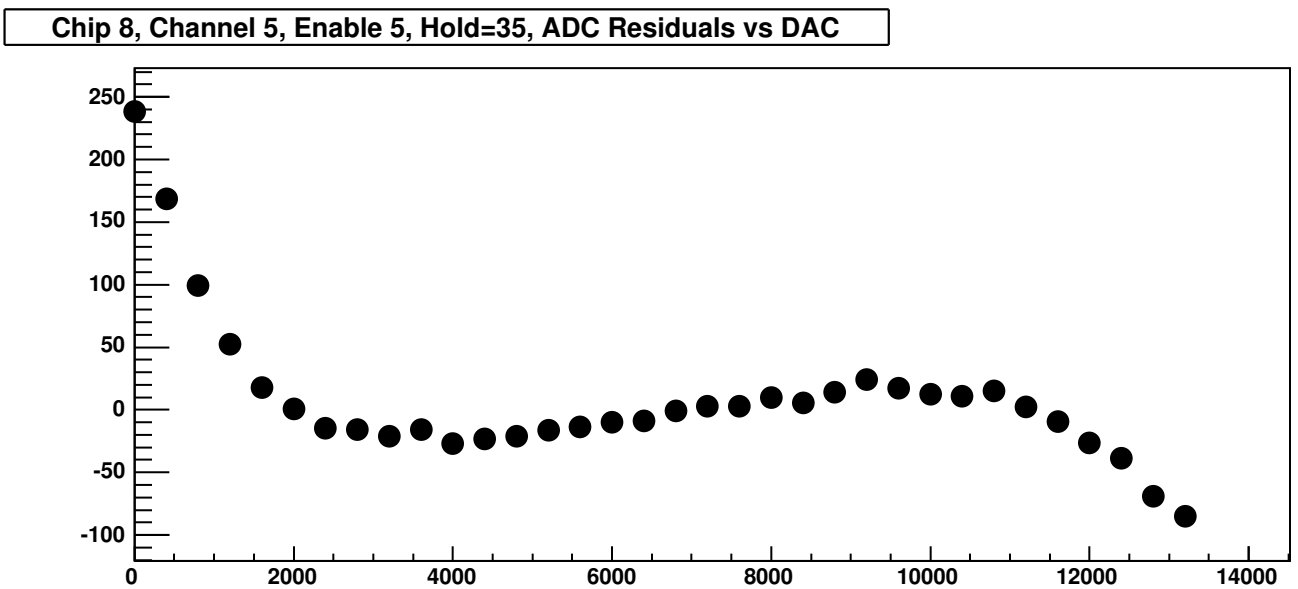

Chip 8, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 12.33 / 23
p0 107.3 ± 0.8259
p1 0.02919 ± 0.000143
550
500
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

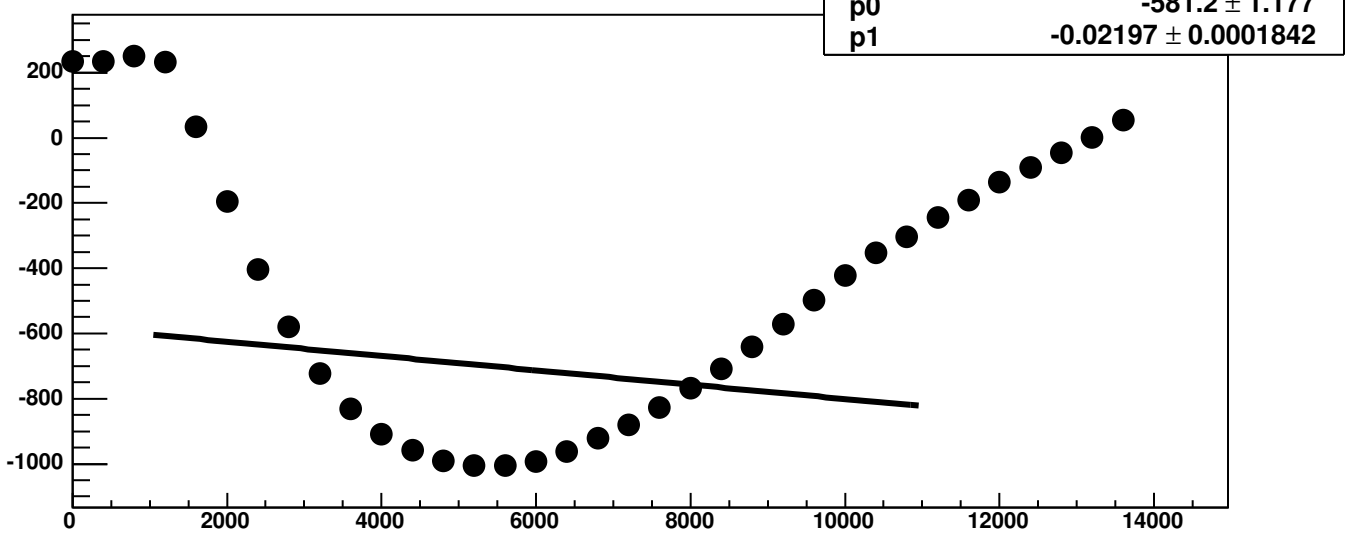

Chip 8, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1768 / 23
p0 -94.67 ± 1.067
p1 0.1736 ± 0.0001918
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

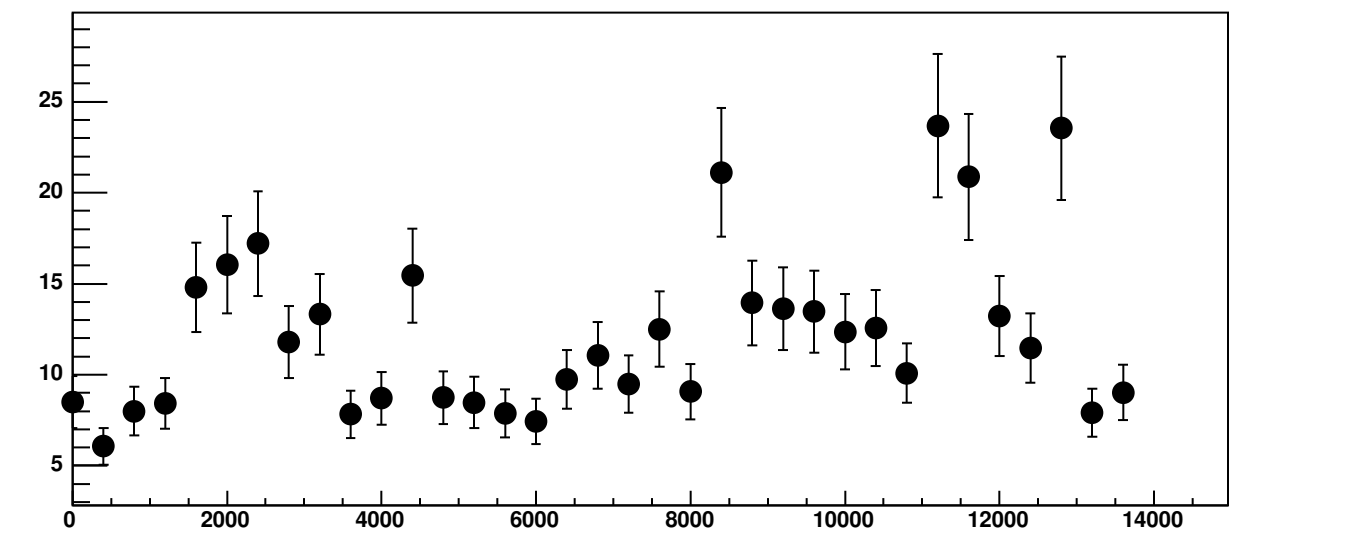

Chip 8, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

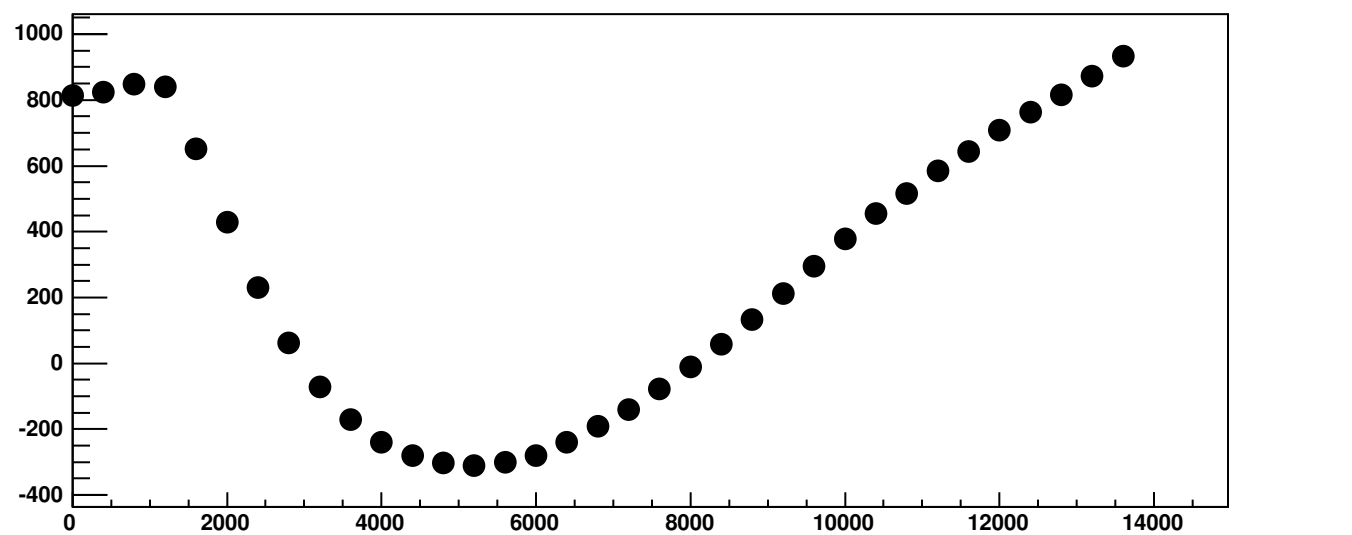
Chip 8, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

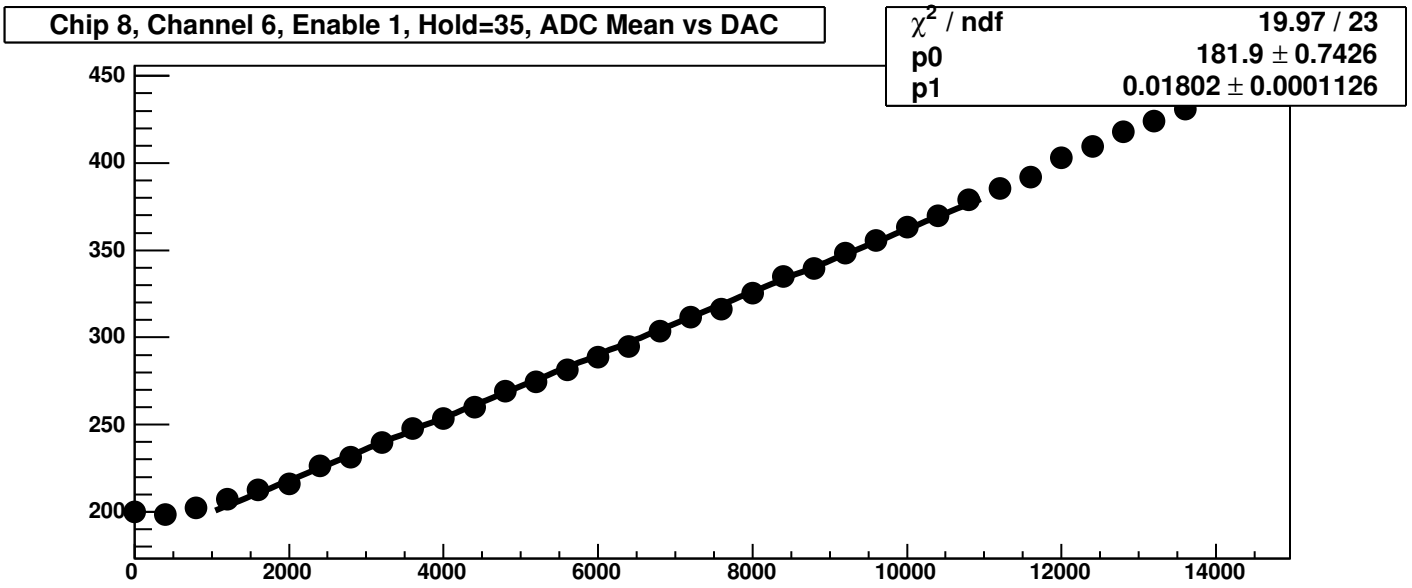


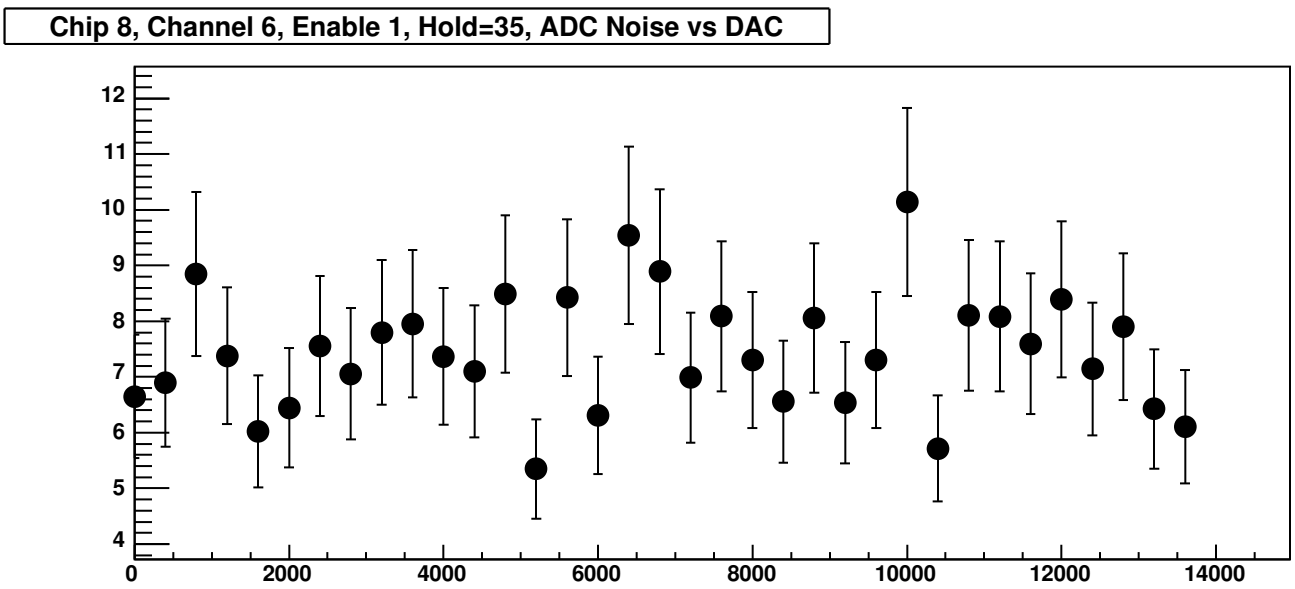
Chip 8, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC

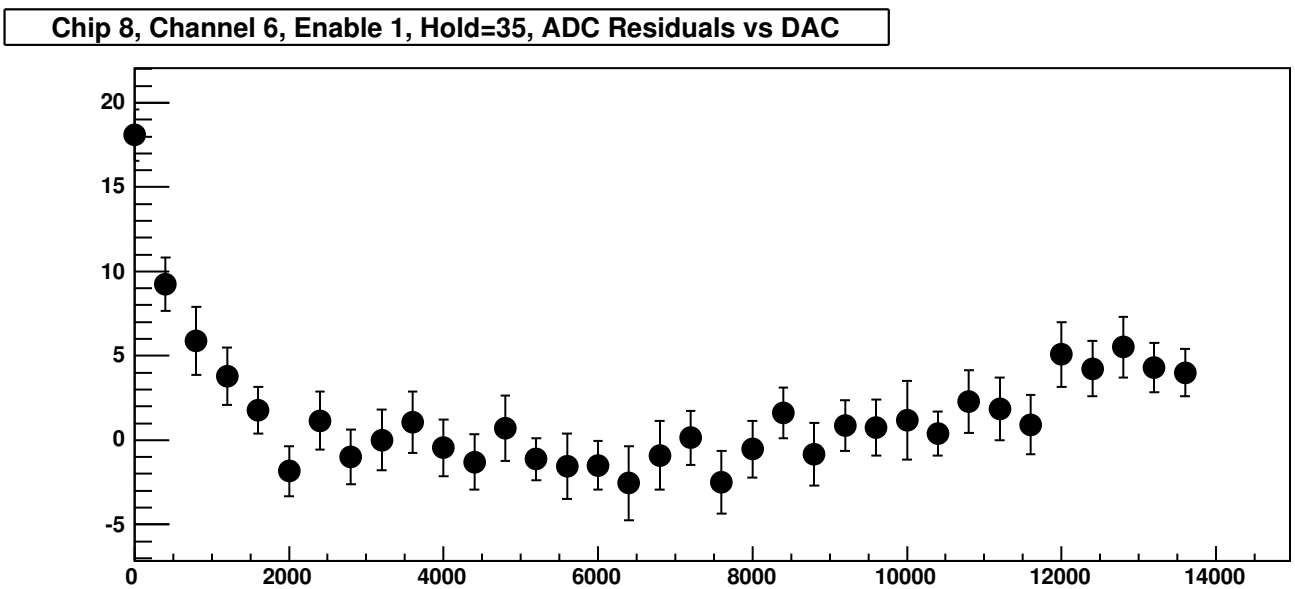


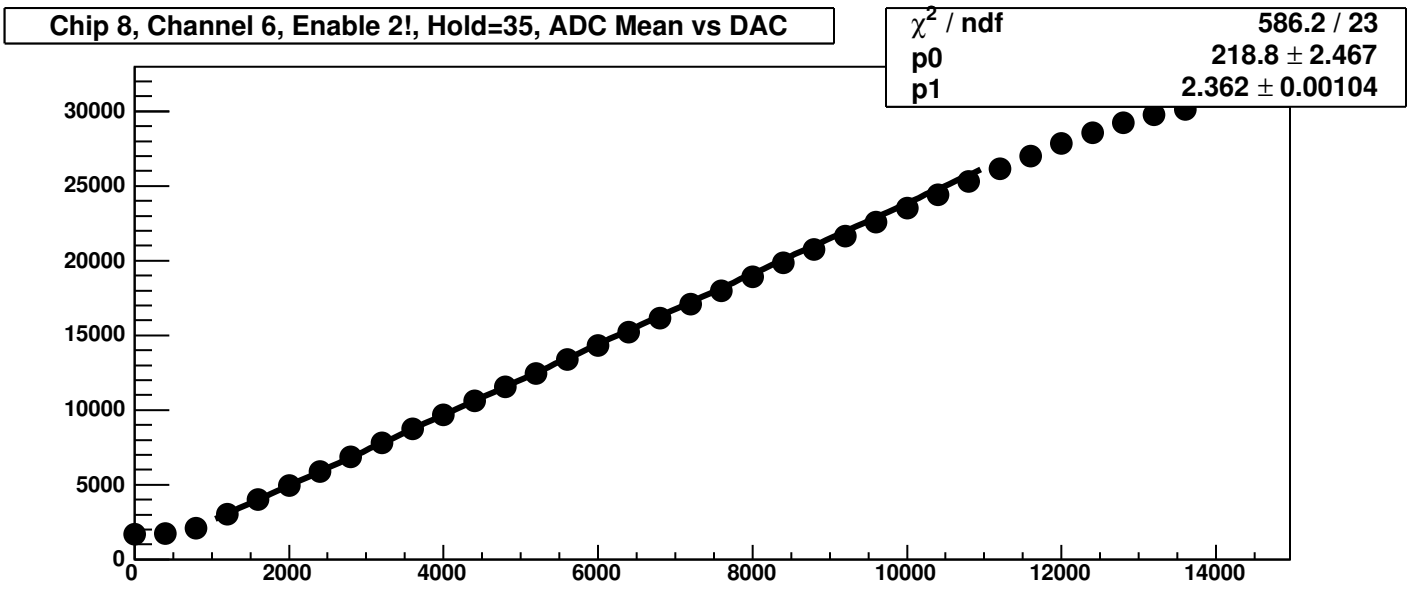
Chip 8, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

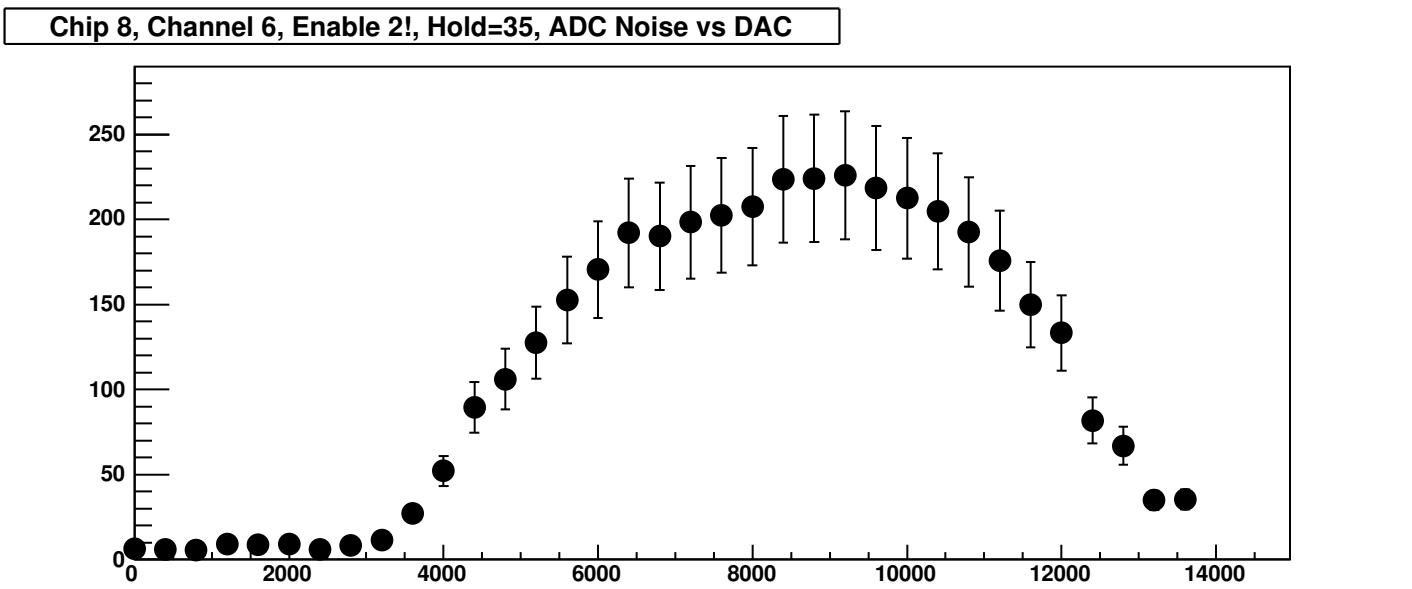


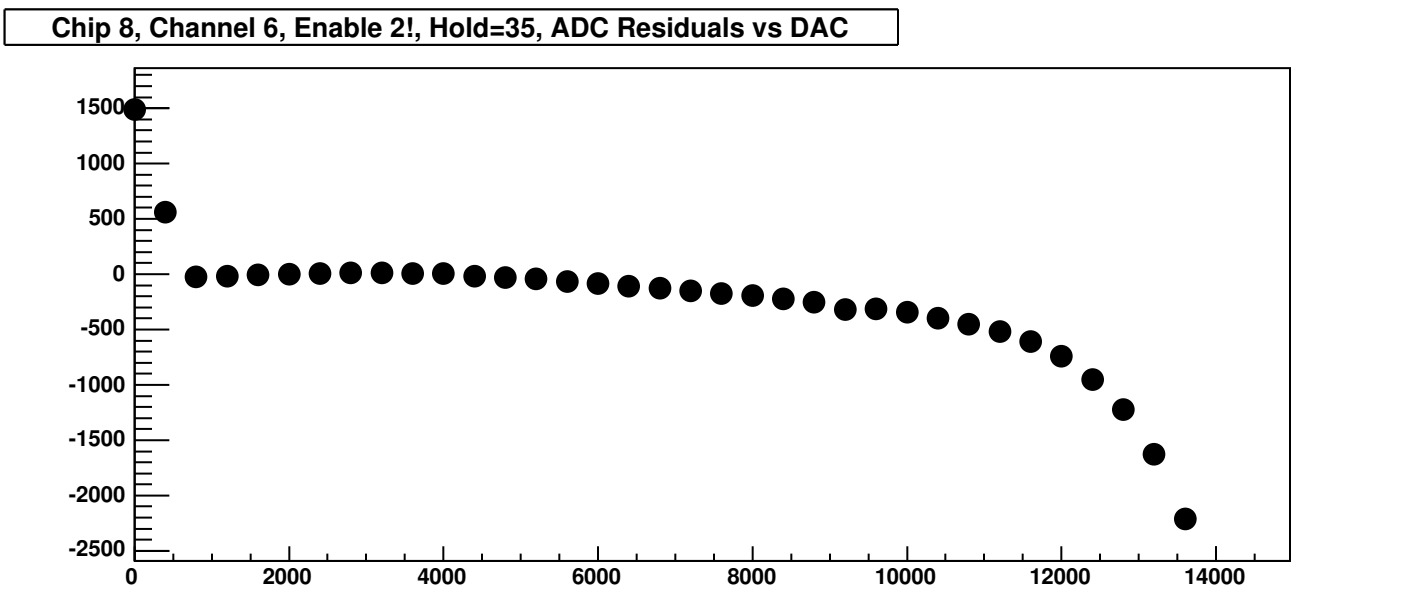

Chip 8, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 19.97 / 23
p0 181.9 ± 0.7426
p1 0.01802 ± 0.0001126
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

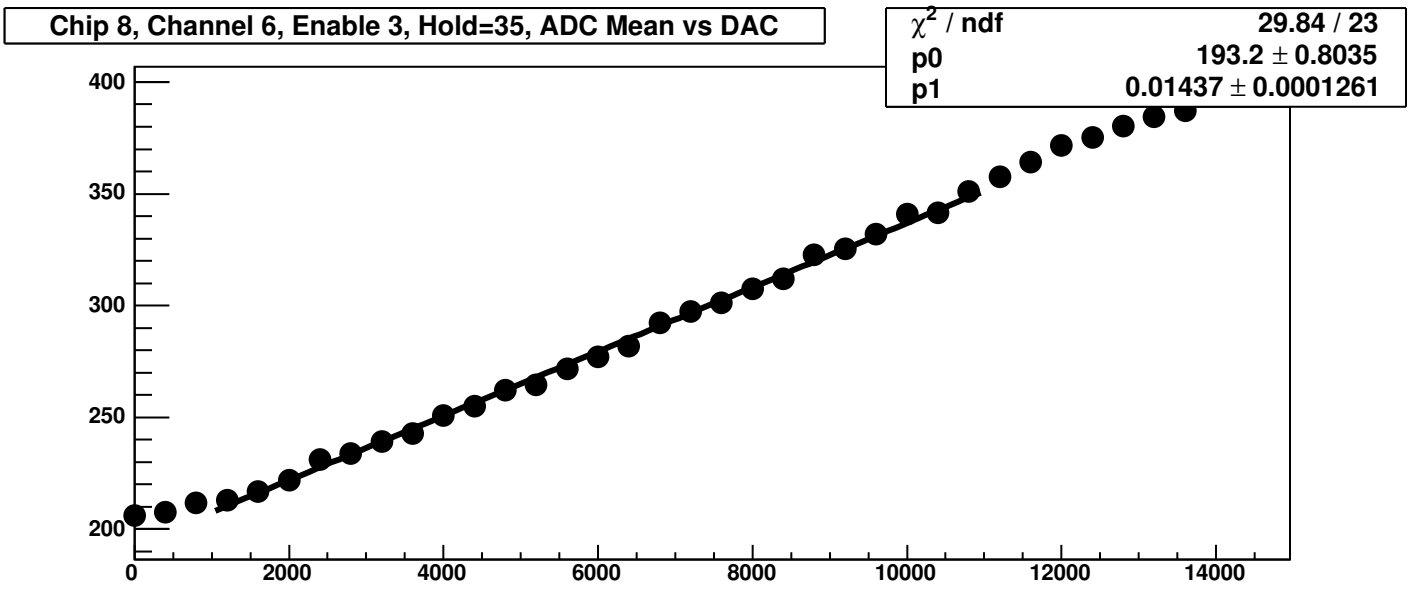

Chip 8, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

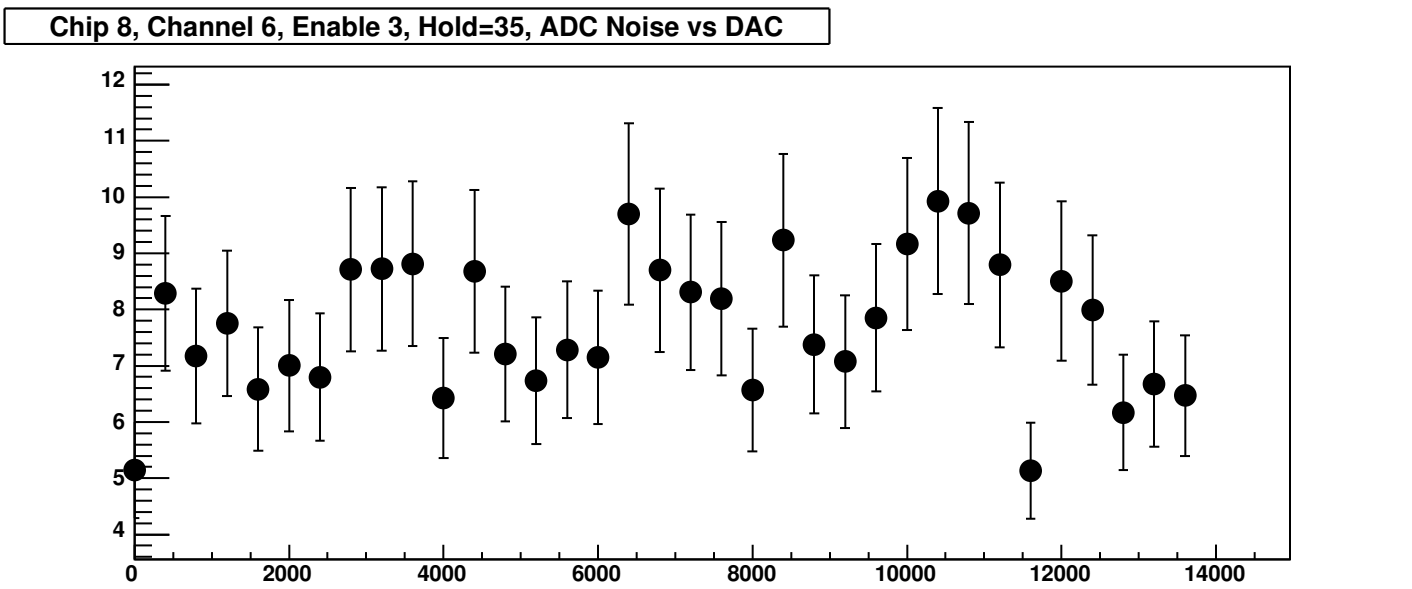

Chip 8, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

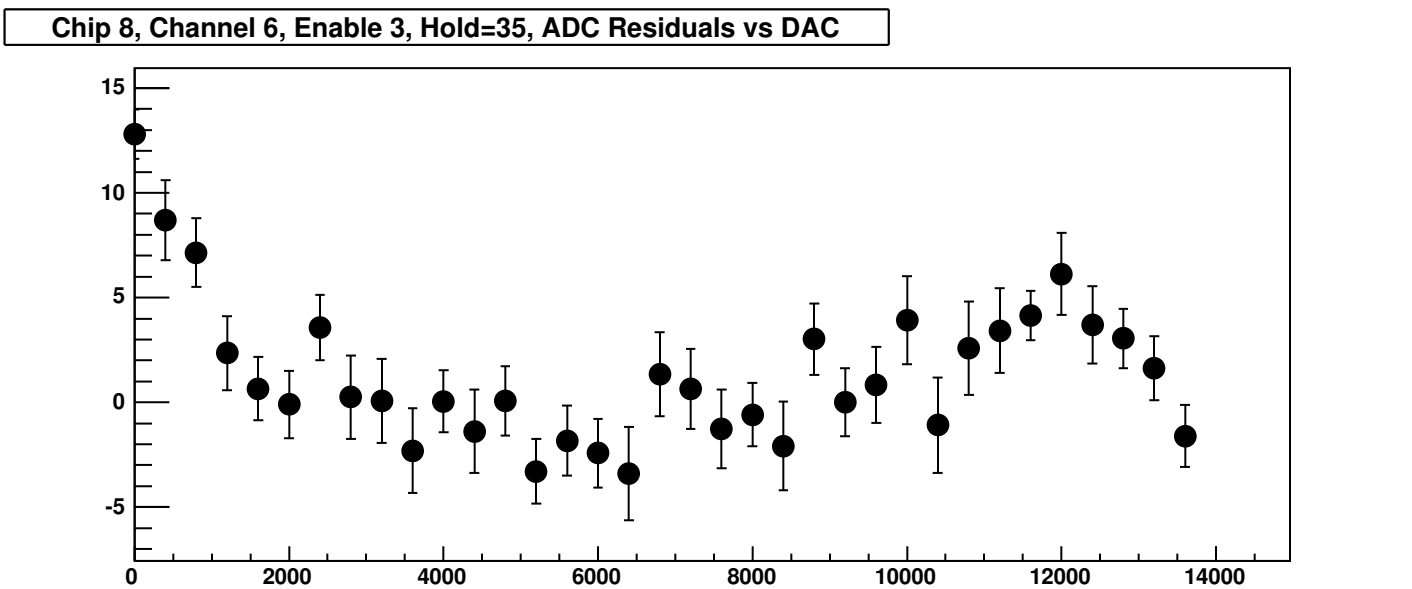

Chip 8, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 586.2 / 23
p0 218.8 ± 2.467
p1 2.362 ± 0.00104
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

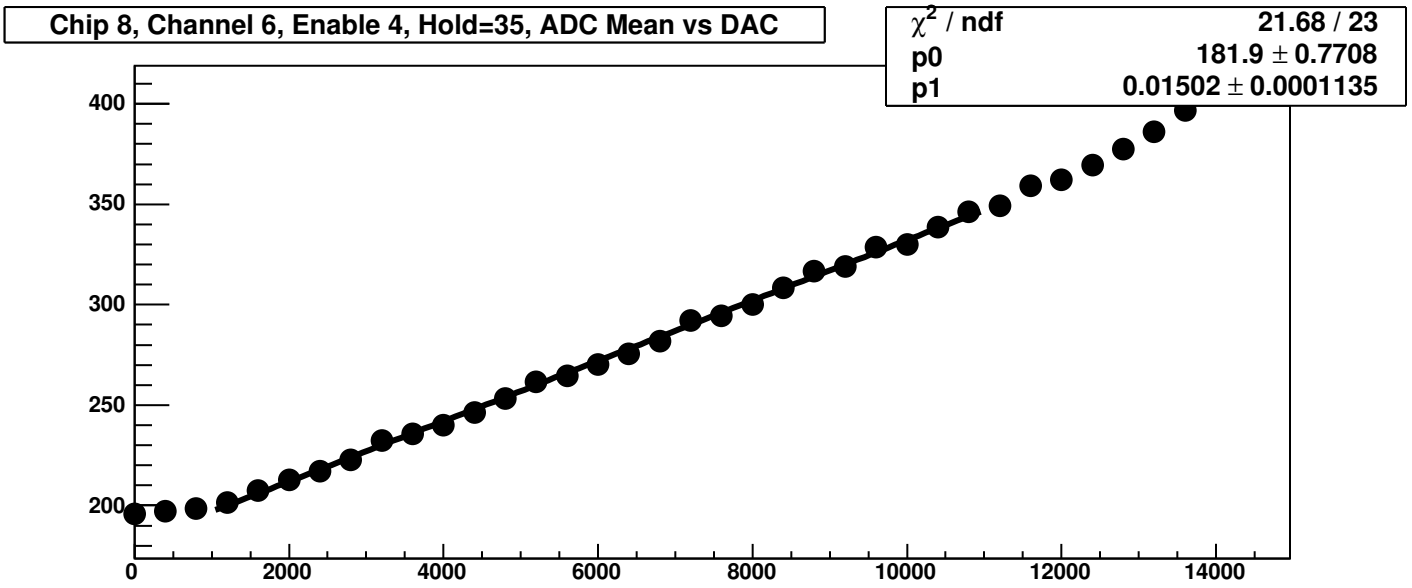

Chip 8, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

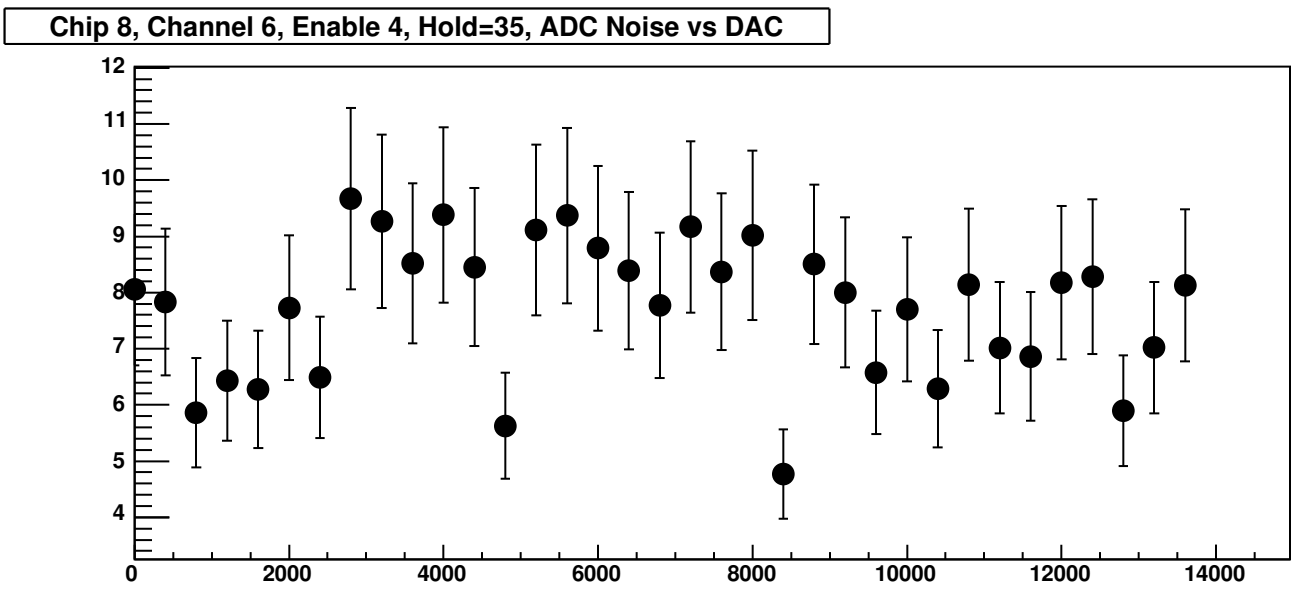

Chip 8, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

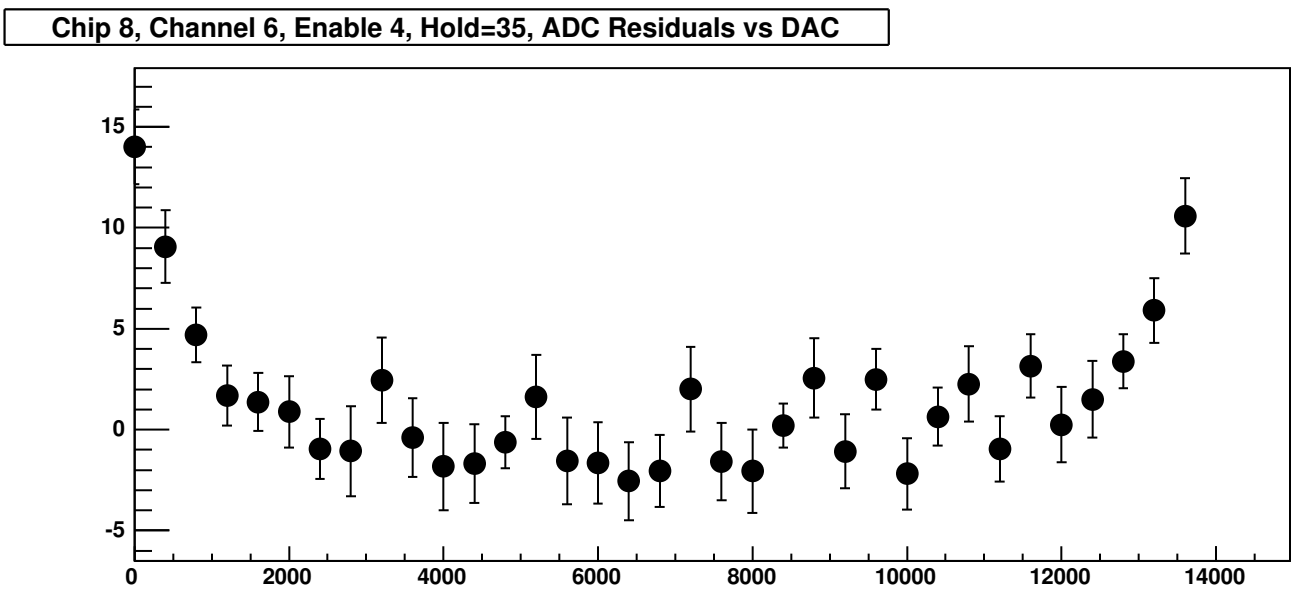

Chip 8, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 29.84 / 23
p0 193.2 ± 0.8035
p1 0.01437 ± 0.0001261
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

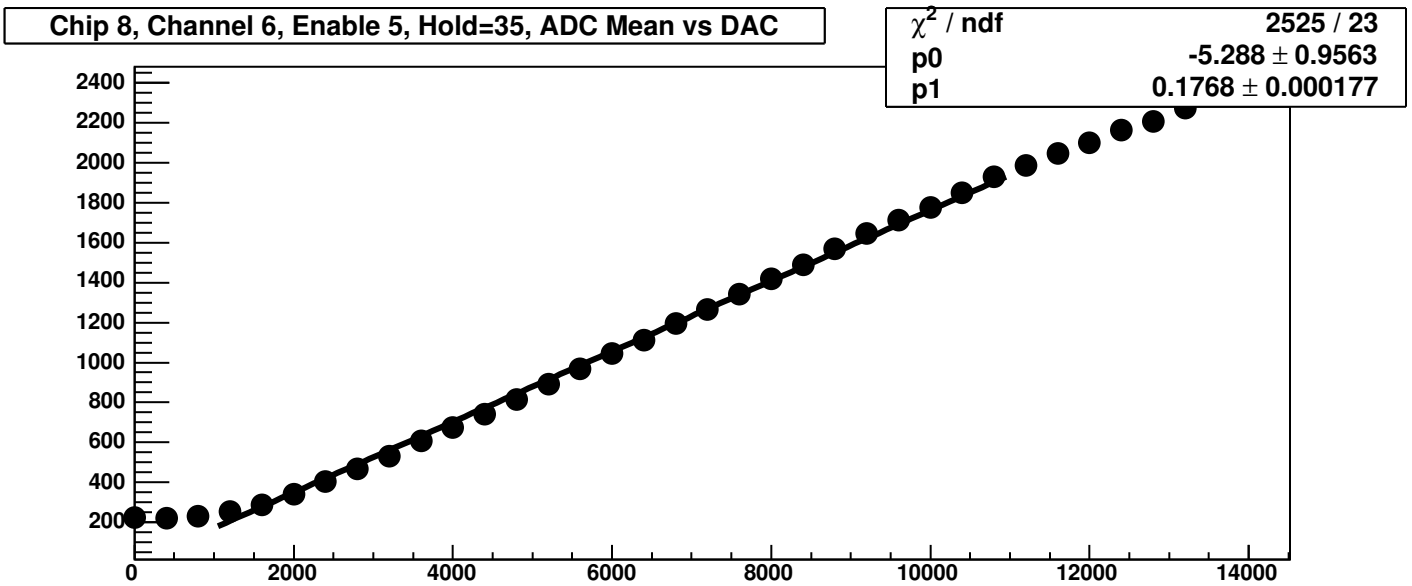

Chip 8, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

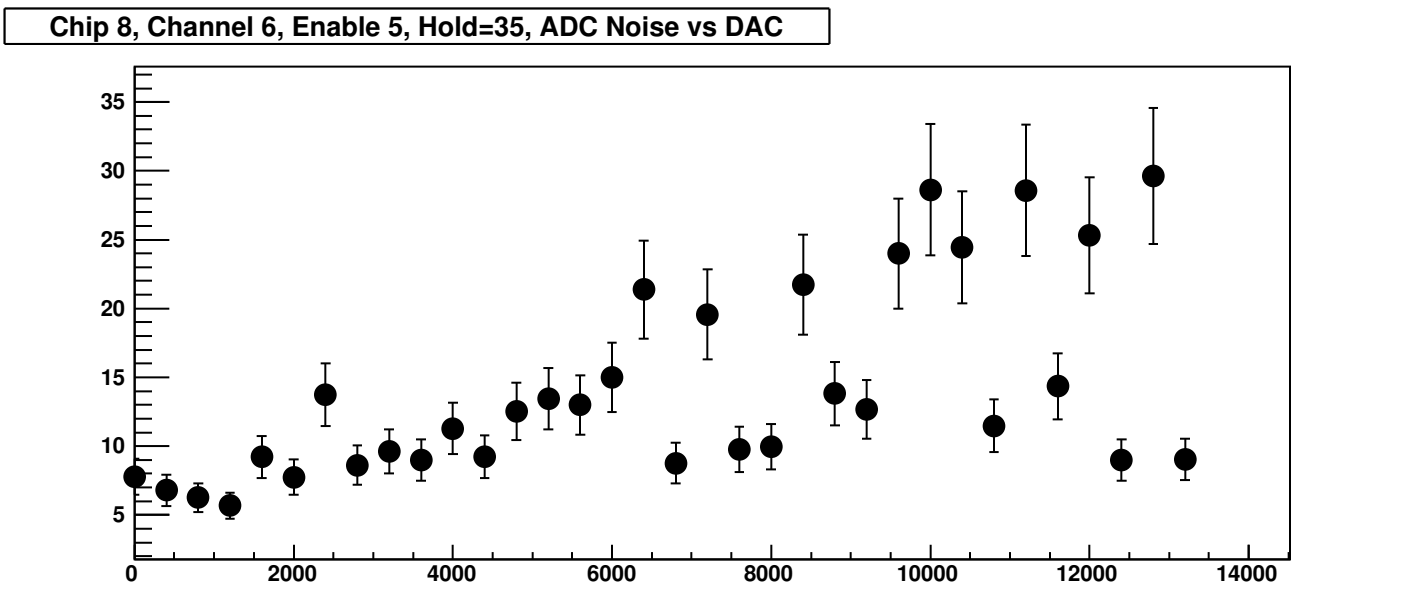

Chip 8, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

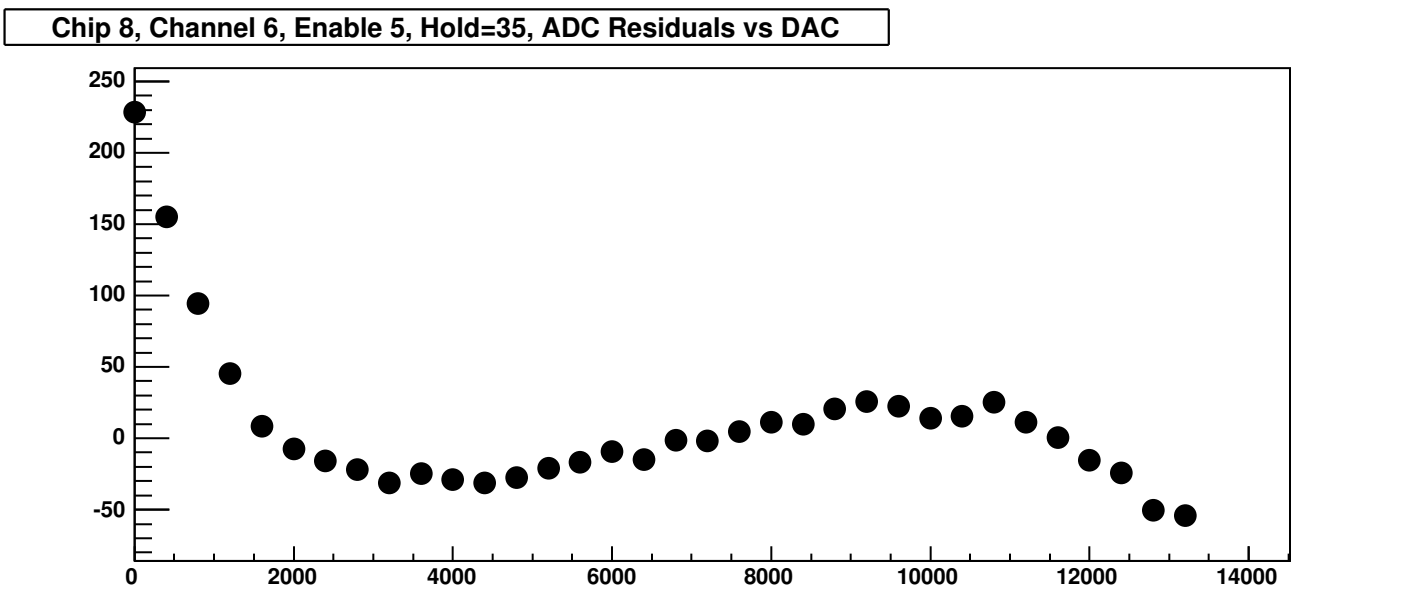

Chip 8, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 21.68 / 23
p0 181.9 ± 0.7708
p1 0.01502 ± 0.0001135
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

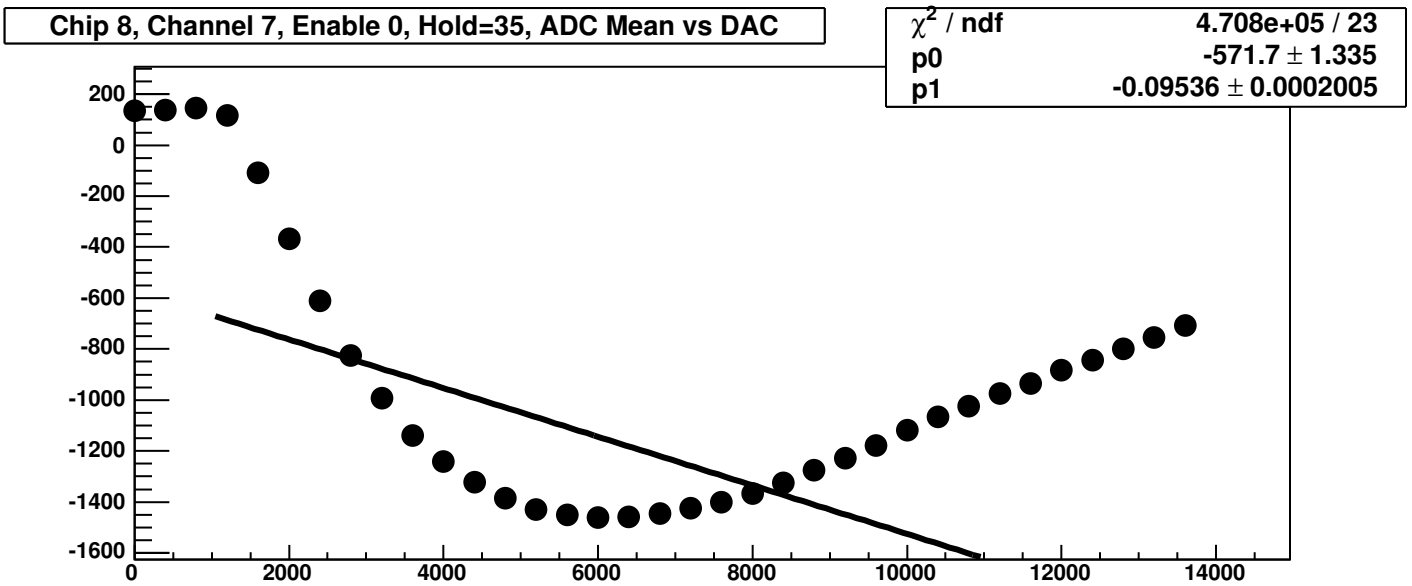

Chip 8, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

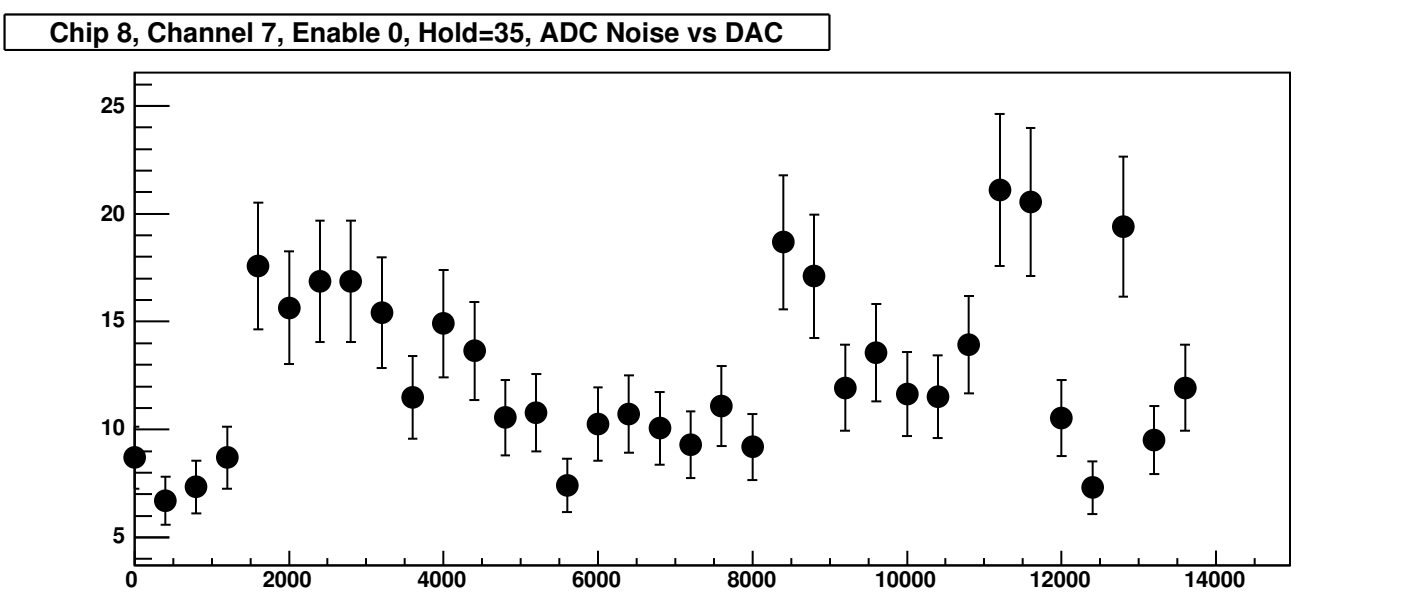

Chip 8, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

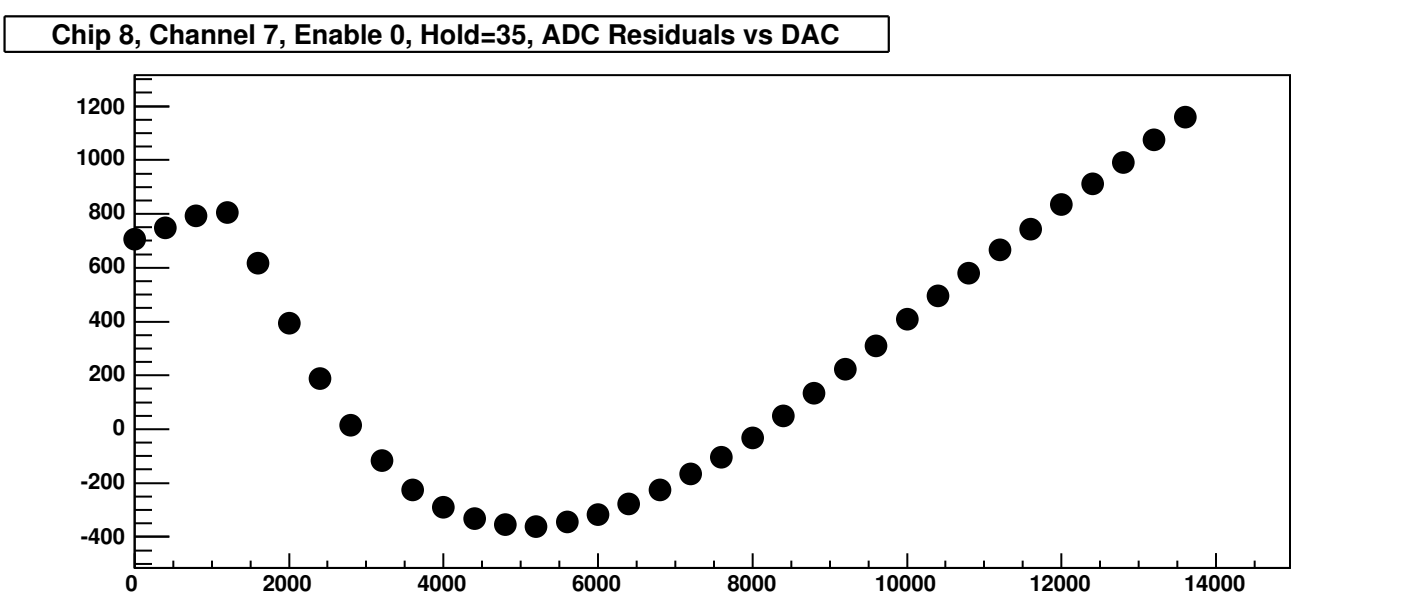

Chip 8, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2525 / 23
p0 -5.288 ± 0.9563
p1 0.1768 ± 0.000177
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

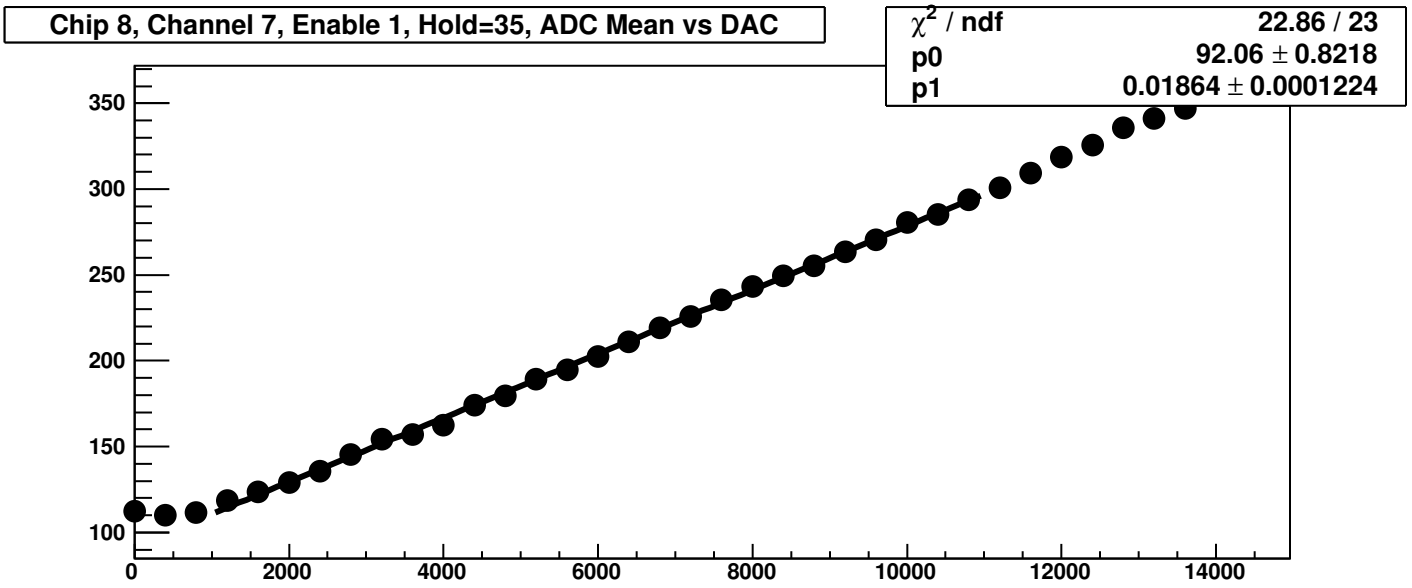

Chip 8, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

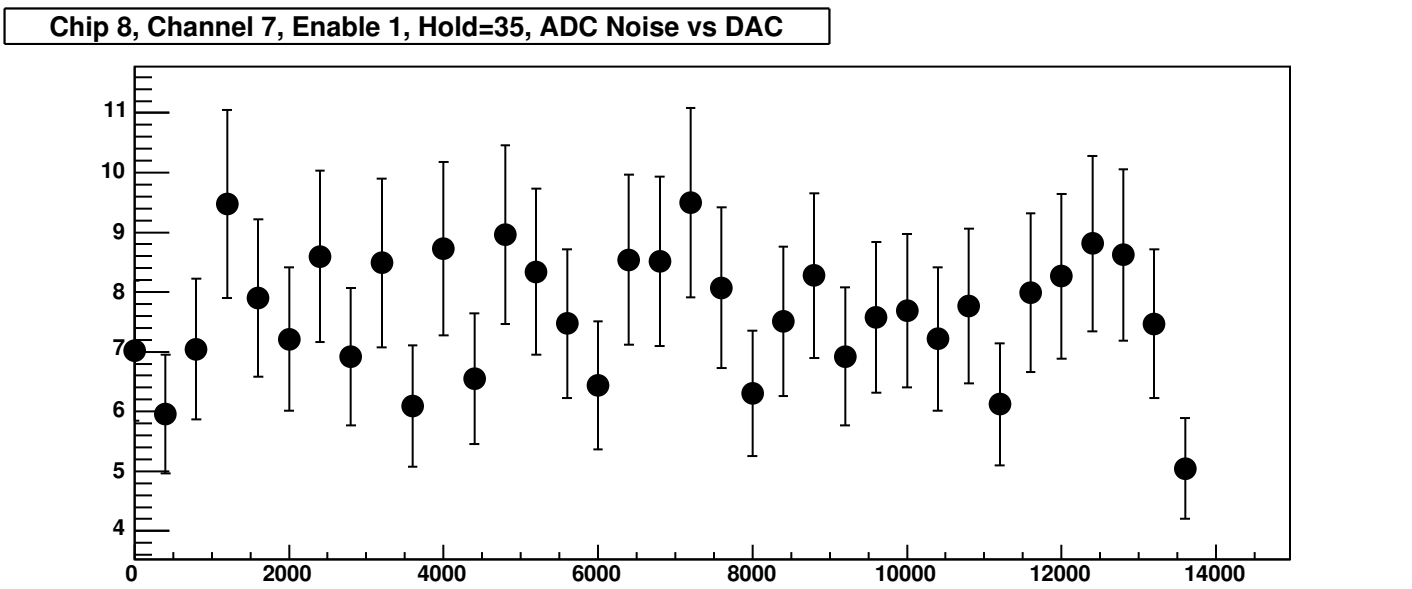

Chip 8, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

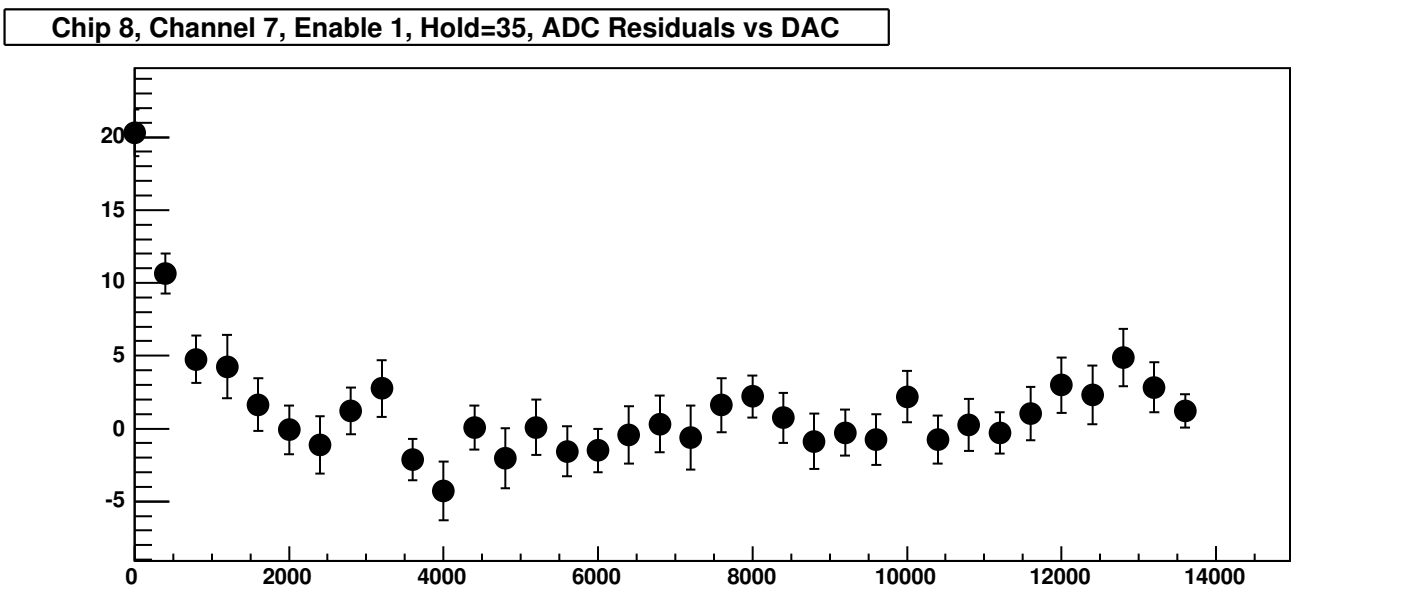

Chip 8, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.708e+05 / 23
p0 -571.7 ± 1.335
p1 -0.09536 ± 0.0002005
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
0
2000
4000
6000
8000
10000
12000
14000

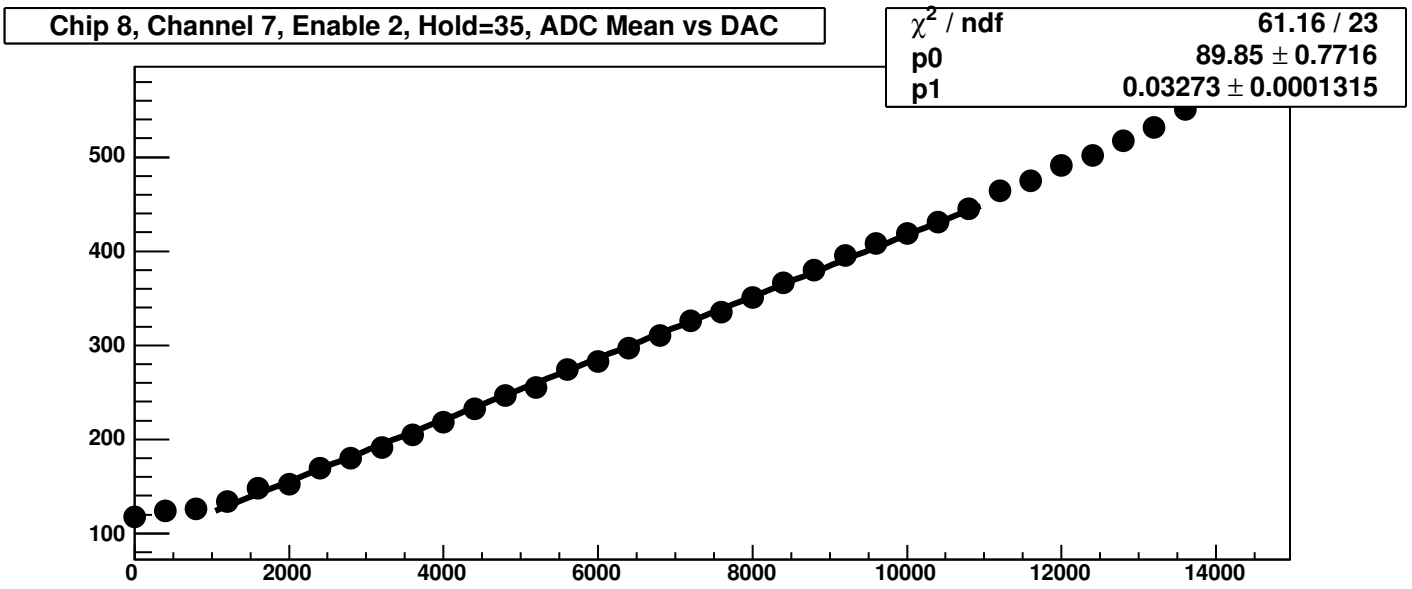

Chip 8, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

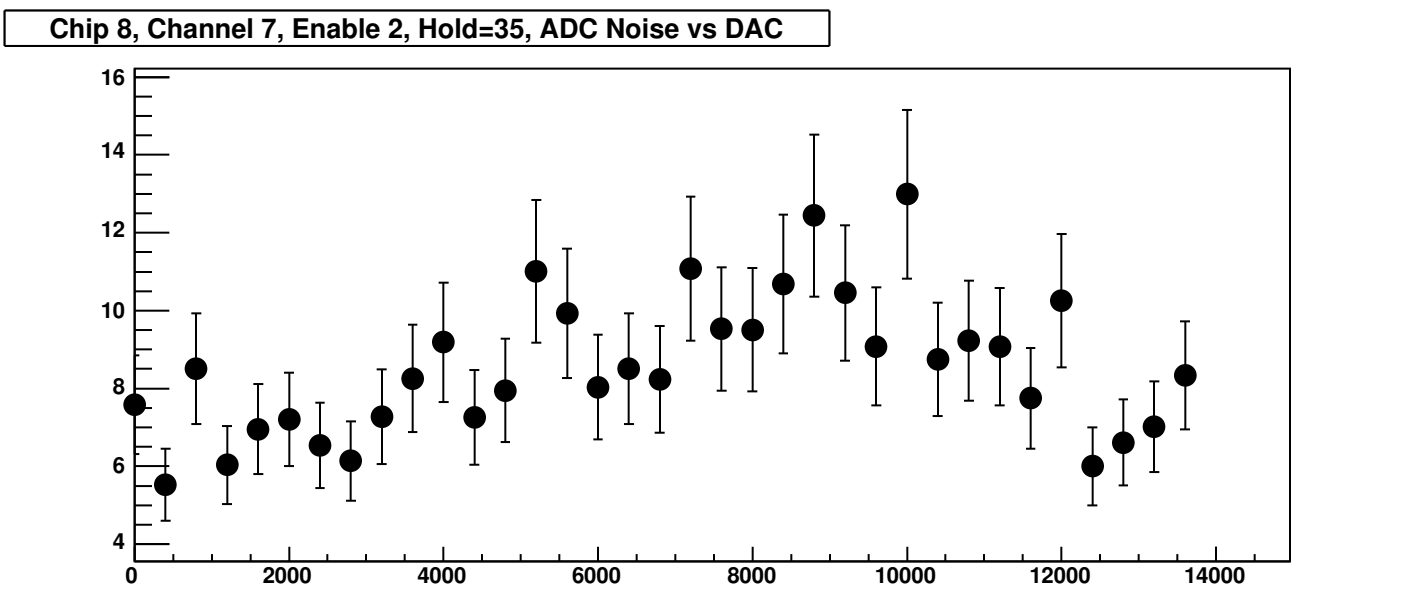

Chip 8, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

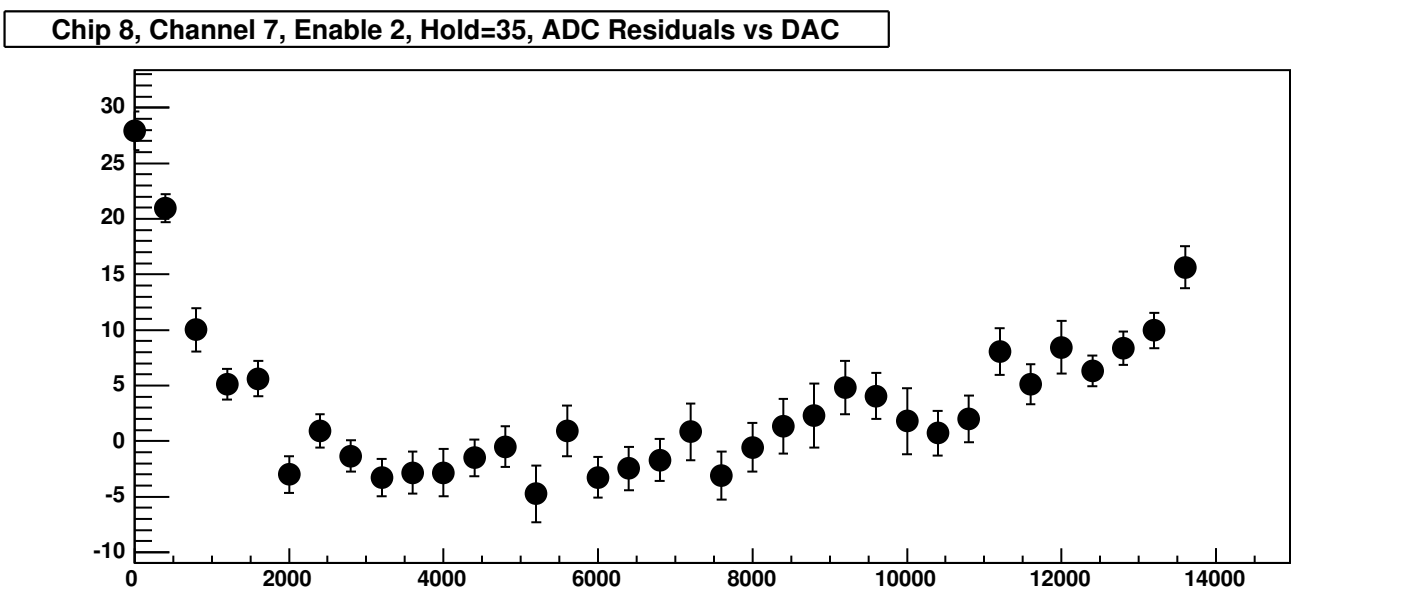

Chip 8, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 22.86 / 23
p0 92.06 ± 0.8218
p1 0.01864 ± 0.0001224
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

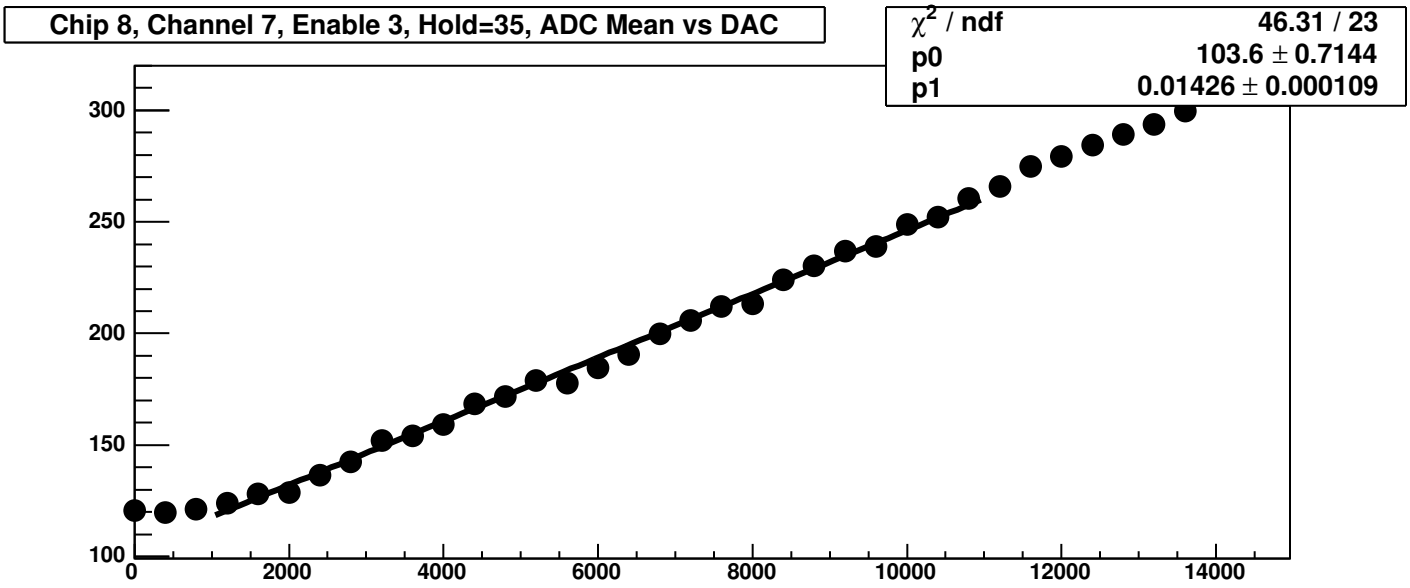

Chip 8, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

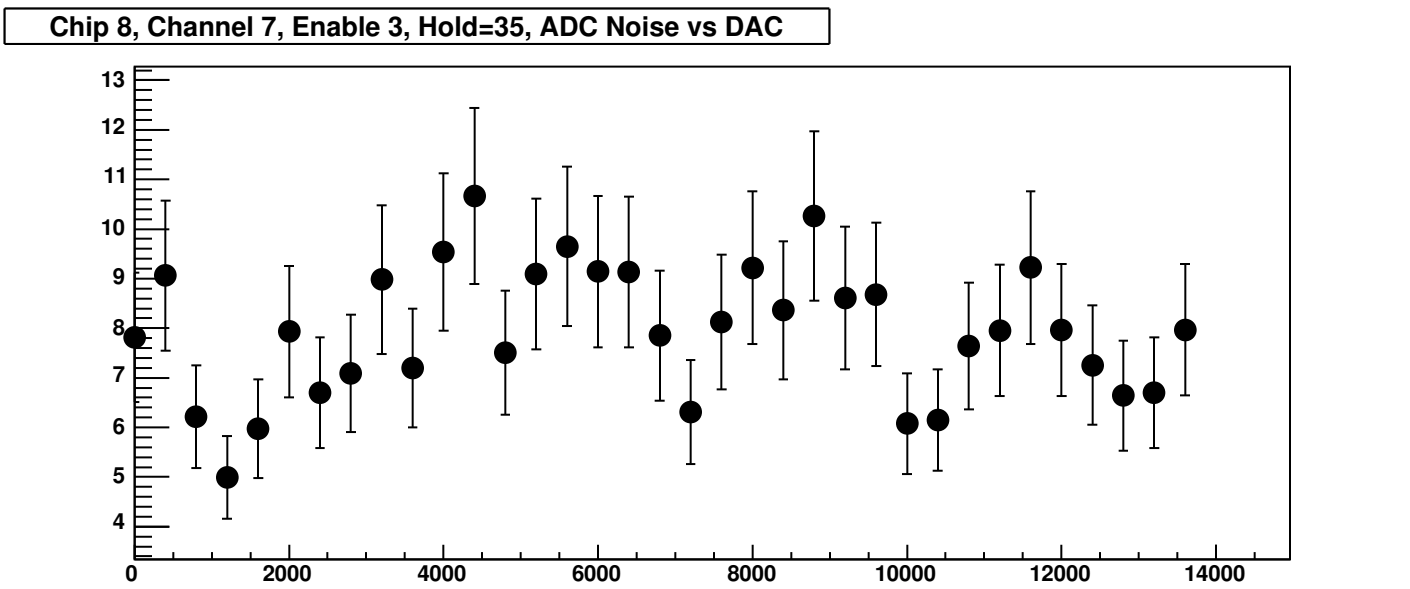

Chip 8, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

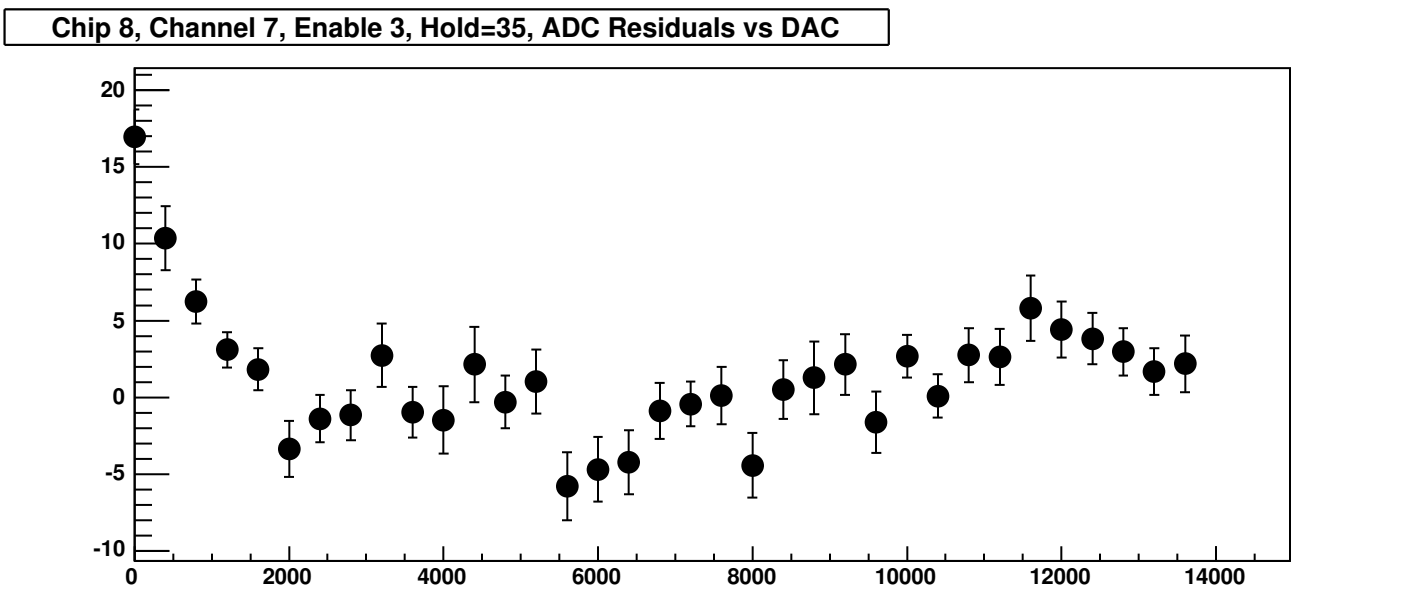

Chip 8, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 61.16 / 23
p0 89.85 ± 0.7716
p1 0.03273 ± 0.0001315
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000

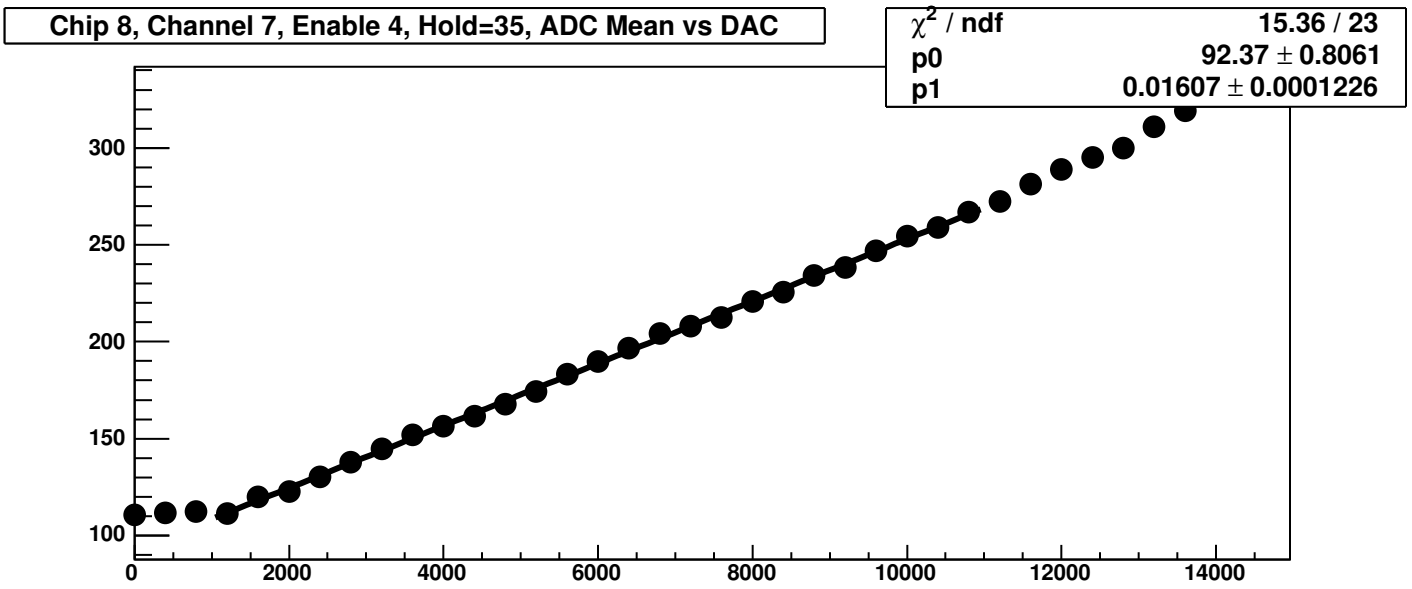

Chip 8, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

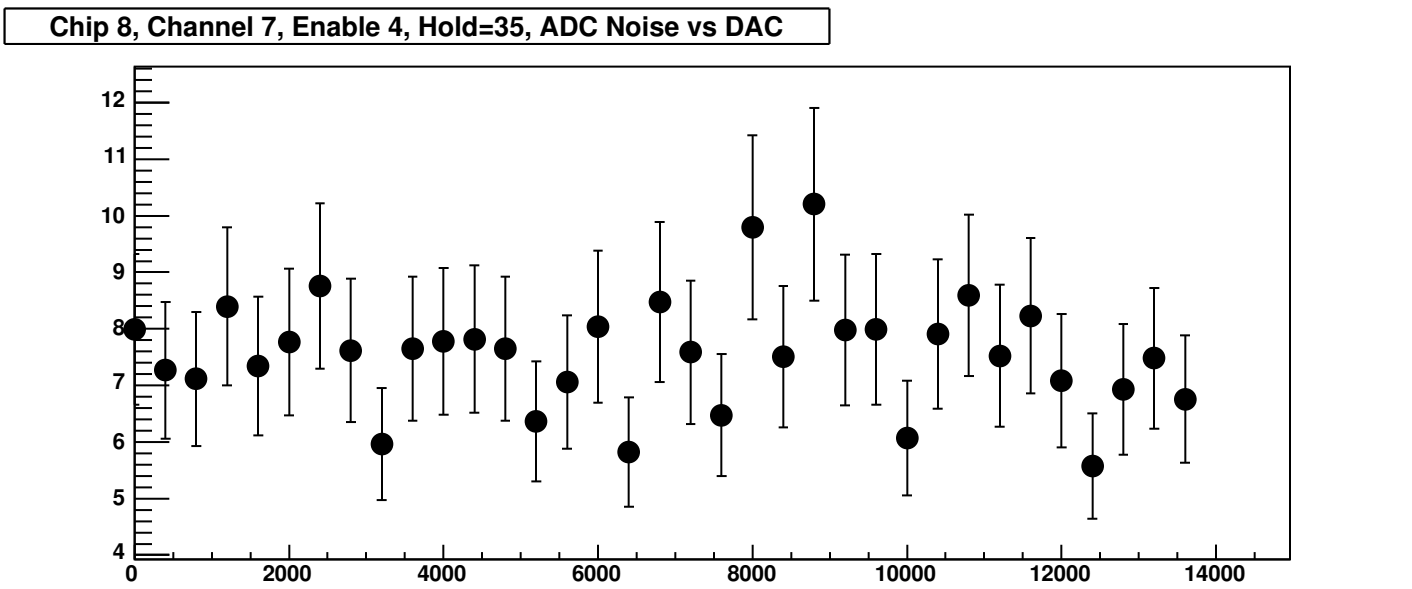

Chip 8, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

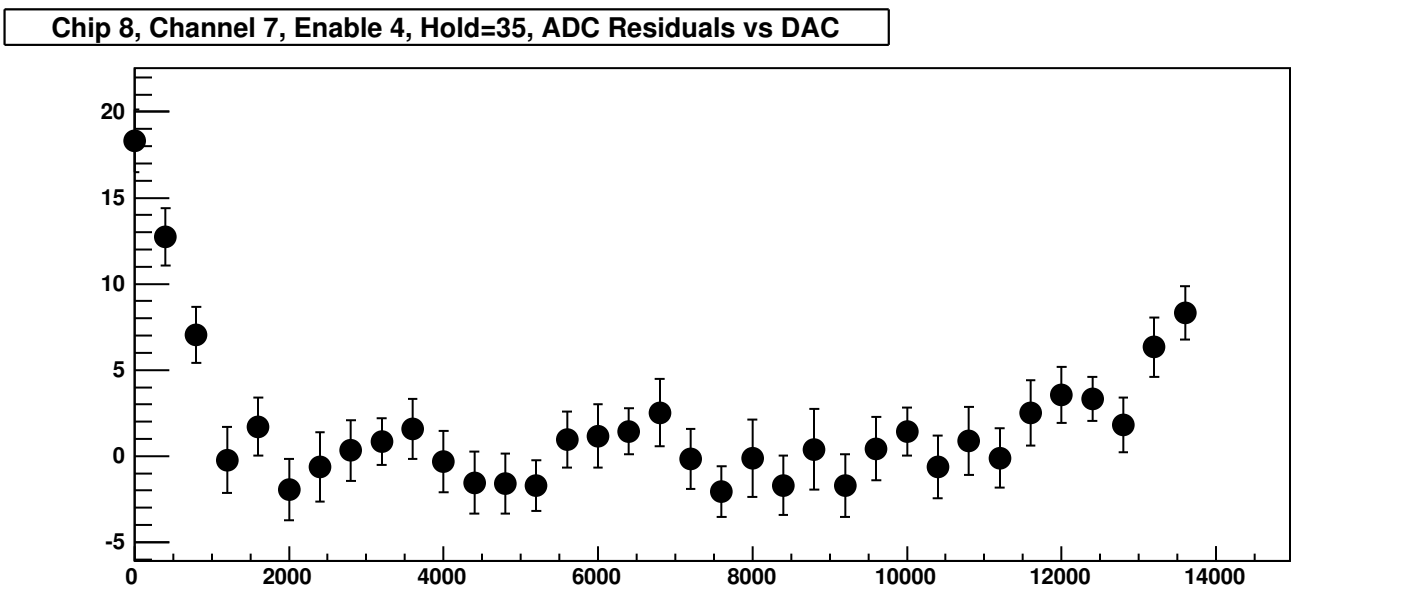

Chip 8, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 46.31 / 23
p0 103.6 ± 0.7144
p1 0.01426 ± 0.000109
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

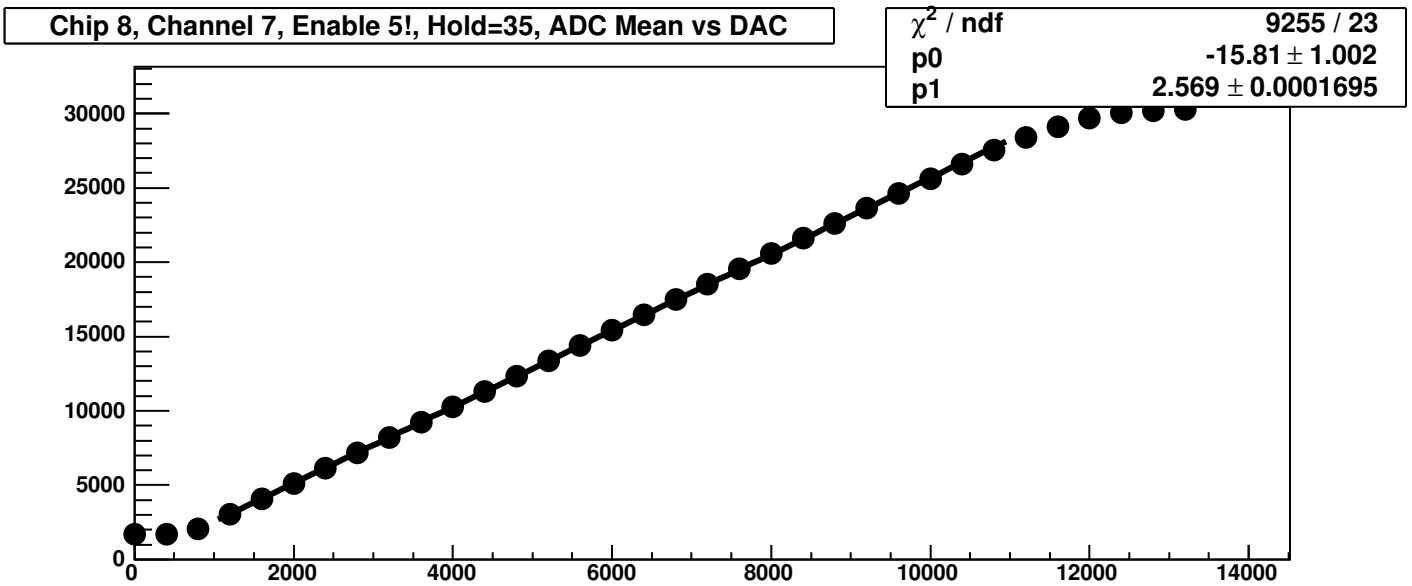

Chip 8, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

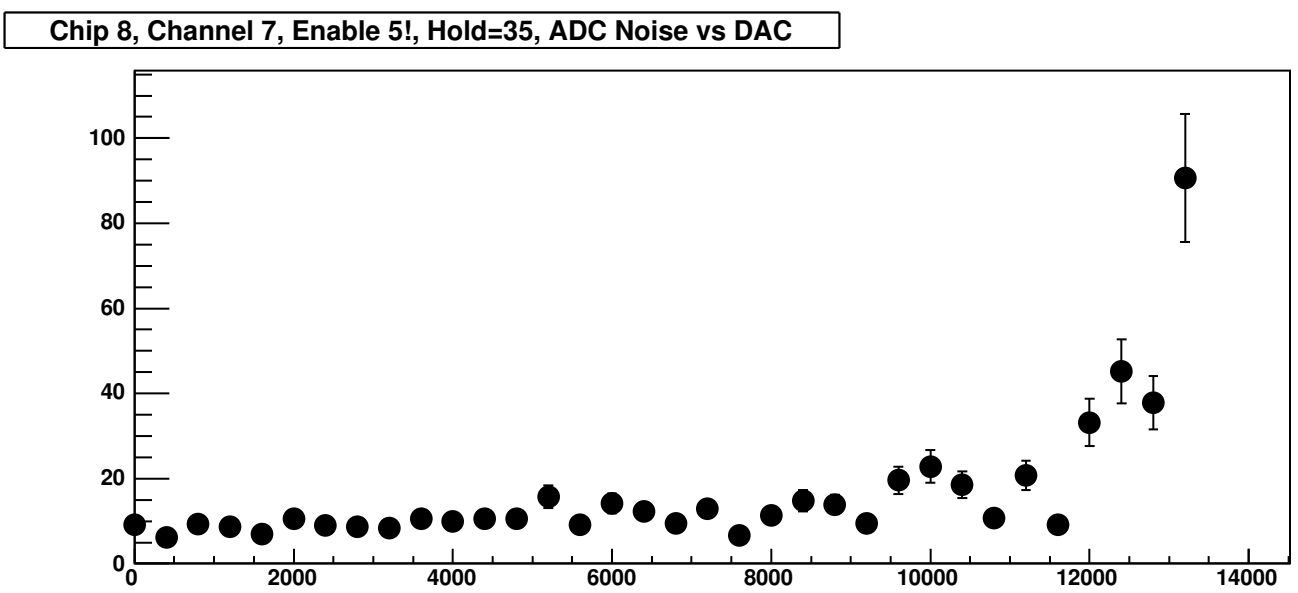

Chip 8, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

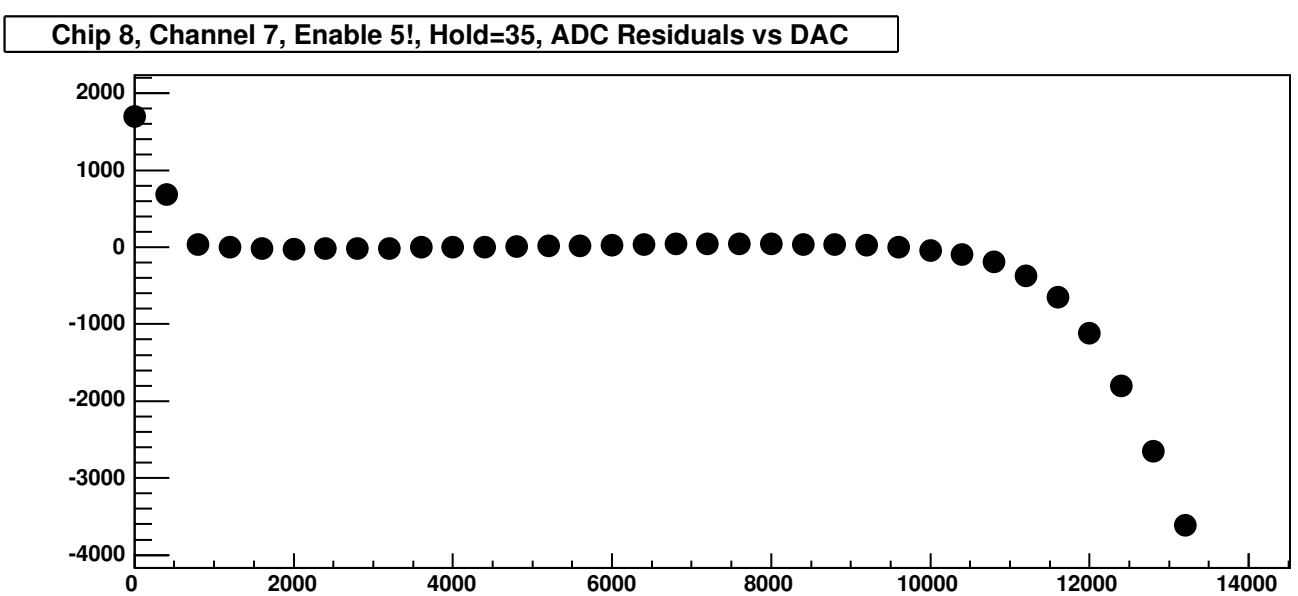

Chip 8, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 15.36 / 23
p0 92.37 ± 0.8061
p1 0.01607 ± 0.0001226
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

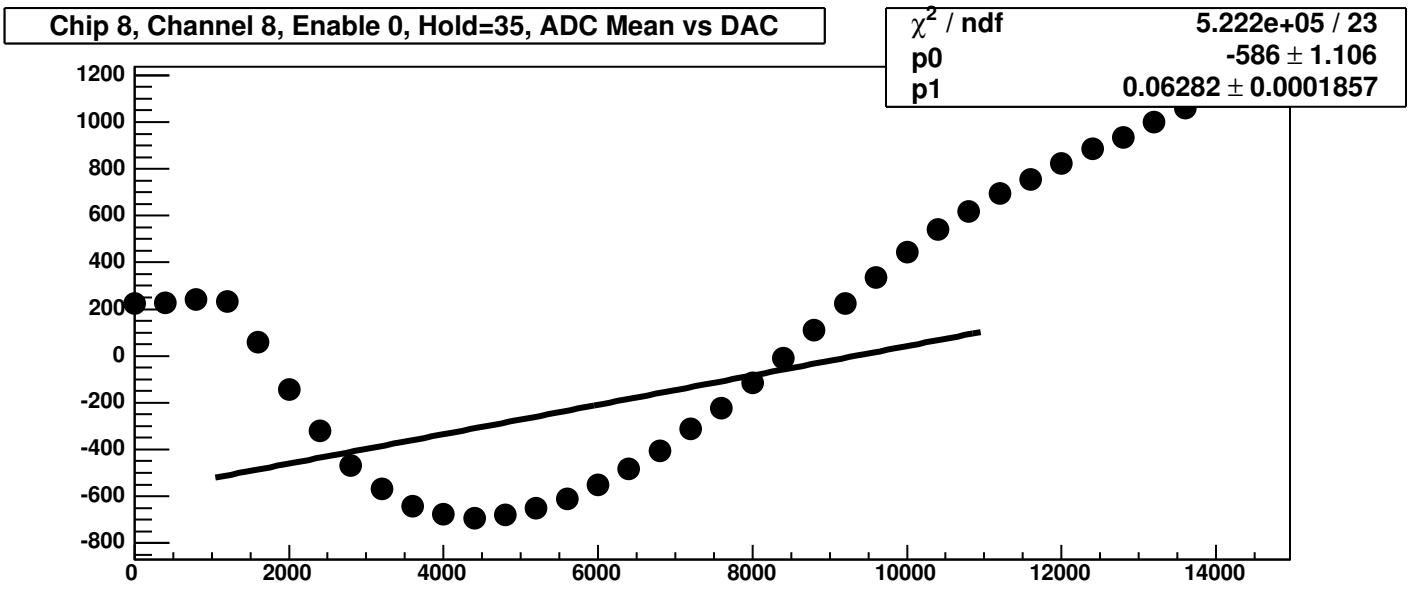

Chip 8, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

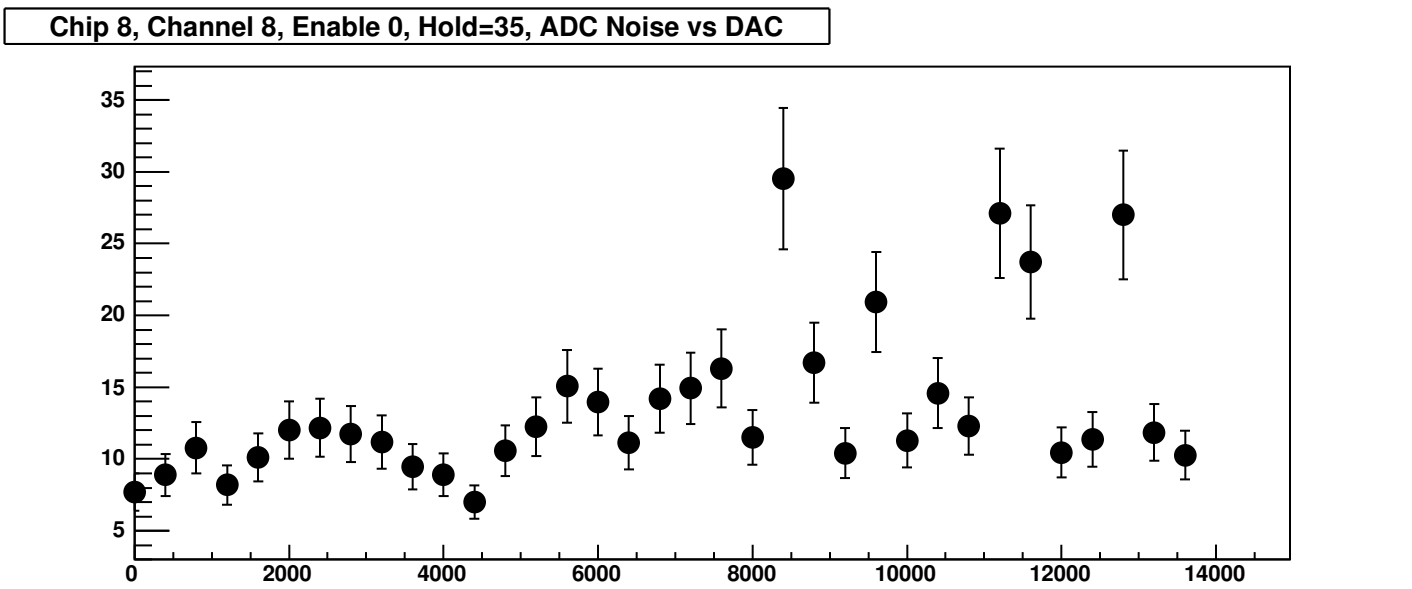

Chip 8, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

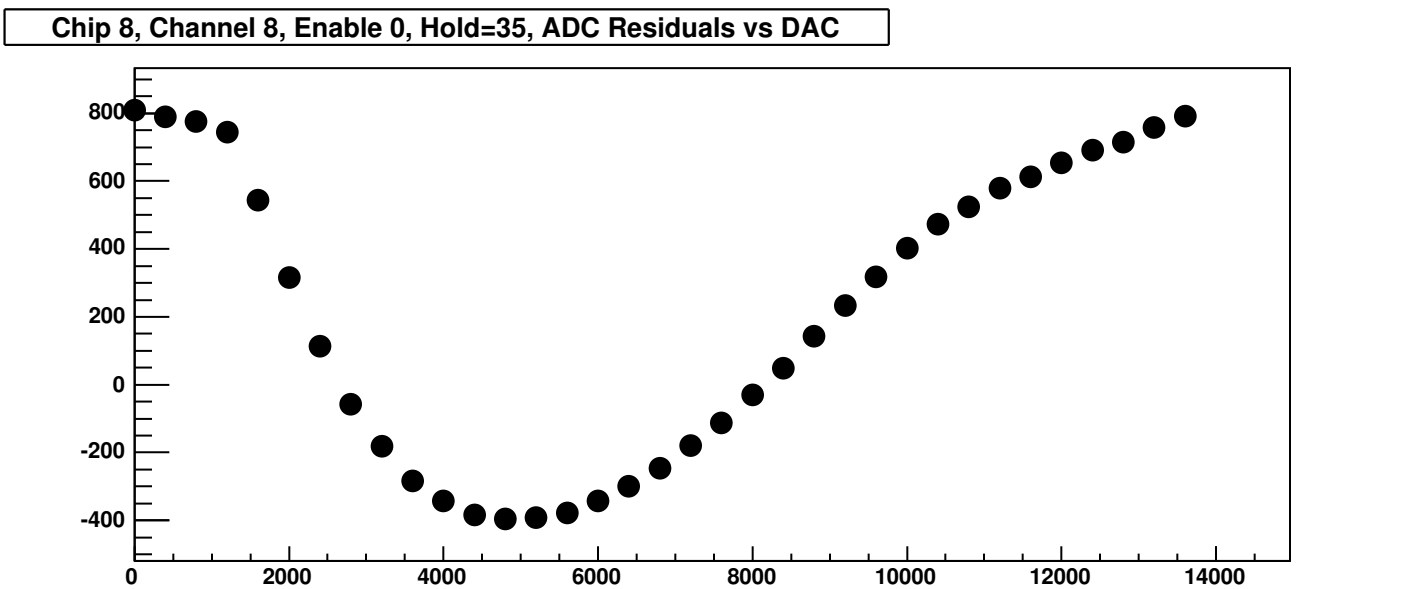

Chip 8, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 9255 / 23
p0 -15.81 ± 1.002
p1 2.569 ± 0.0001695
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

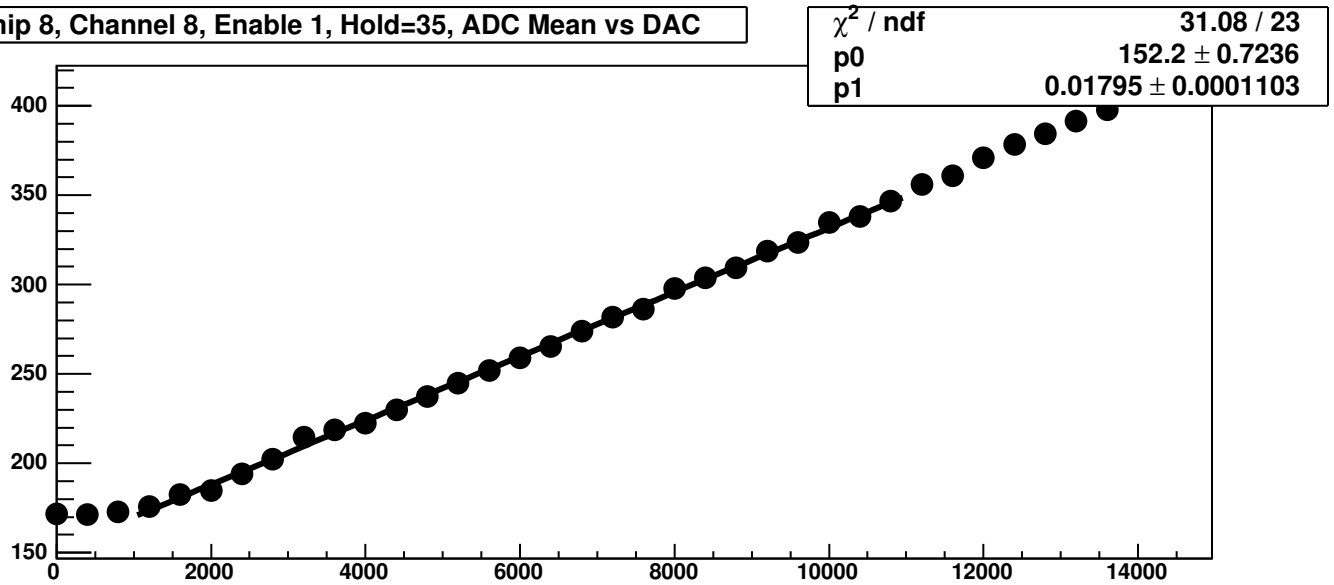

Chip 8, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.222e+05 / 23
p0 -586 ± 1.106
p1 0.06282 ± 0.0001857
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

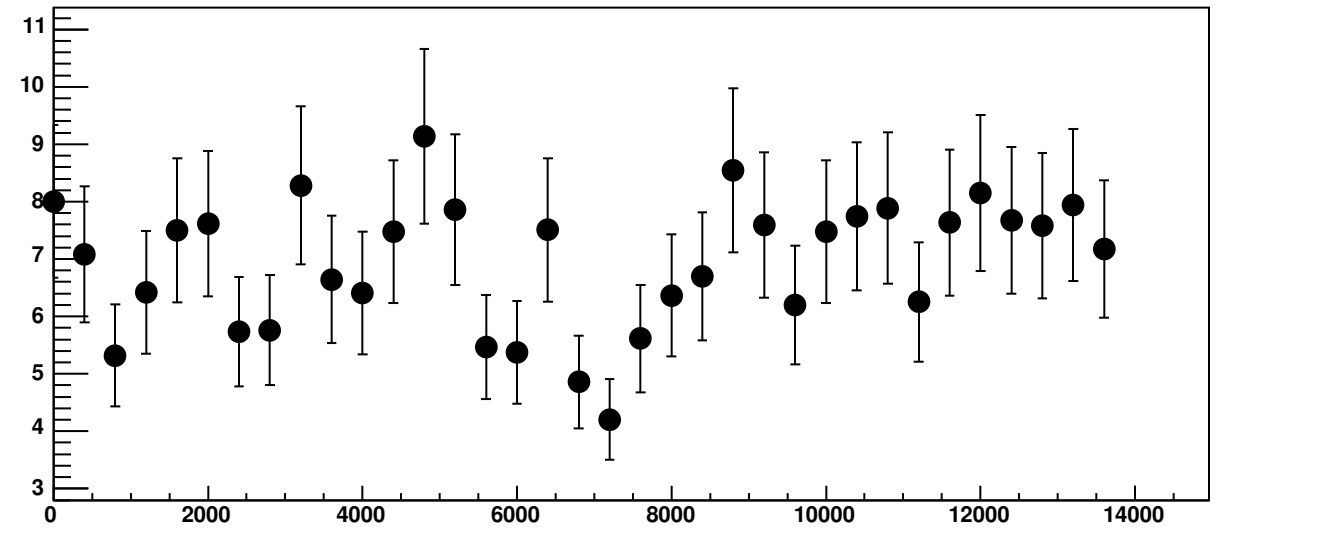

Chip 8, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

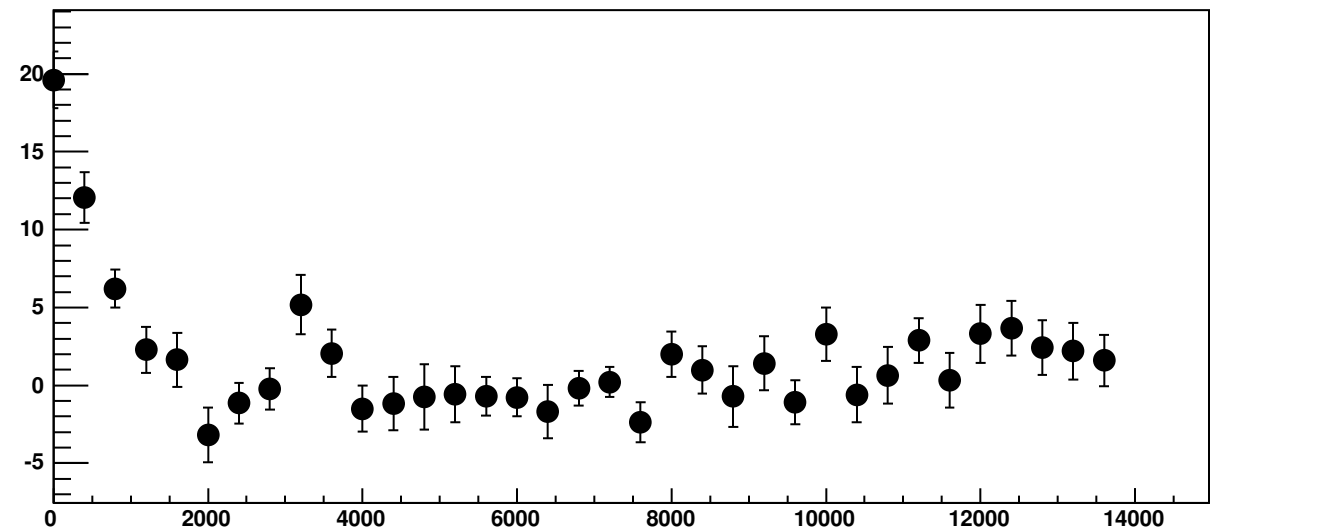
Chip 8, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

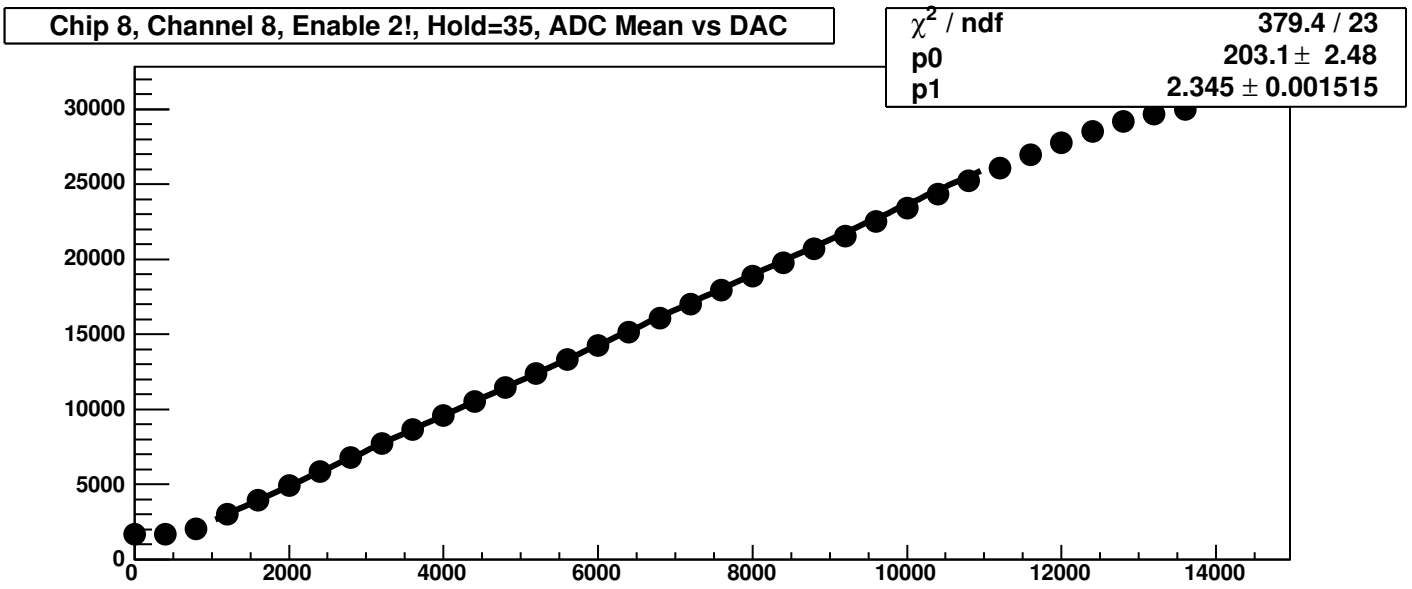


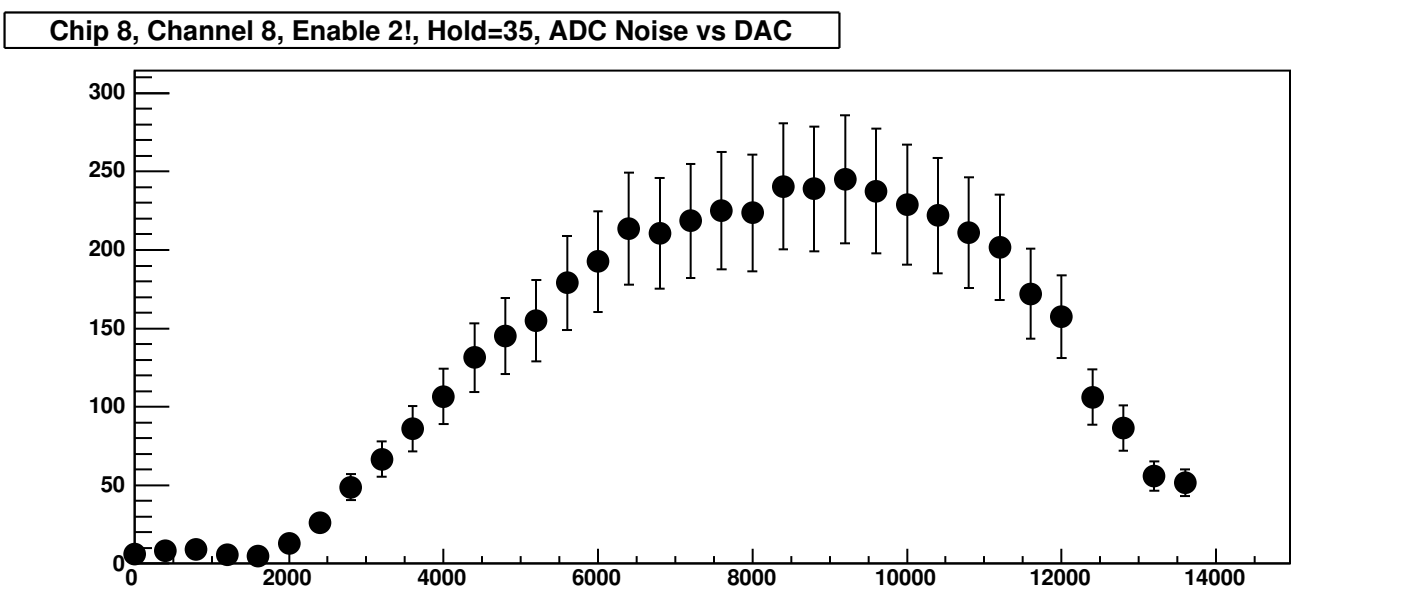
Chip 8, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC

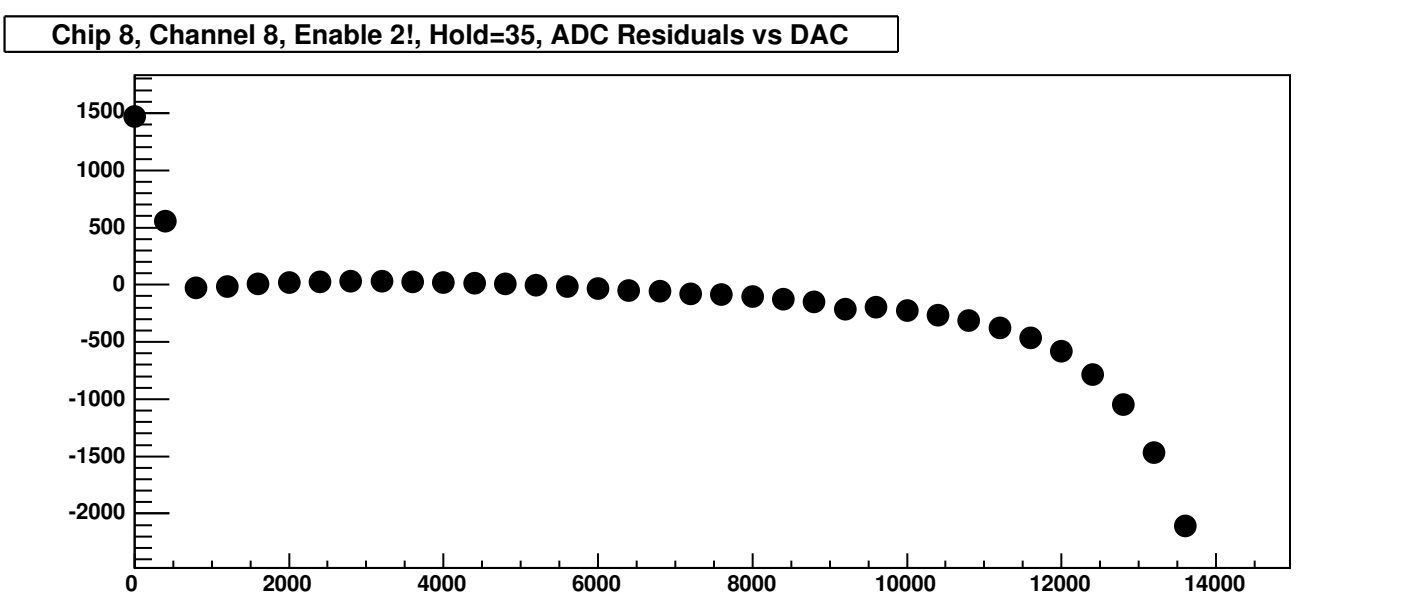


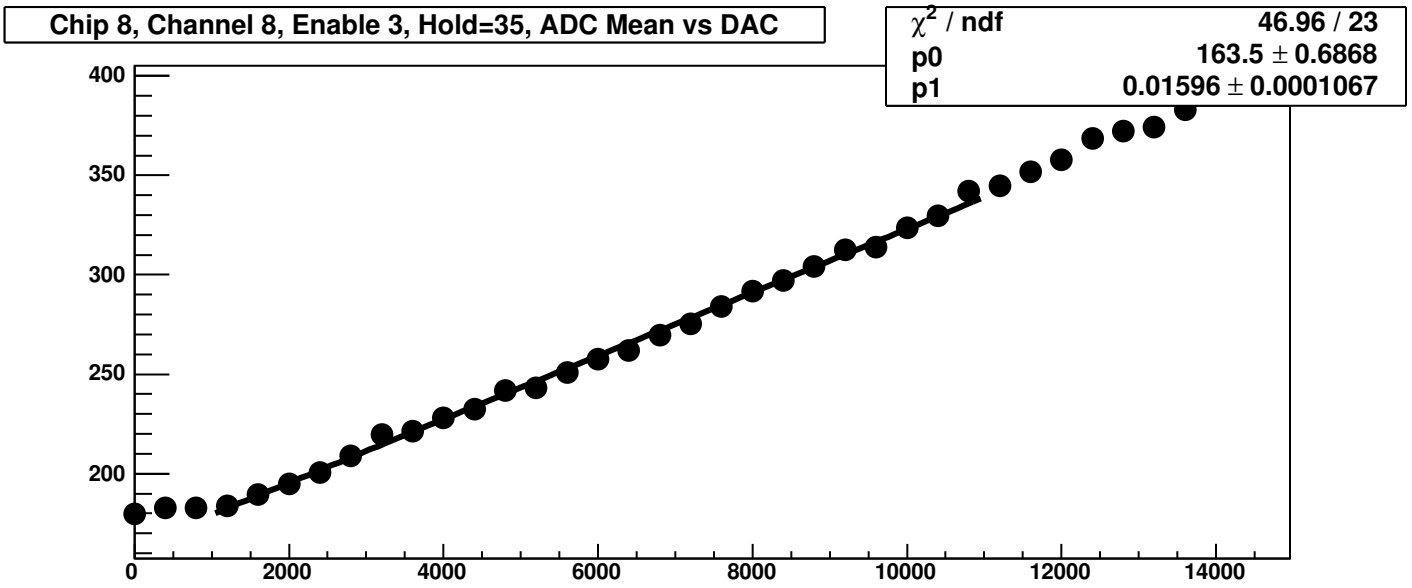
Chip 8, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

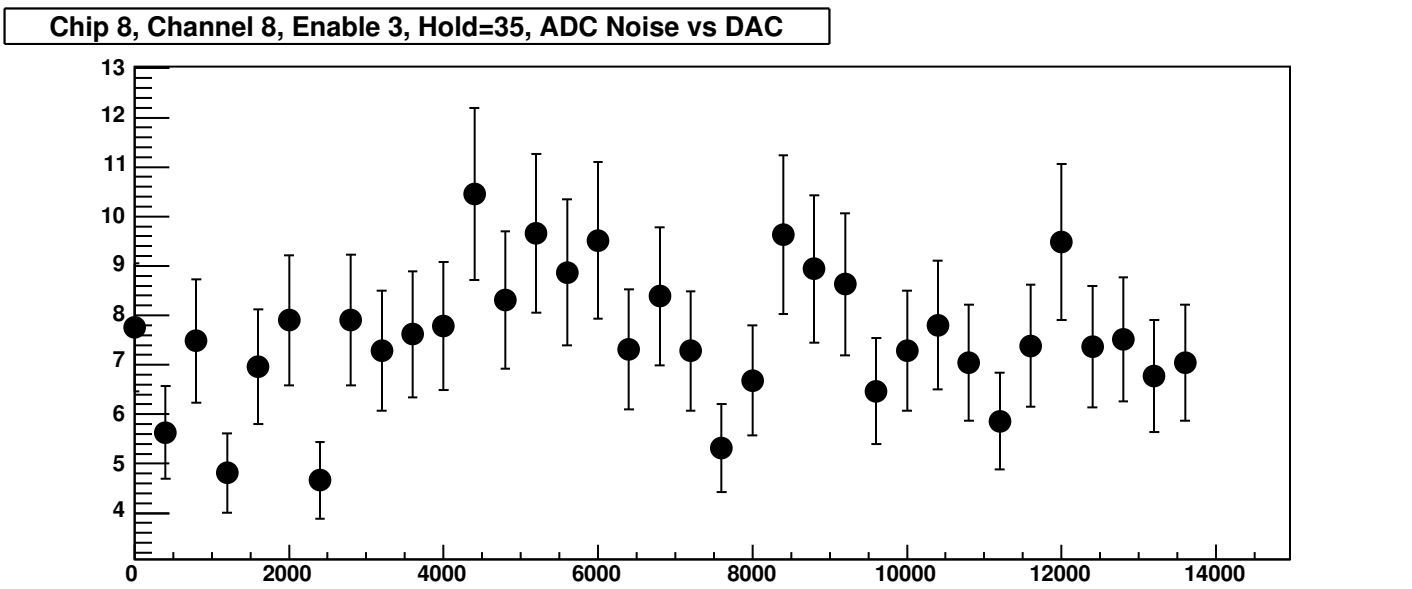


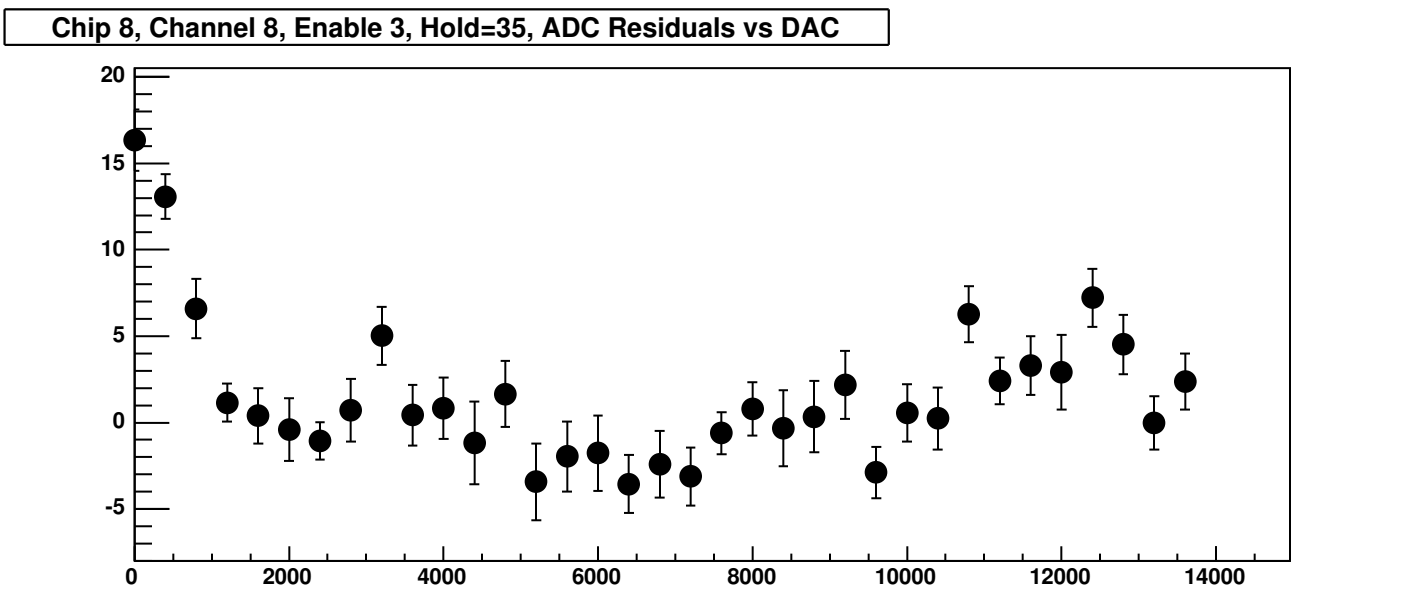

Chip 8, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 379.4 / 23
p0 203.1 ± 2.48
p1 2.345 ± 0.001515
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

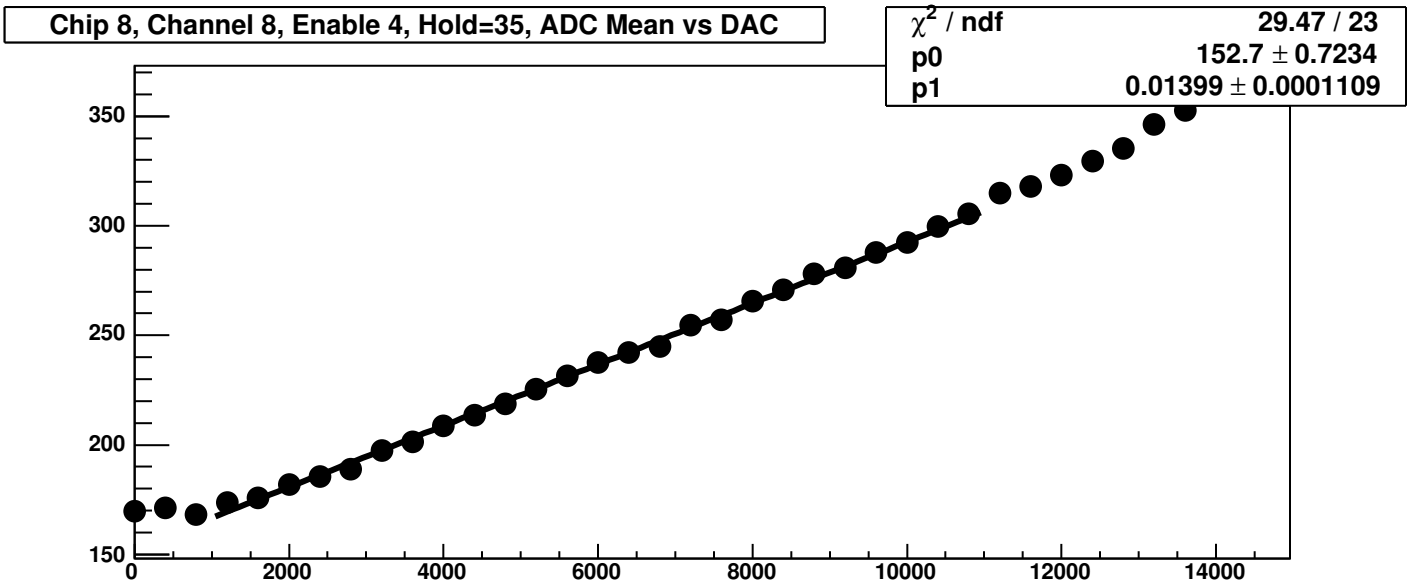

Chip 8, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

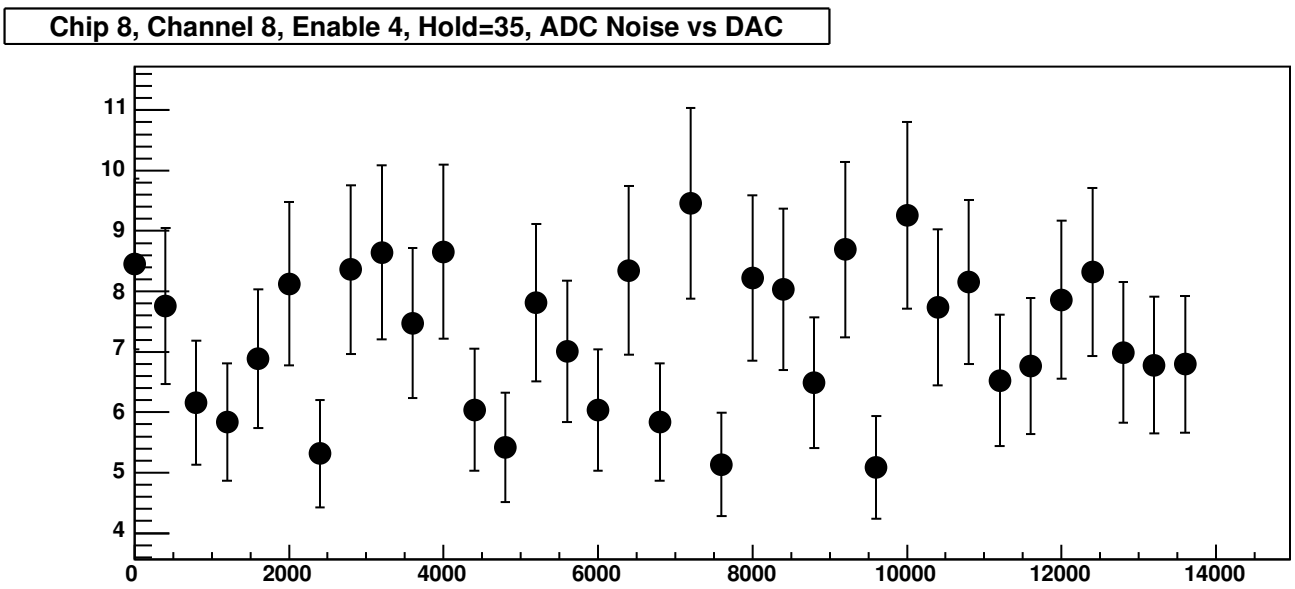

Chip 8, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

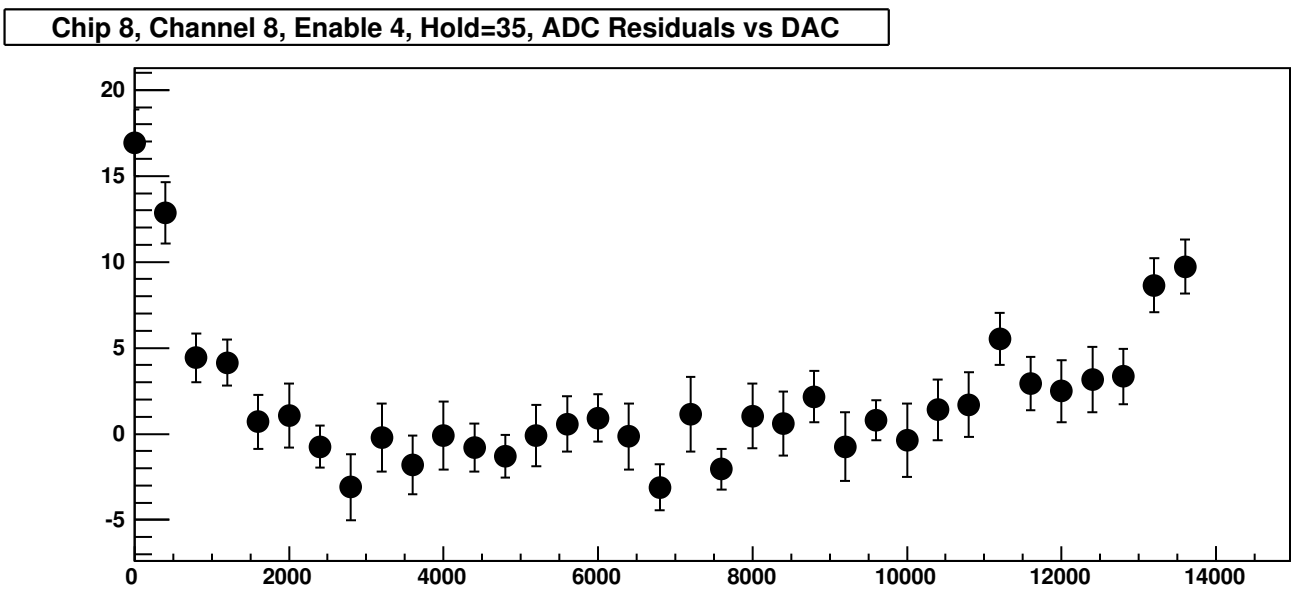

Chip 8, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 46.96 / 23
p0 163.5 ± 0.6868
p1 0.01596 ± 0.0001067
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

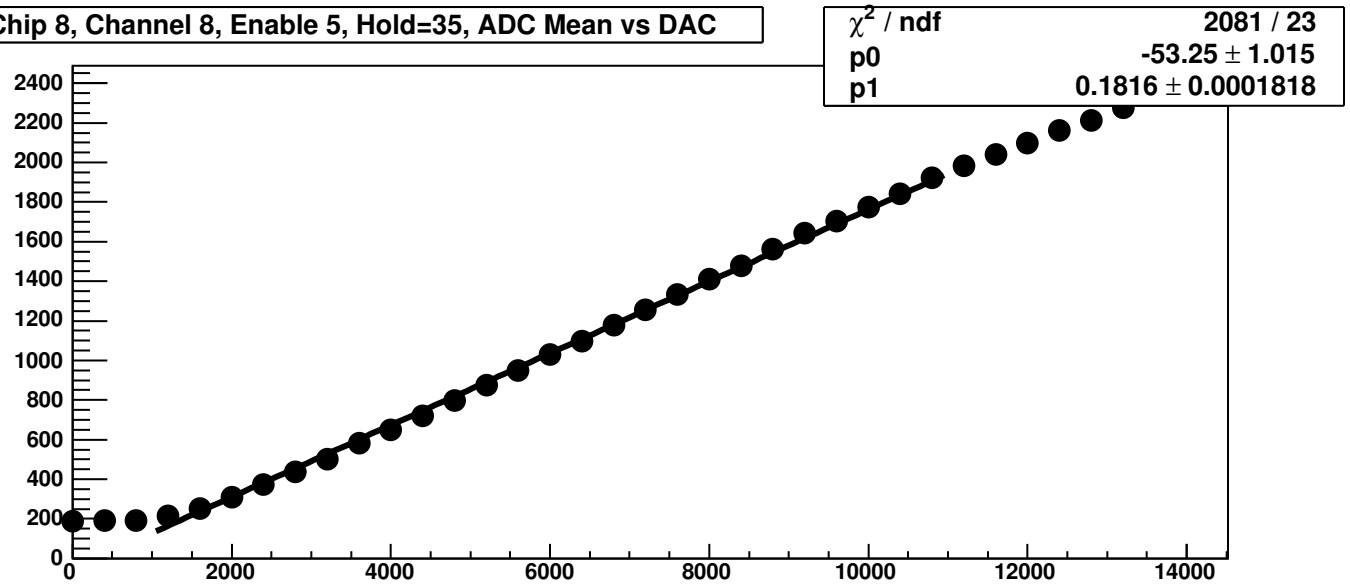

Chip 8, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.47 / 23
p0 152.7 ± 0.7234
p1 0.01399 ± 0.0001109
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

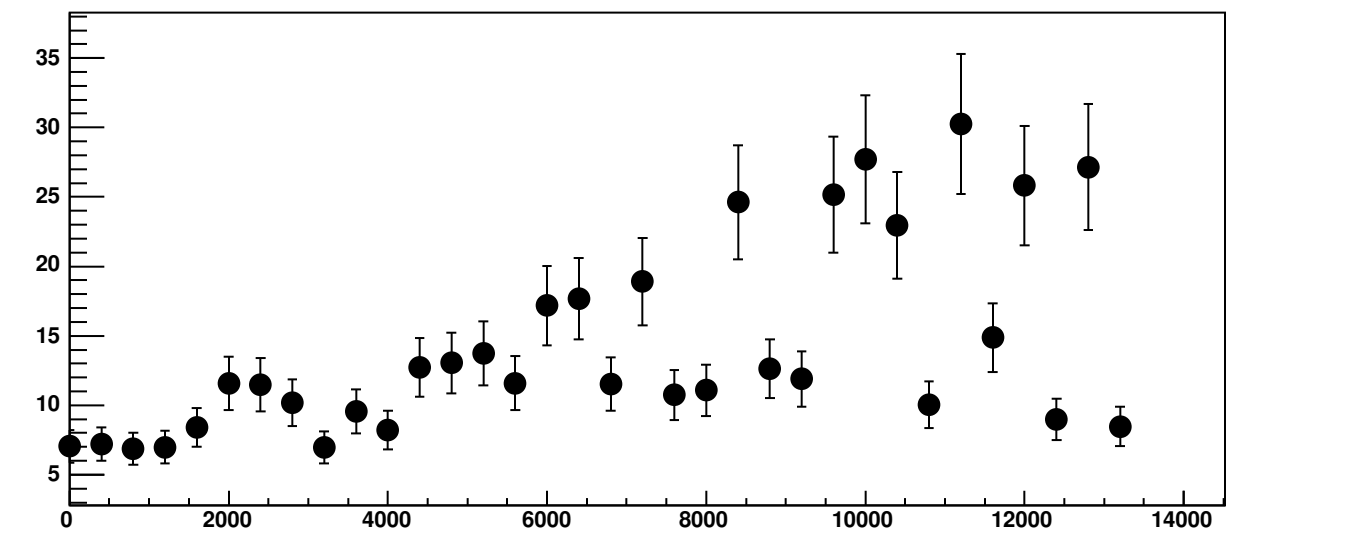

Chip 8, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

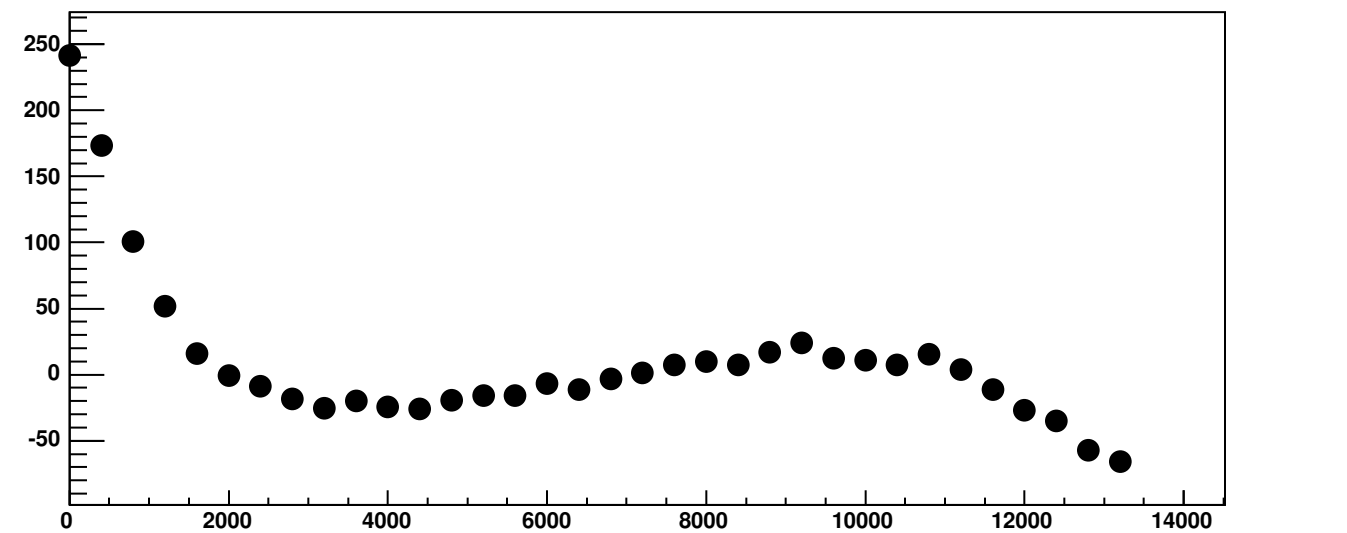
Chip 8, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC

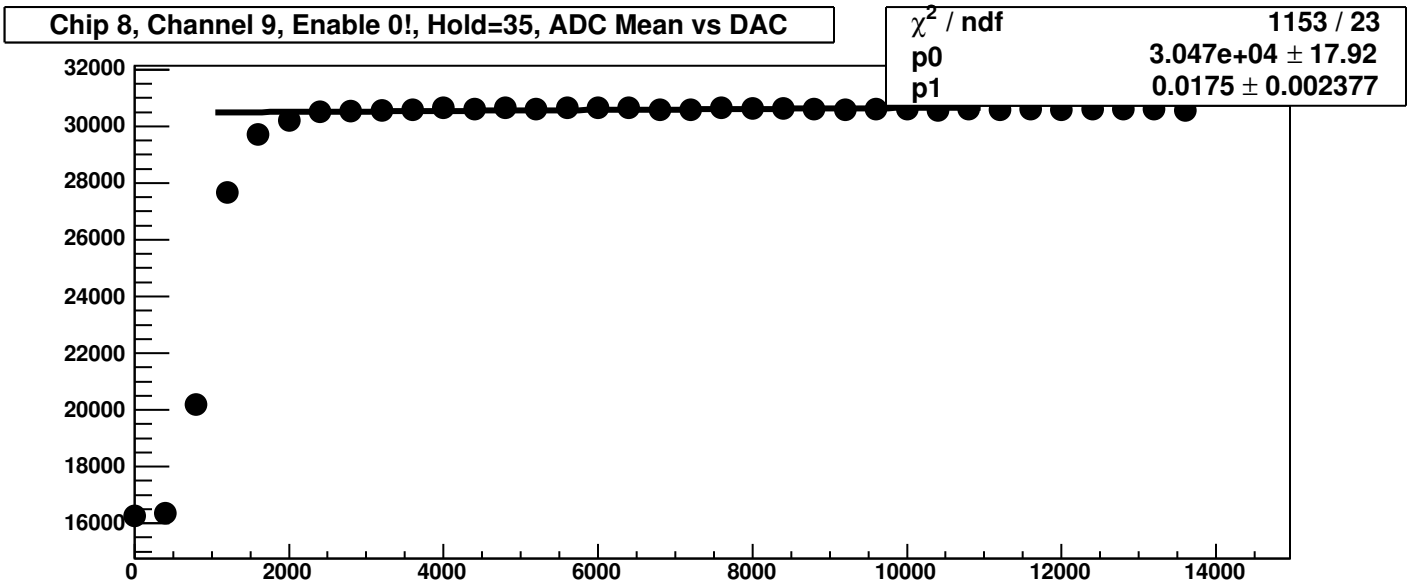


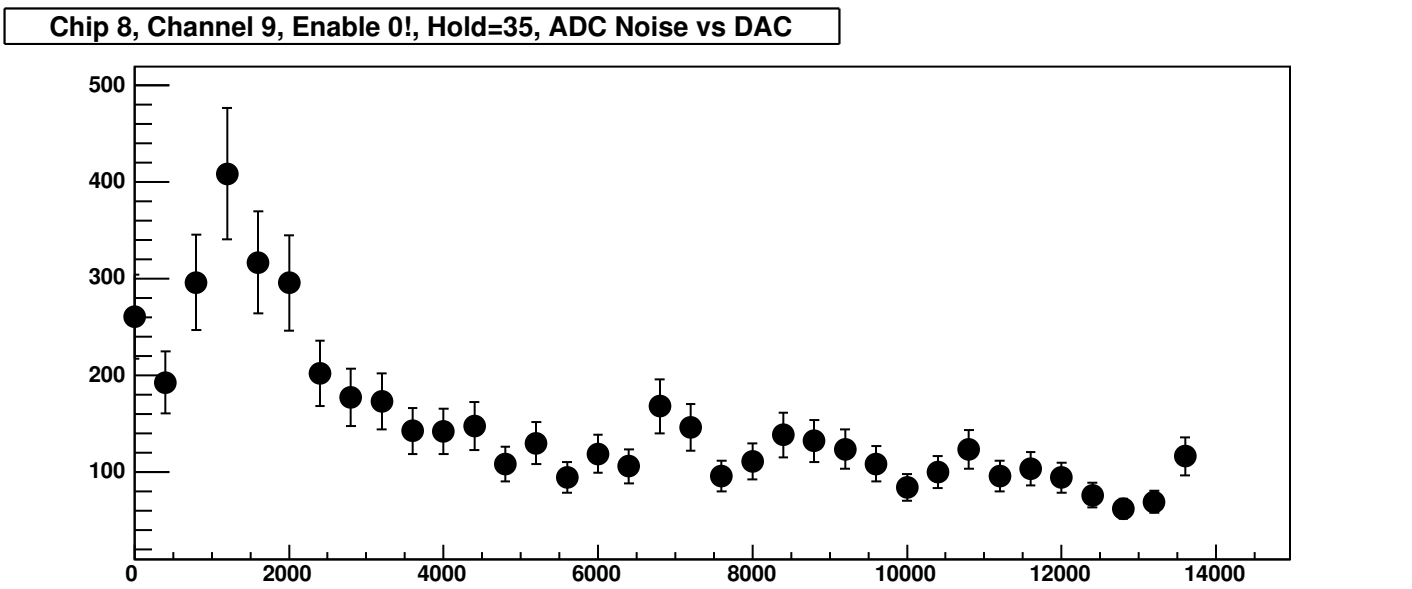
Chip 8, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC

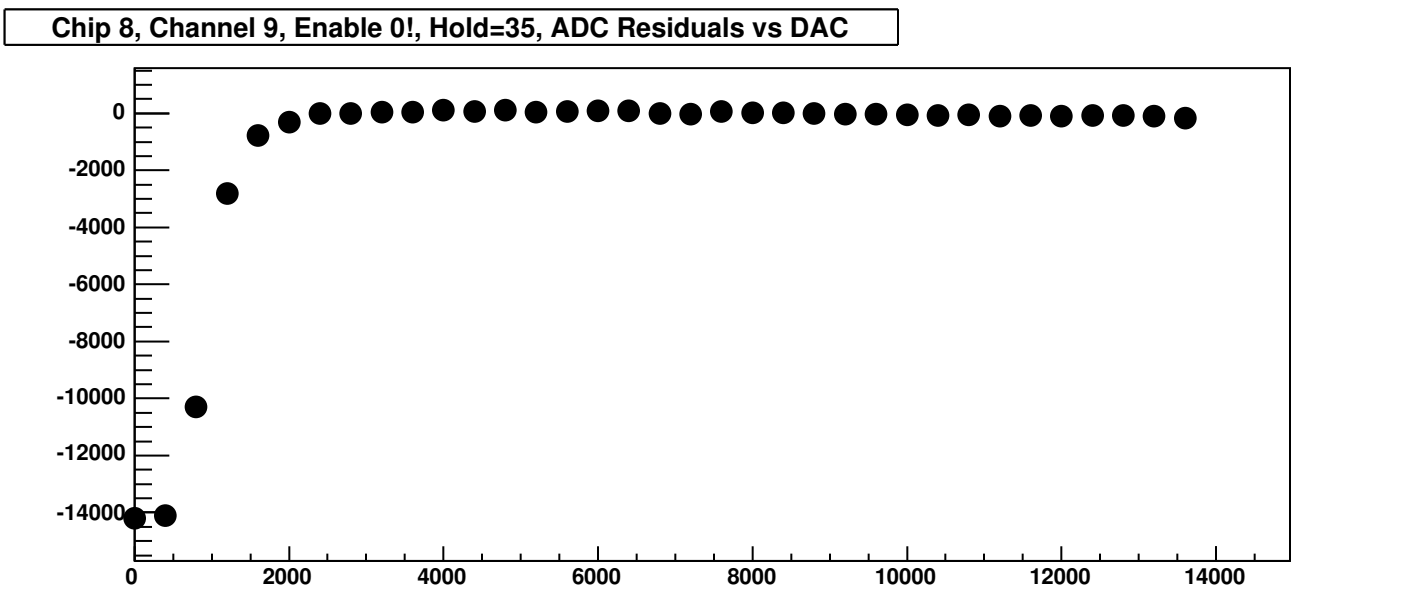


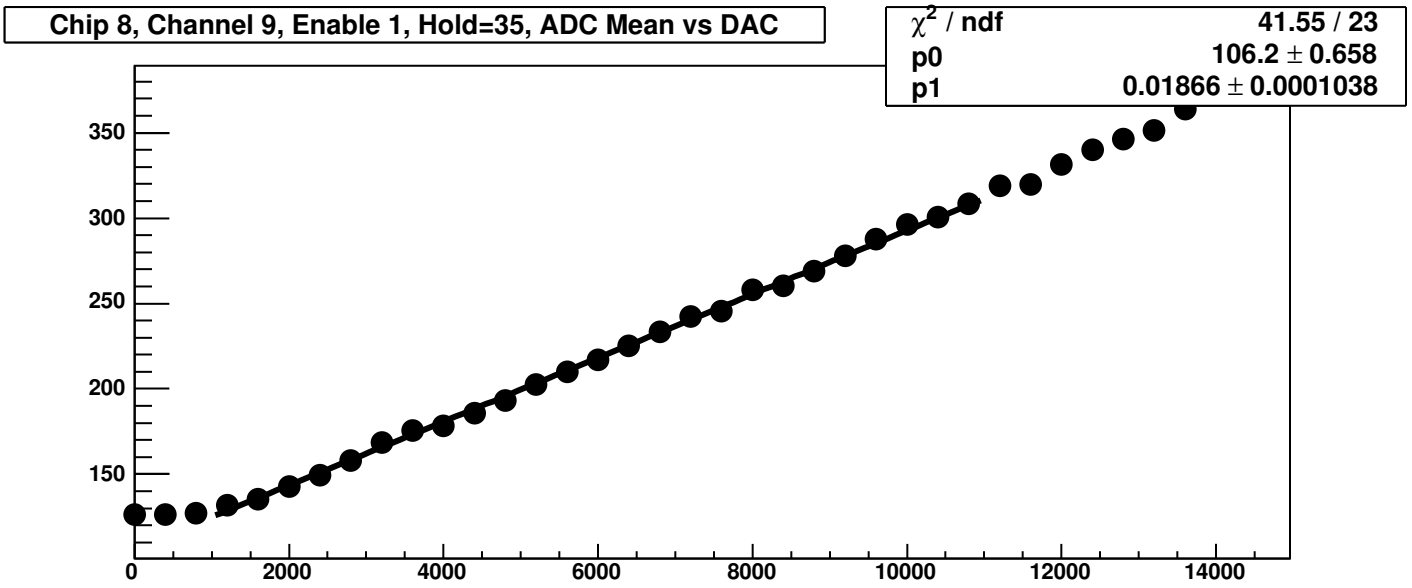
Chip 8, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC

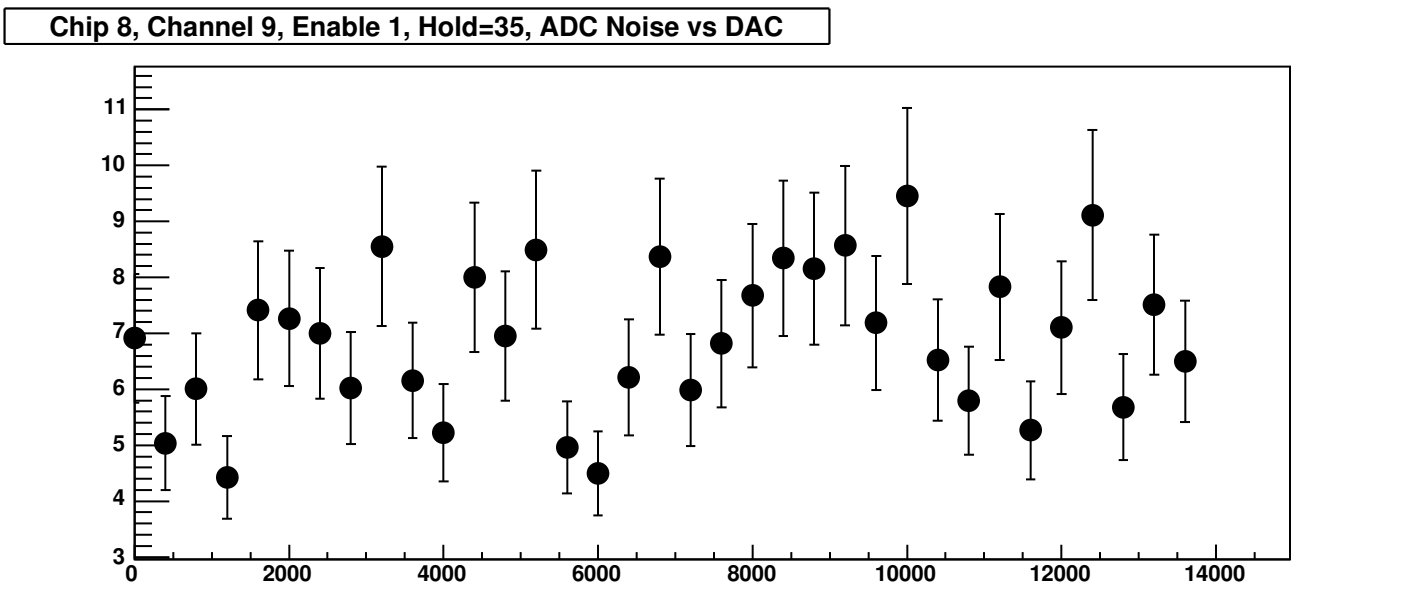


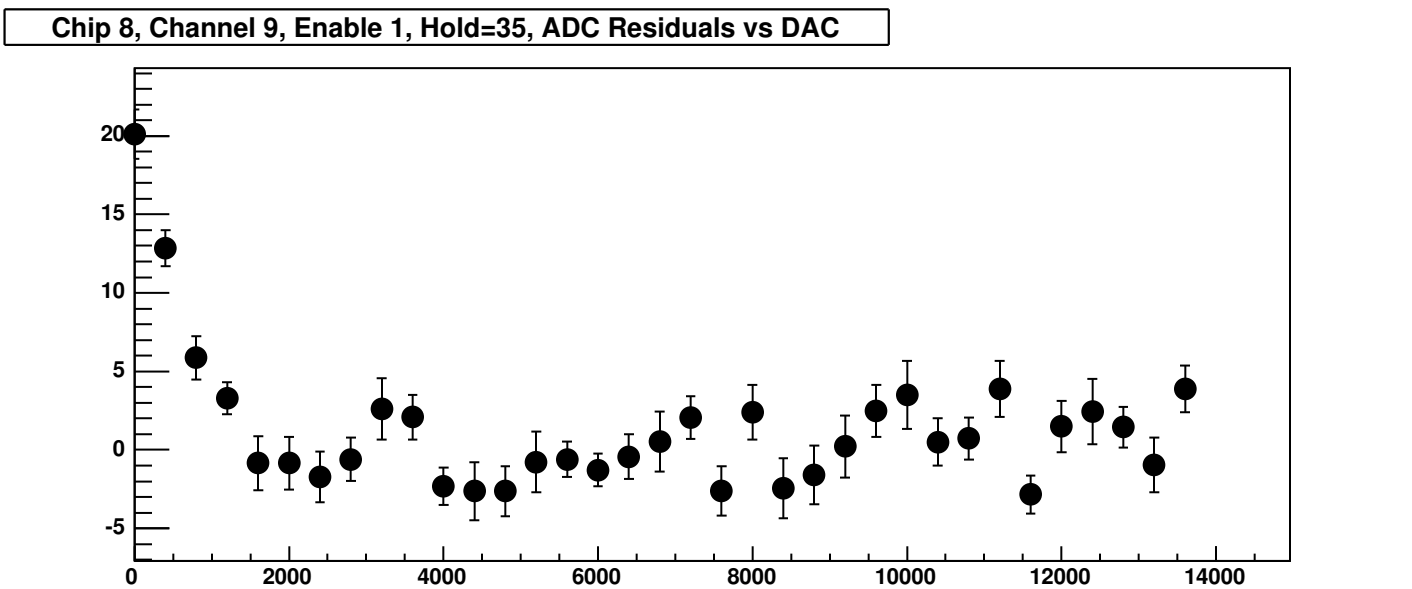

Chip 8, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1153 / 23
p0 3.047e+04 ± 17.92
p1 0.0175 ± 0.002377
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000

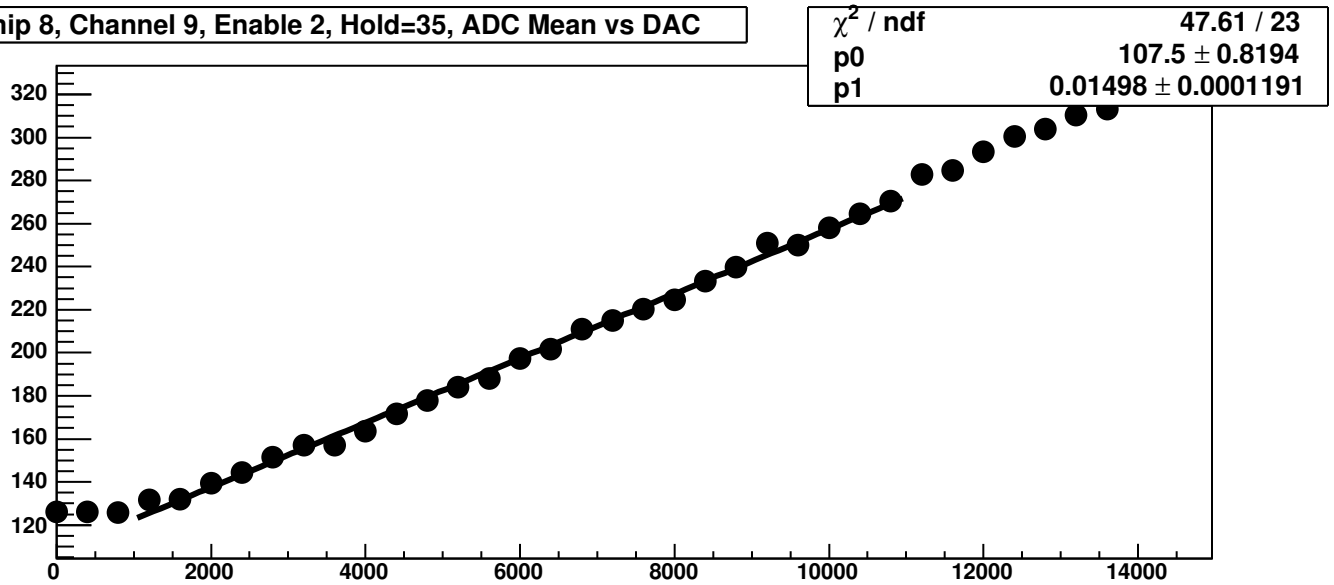

Chip 8, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 41.55 / 23
p0 106.2 ± 0.658
p1 0.01866 ± 0.0001038
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

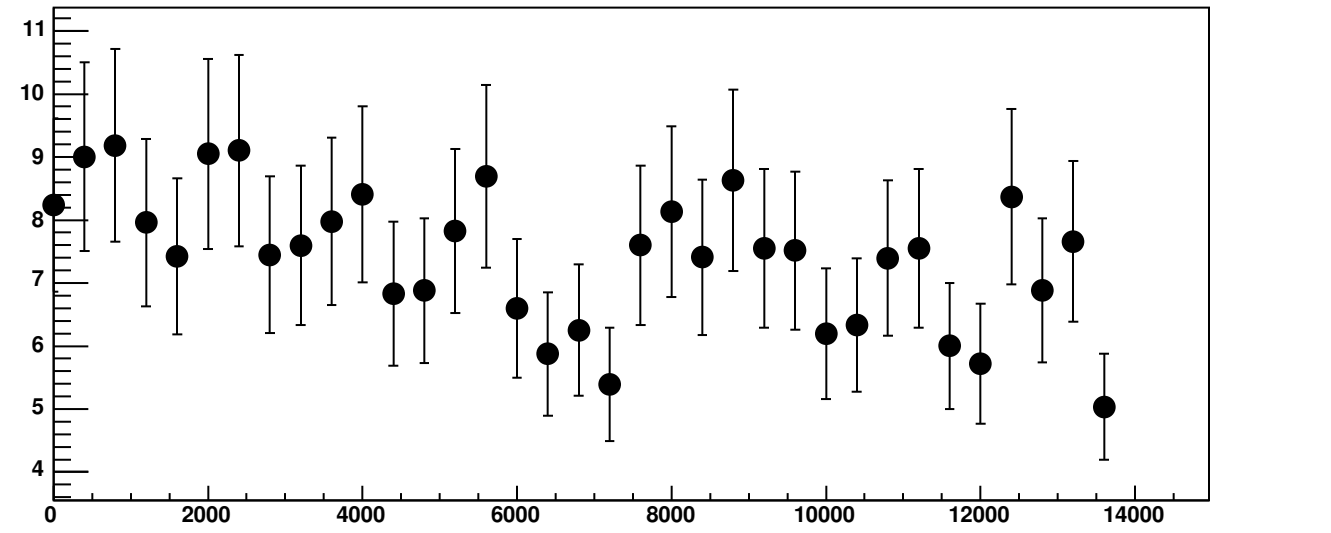

Chip 8, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

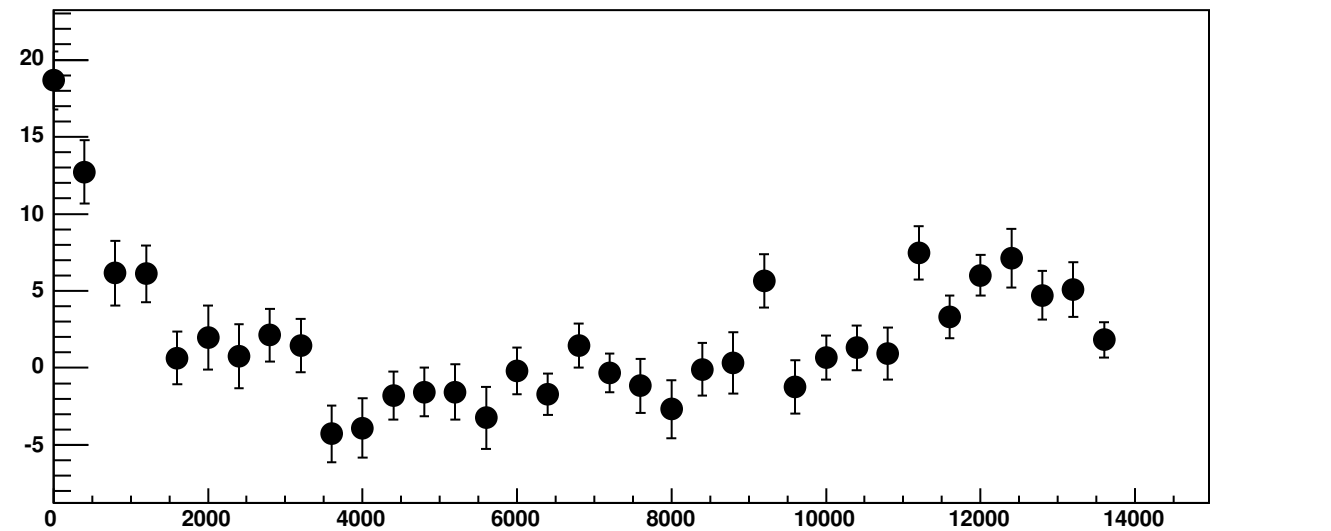
Chip 8, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

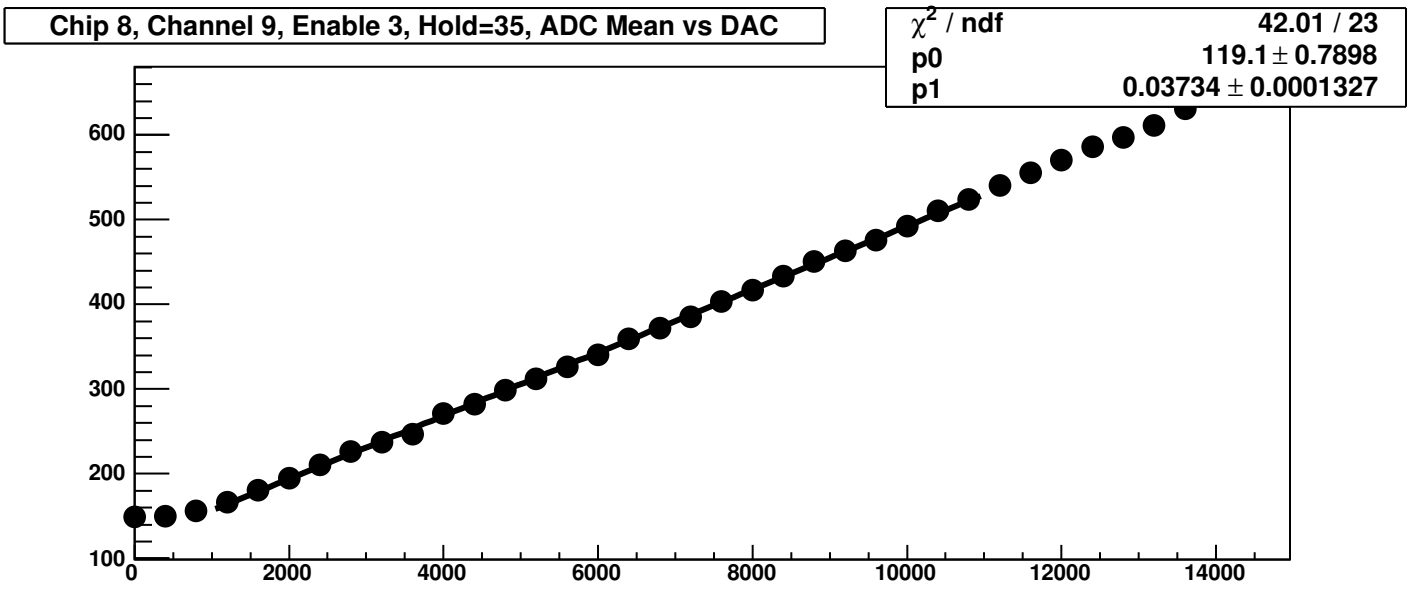


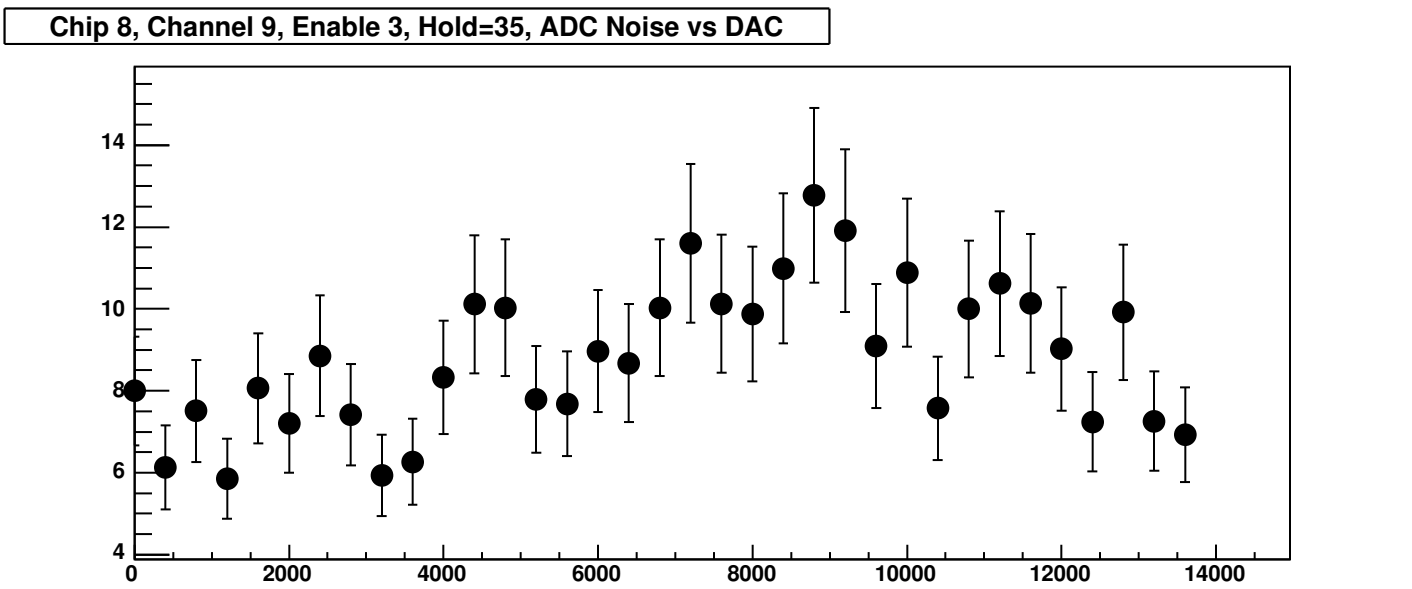
Chip 8, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC

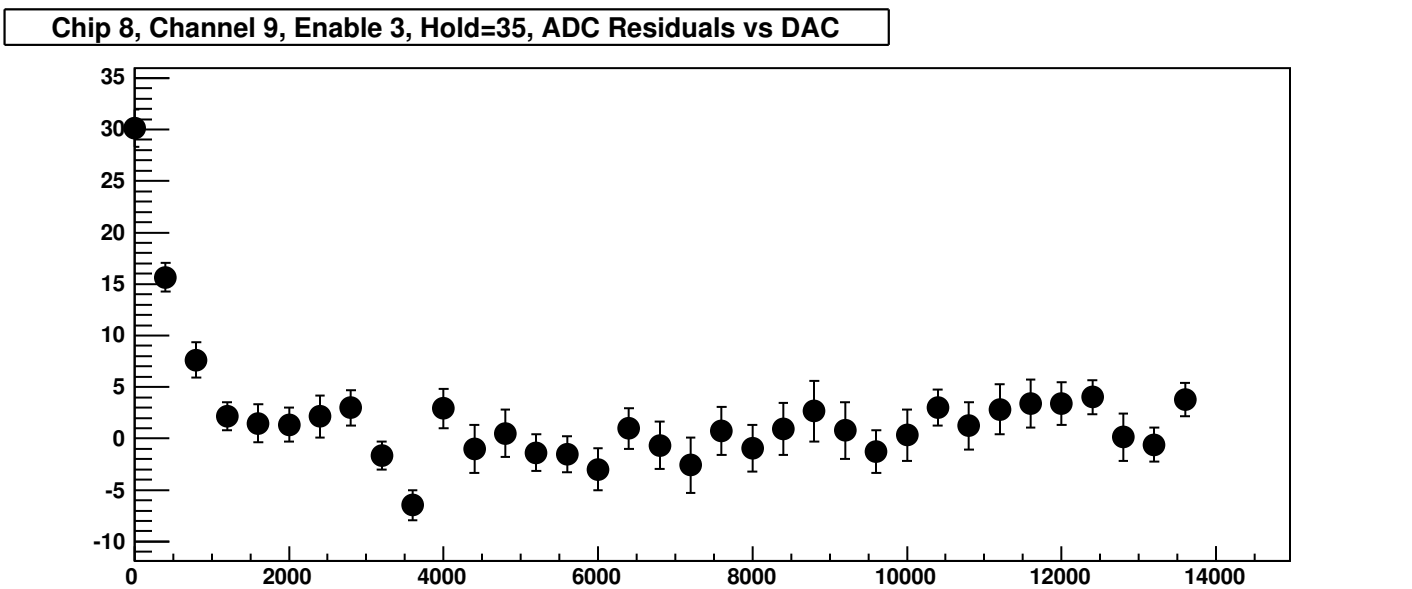


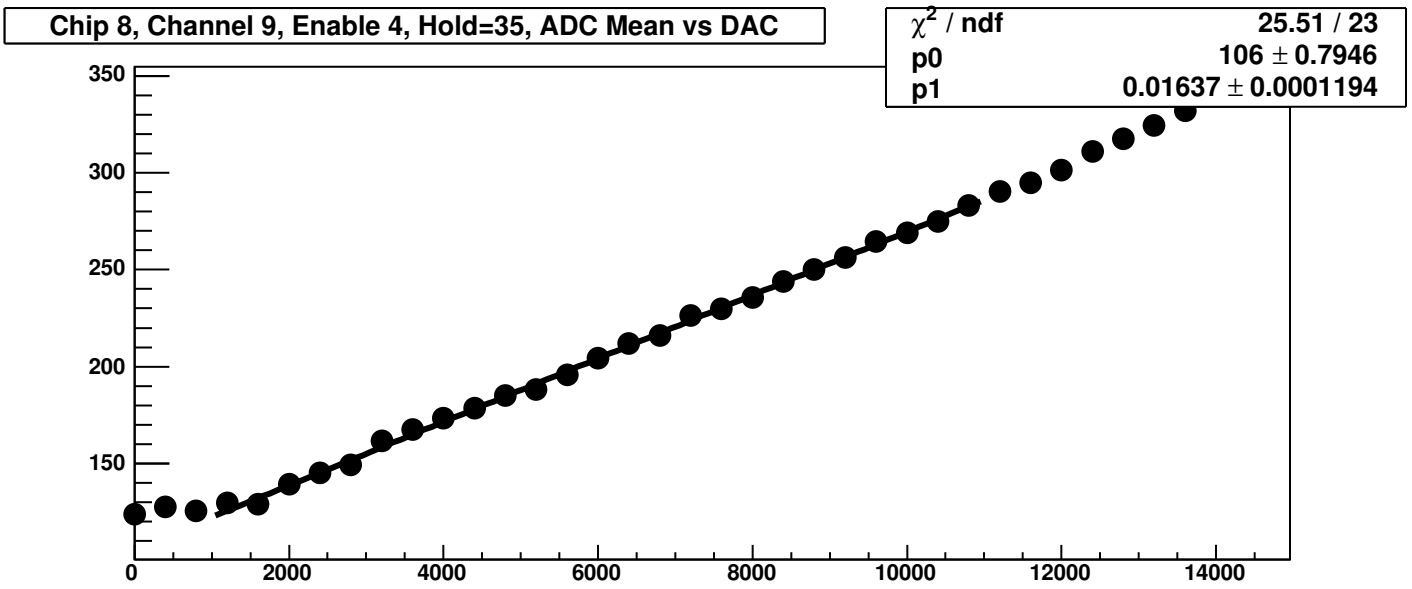
Chip 8, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

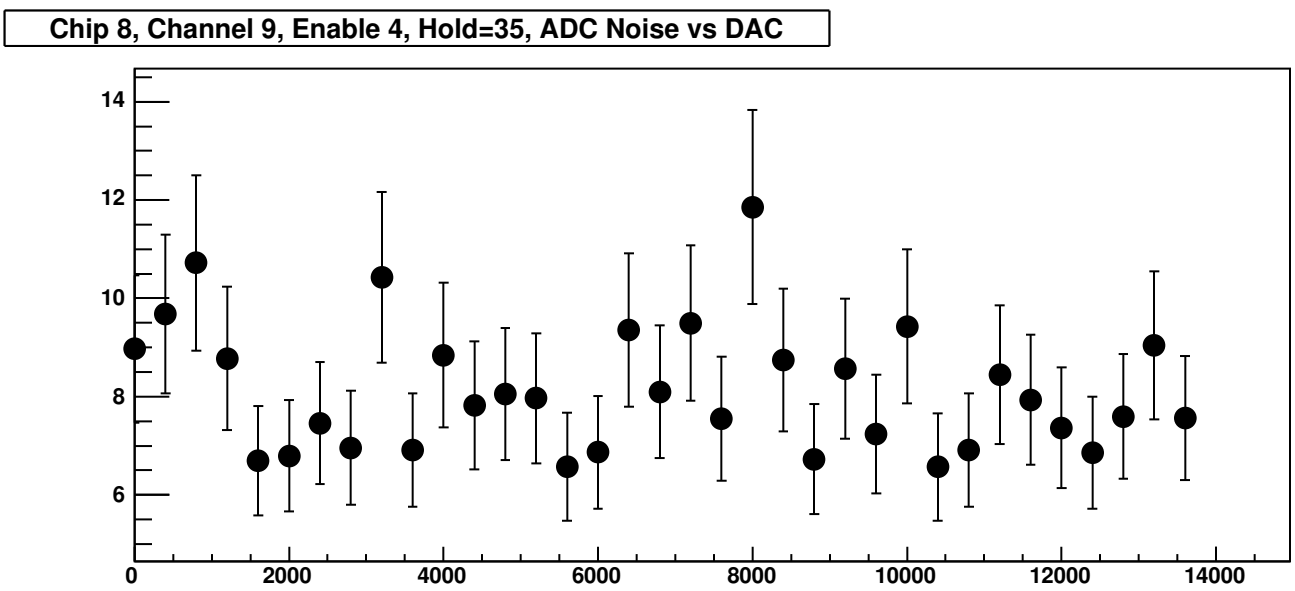


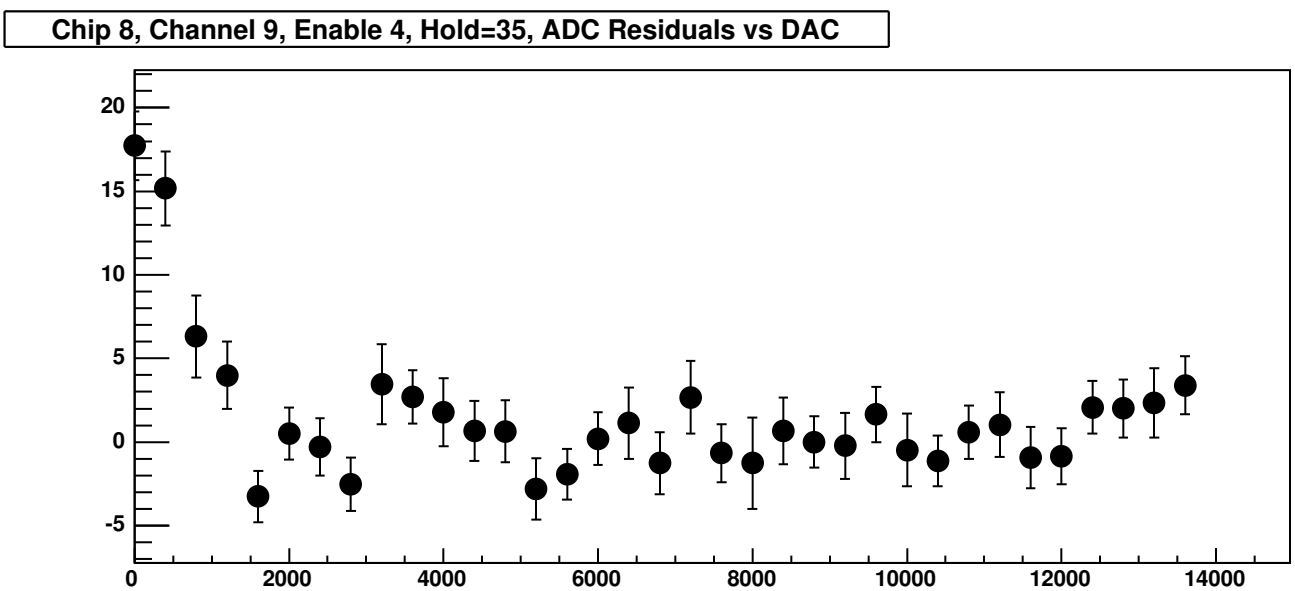

Chip 8, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 42.01 / 23
p0 119.1 ± 0.7898
p1 0.03734 ± 0.0001327
600
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

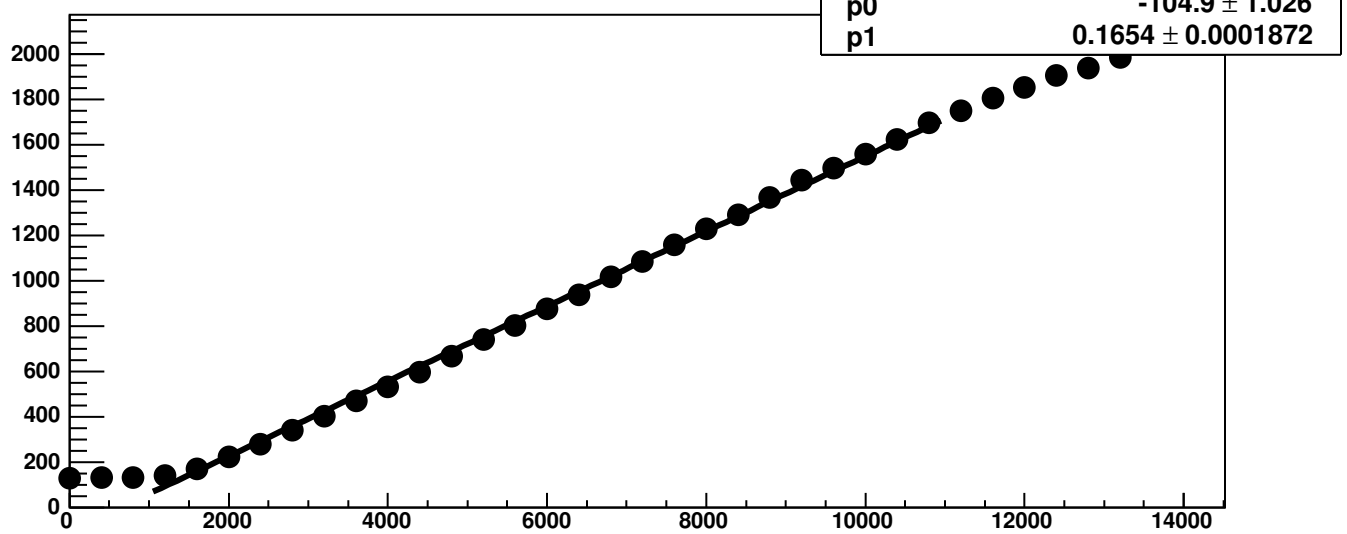

Chip 8, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 25.51 / 23
p0 106 ± 0.7946
p1 0.01637 ± 0.0001194
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

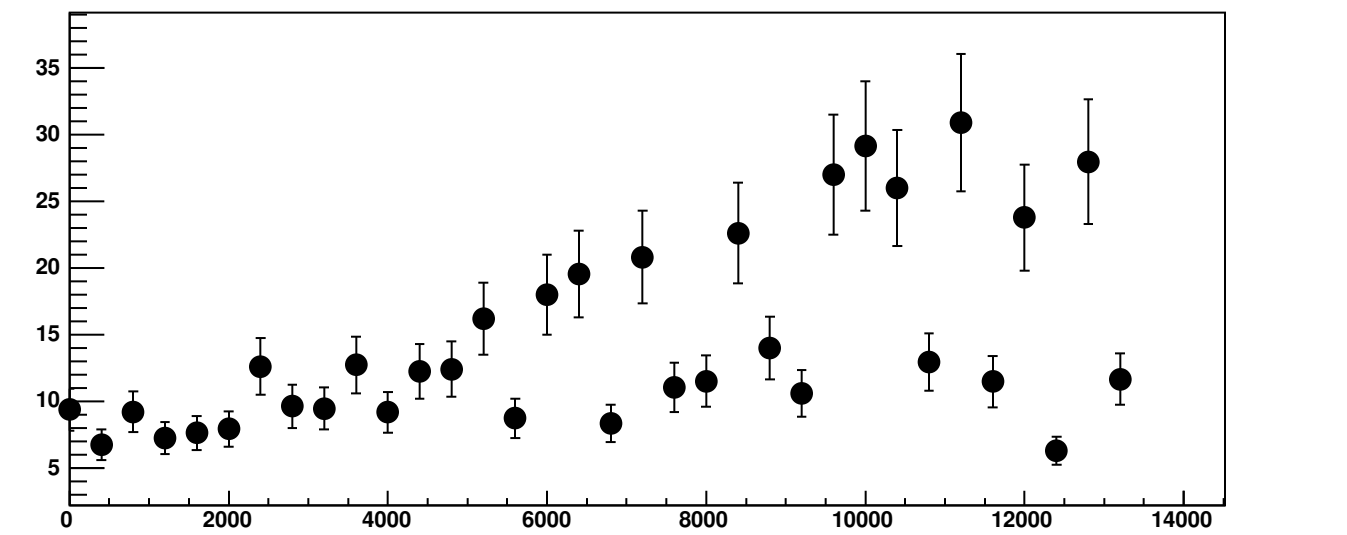

Chip 8, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

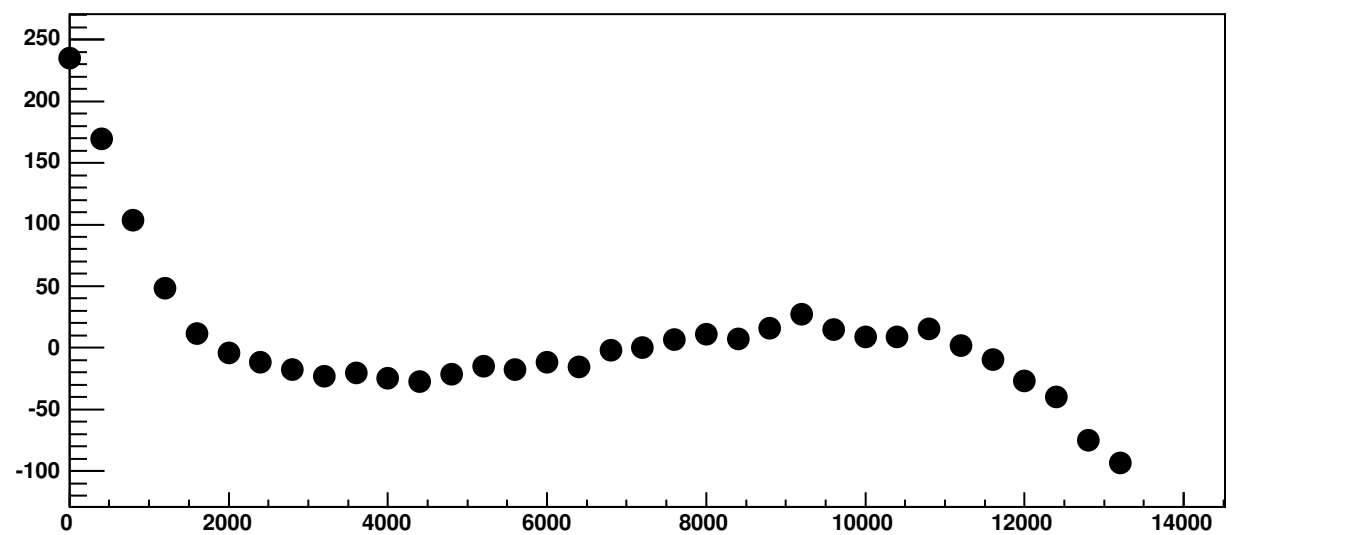
Chip 8, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

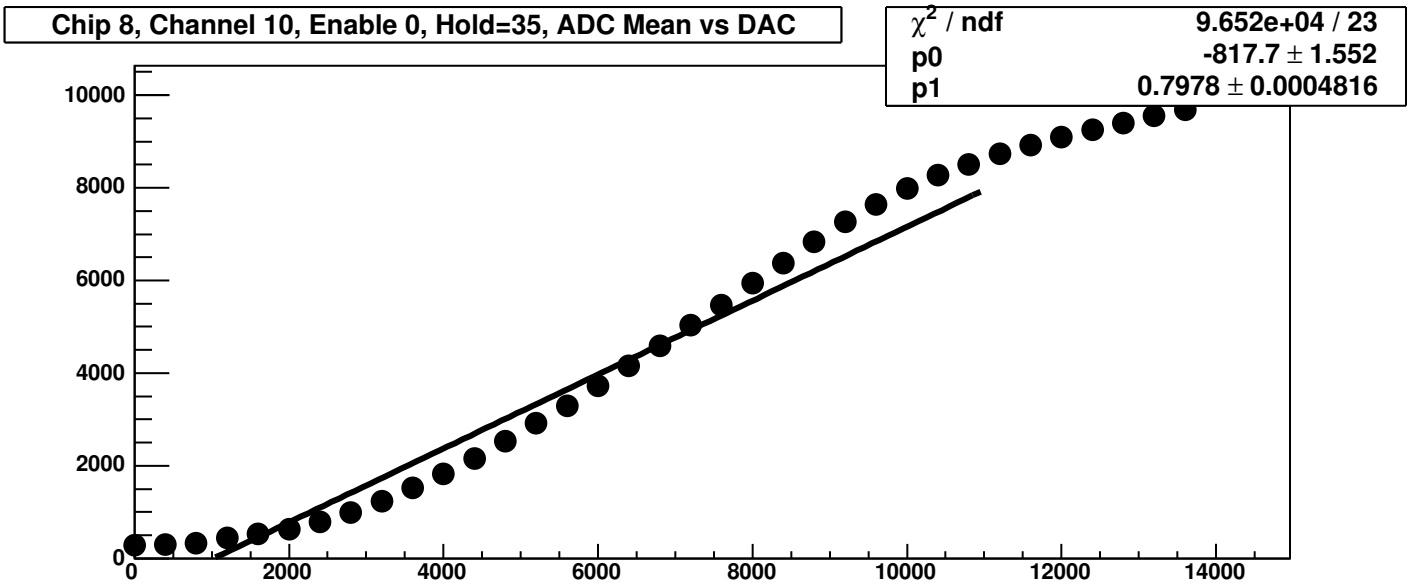


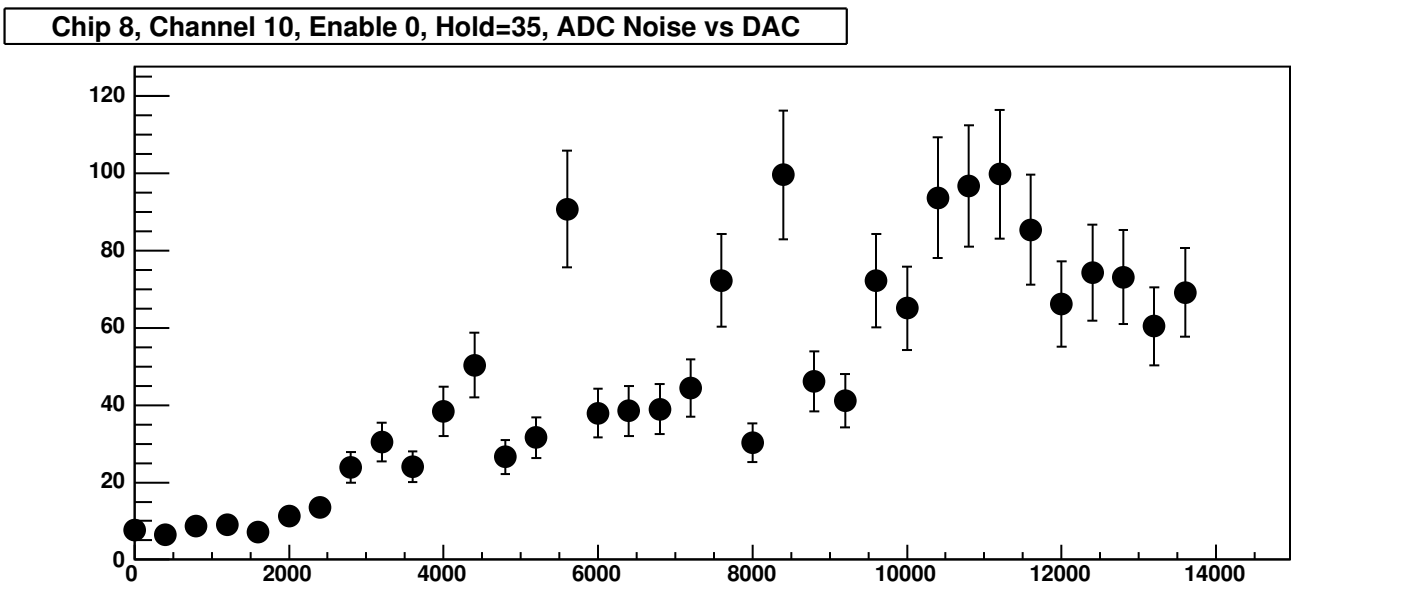
Chip 8, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

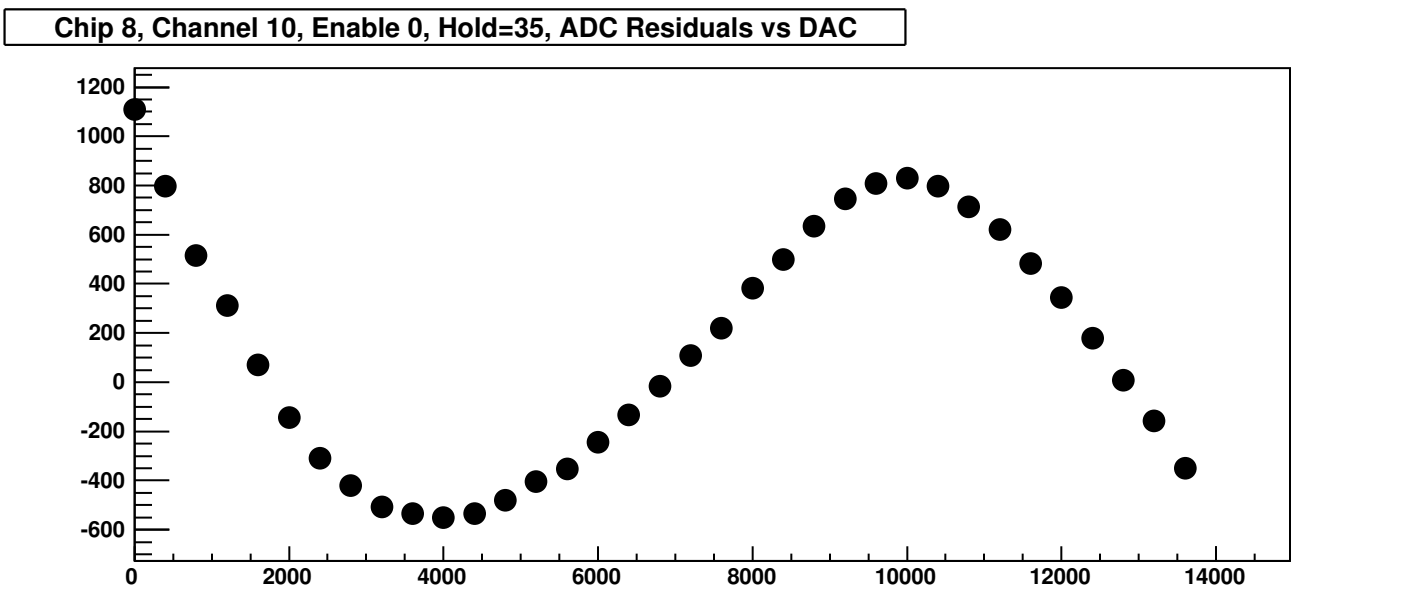


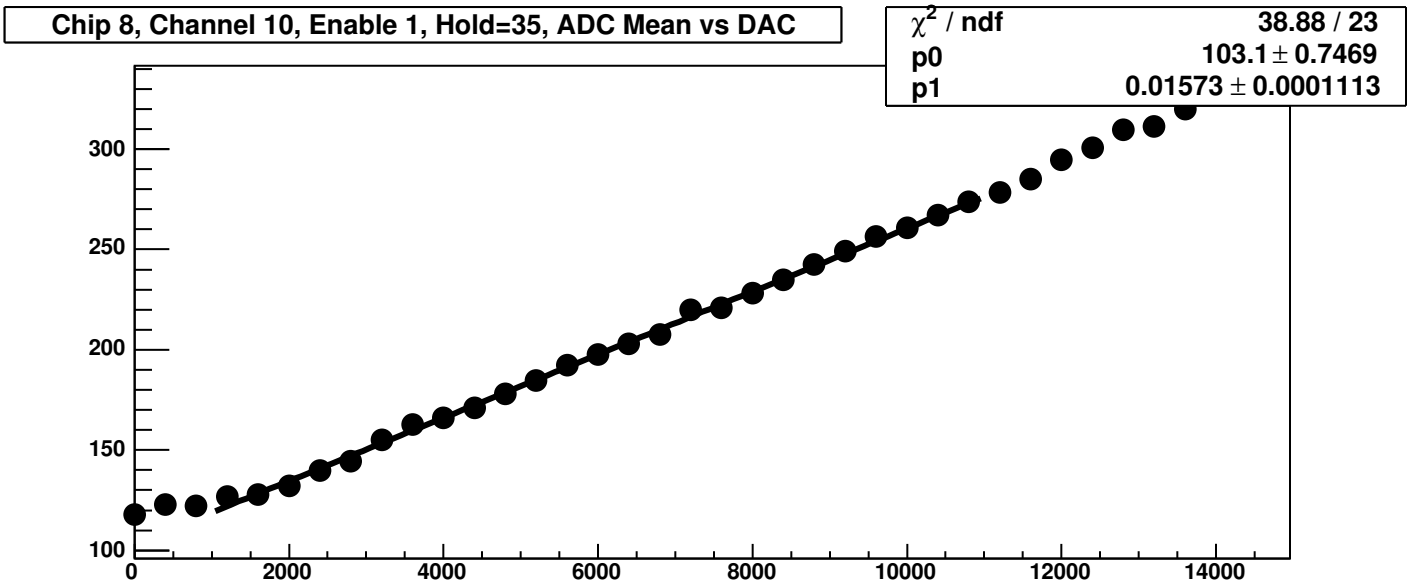
Chip 8, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

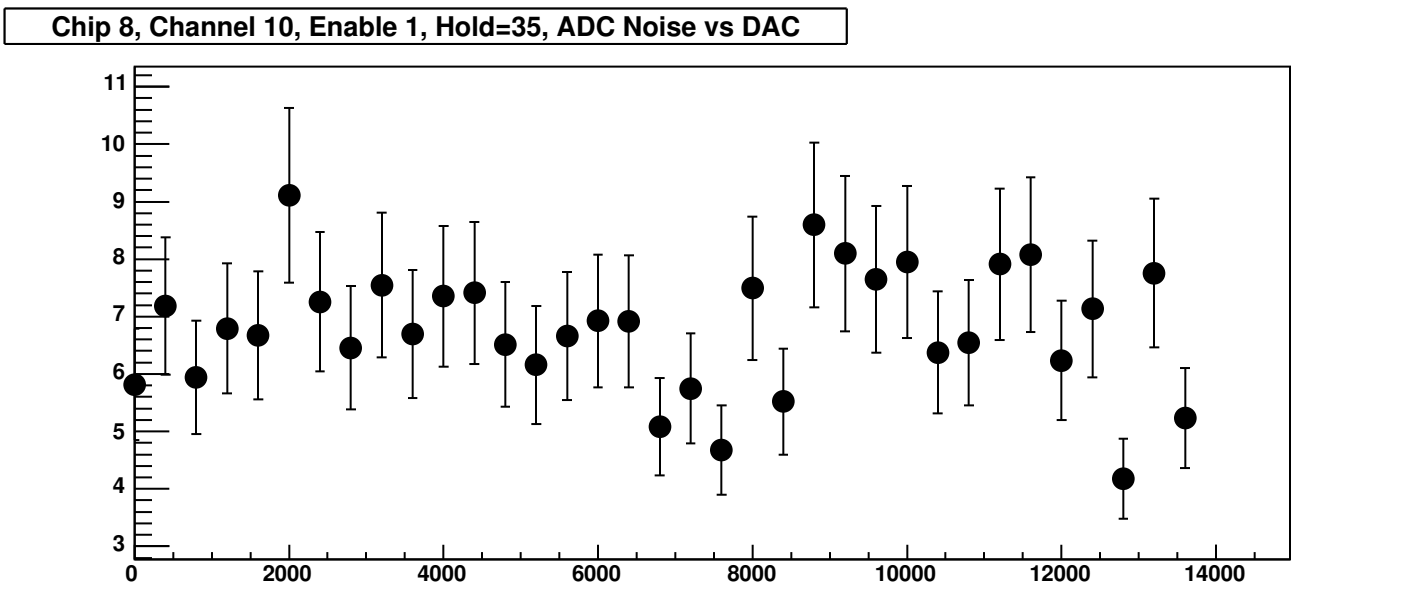


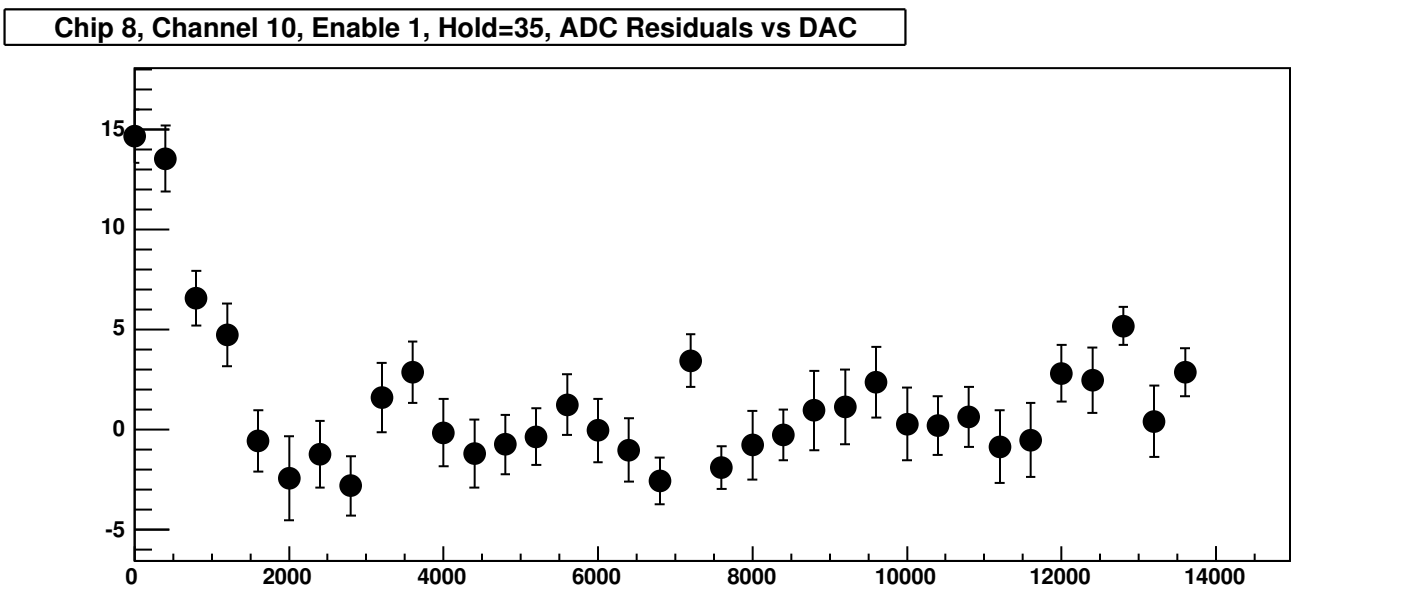

Chip 8, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 9.652e+04 / 23
p0 -817.7 ± 1.552
p1 0.7978 ± 0.0004816
10000
8000
6000
4000
2000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

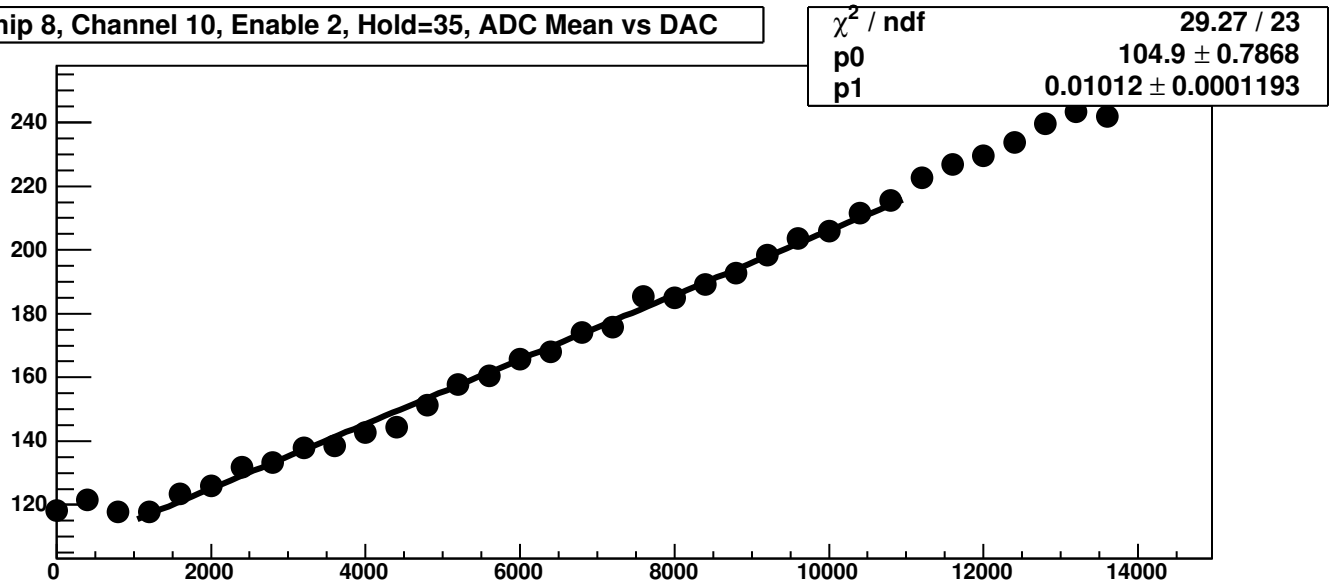

Chip 8, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 38.88 / 23
p0 103.1 ± 0.7469
p1 0.01573 ± 0.0001113
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

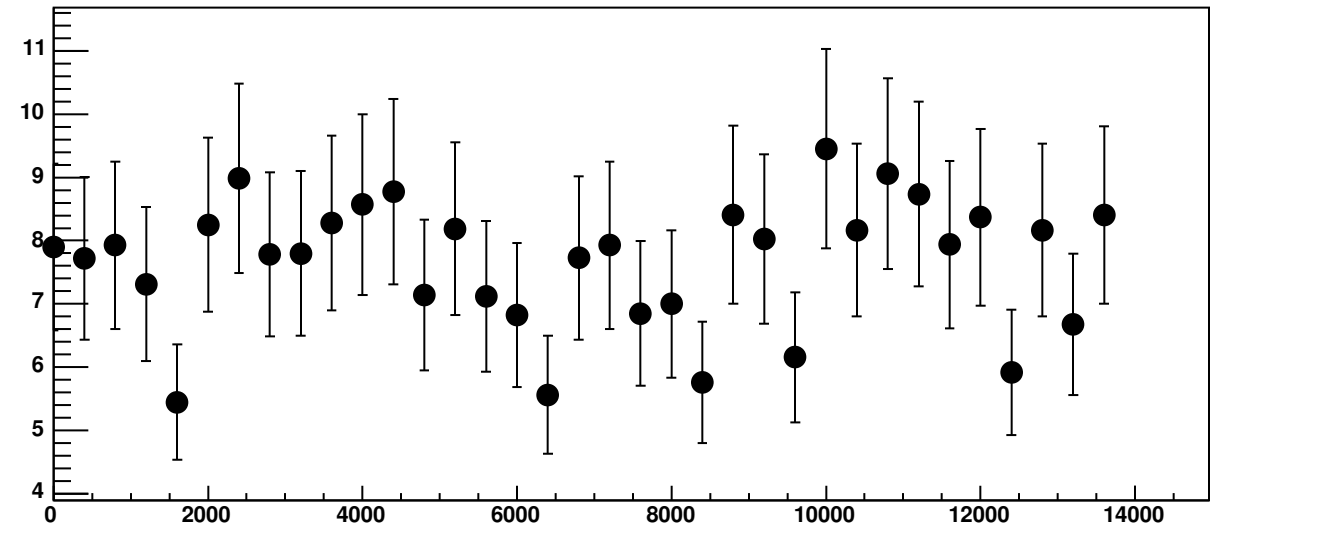

Chip 8, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

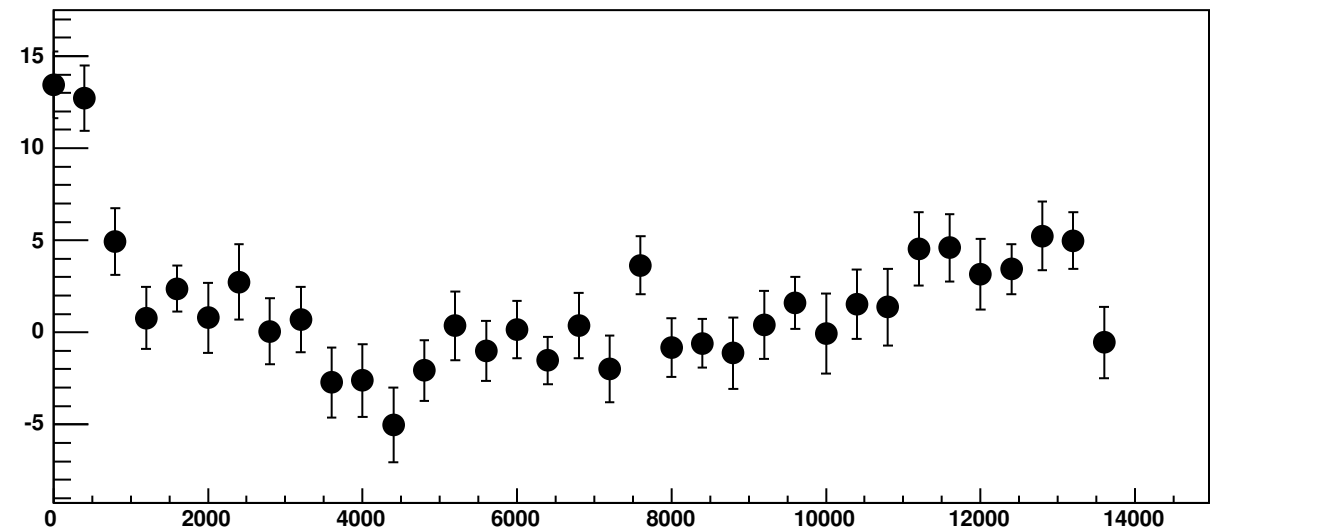
Chip 8, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

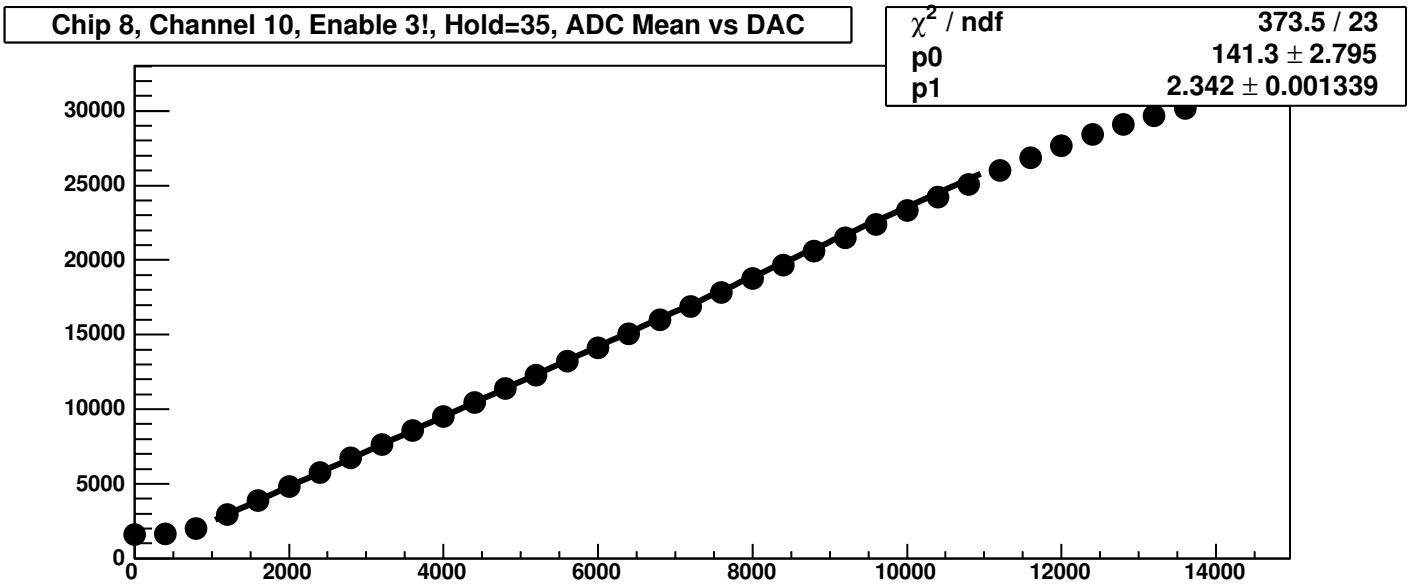


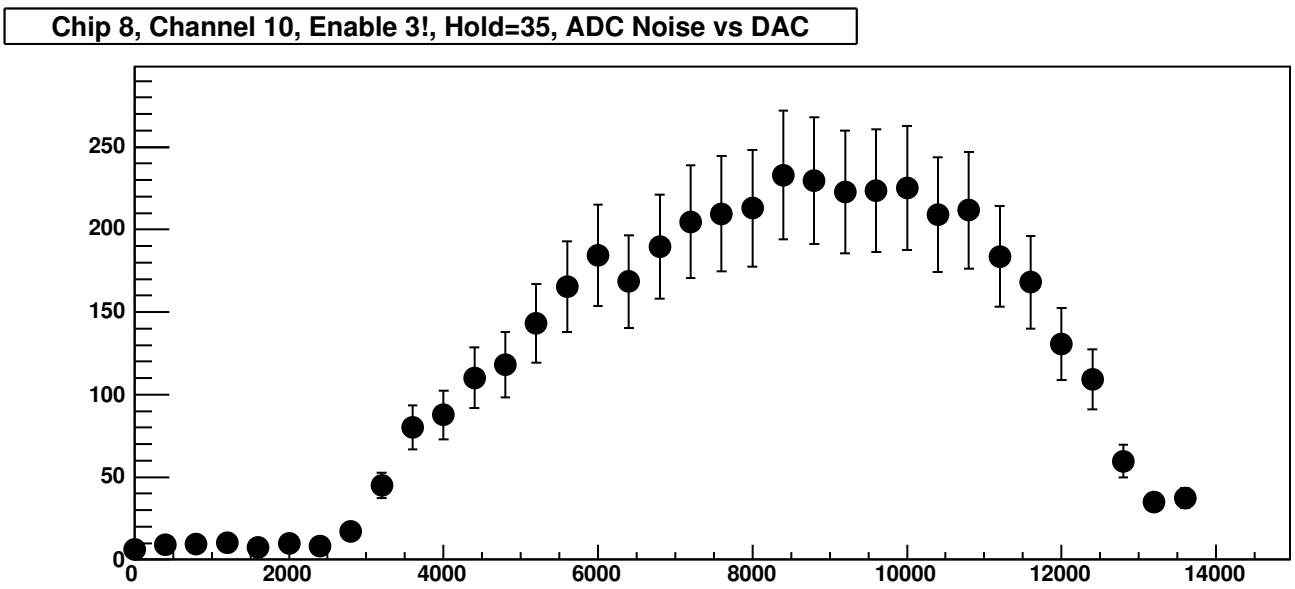
Chip 8, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

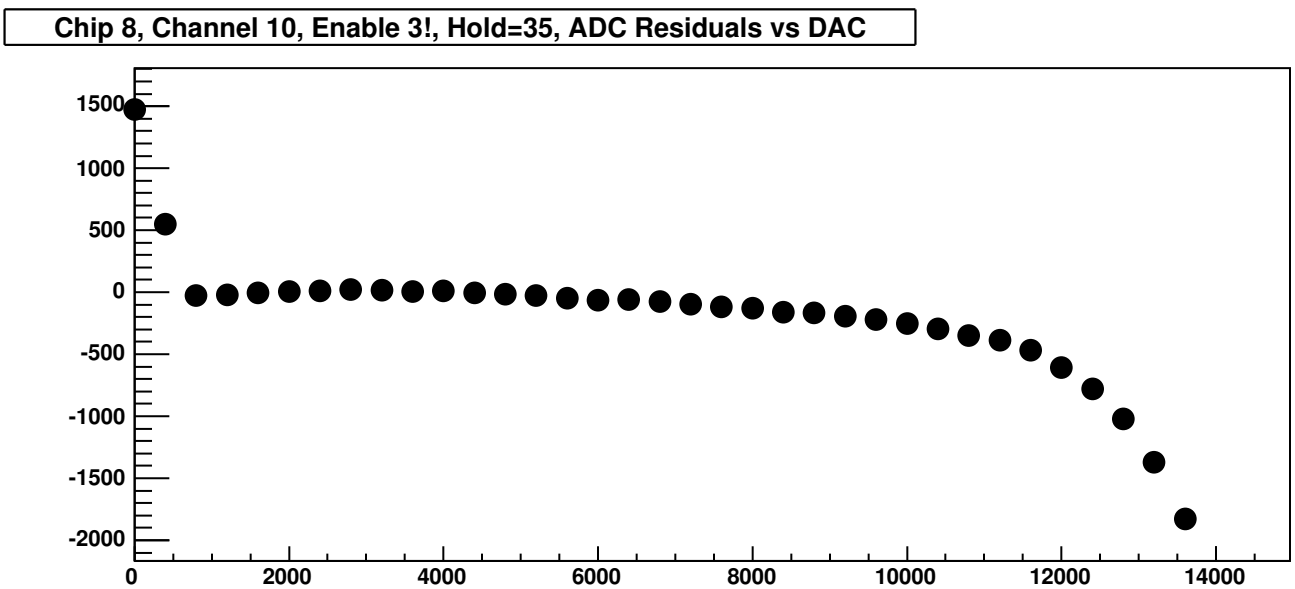


Chip 8, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

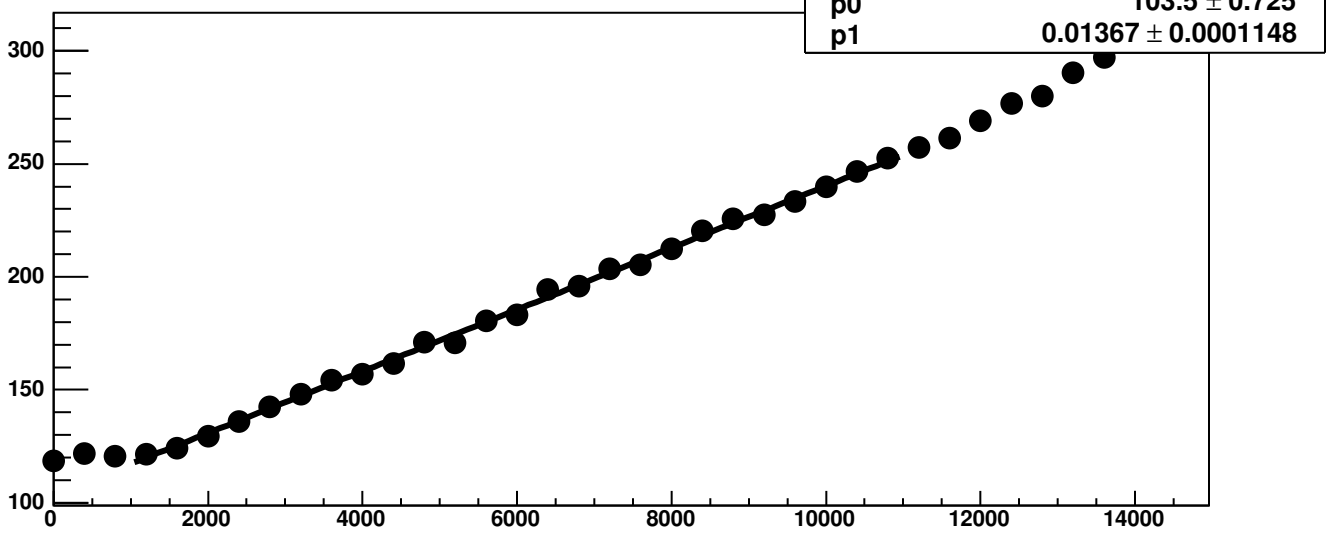



Chip 8, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 373.5 / 23
p0 141.3 ± 2.795
p1 2.342 ± 0.001339
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

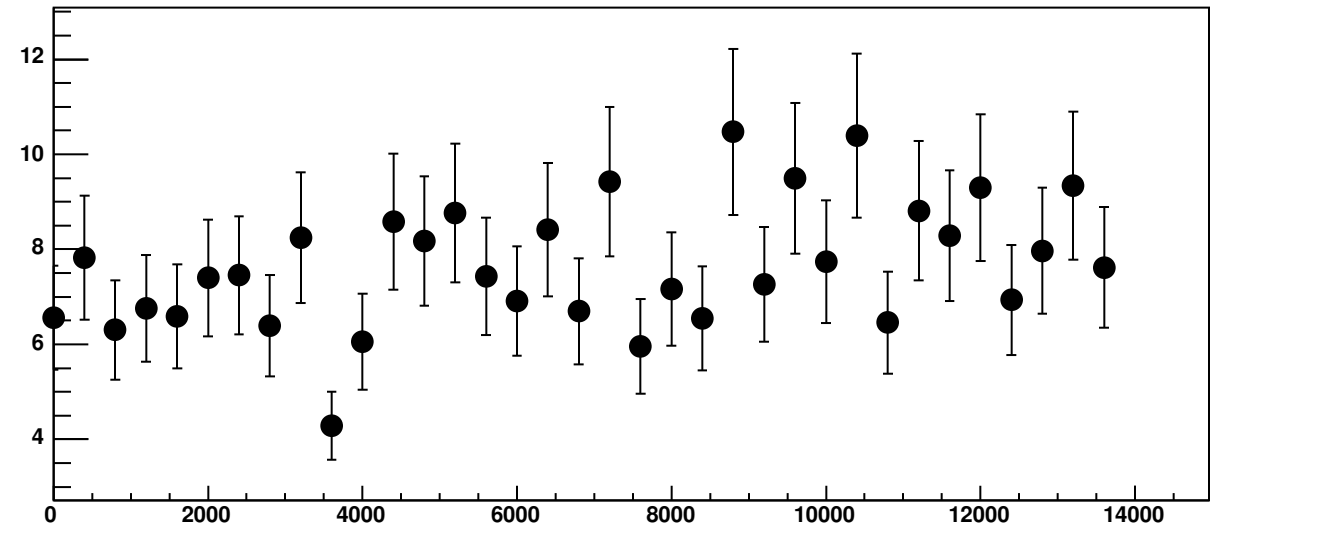

Chip 8, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

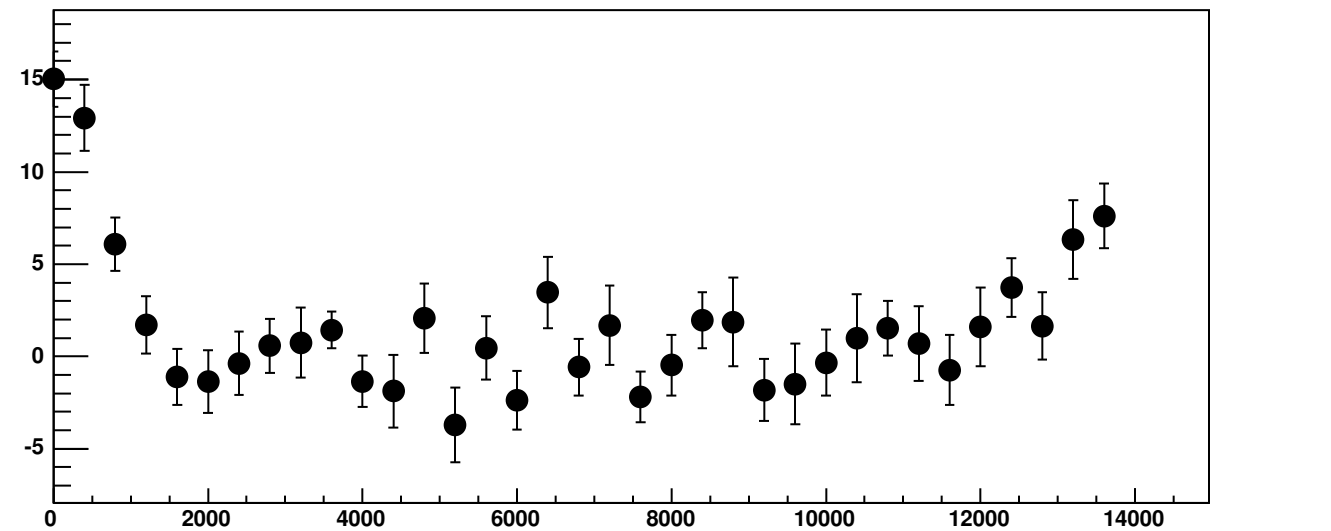
Chip 8, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC



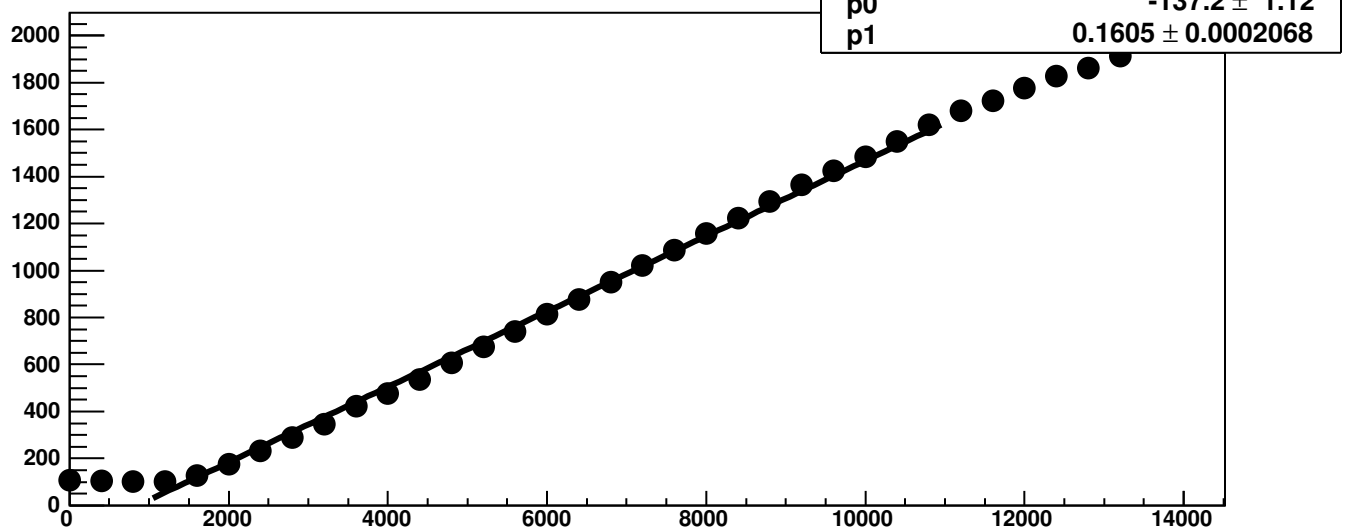
Chip 8, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC



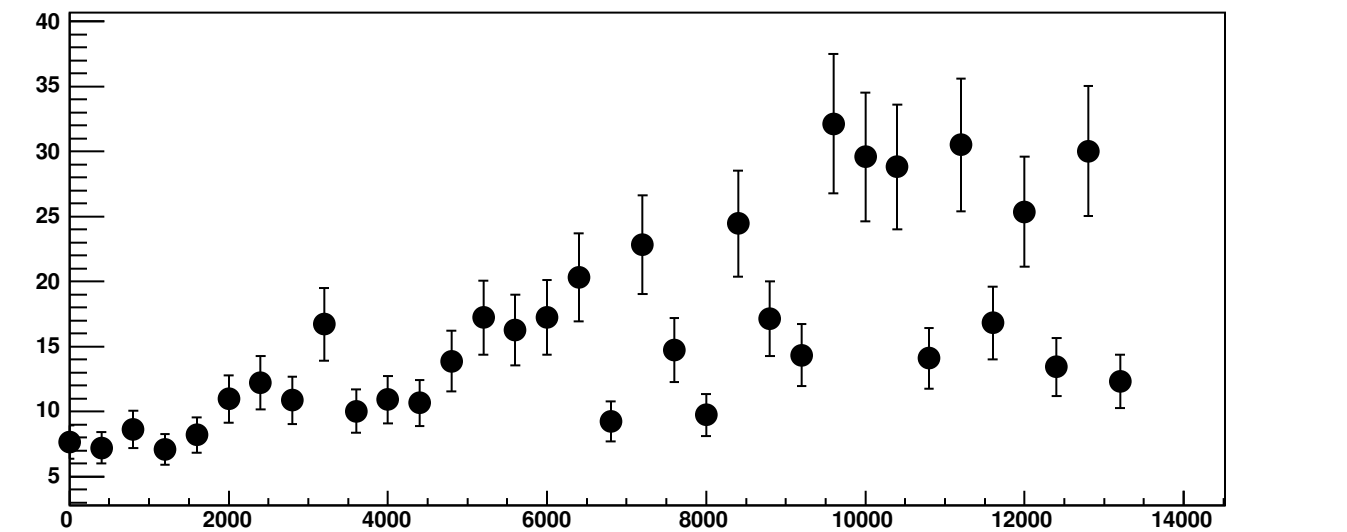
Chip 8, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC



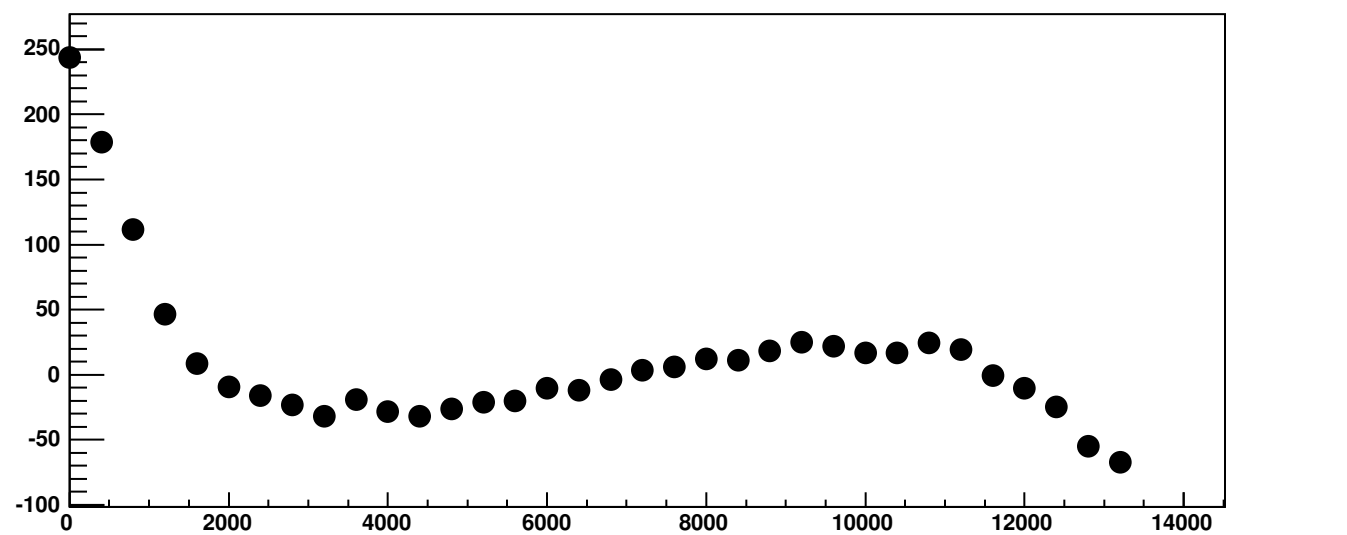
Chip 8, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

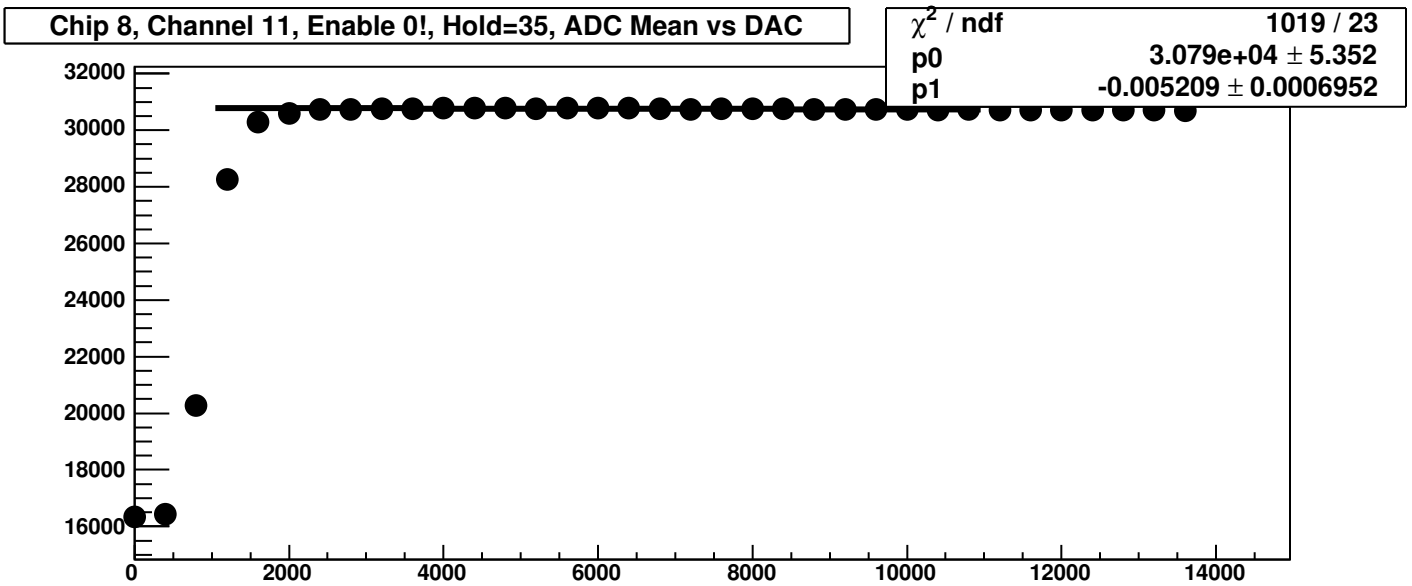


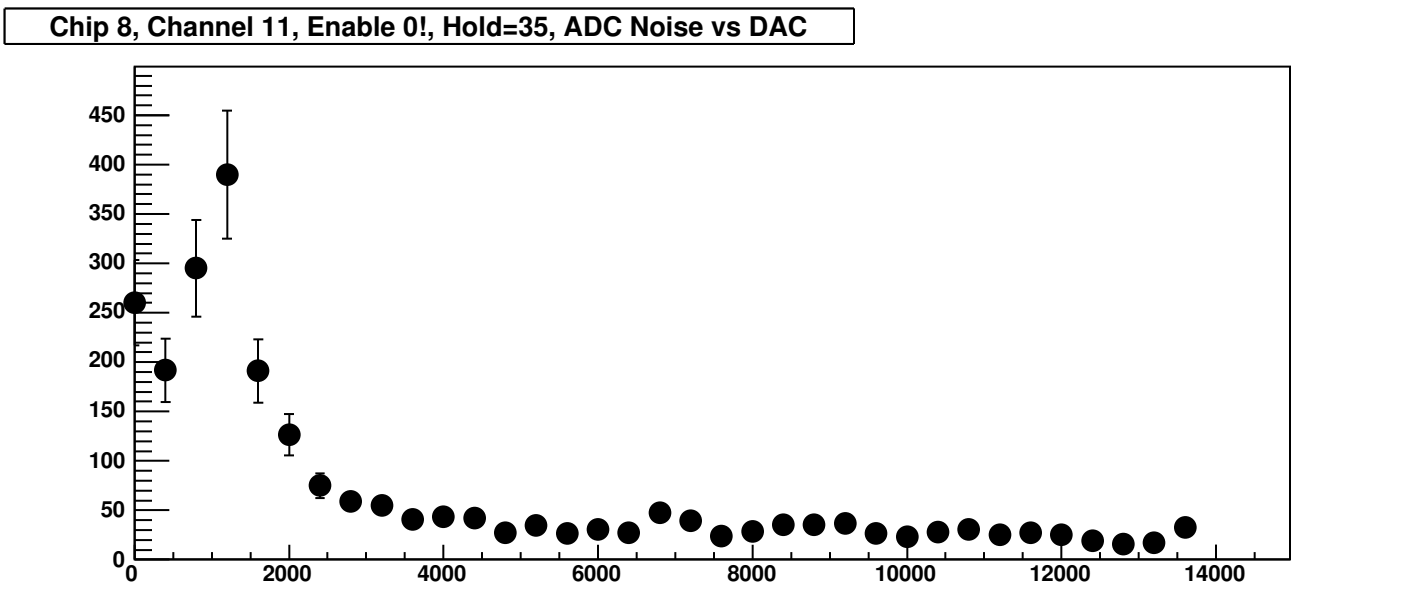
Chip 8, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

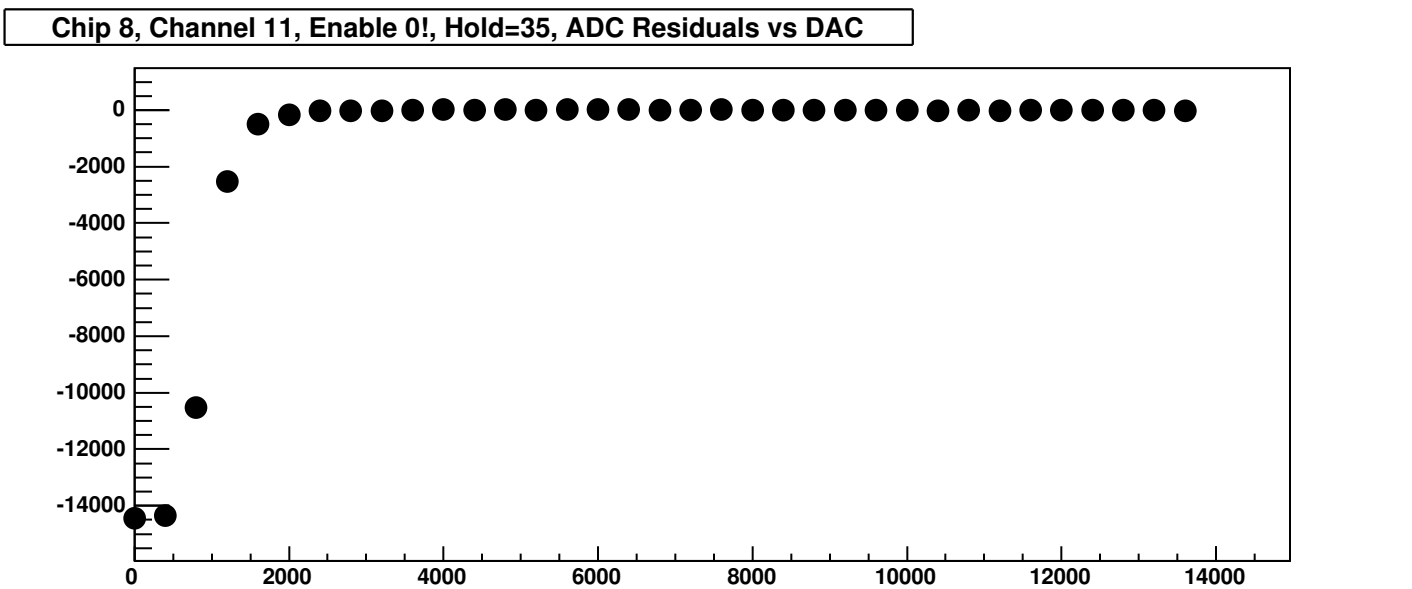


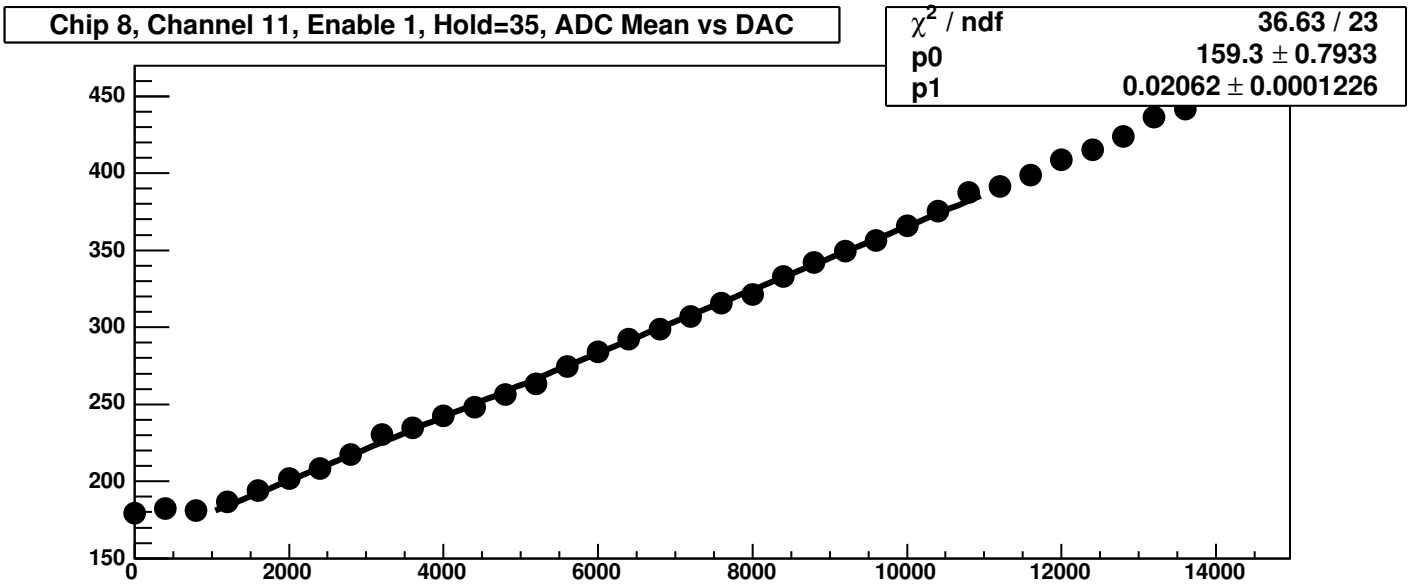
Chip 8, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

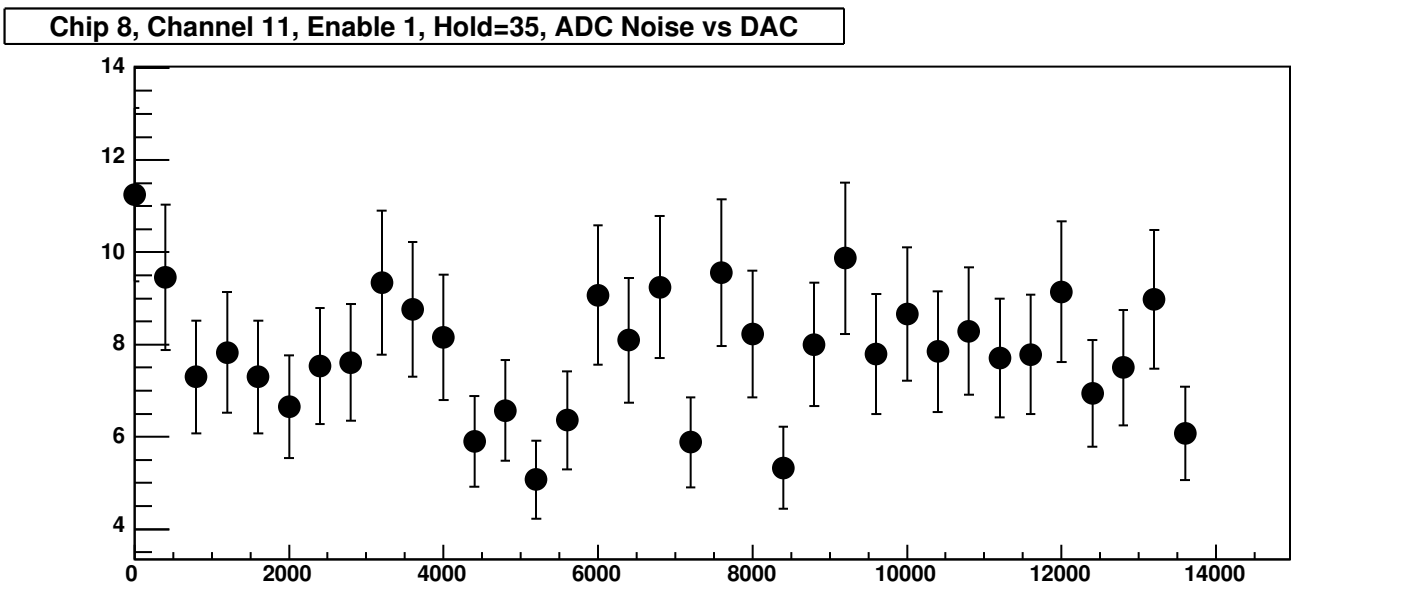


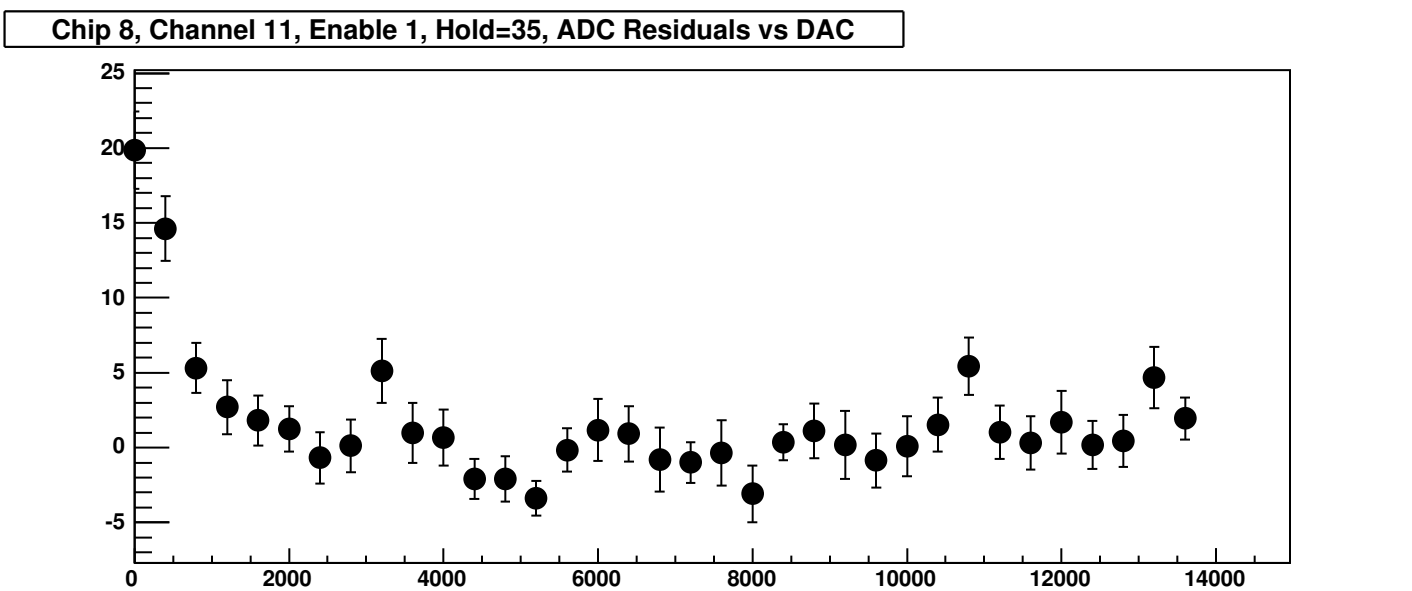

Chip 8, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1019 / 23
p0 3.079e+04 ± 5.352
p1 -0.005209 ± 0.0006952
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

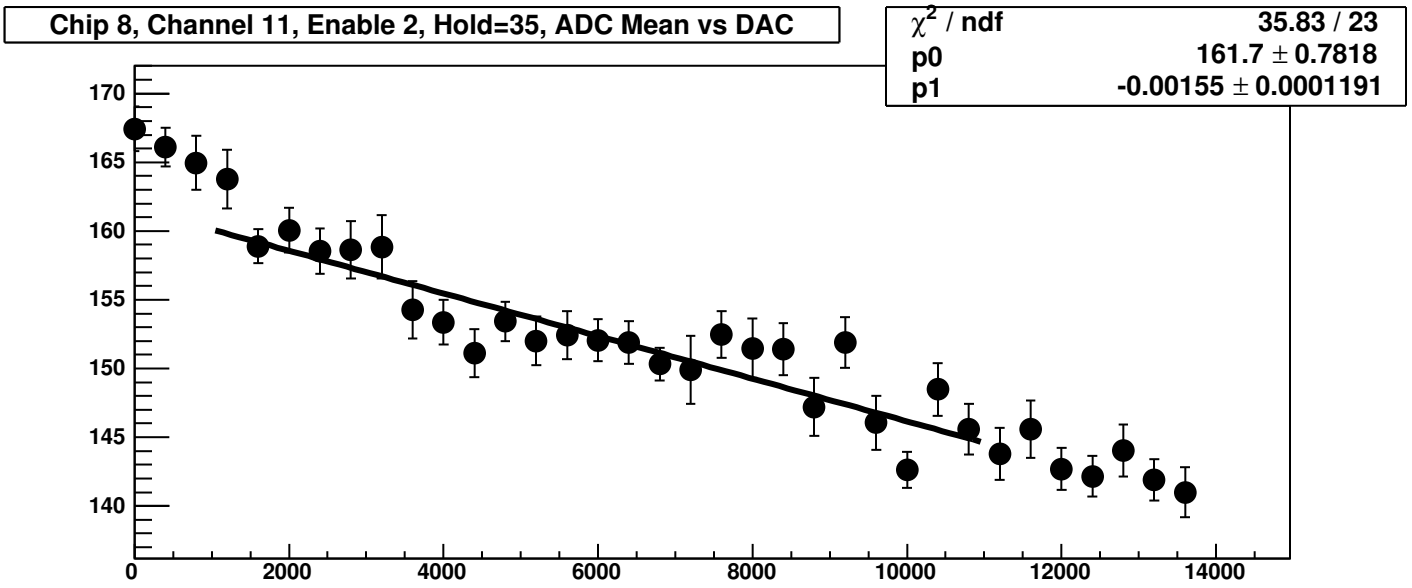

Chip 8, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

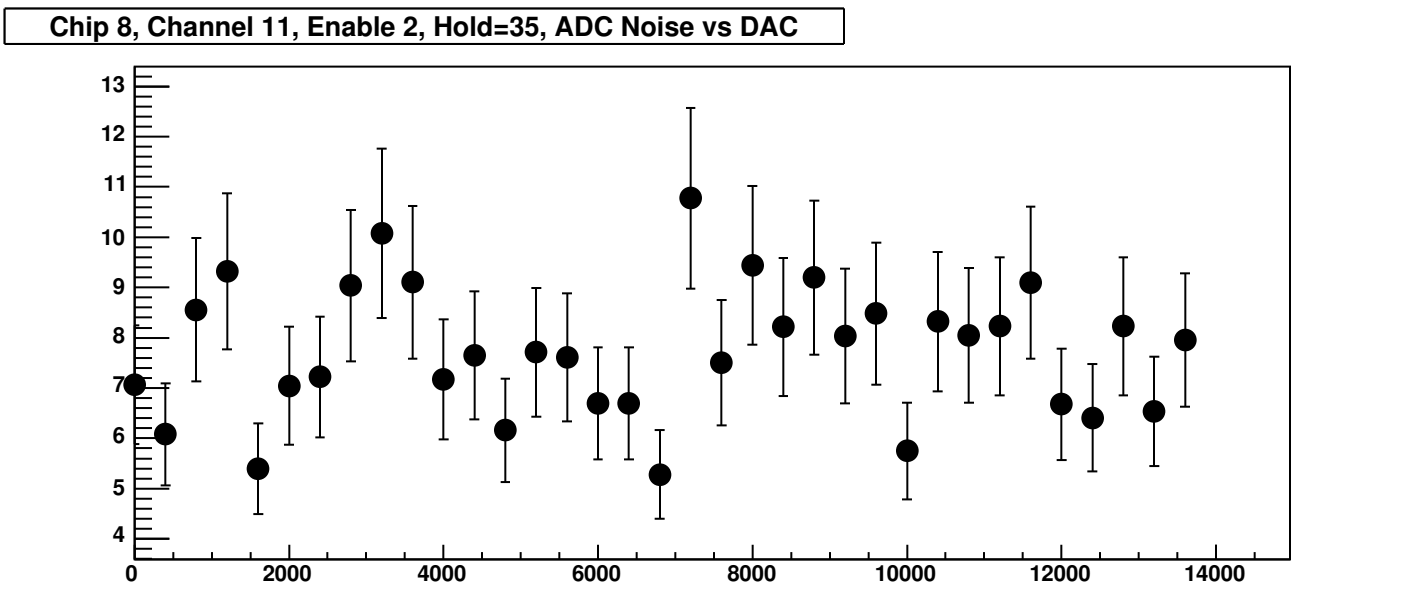

Chip 8, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

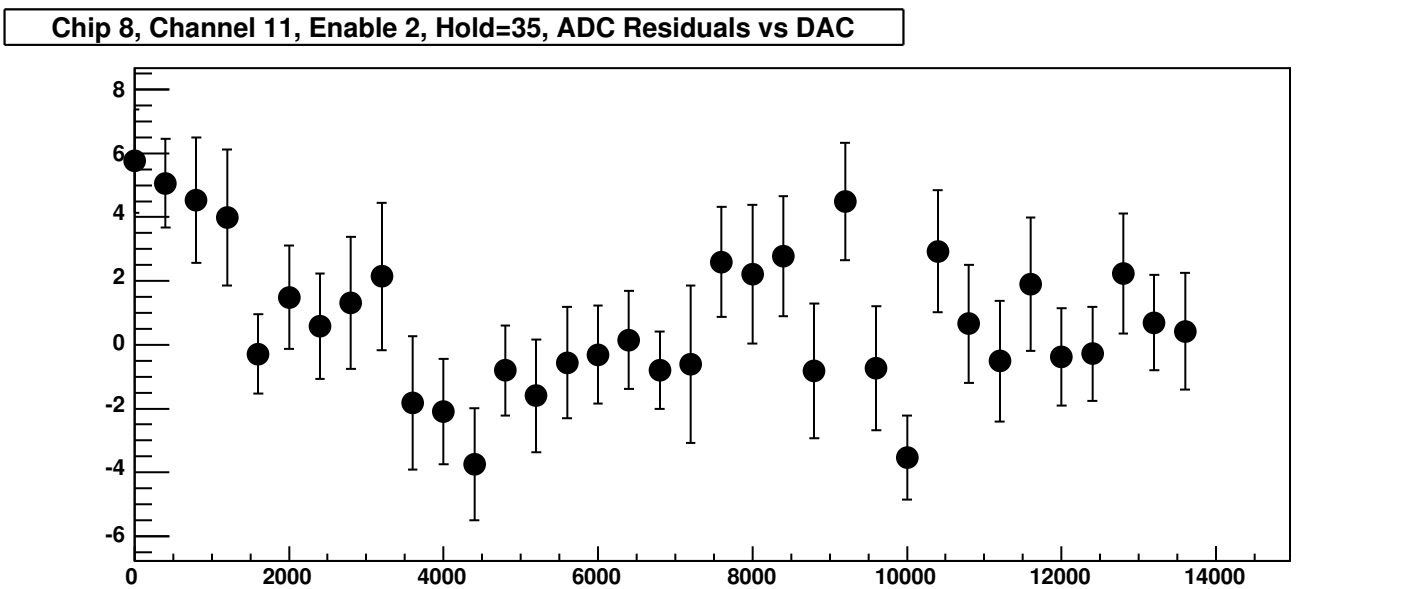

Chip 8, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 36.63 / 23
p0 159.3 ± 0.7933
p1 0.02062 ± 0.0001226
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

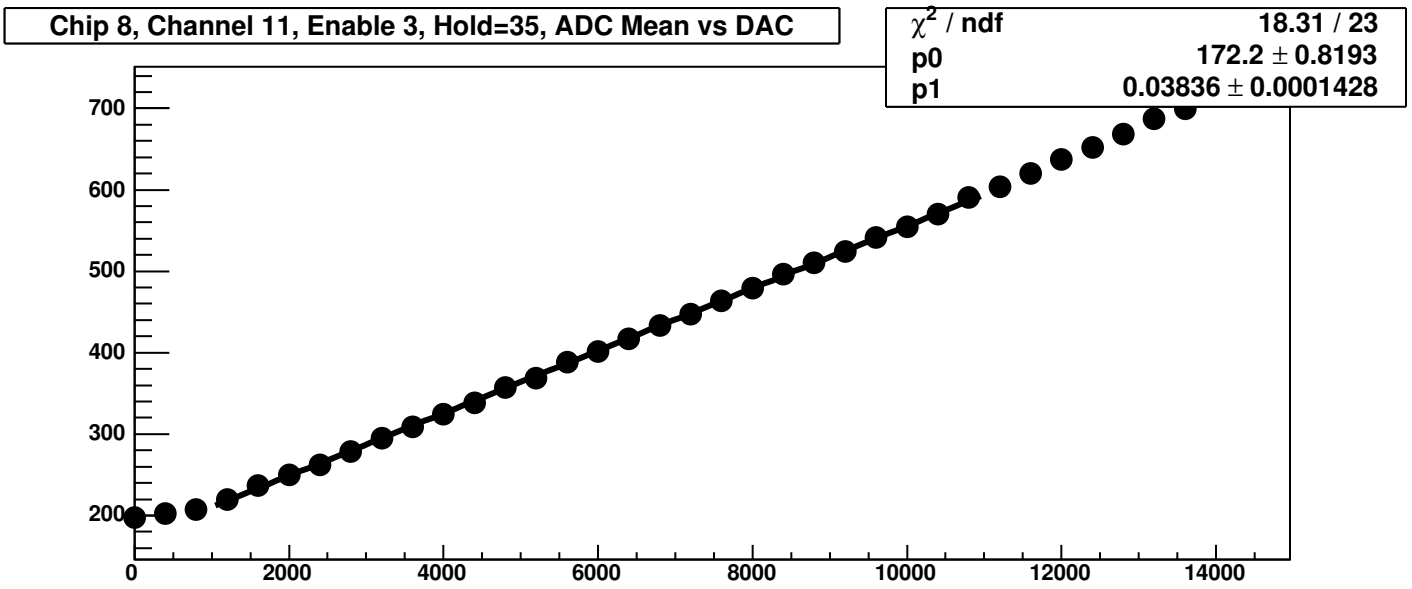

Chip 8, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

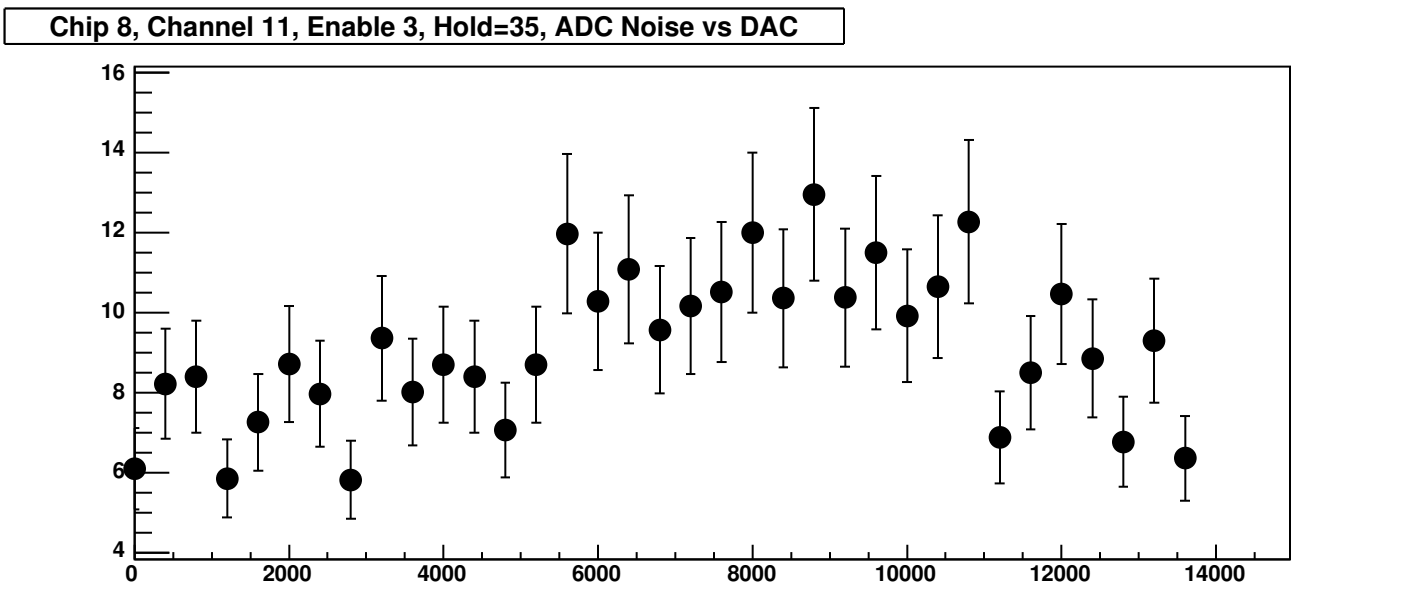

Chip 8, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

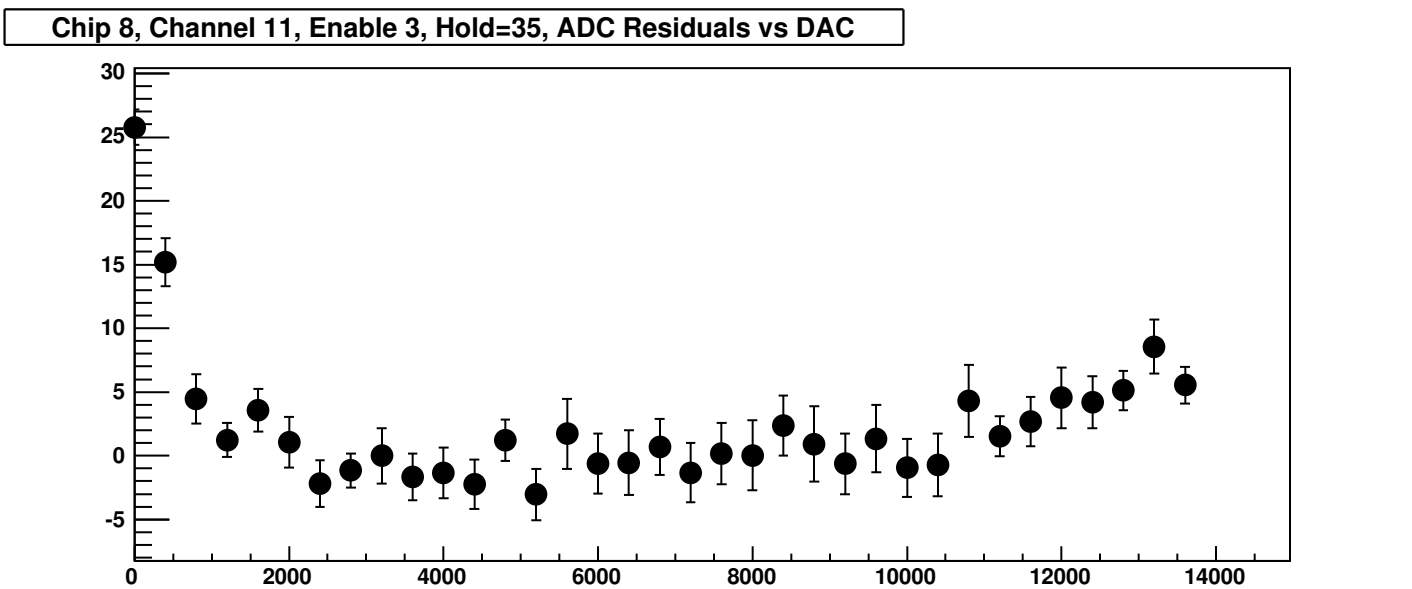

Chip 8, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 35.83 / 23
p0 161.7 ± 0.7818
p1 -0.00155 ± 0.0001191
170
165
160
155
150
145
140
0
2000
4000
6000
8000
10000
12000
14000

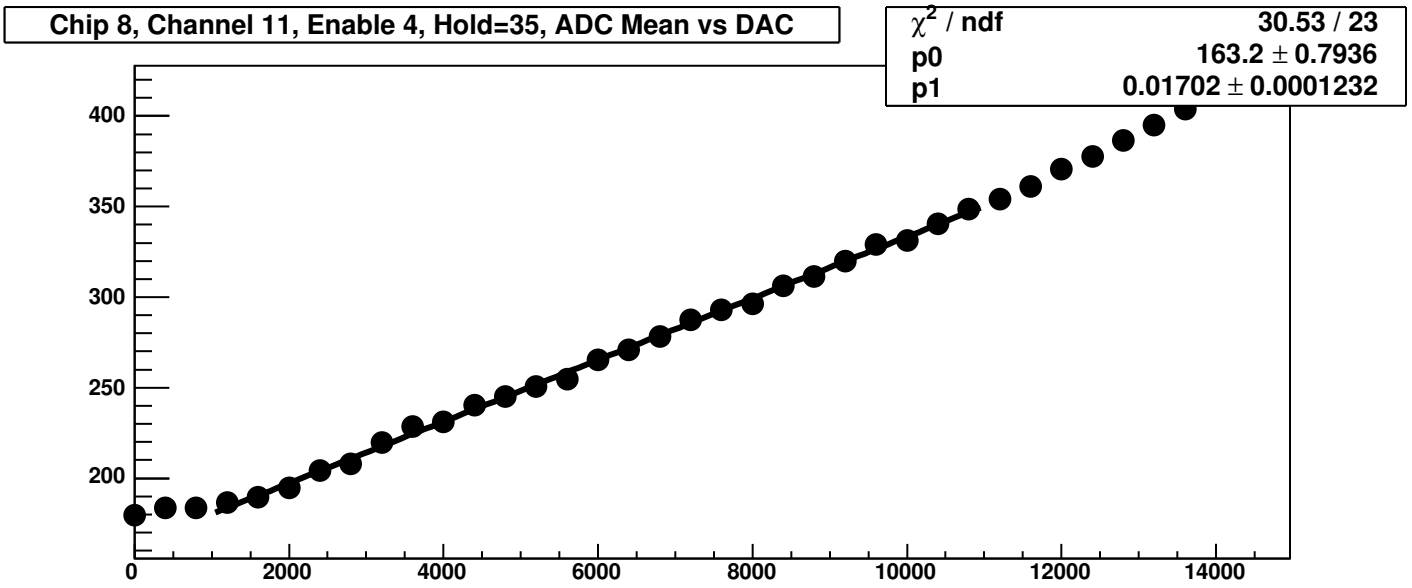

Chip 8, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

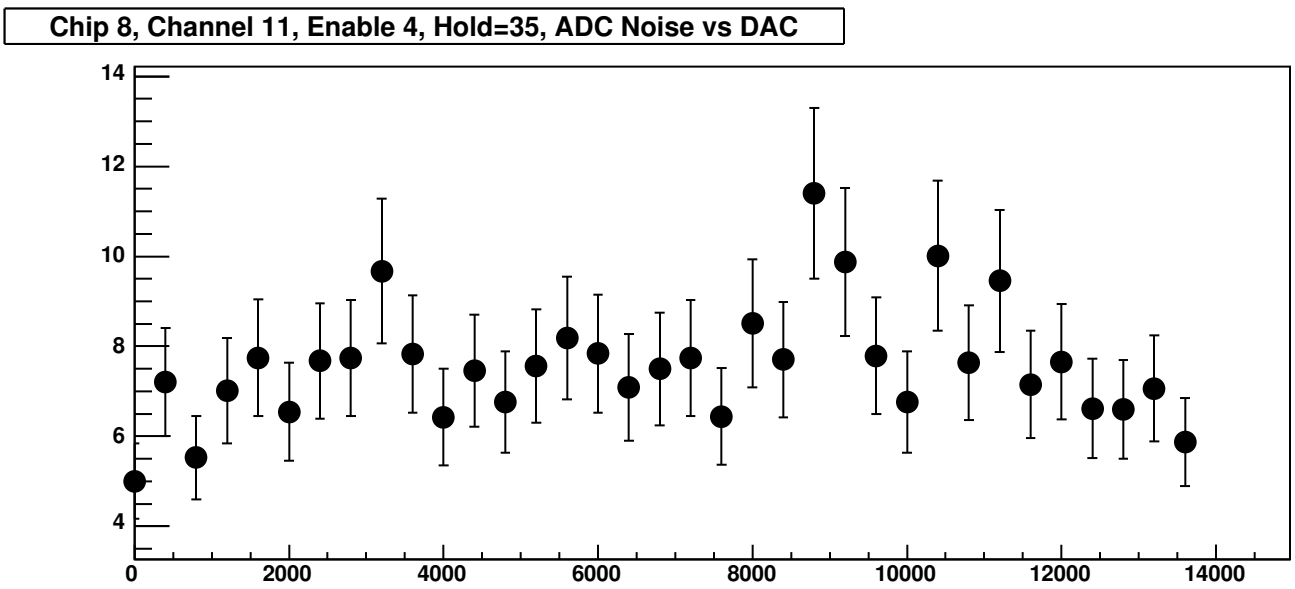

Chip 8, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000

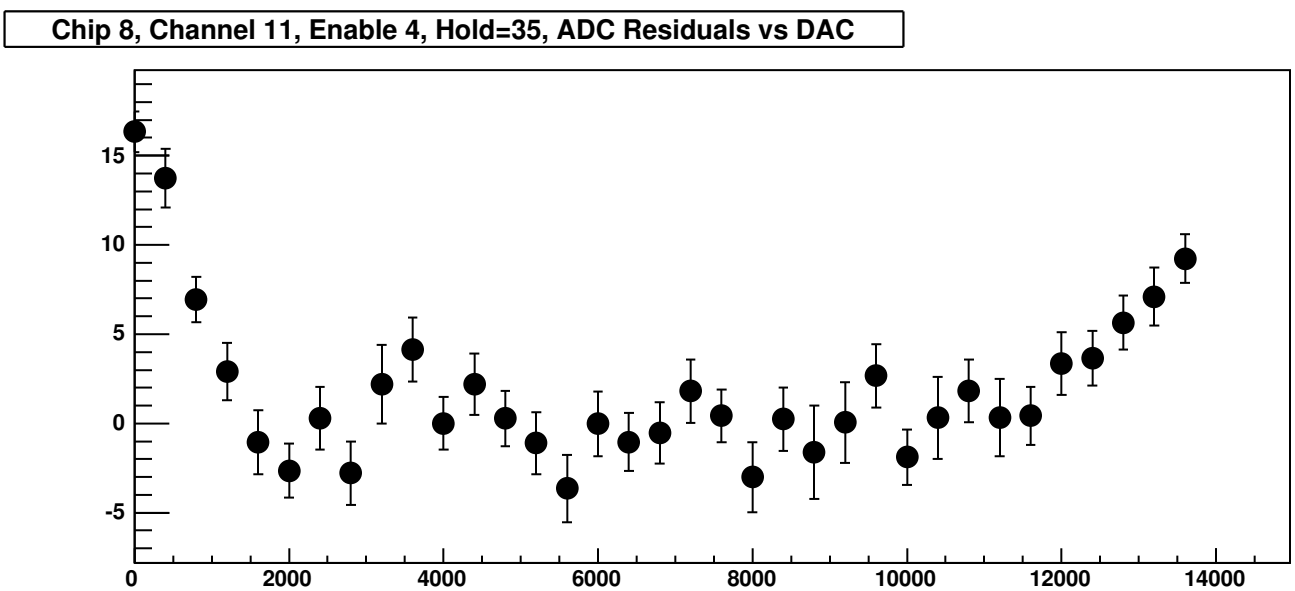

Chip 8, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 18.31 / 23
p0 172.2 ± 0.8193
p1 0.03836 ± 0.0001428
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000

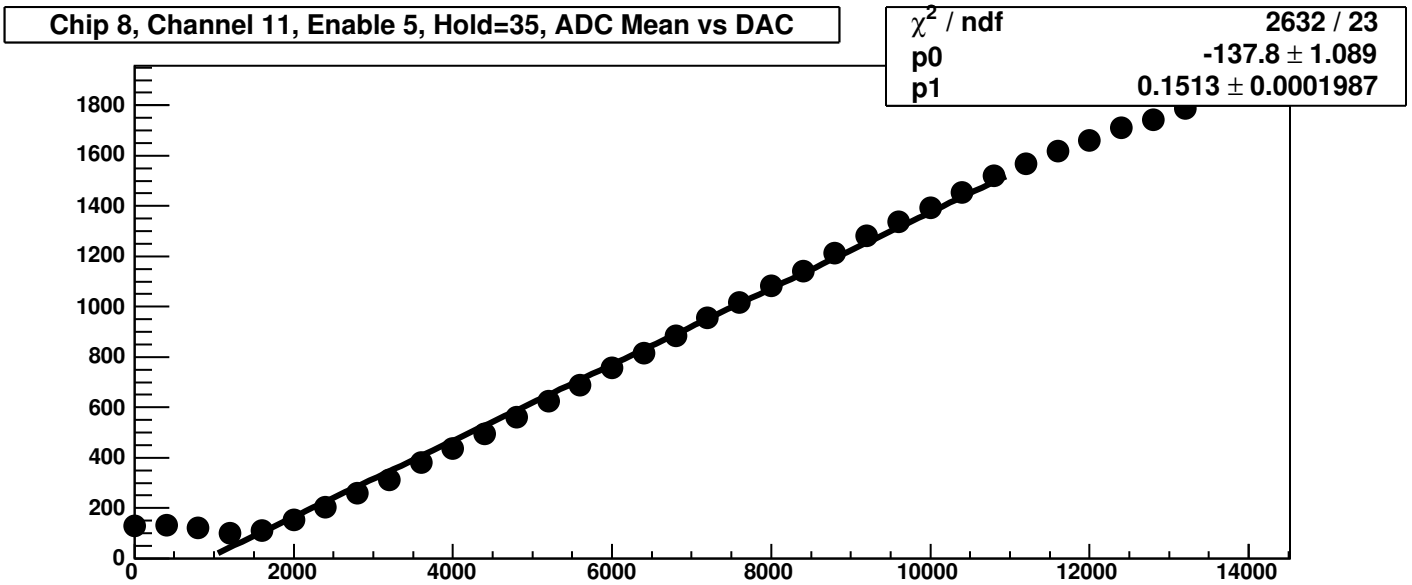

Chip 8, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

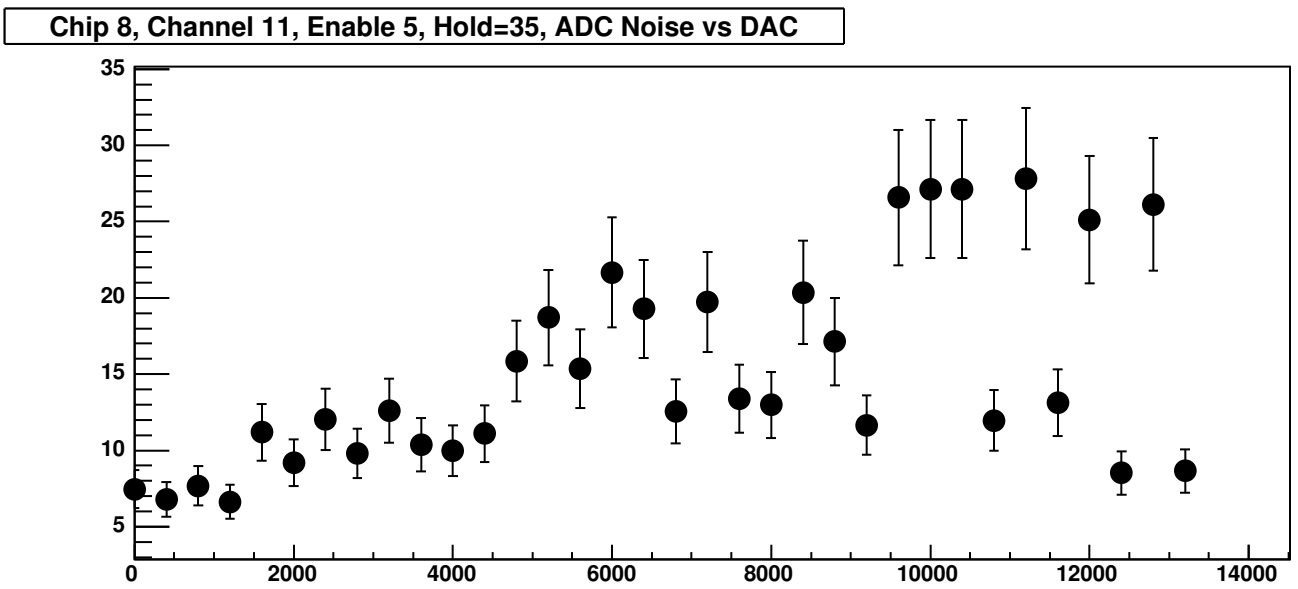

Chip 8, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

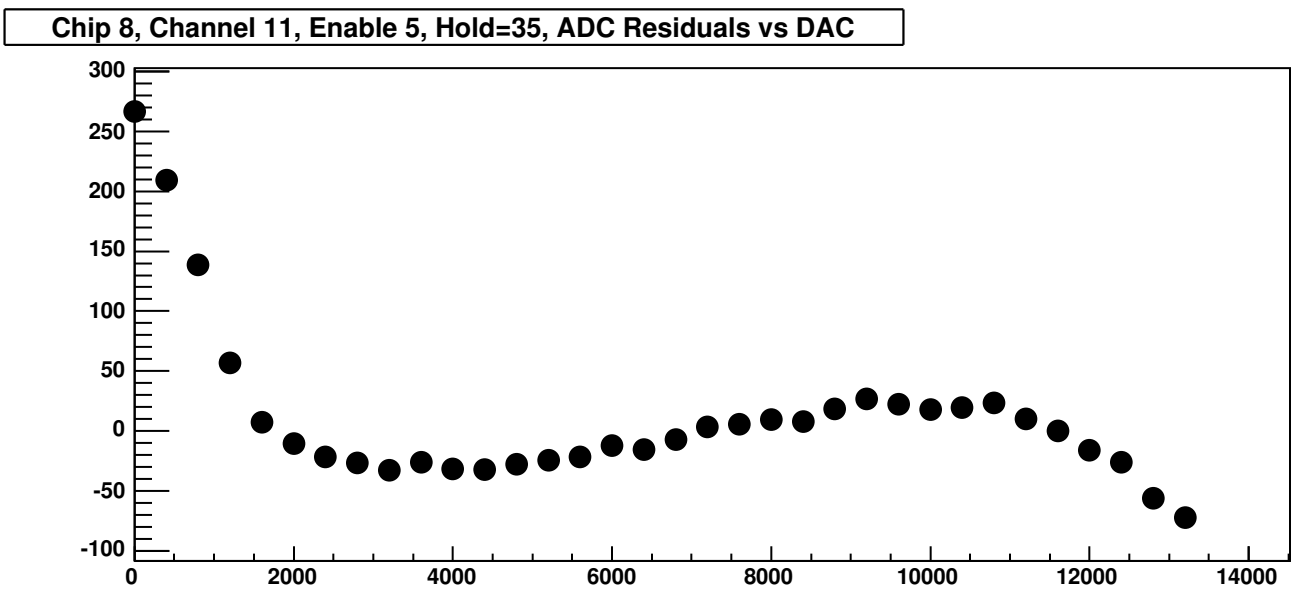

Chip 8, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.53 / 23
p0 163.2 ± 0.7936
p1 0.01702 ± 0.0001232
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

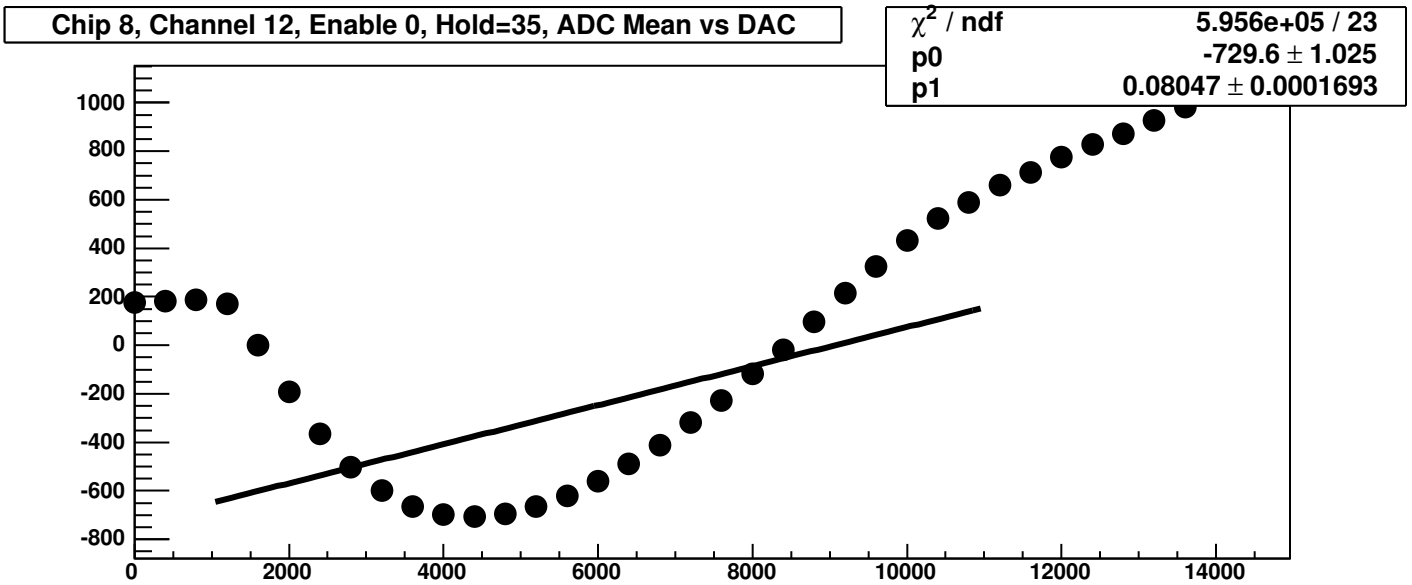

Chip 8, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

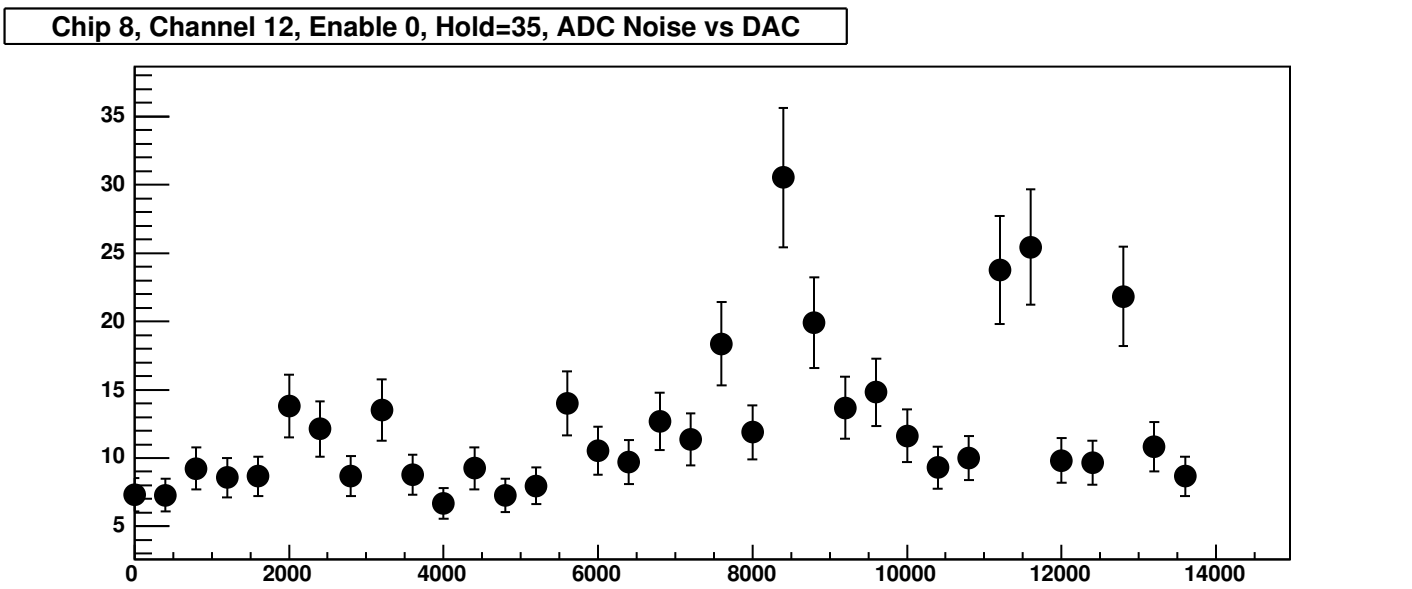

Chip 8, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

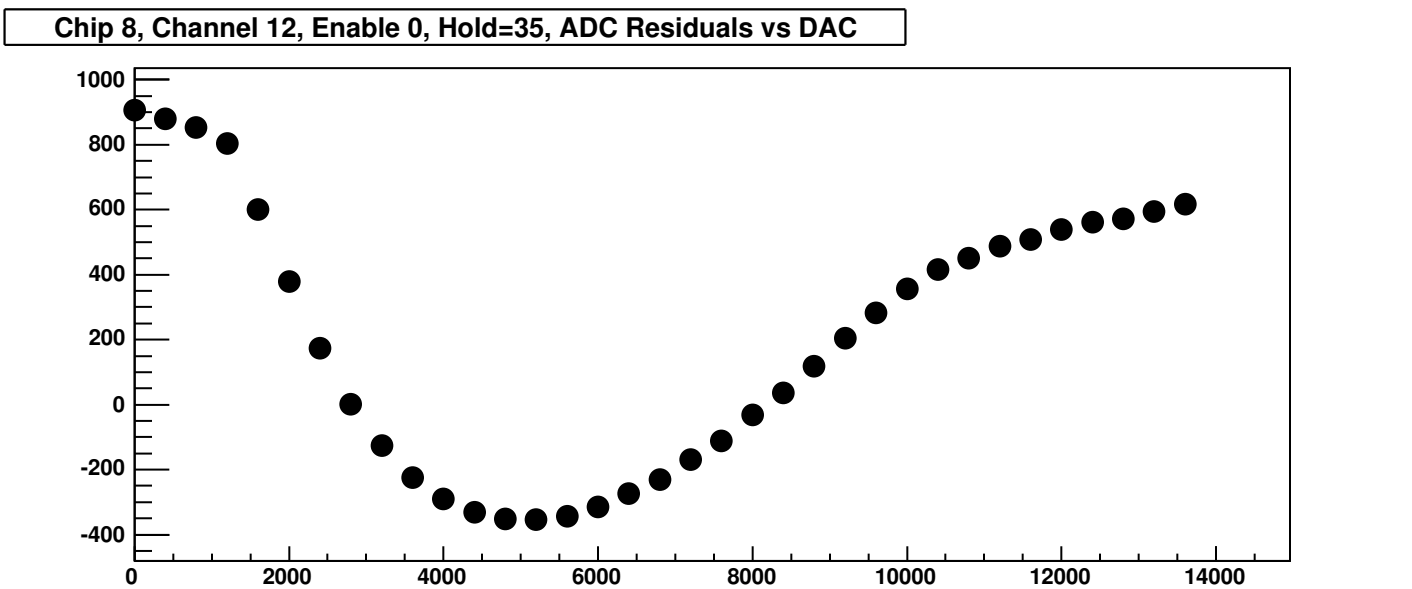

Chip 8, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2632 / 23
p0 -137.8 ± 1.089
p1 0.1513 ± 0.0001987
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

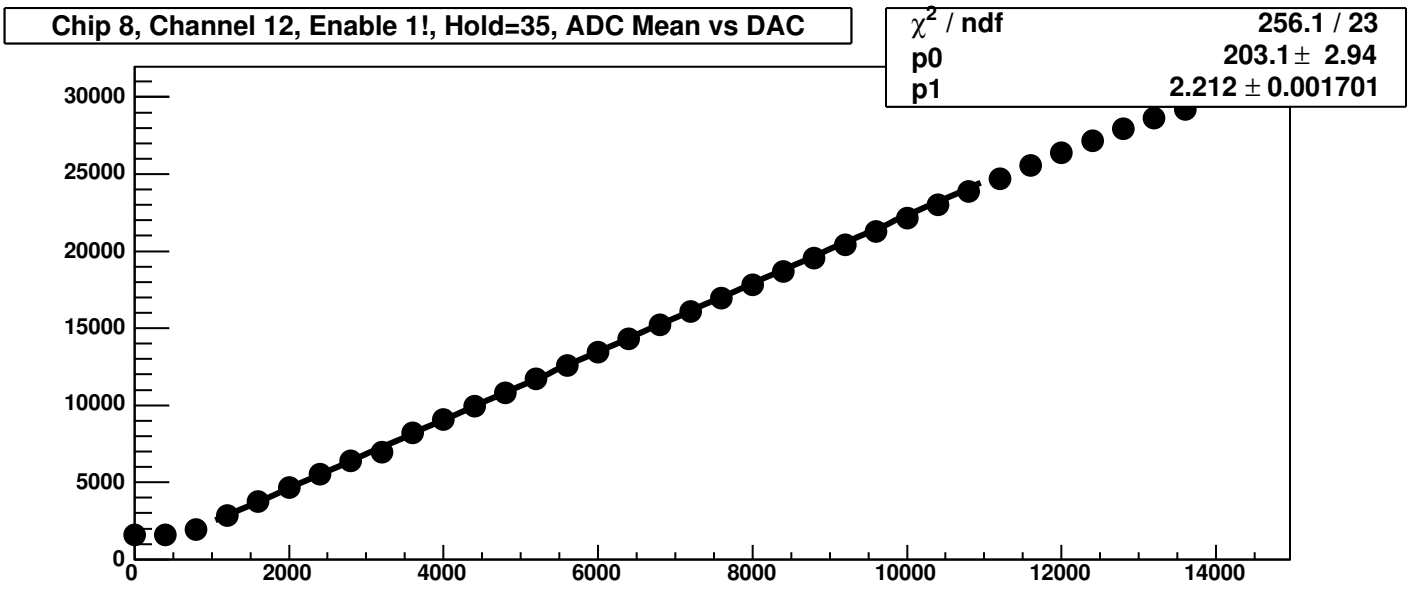

Chip 8, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

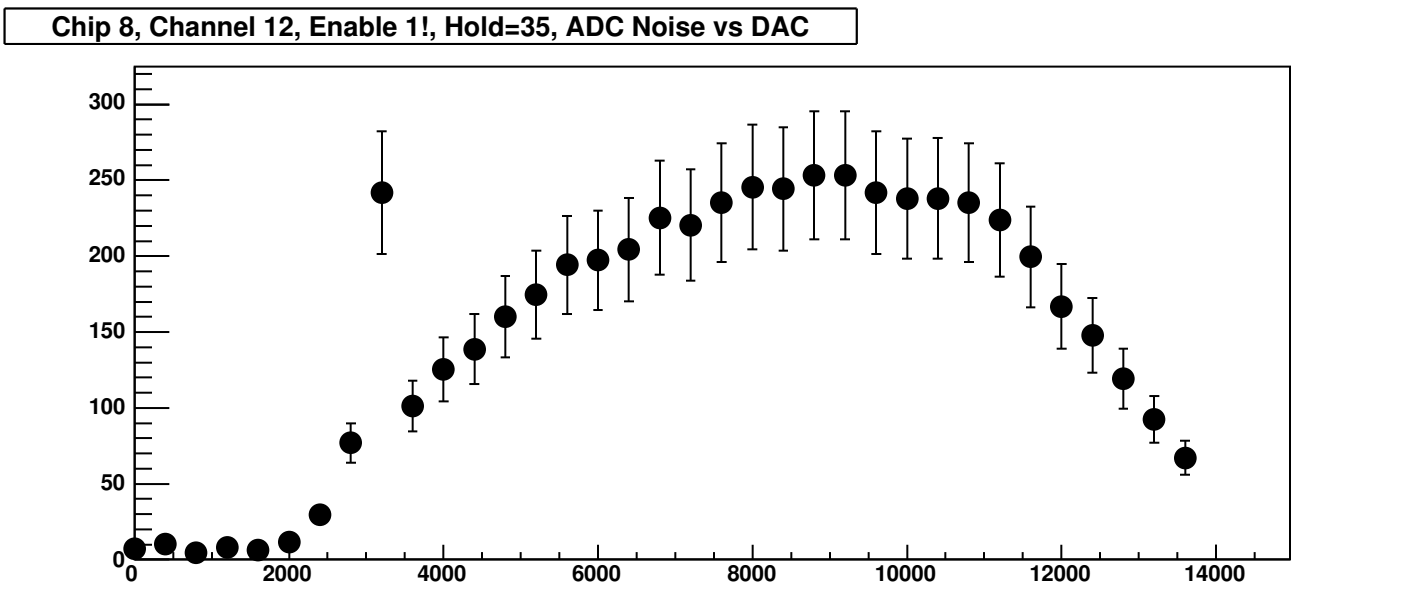

Chip 8, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

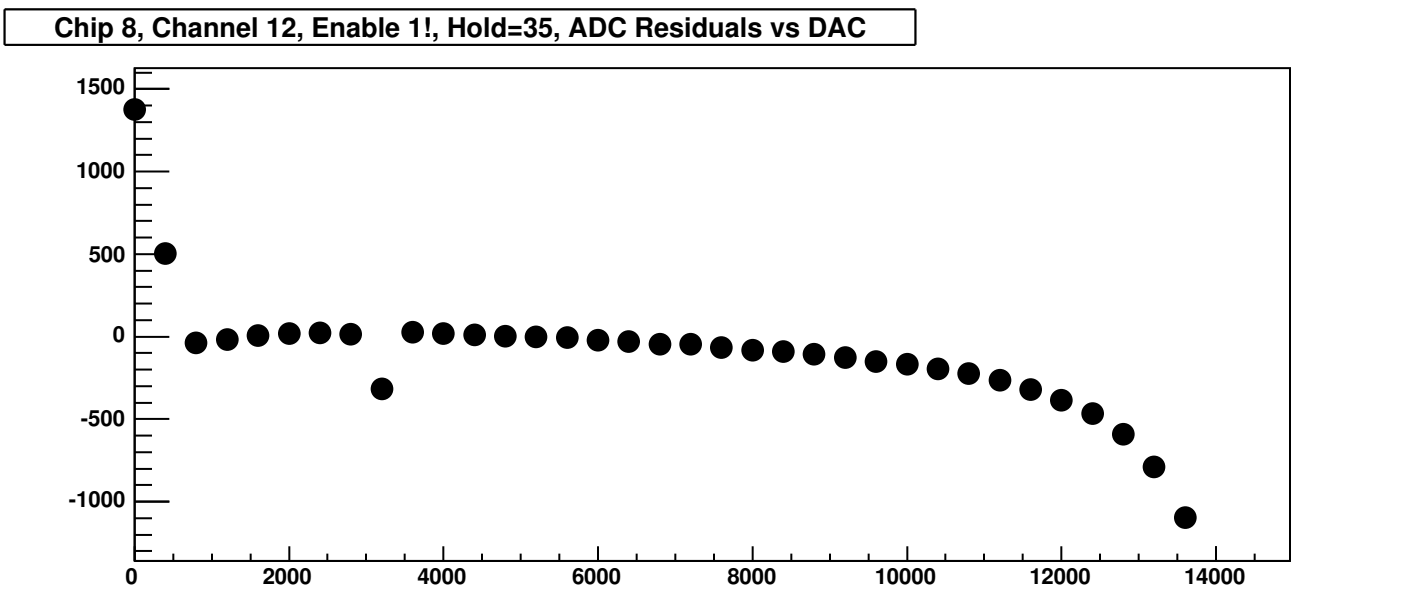

Chip 8, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.956e+05 / 23
p0 -729.6 ± 1.025
p1 0.08047 ± 0.0001693
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

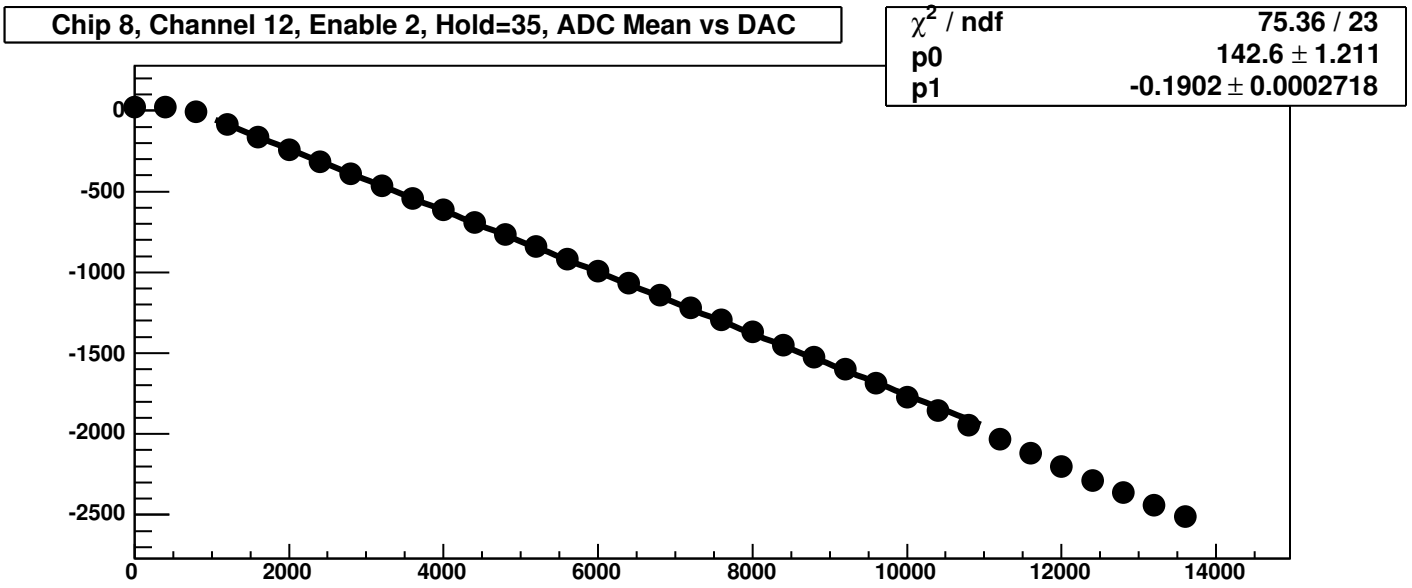

Chip 8, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

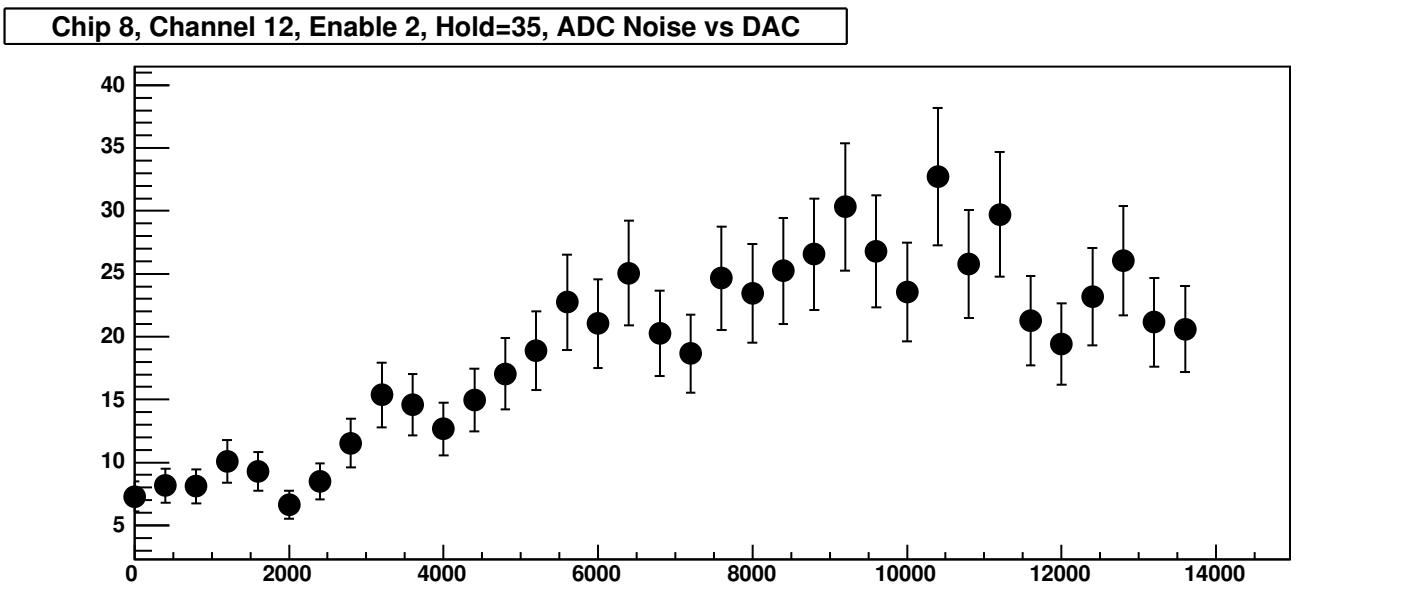

Chip 8, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

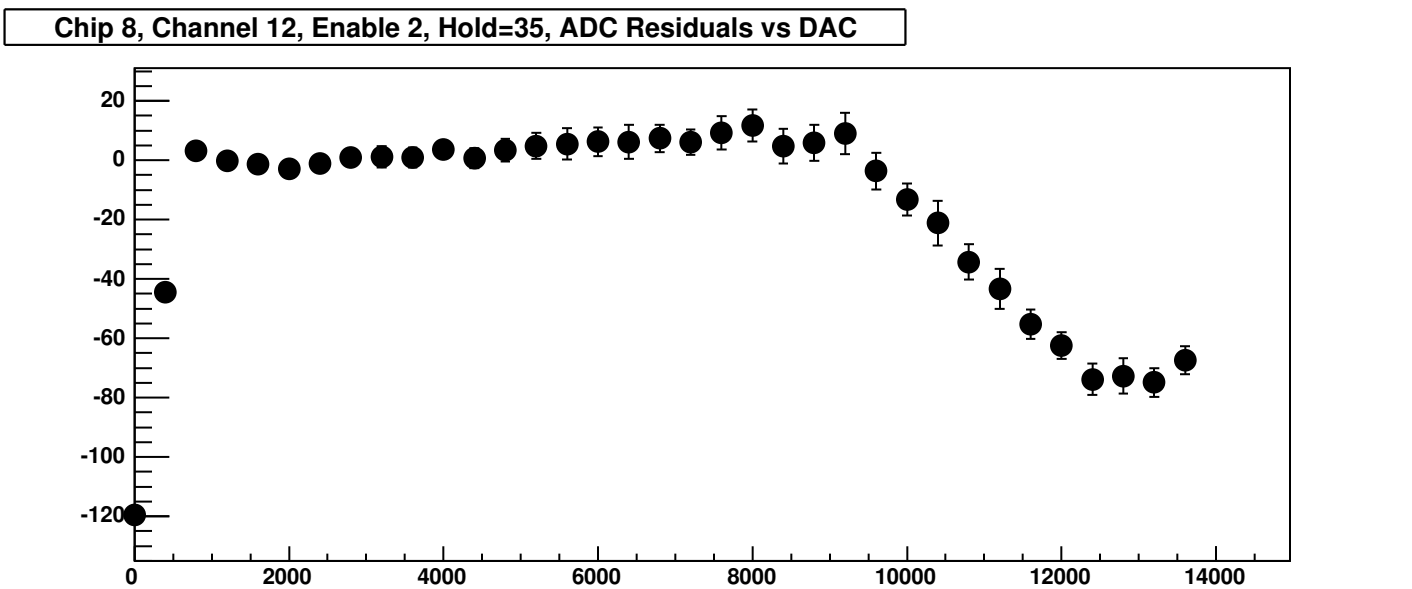

Chip 8, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 256.1 / 23
p0 203.1 ± 2.94
p1 2.212 ± 0.001701
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

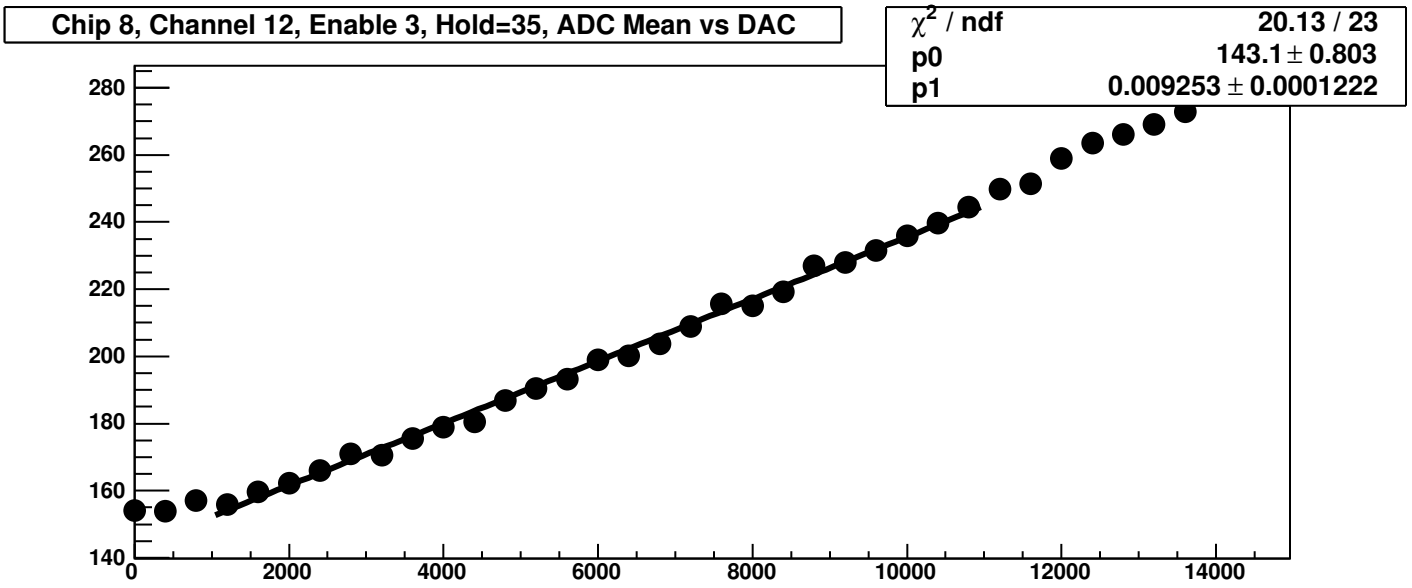

Chip 8, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

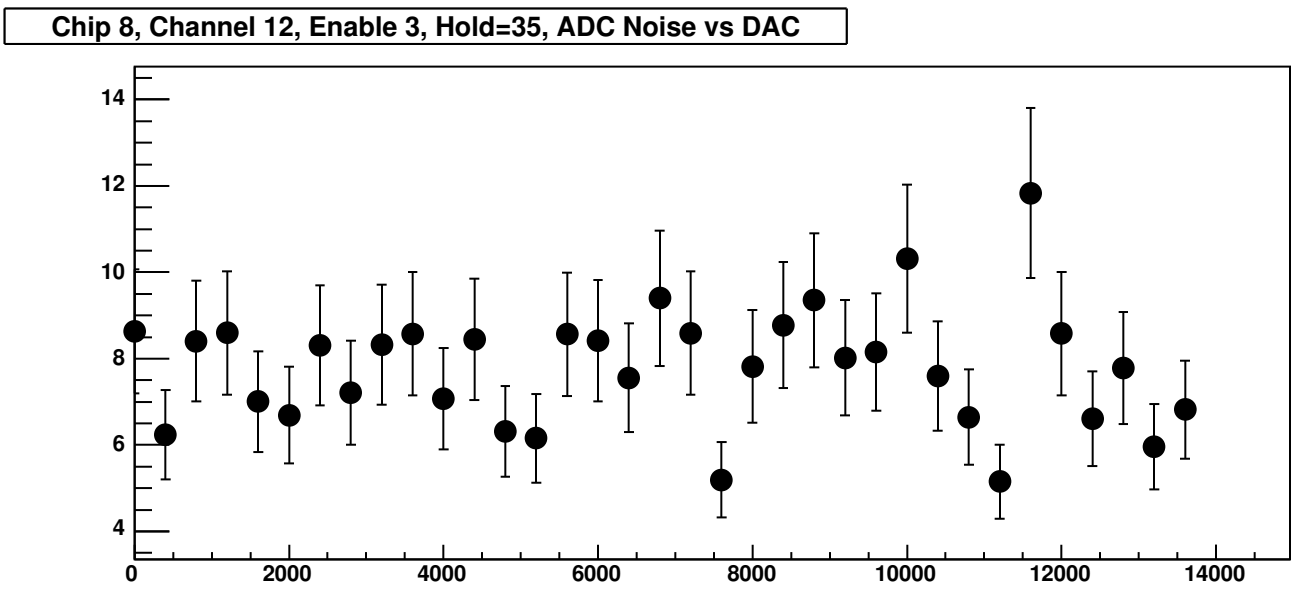

Chip 8, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

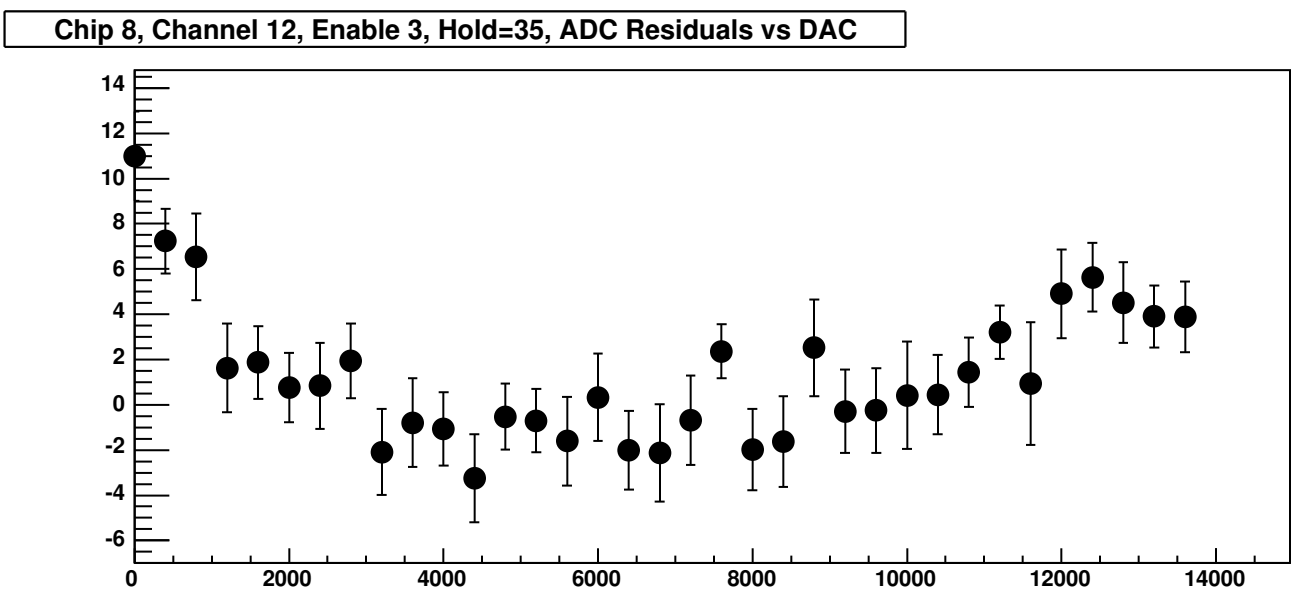

Chip 8, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 75.36 / 23
p0 142.6 ± 1.211
p1 -0.1902 ± 0.0002718
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

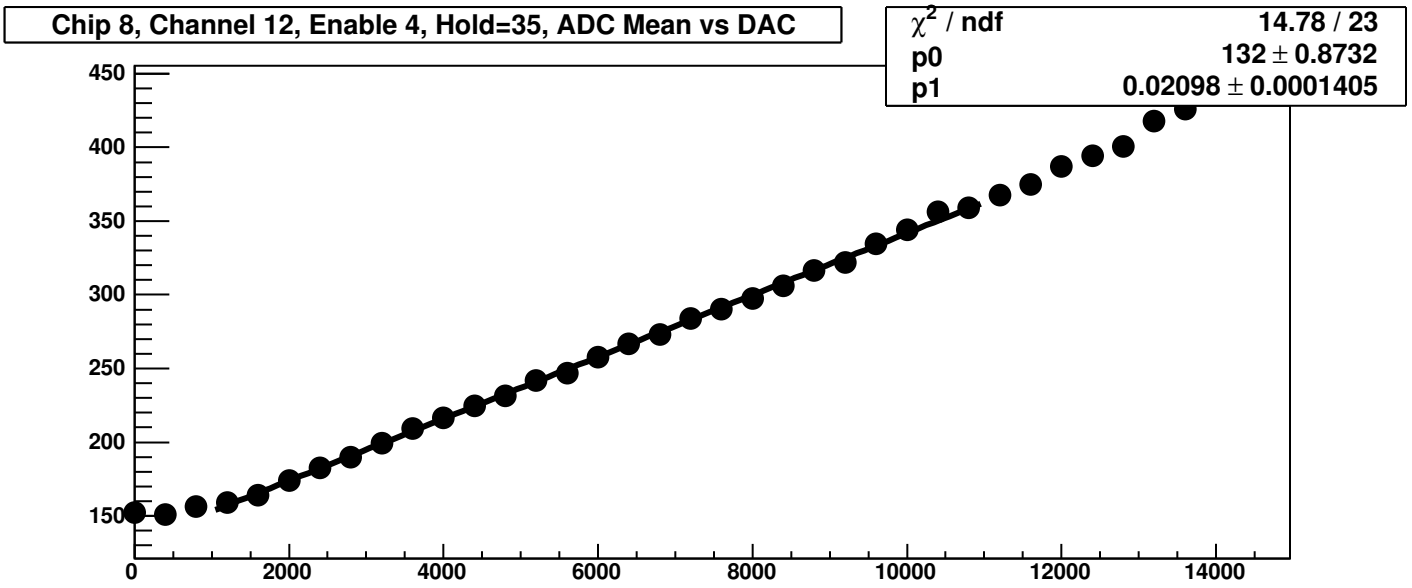

Chip 8, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

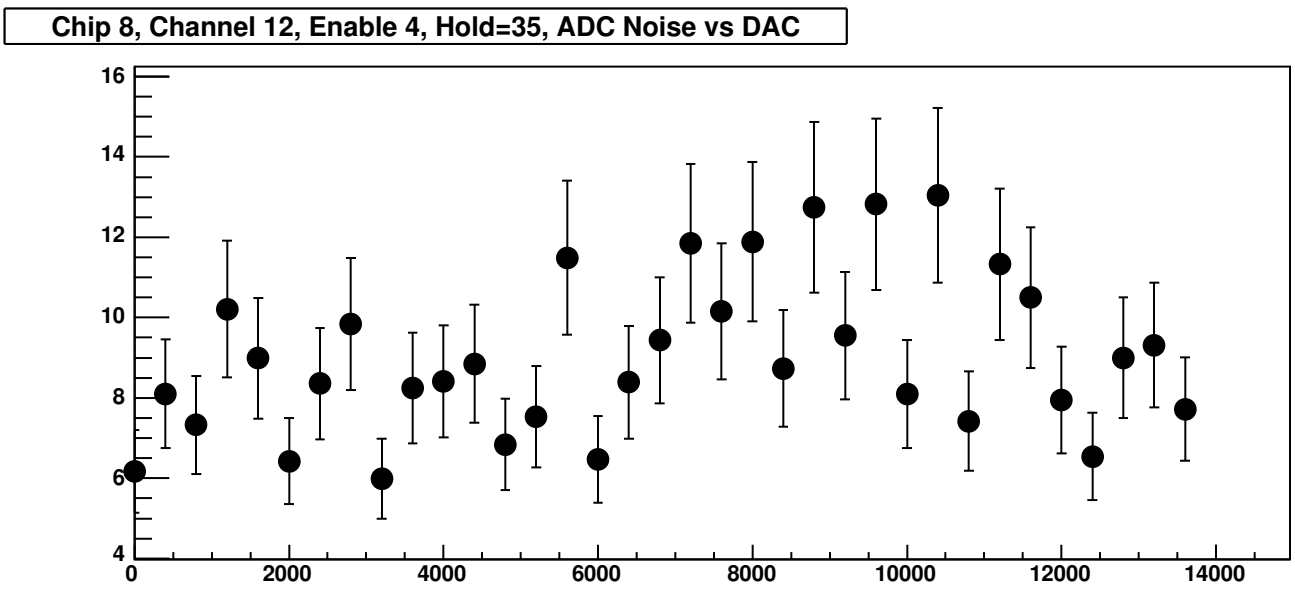

Chip 8, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
2000
4000
6000
8000
10000
12000
14000

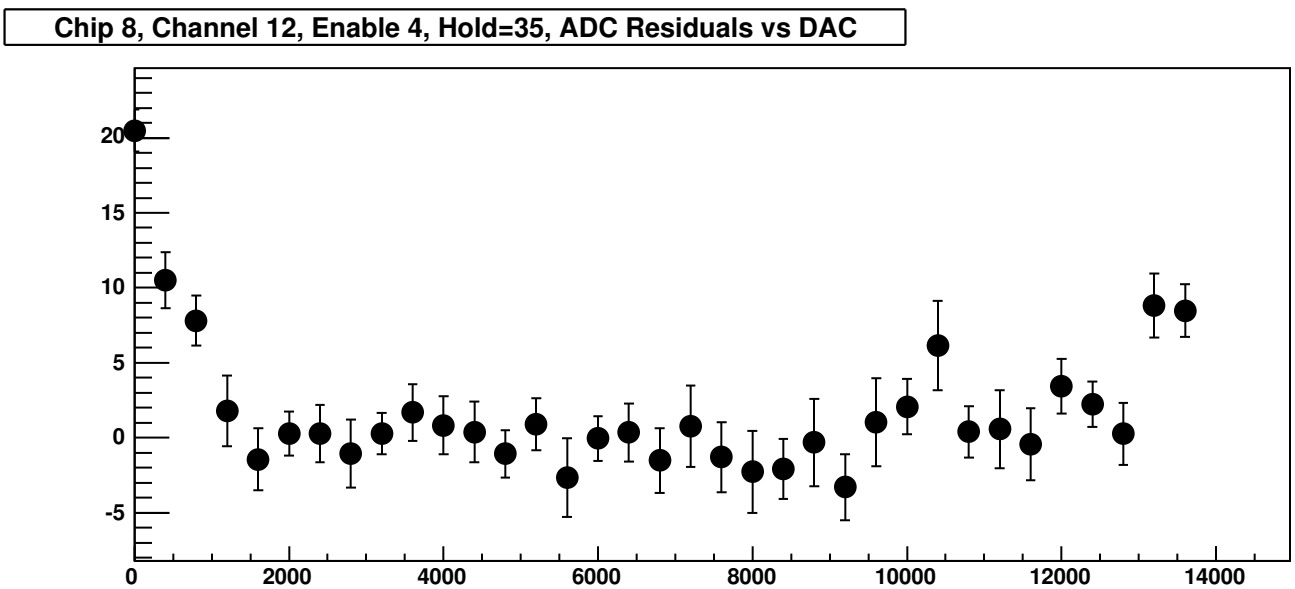

Chip 8, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 20.13 / 23
p0 143.1 ± 0.803
p1 0.009253 ± 0.0001222
280
260
240
220
200
180
160
140
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

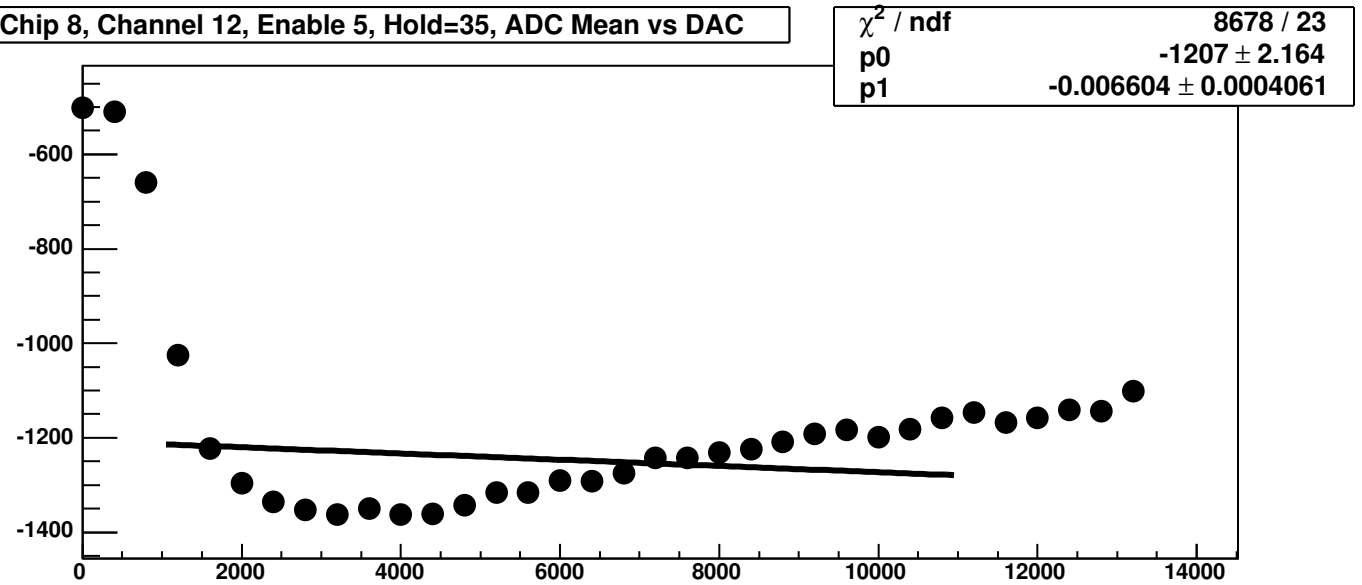

Chip 8, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 14.78 / 23
p0 132 ± 0.8732
p1 0.02098 ± 0.0001405
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

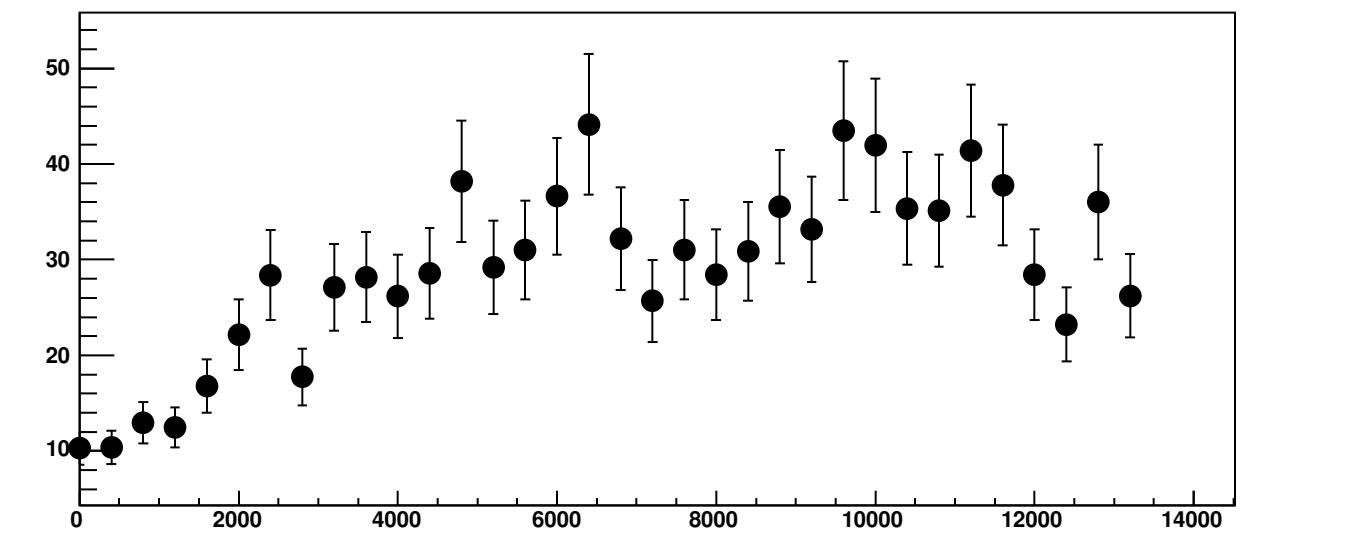

Chip 8, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

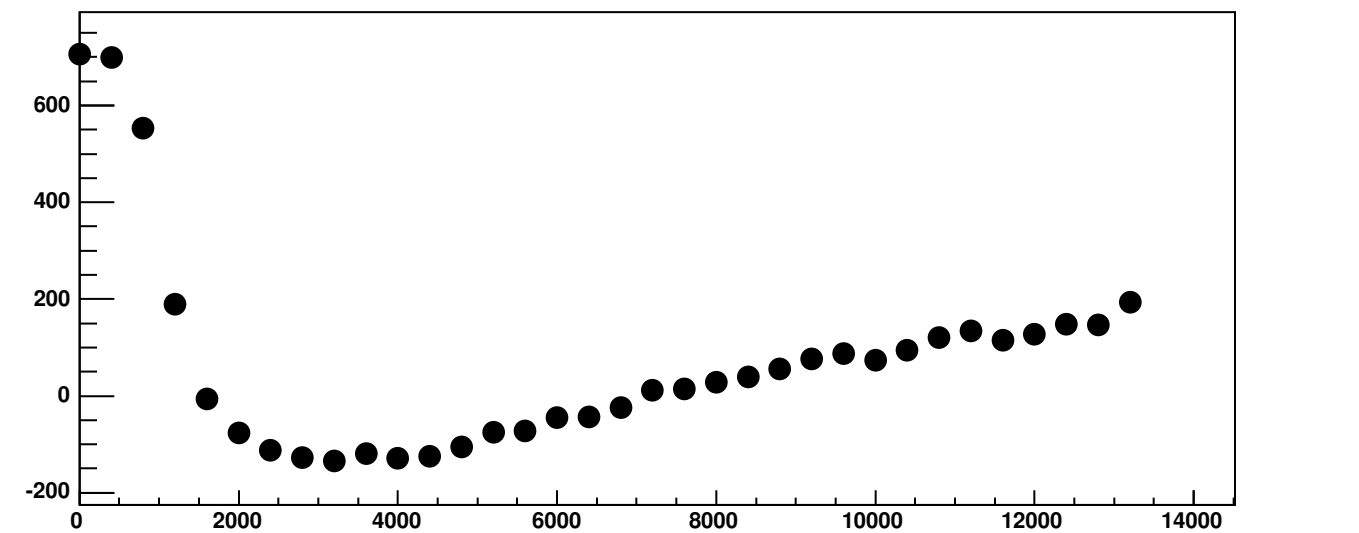
Chip 8, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

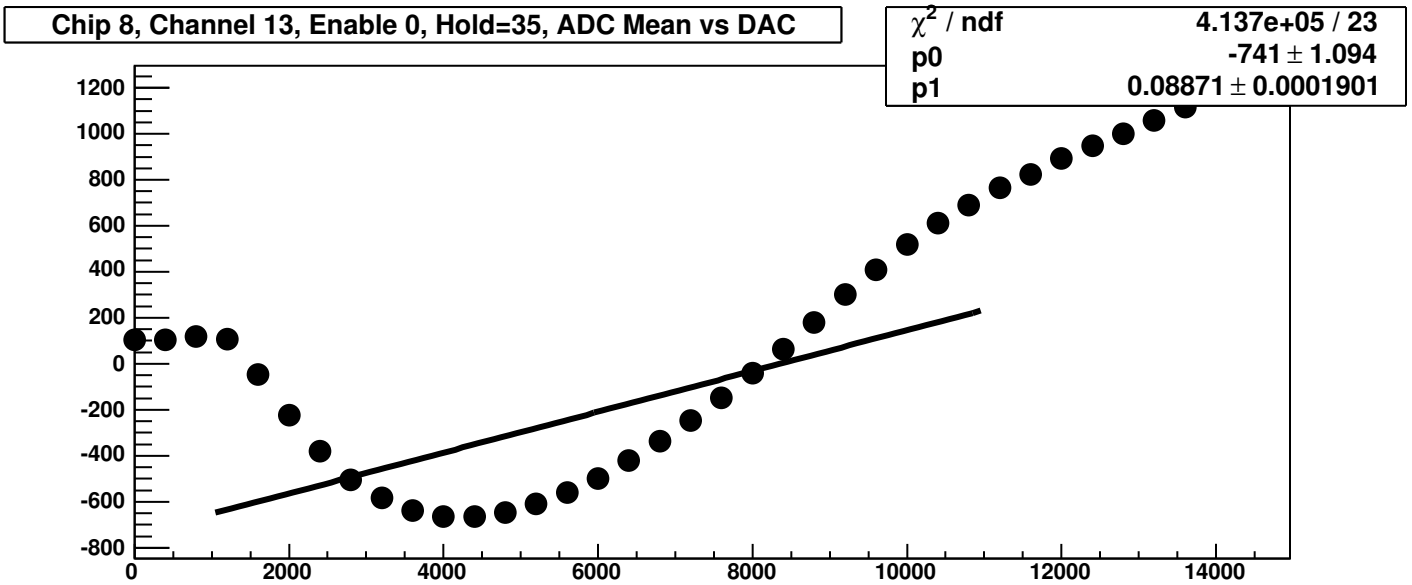


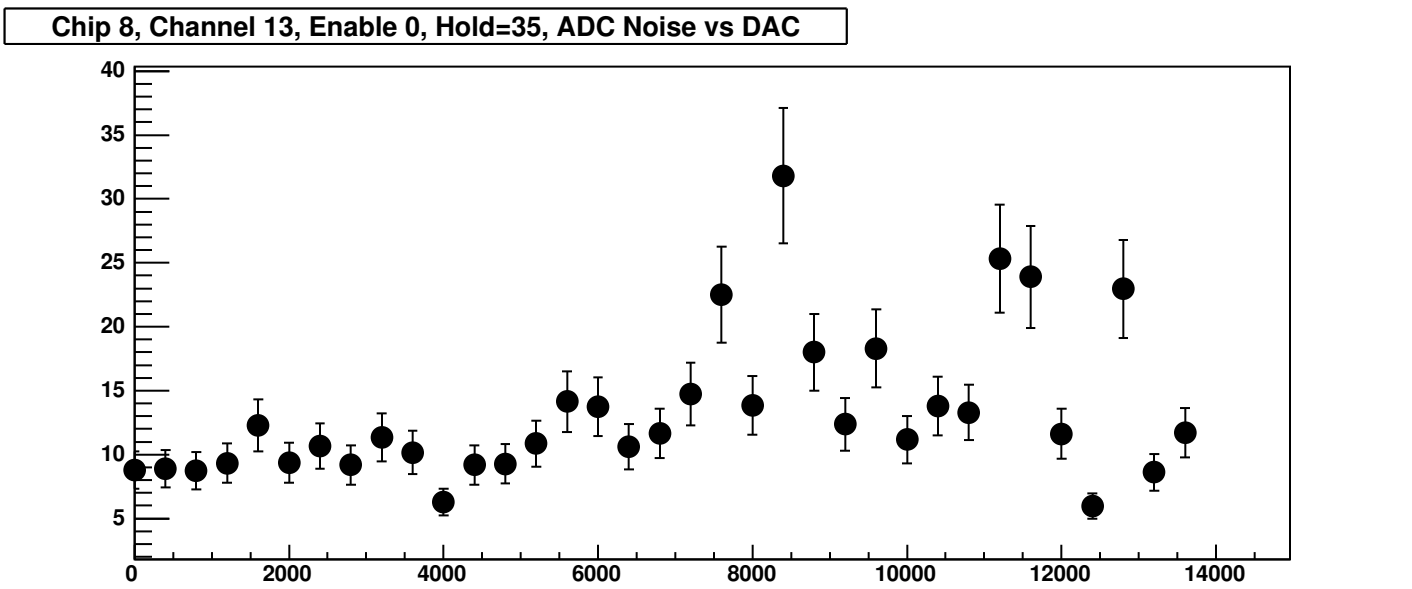
Chip 8, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

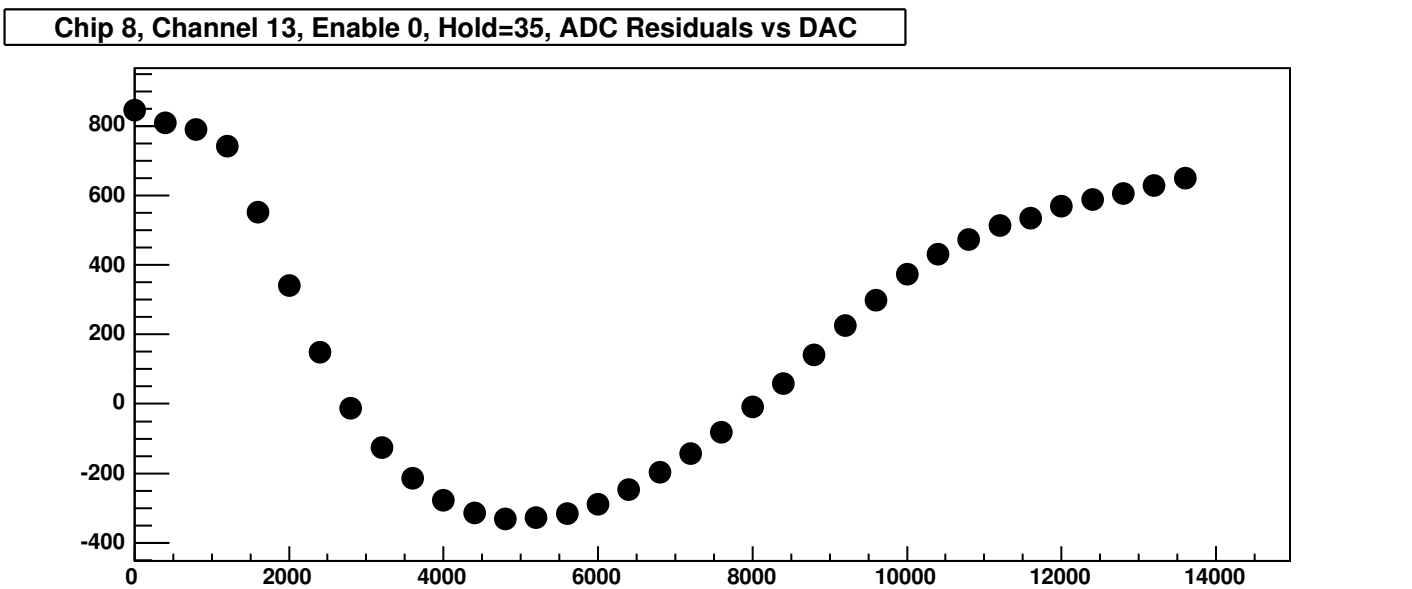


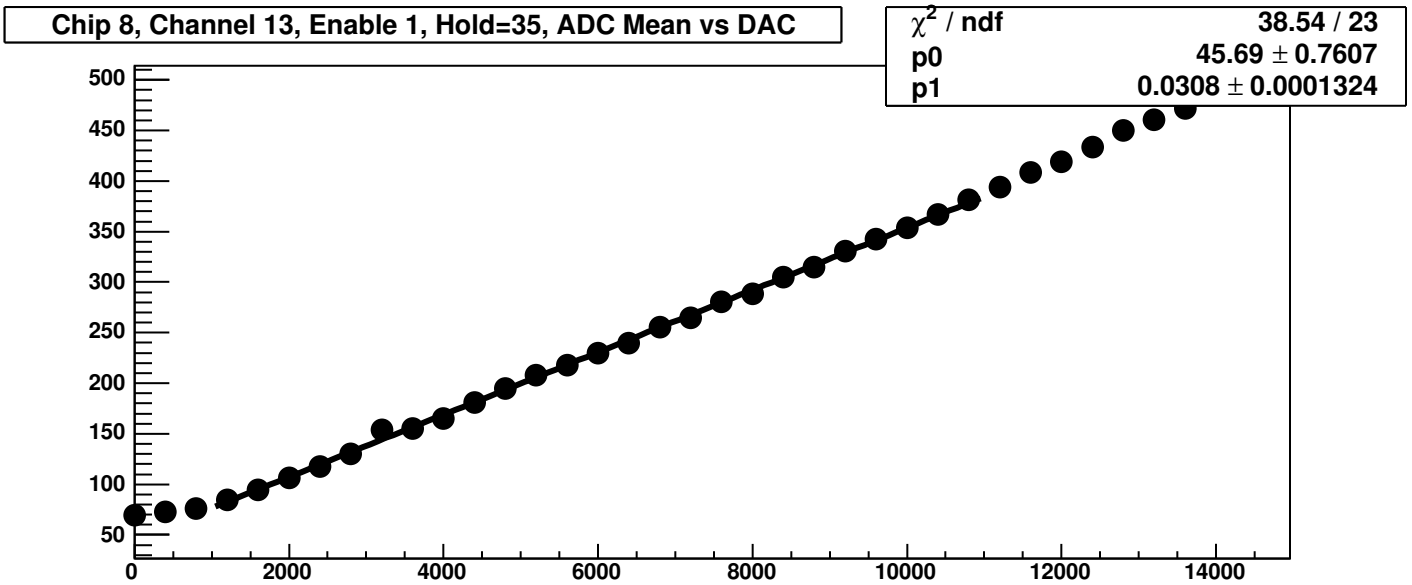
Chip 8, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

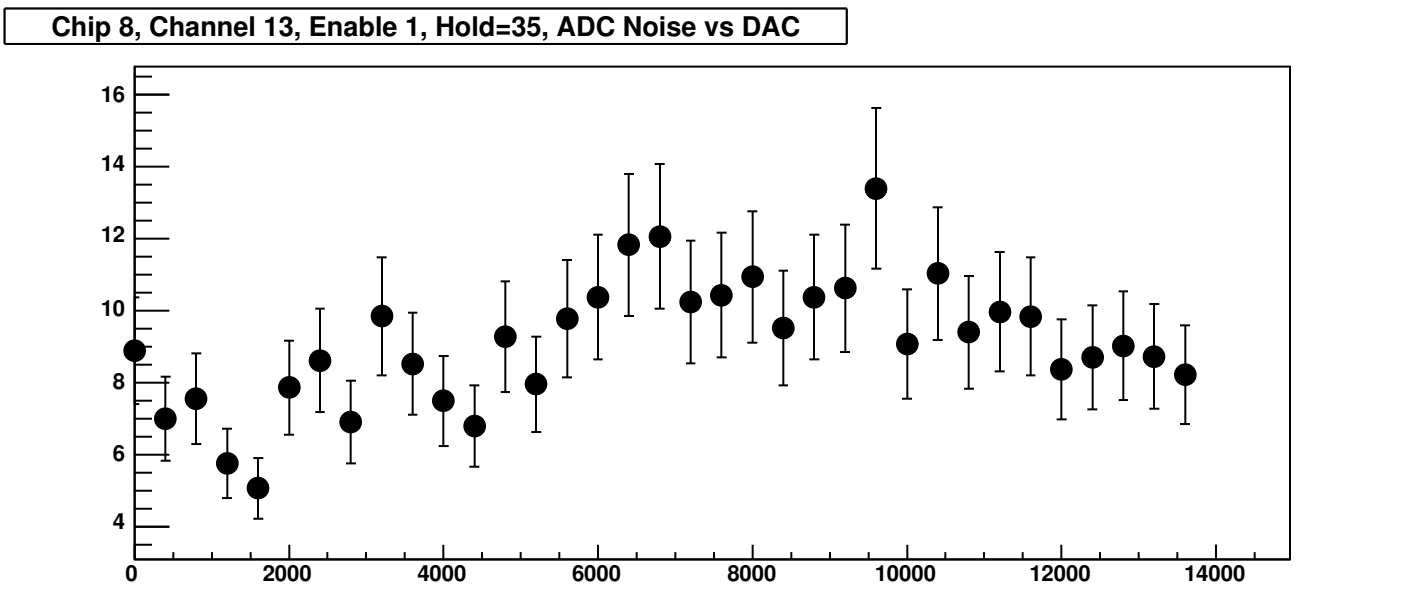


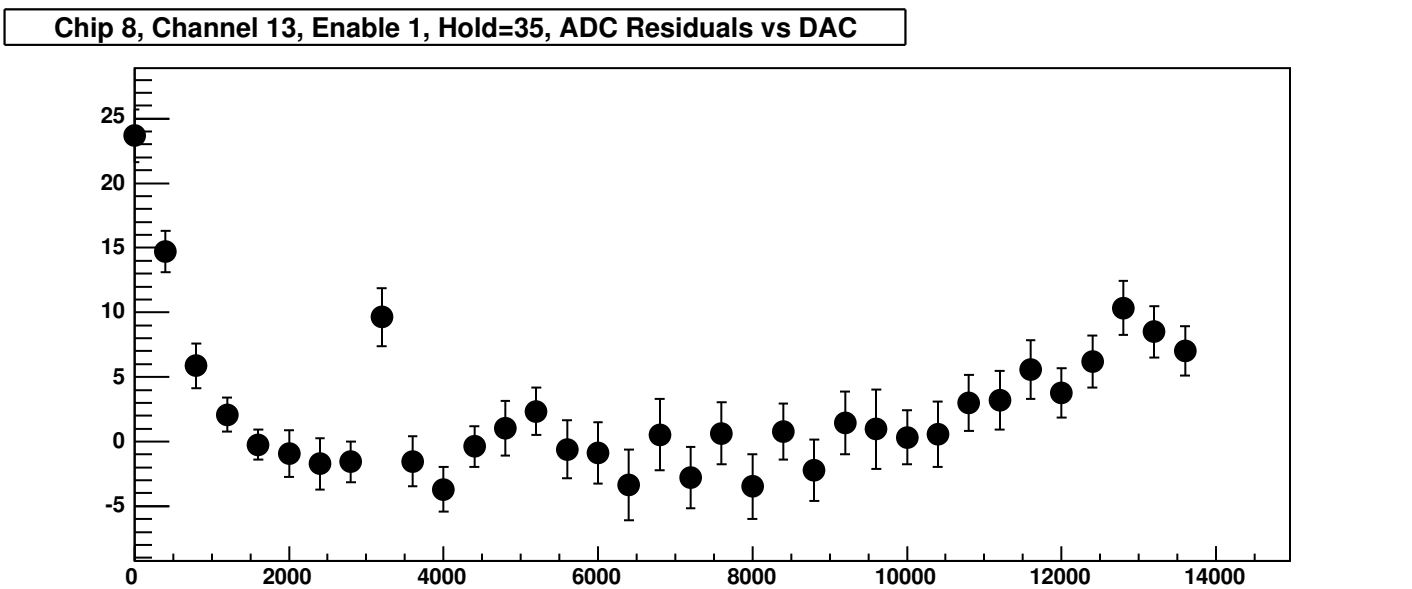

Chip 8, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.137e+05 / 23
p0 -741 ± 1.094
p1 0.08871 ± 0.0001901
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

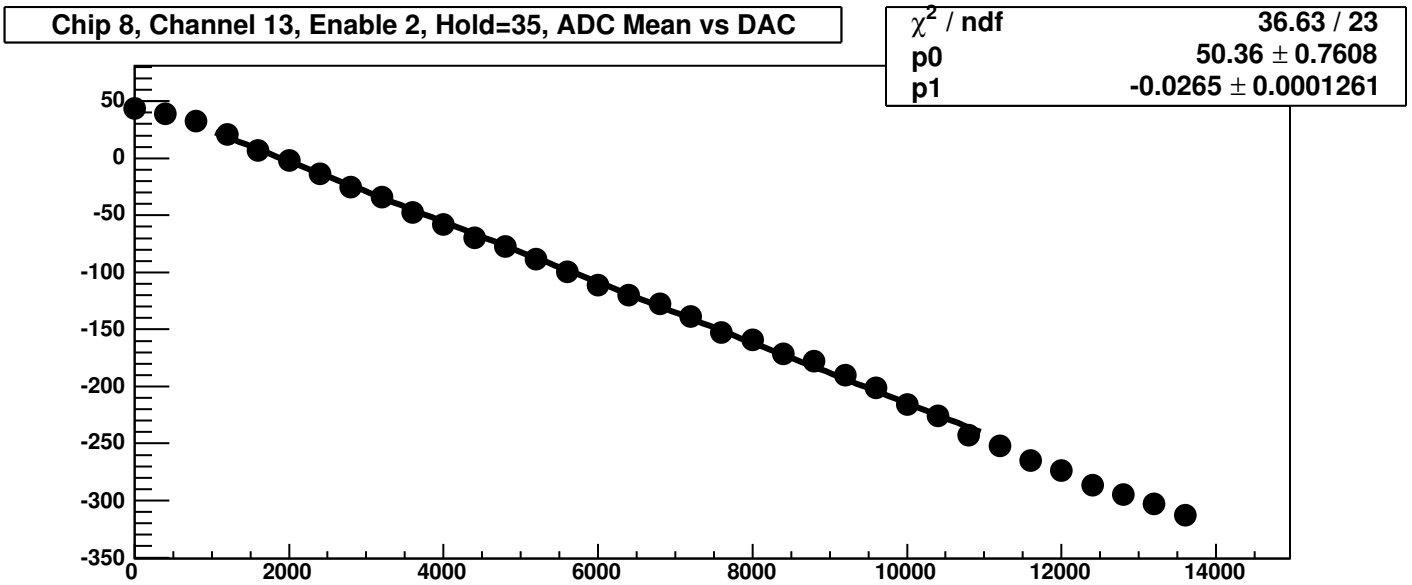

Chip 8, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

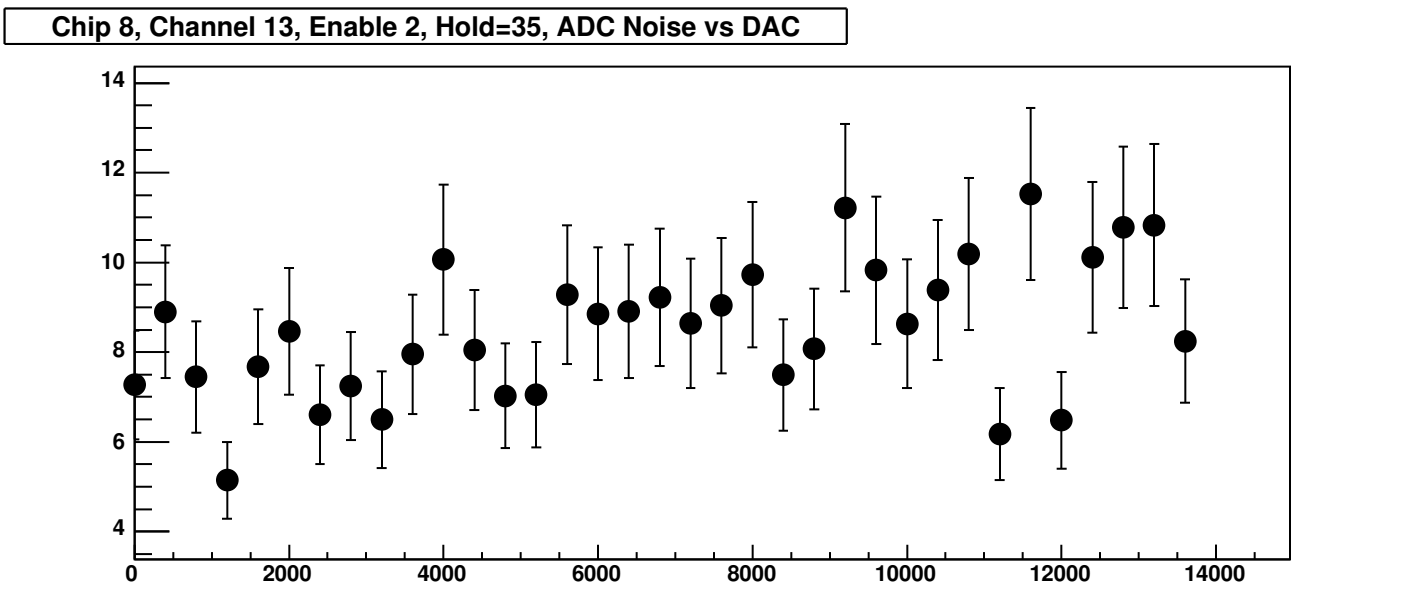

Chip 8, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

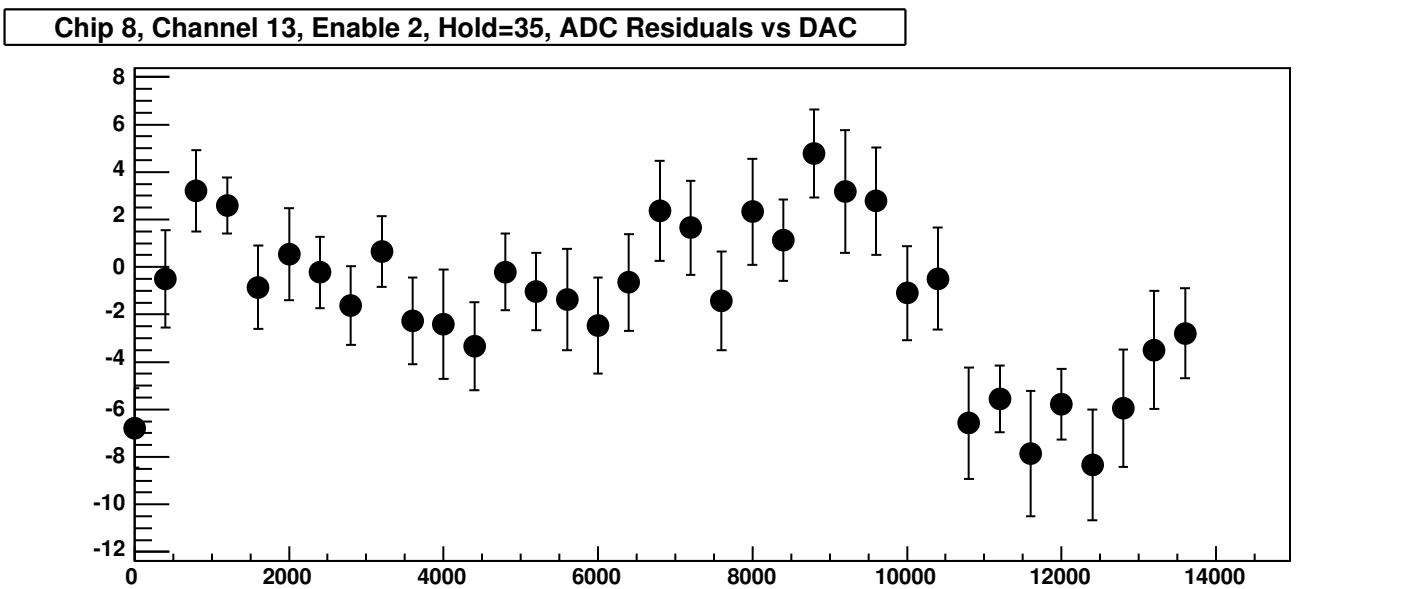

Chip 8, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 38.54 / 23
p0 45.69 ± 0.7607
p1 0.0308 ± 0.0001324
500
450
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

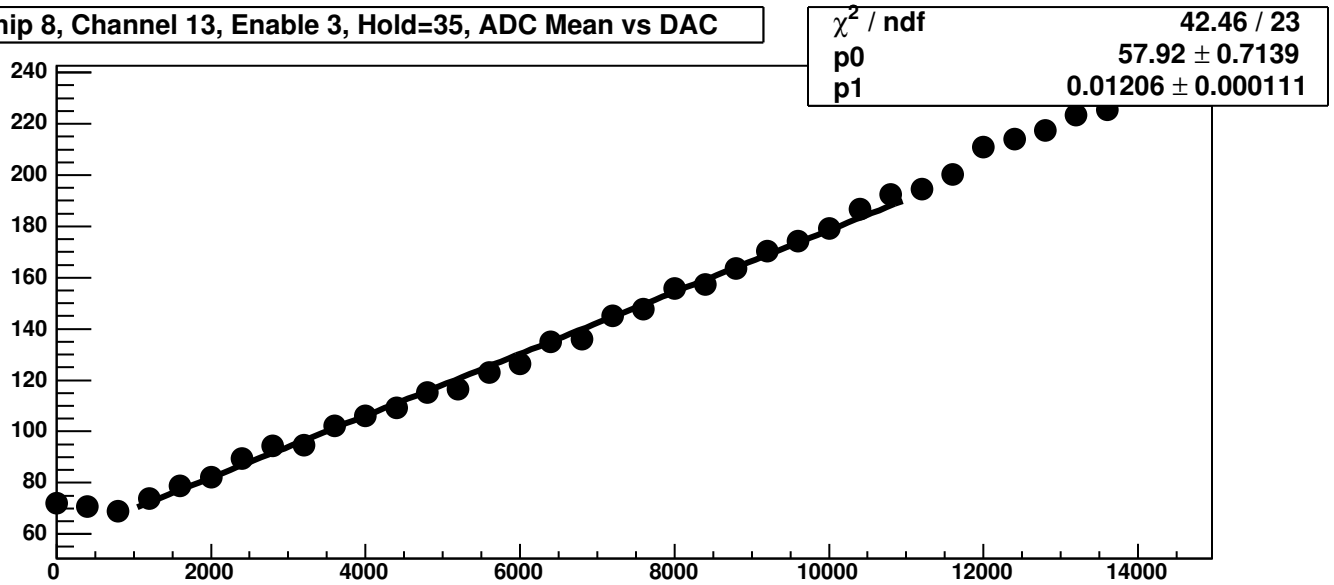

Chip 8, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 36.63 / 23
p0 50.36 ± 0.7608
p1 -0.0265 ± 0.0001261
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

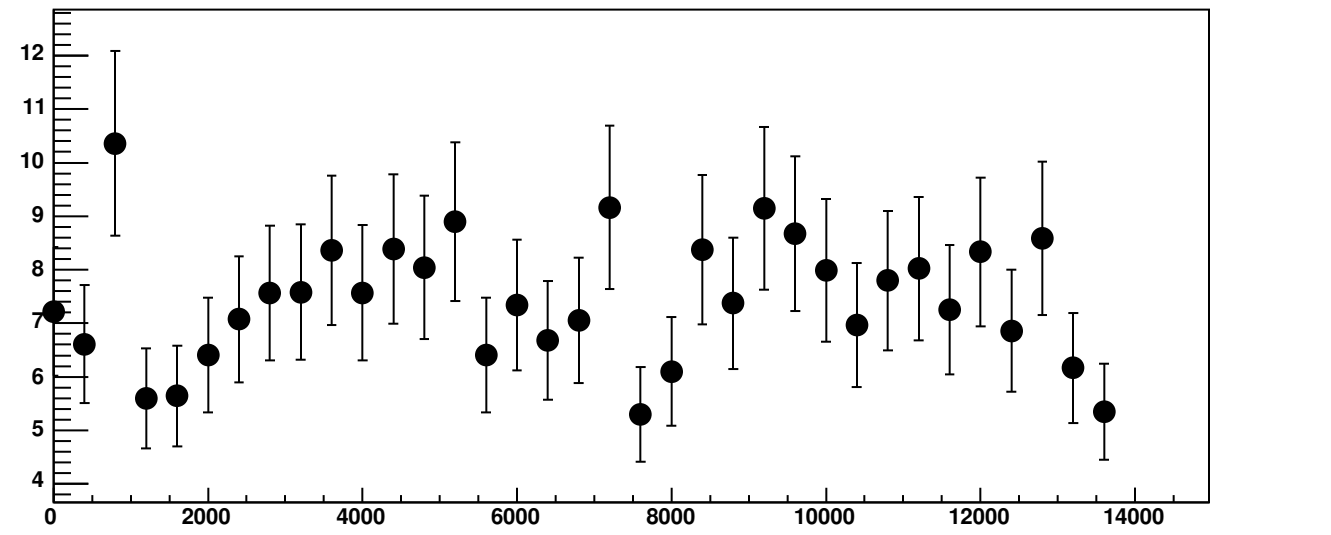

Chip 8, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
0
2000
4000
6000
8000
10000
12000
14000

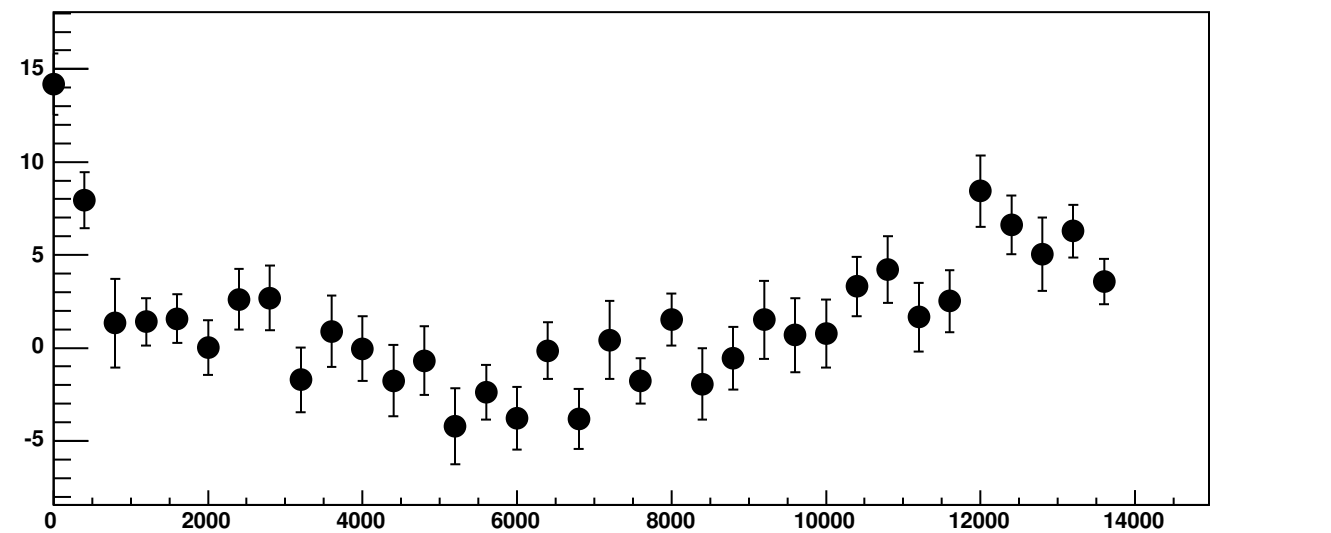
Chip 8, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

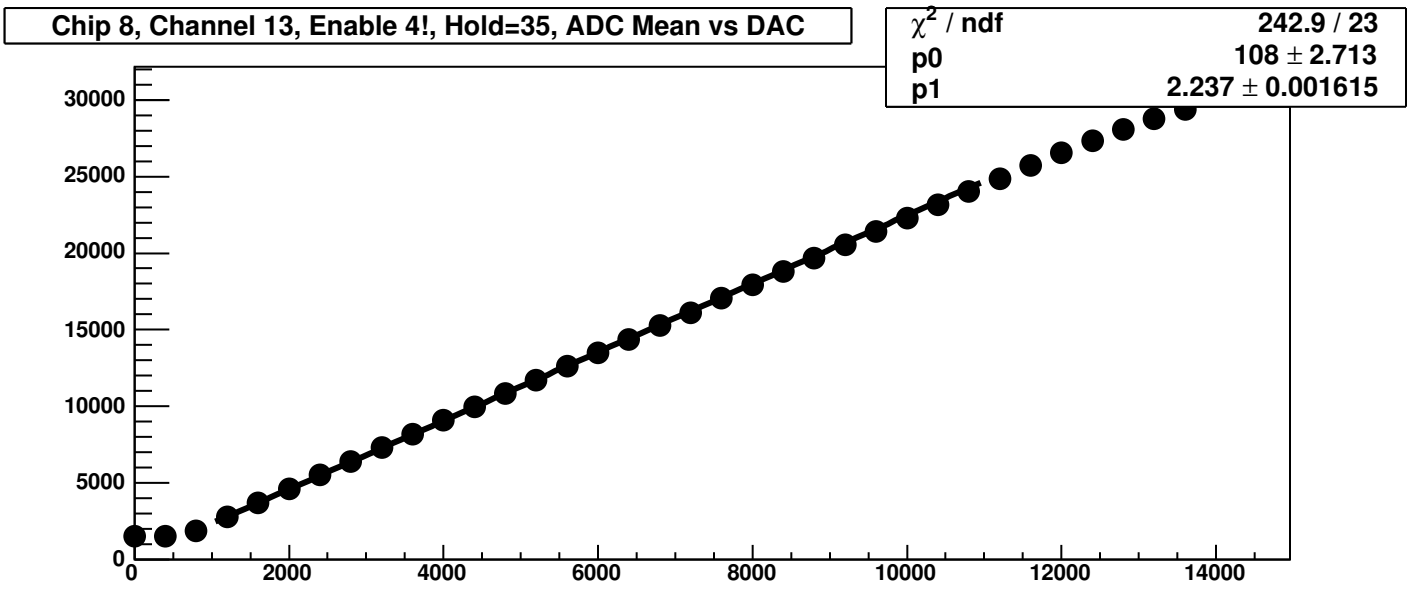


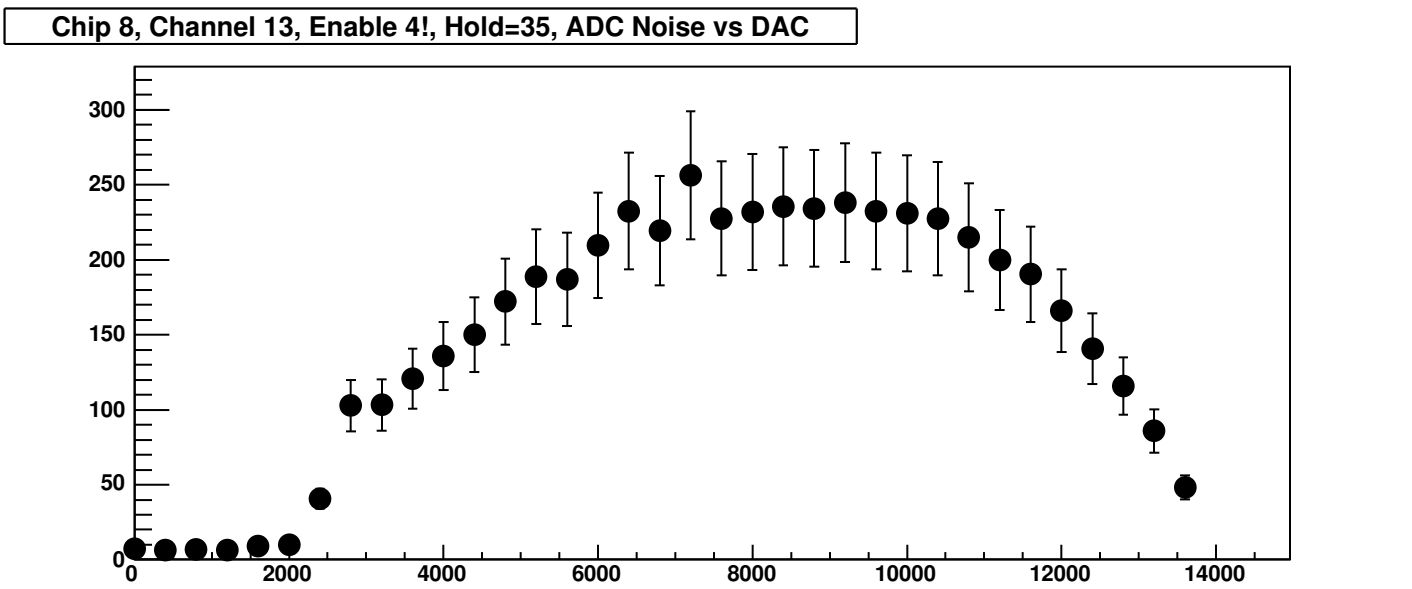
Chip 8, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

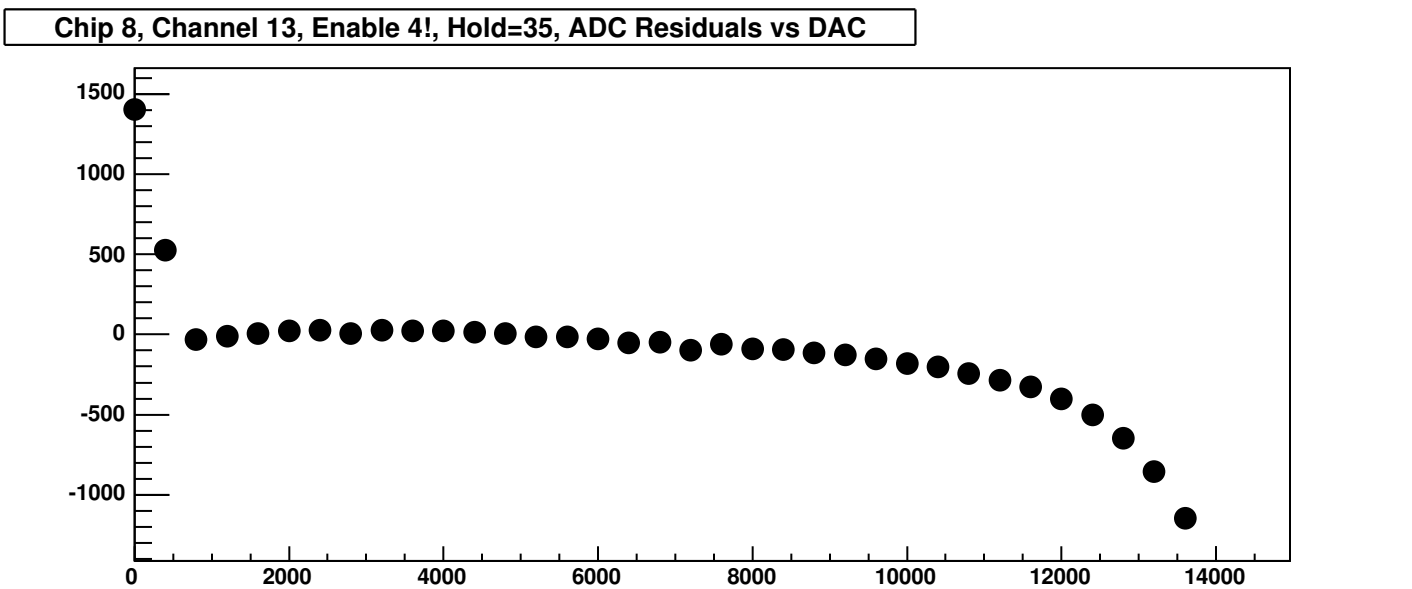


Chip 8, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

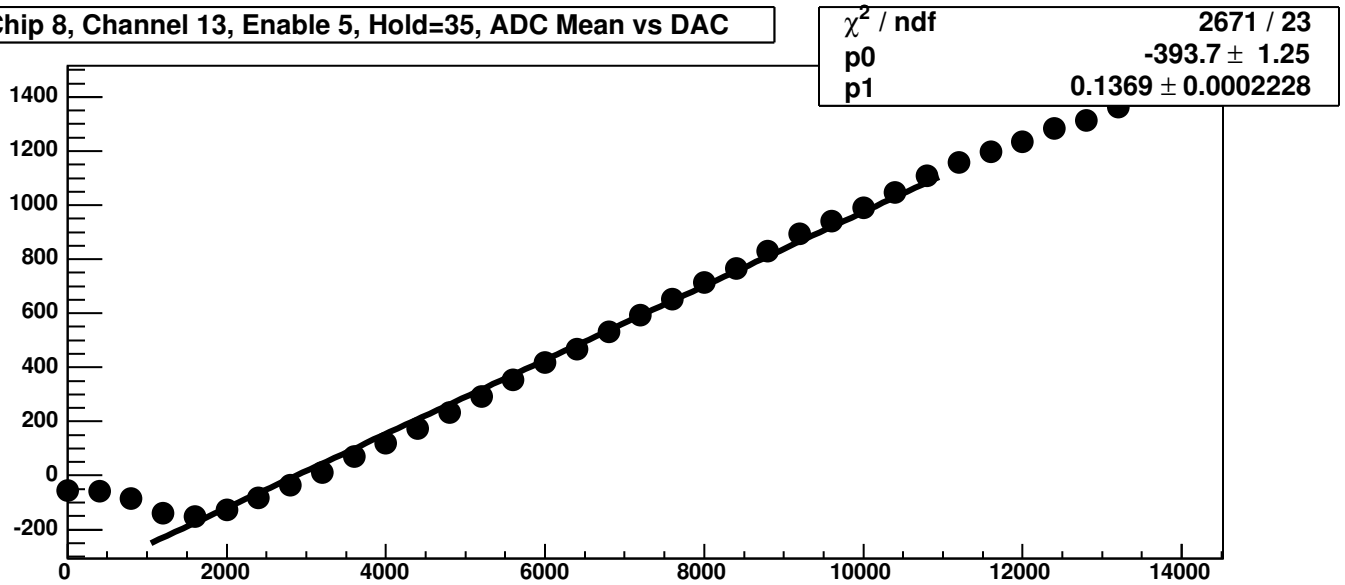



Chip 8, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 242.9 / 23
p0 108 ± 2.713
p1 2.237 ± 0.001615
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

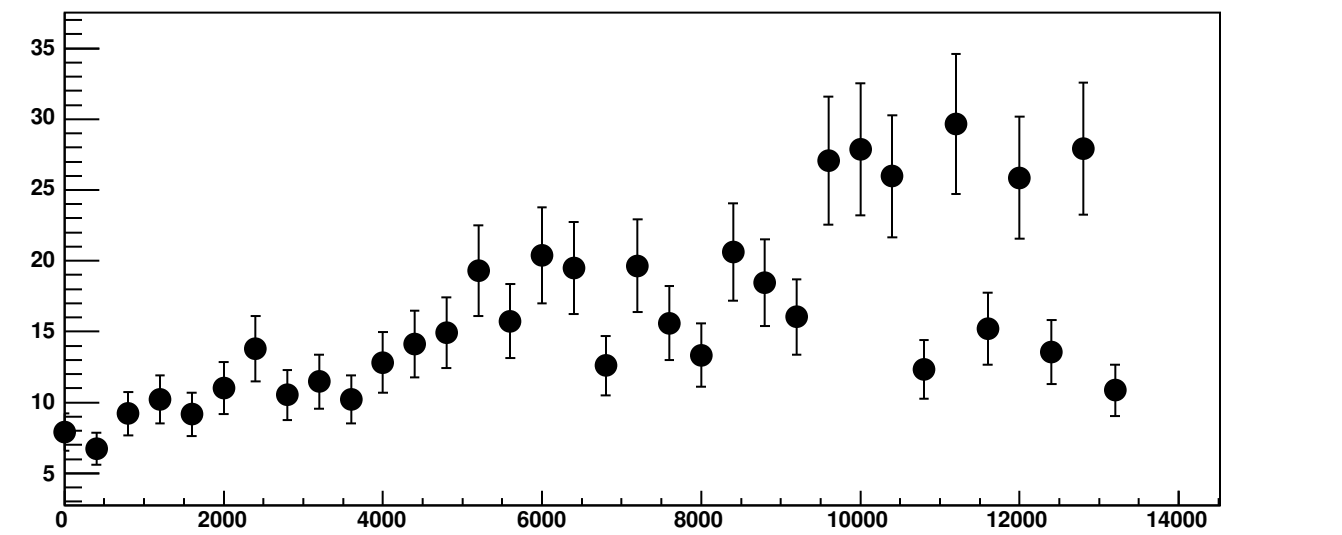

Chip 8, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

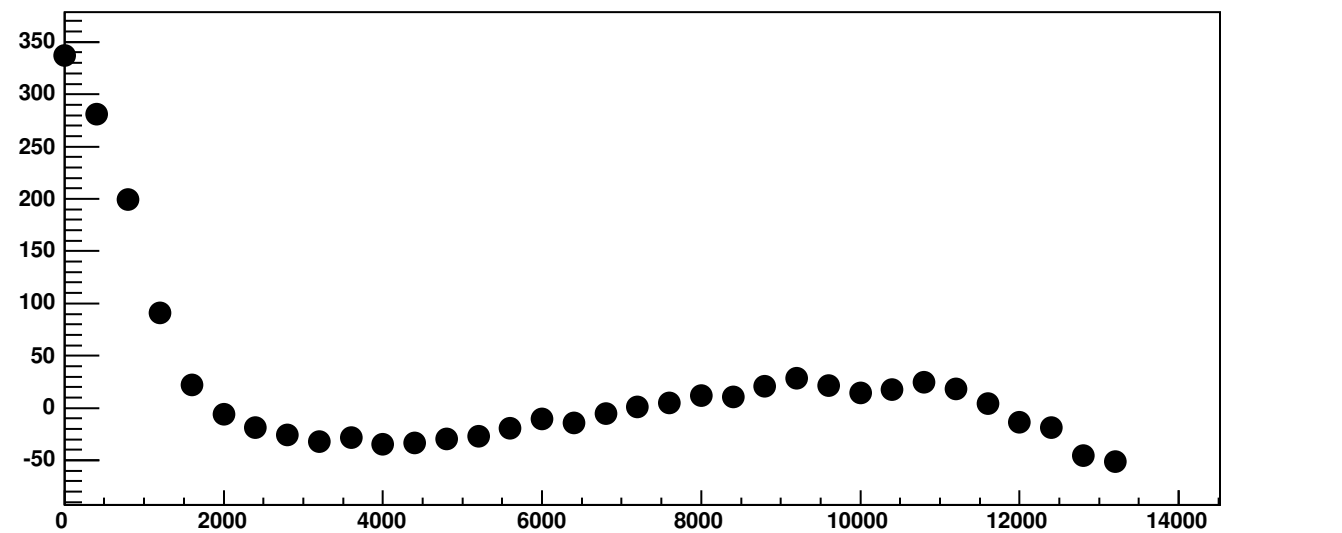
Chip 8, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



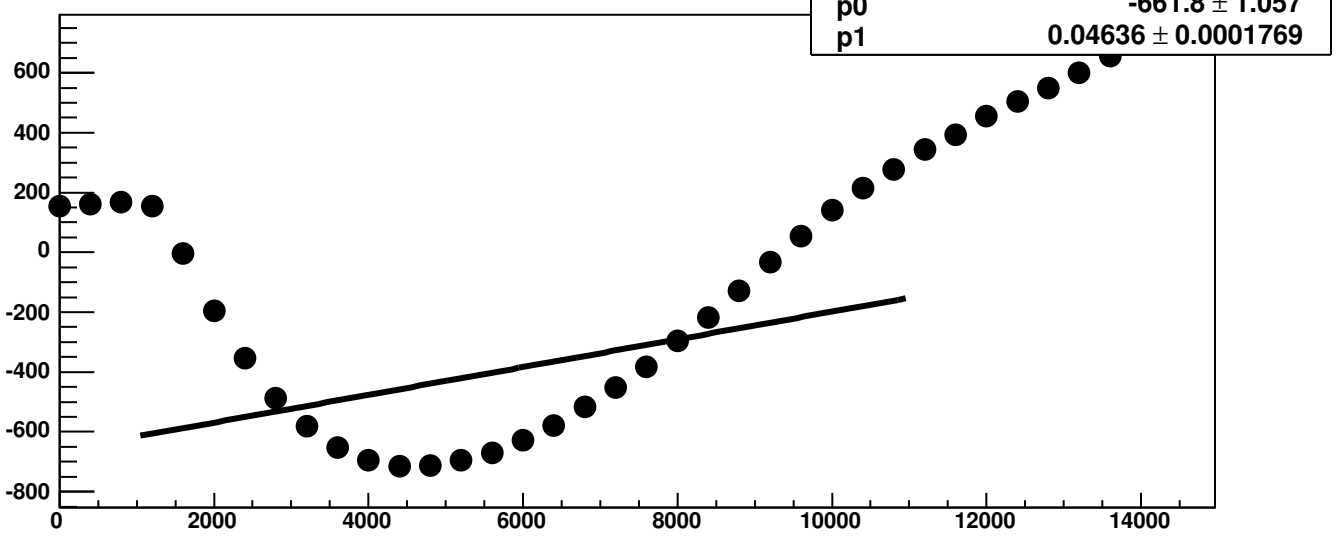
Chip 8, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC



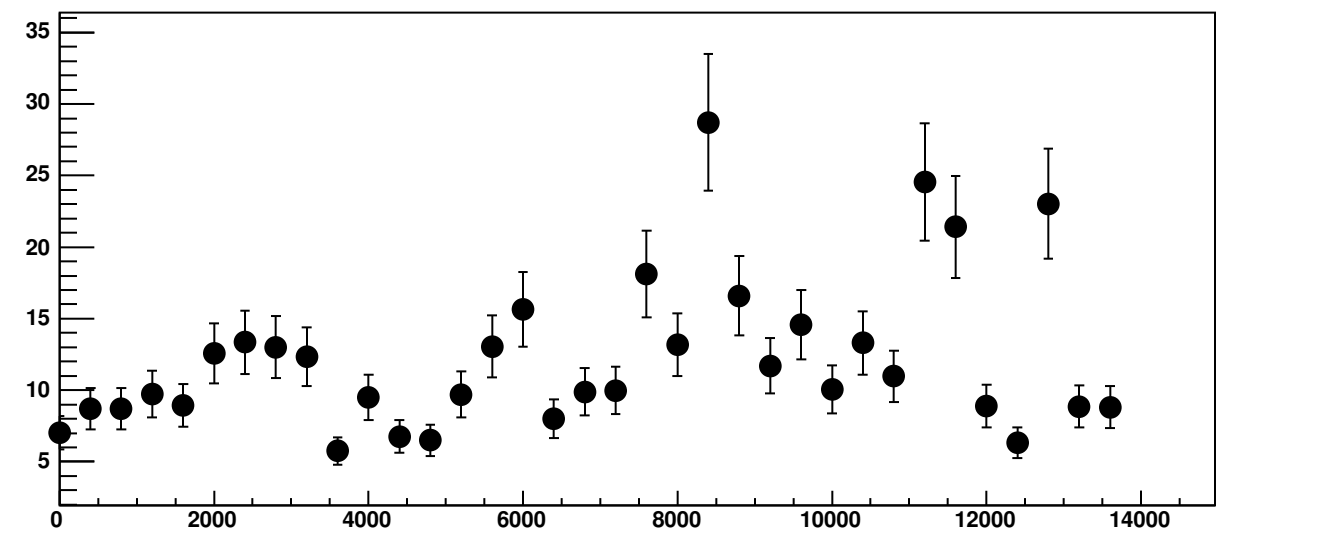
Chip 8, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC



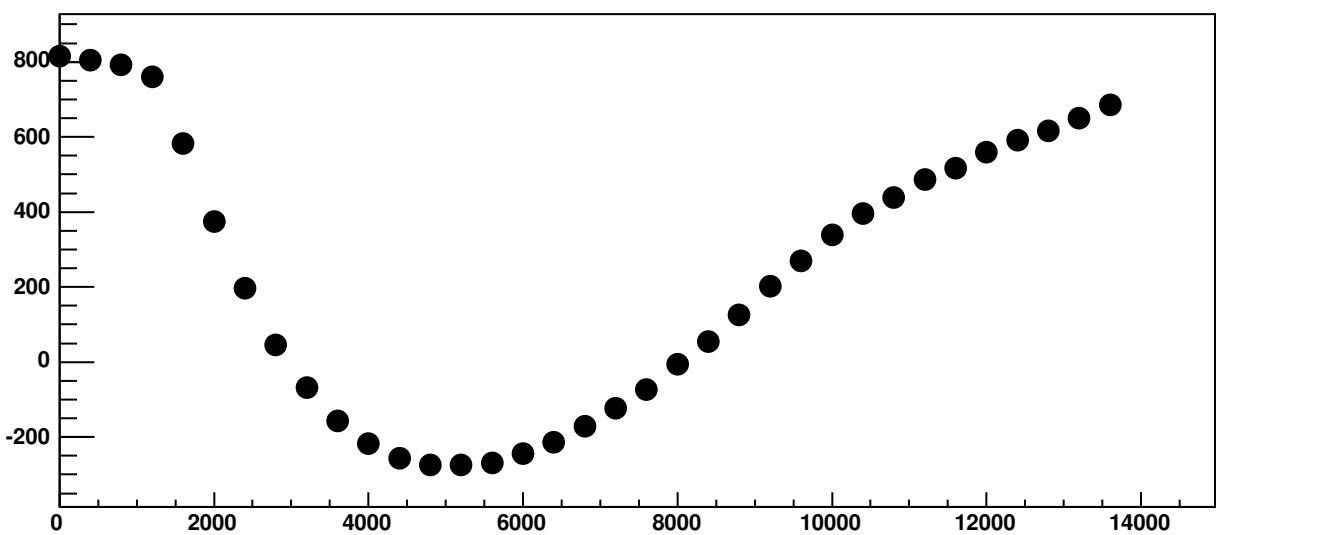
Chip 8, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

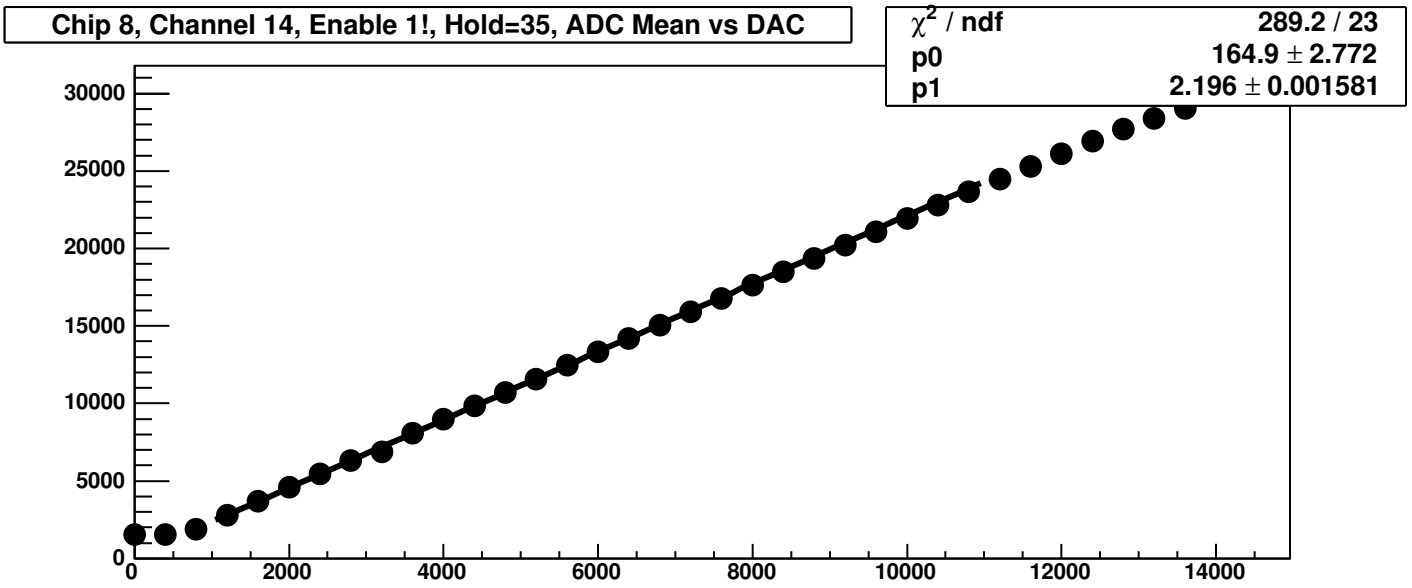


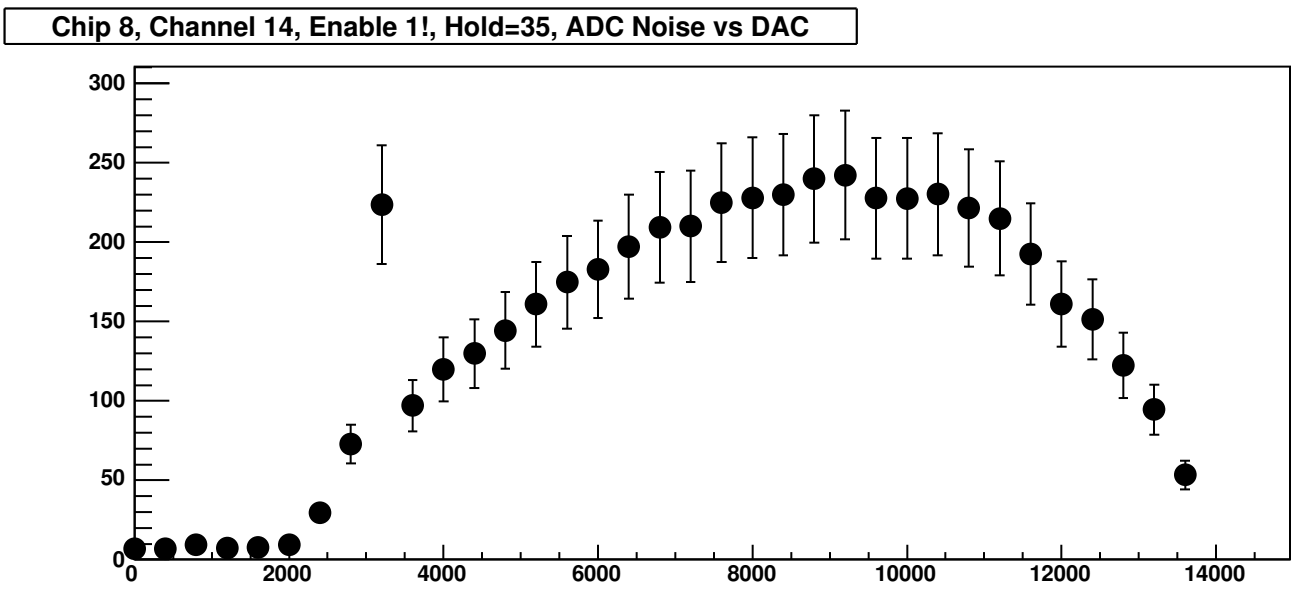
Chip 8, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

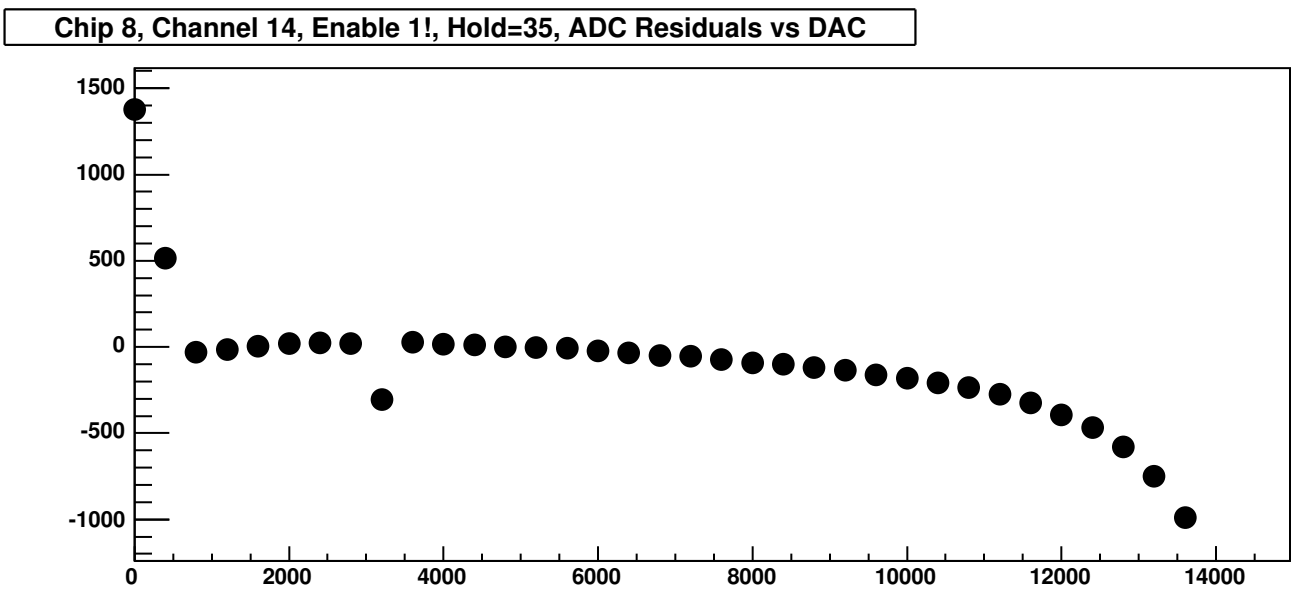


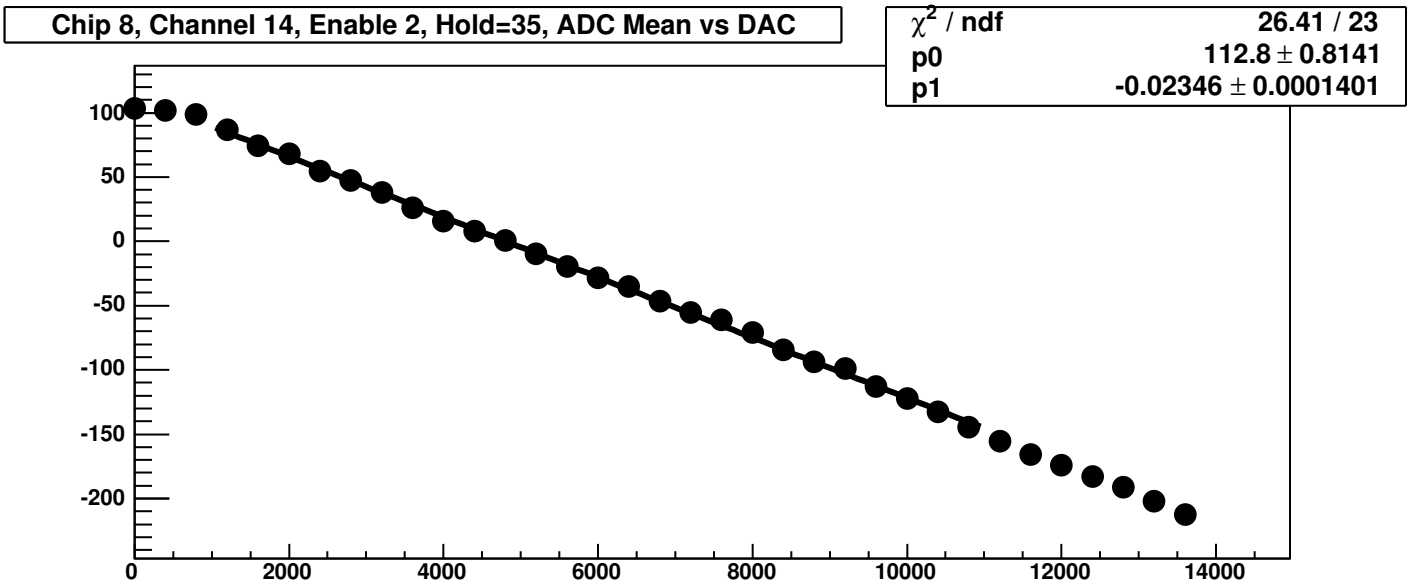
Chip 8, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

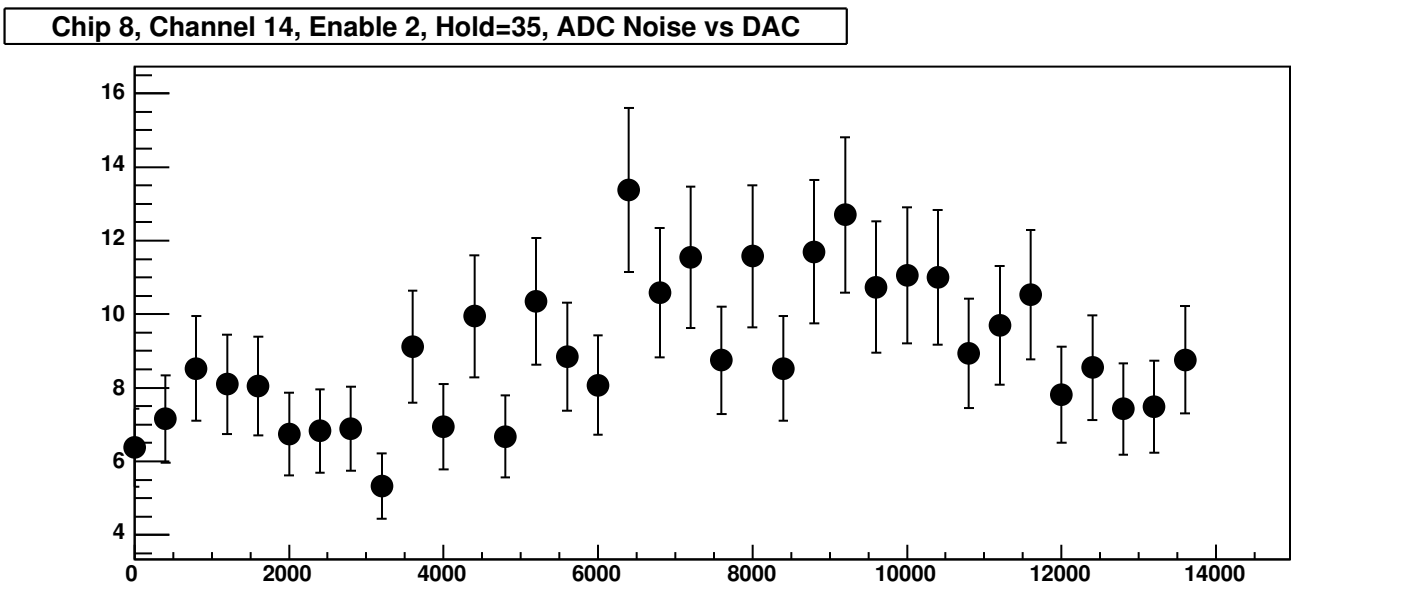


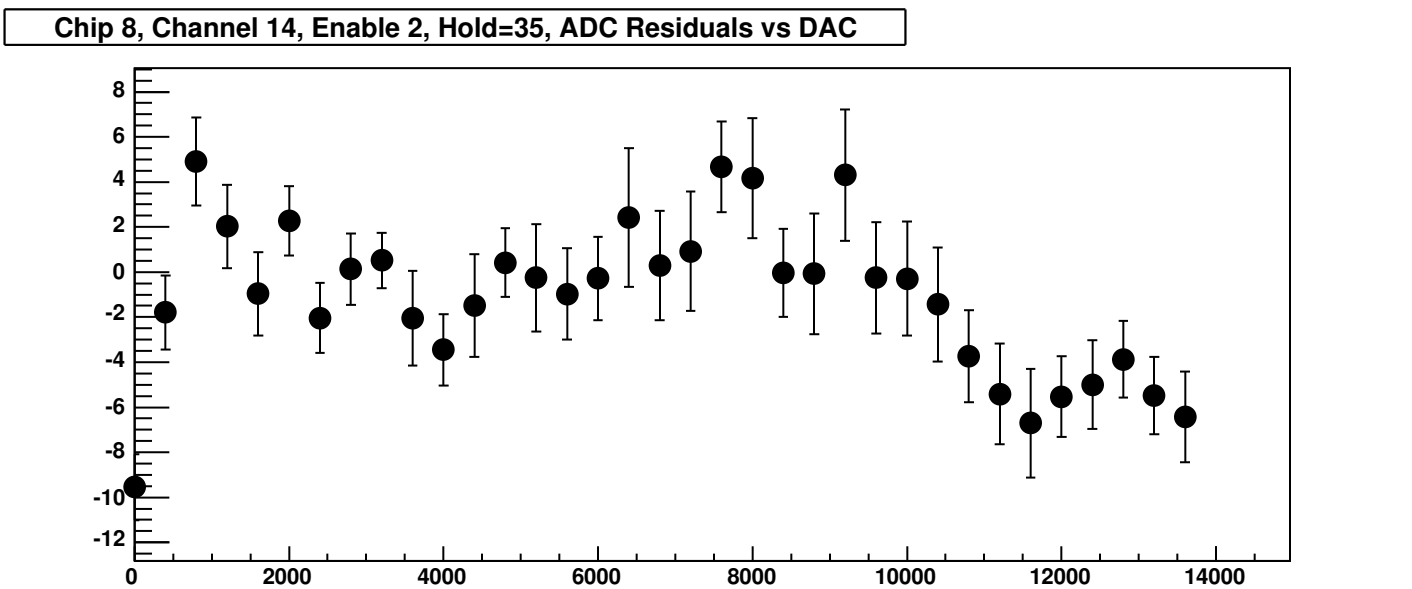

Chip 8, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 289.2 / 23
p0 164.9 ± 2.772
p1 2.196 ± 0.001581
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

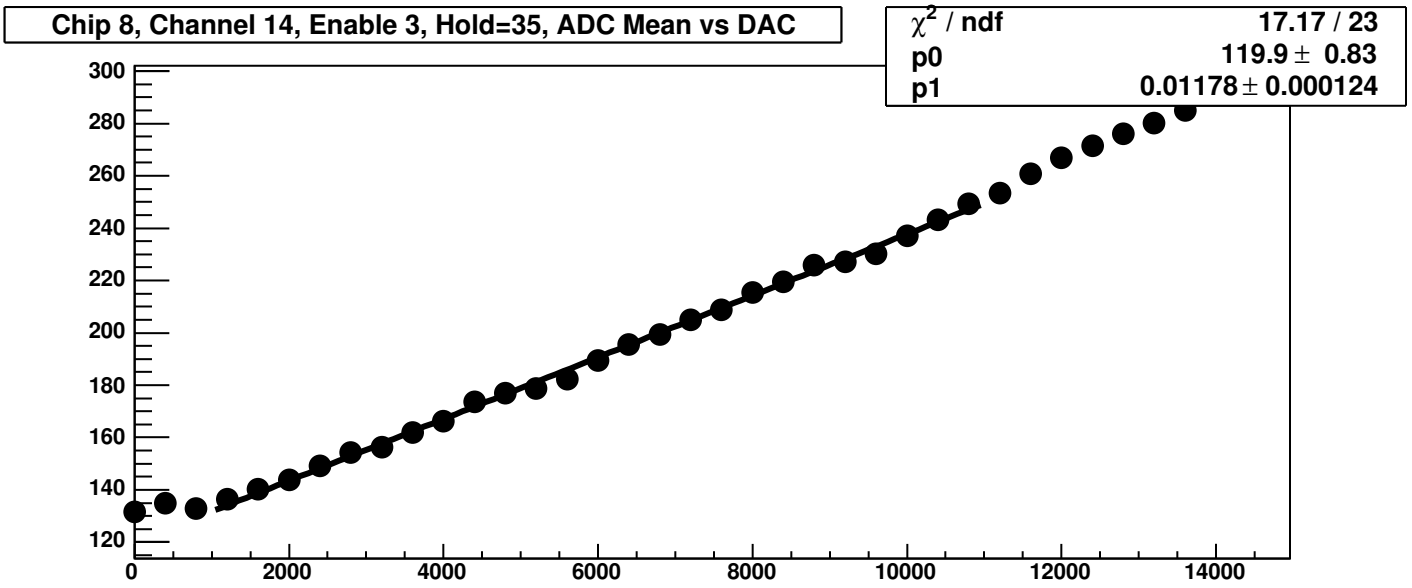

Chip 8, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

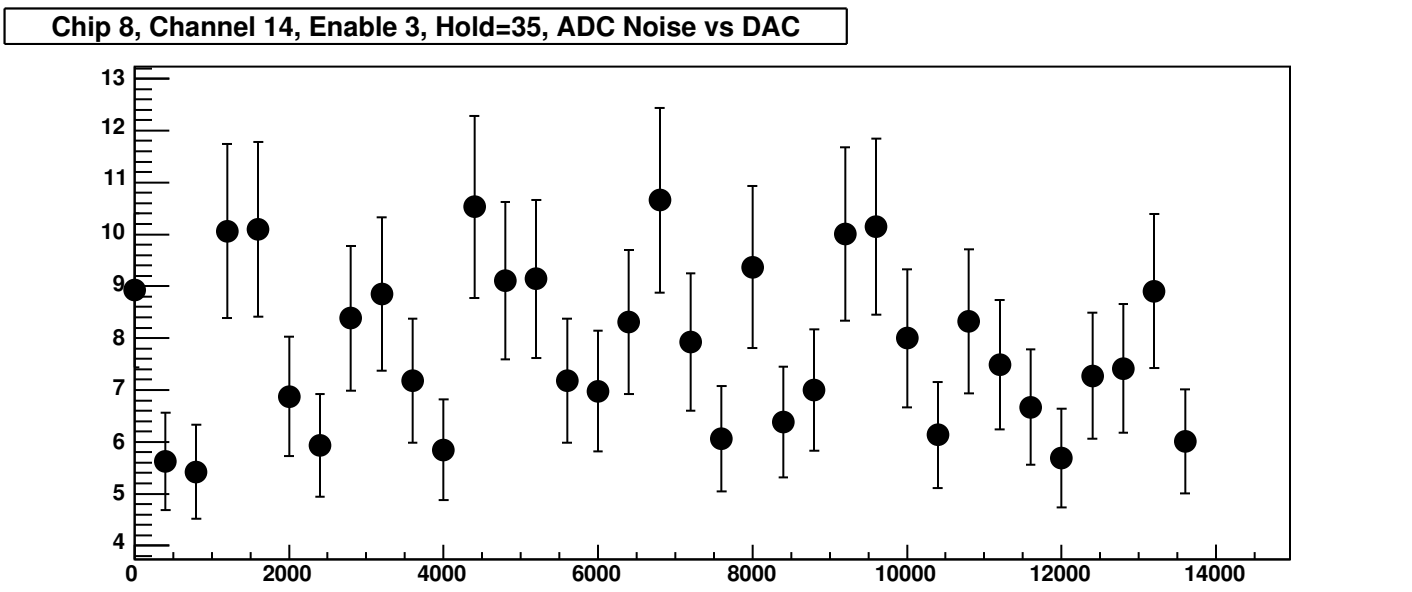

Chip 8, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

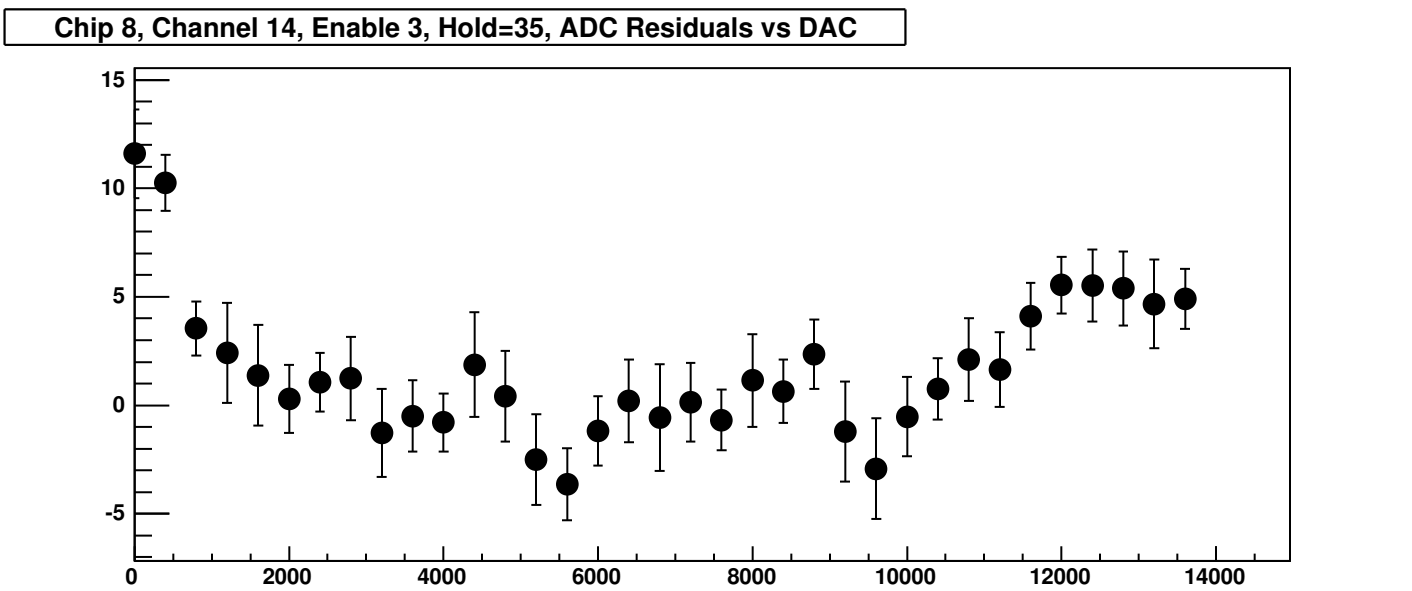

Chip 8, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 26.41 / 23
p0 112.8 ± 0.8141
p1 -0.02346 ± 0.0001401
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

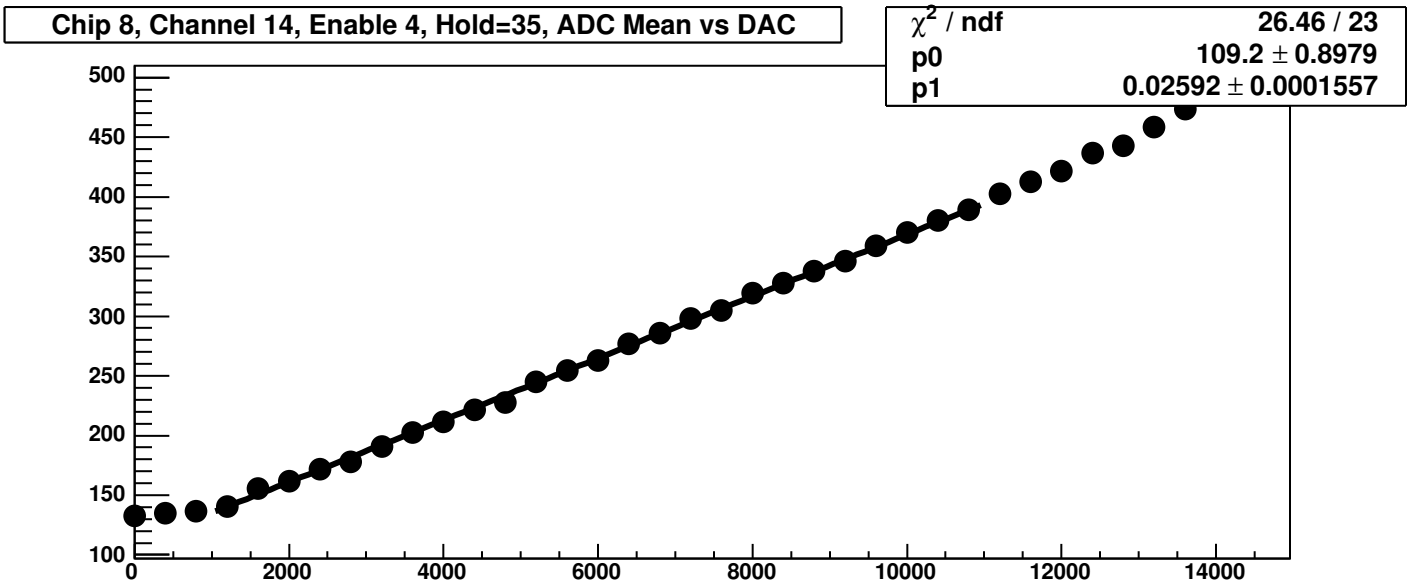

Chip 8, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

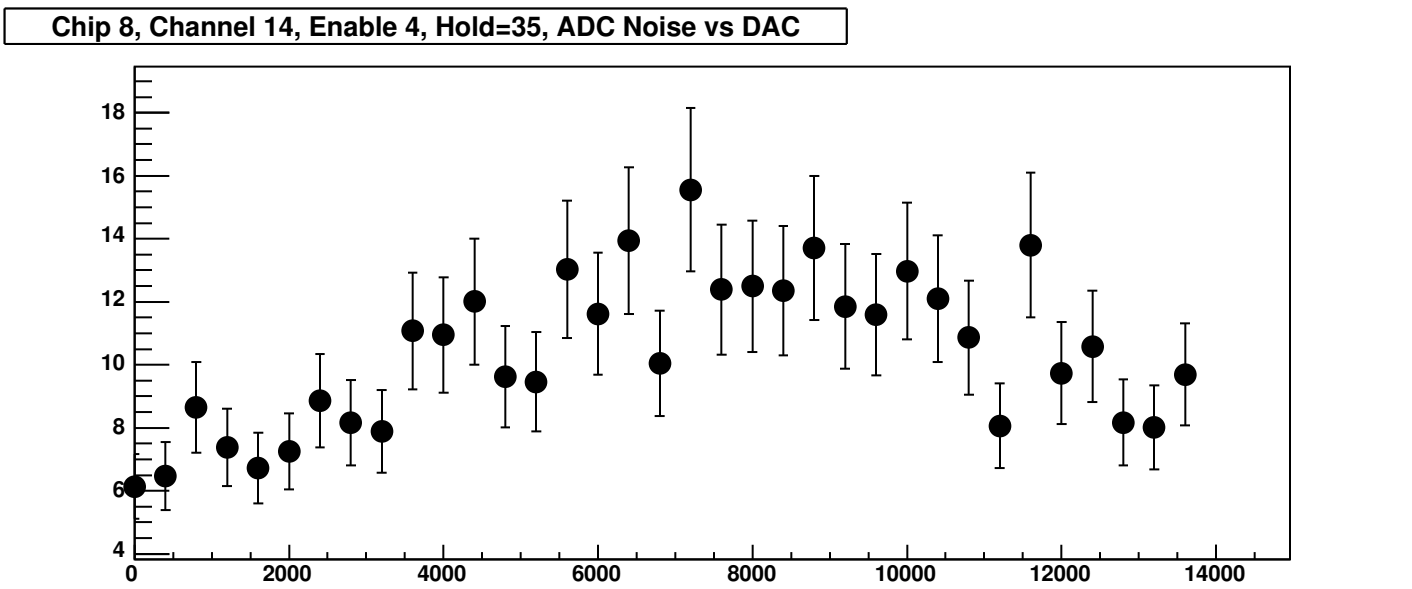

Chip 8, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
0
2000
4000
6000
8000
10000
12000
14000

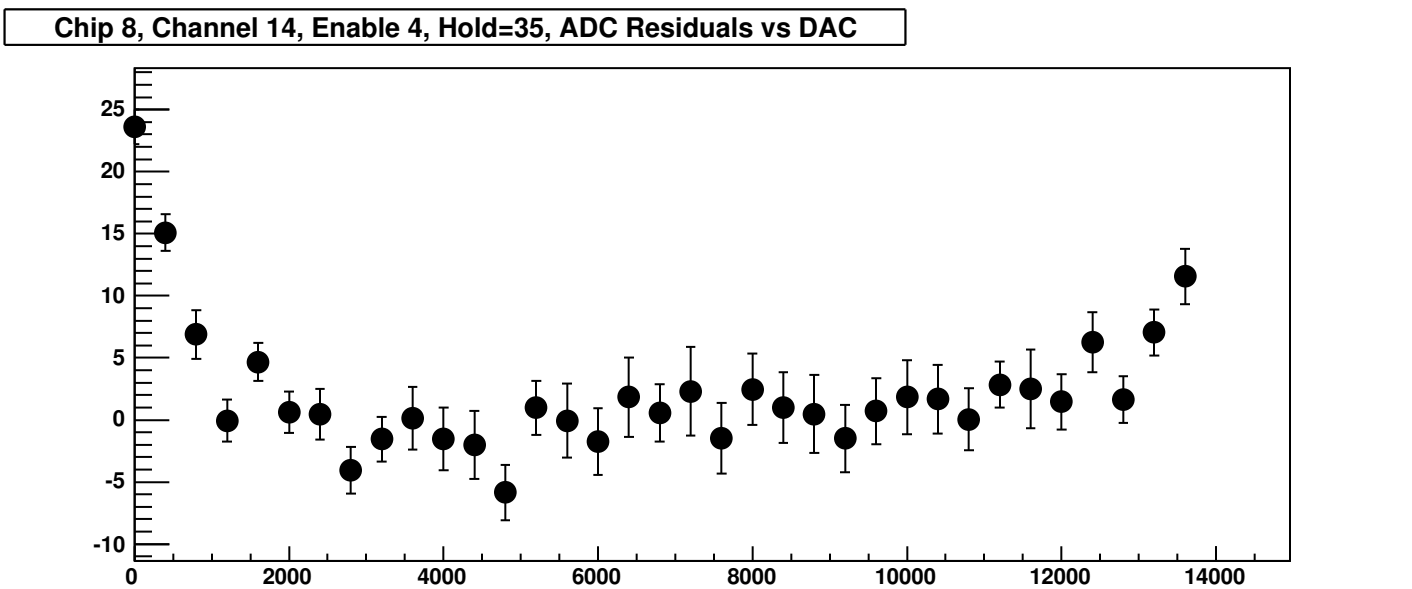

Chip 8, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 17.17 / 23
p0 119.9 ± 0.83
p1 0.01178 ± 0.000124
300
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

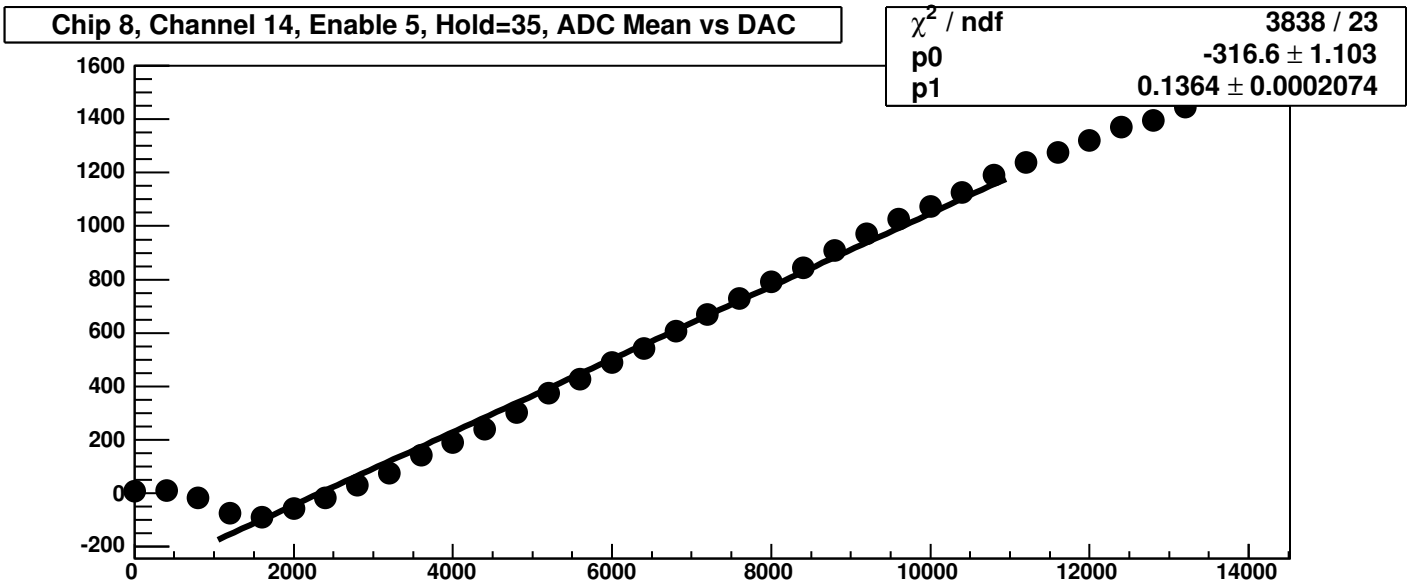

Chip 8, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

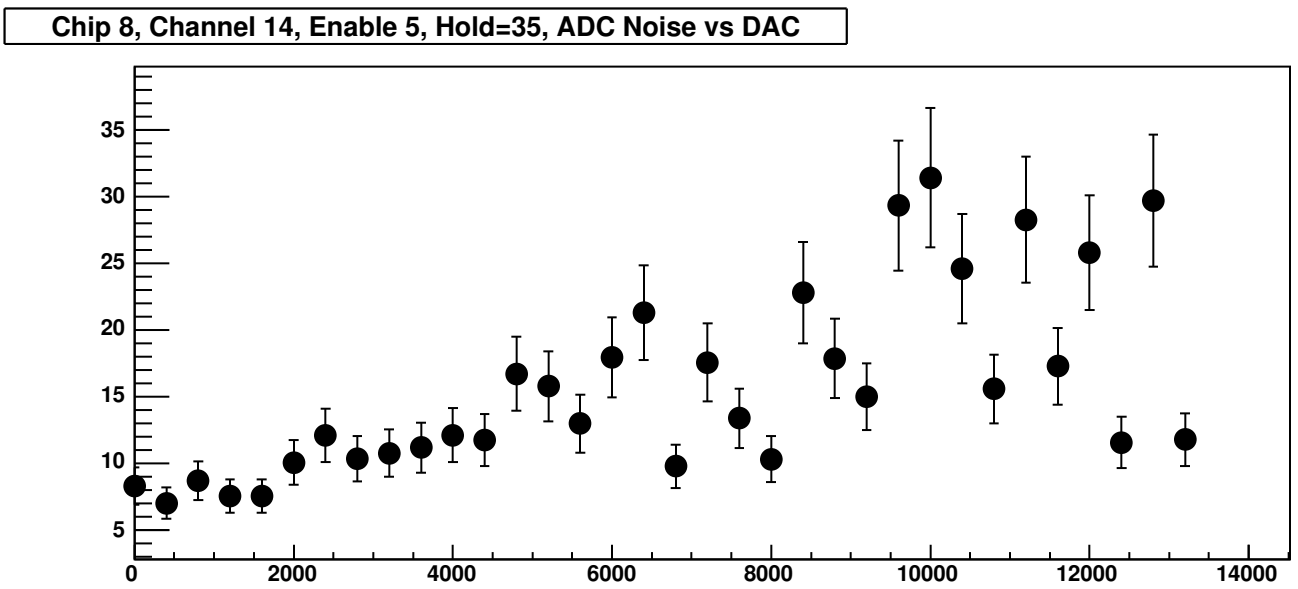

Chip 8, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

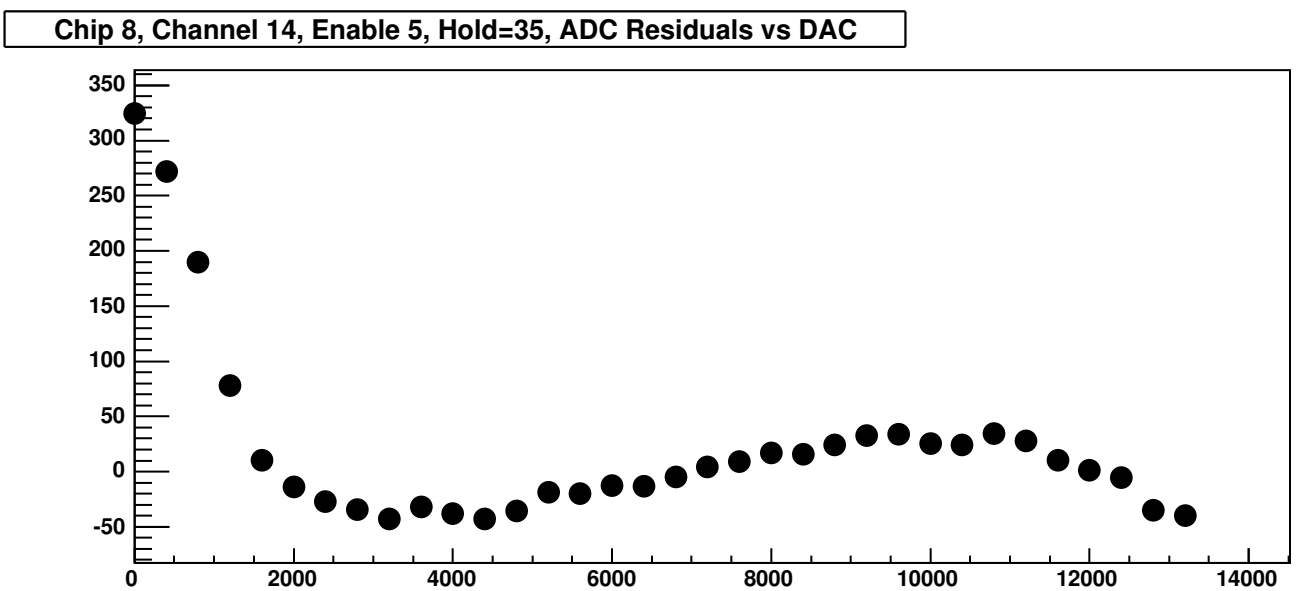

Chip 8, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.46 / 23
p0 109.2 ± 0.8979
p1 0.02592 ± 0.0001557
500
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

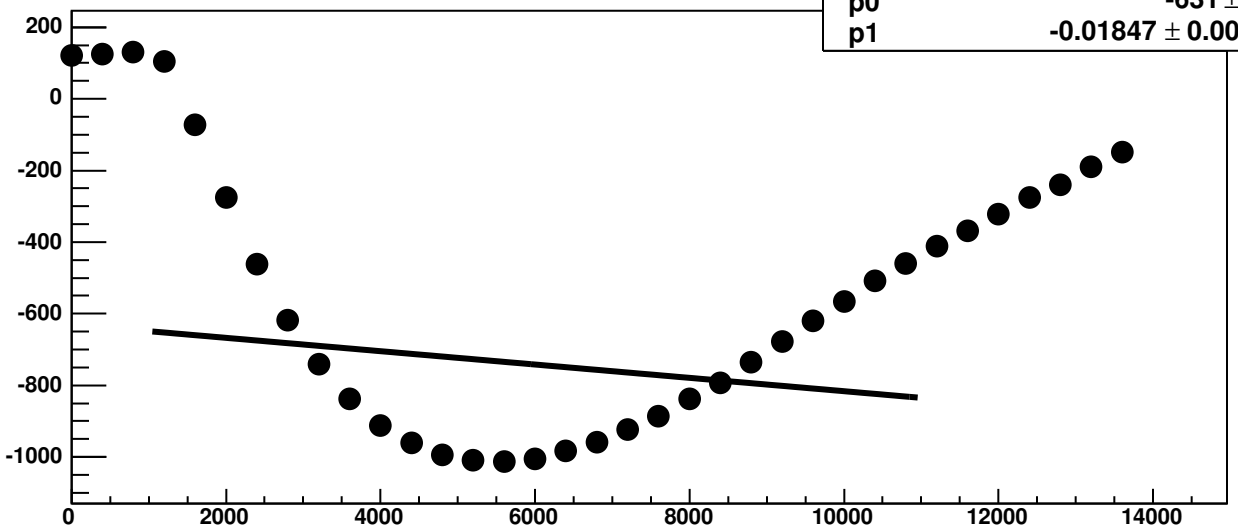

Chip 8, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3838 / 23
p0 -316.6 ± 1.103
p1 0.1364 ± 0.0002074
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

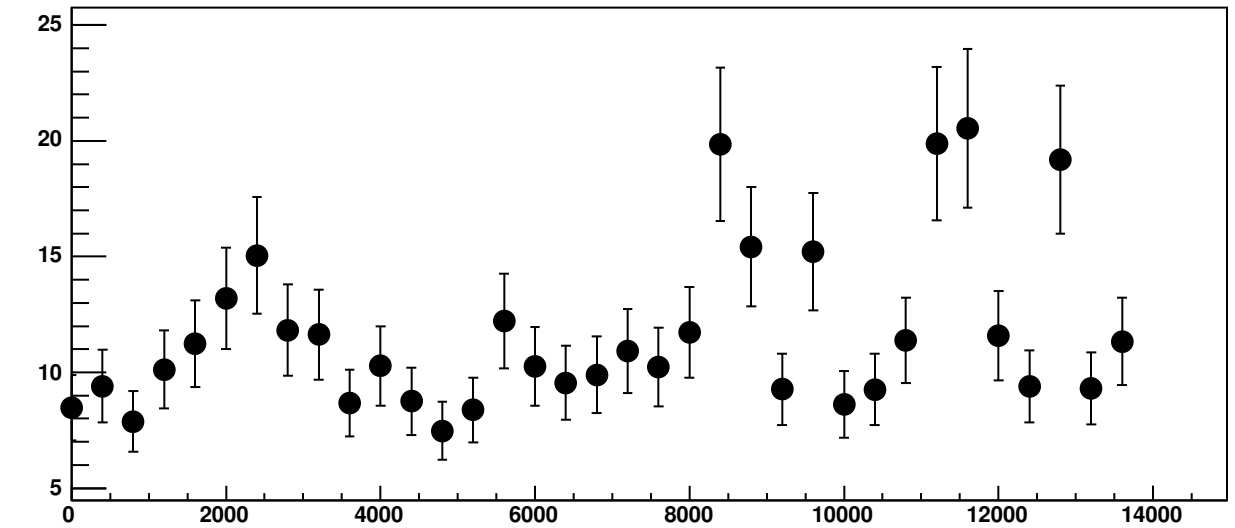

Chip 8, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

Chip 8, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

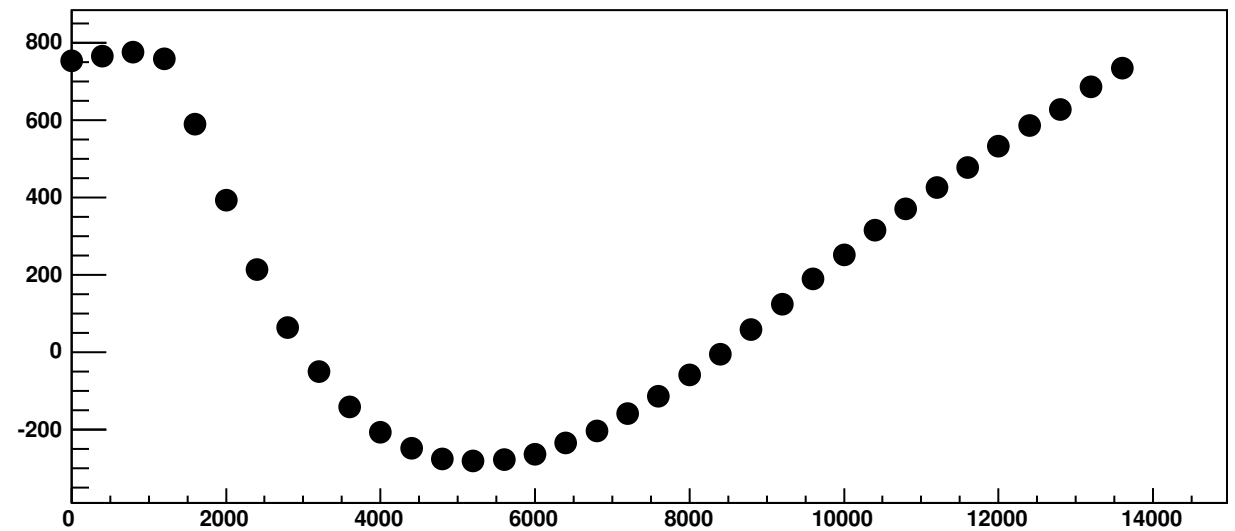
χ^2 / ndf	3.696e+05 / 23
p0	-631 ± 1.138
p1	-0.01847 ± 0.0001726

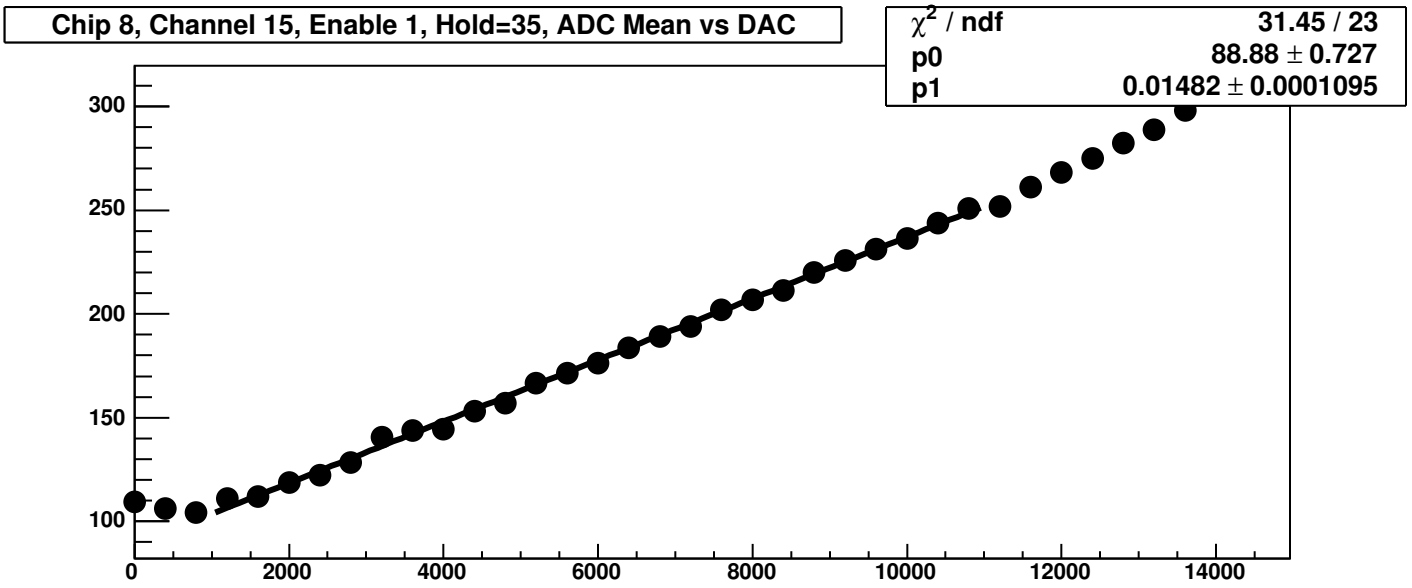


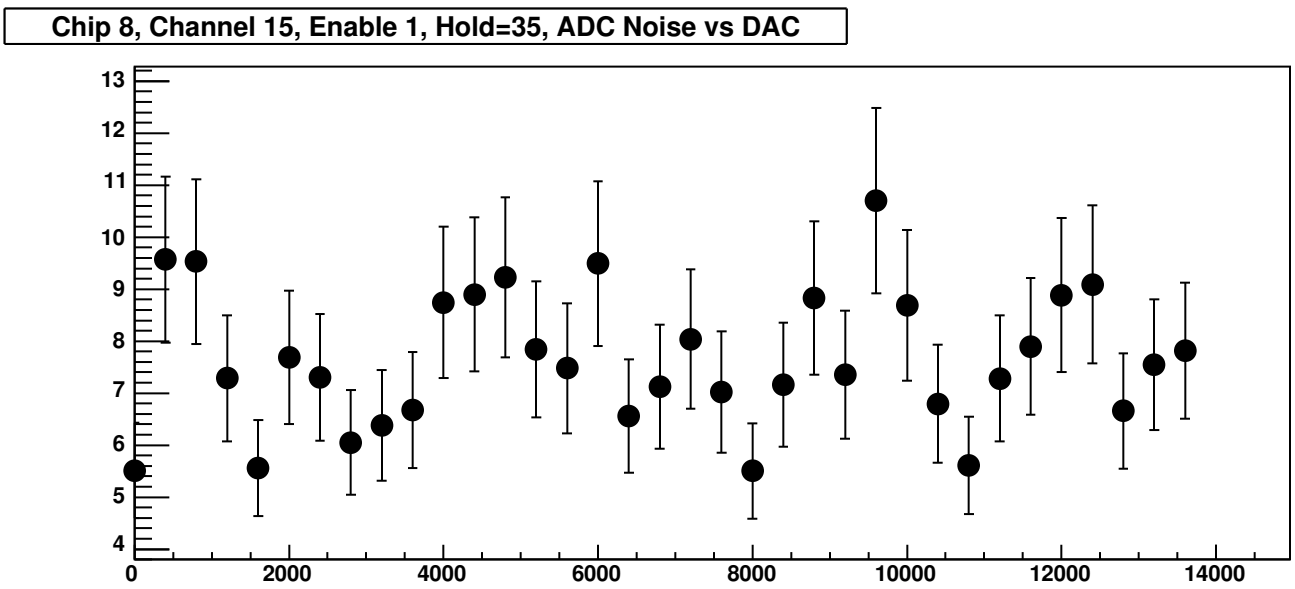
Chip 8, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC

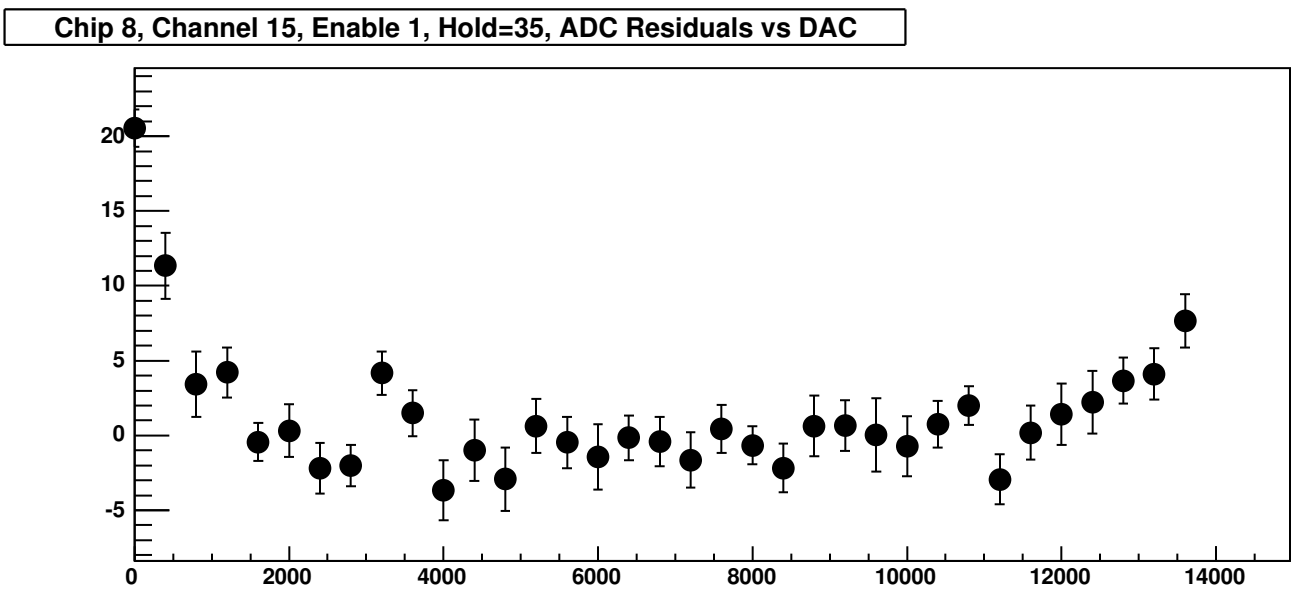


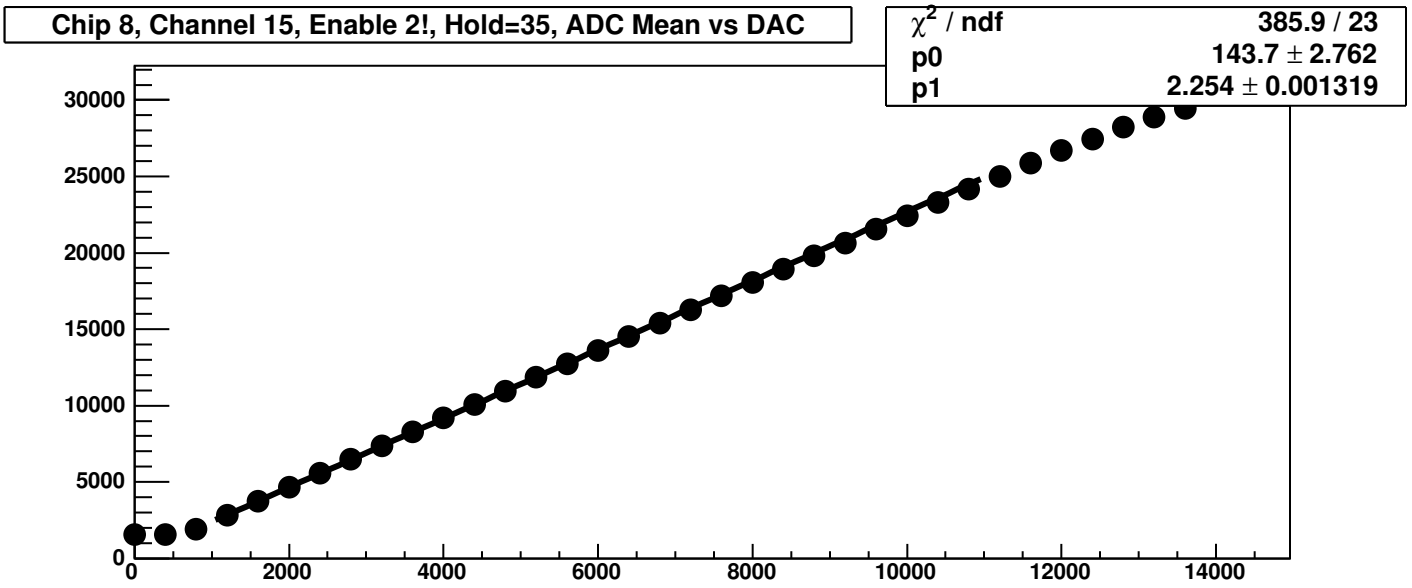
Chip 8, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

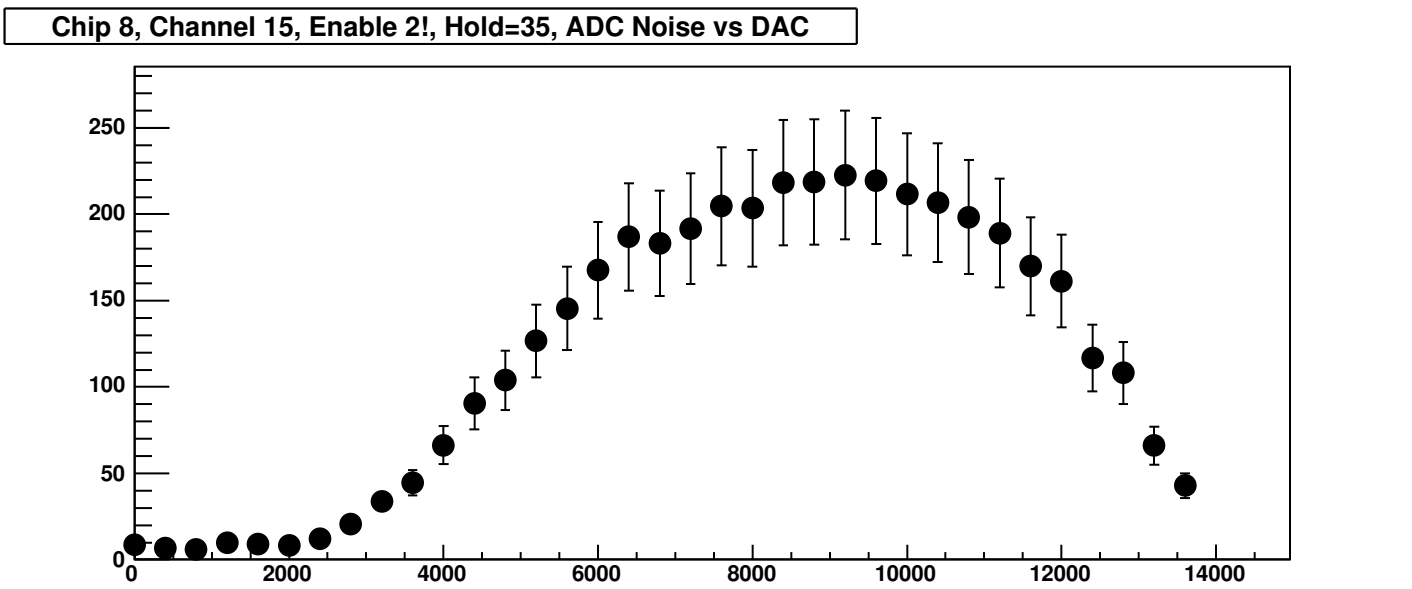


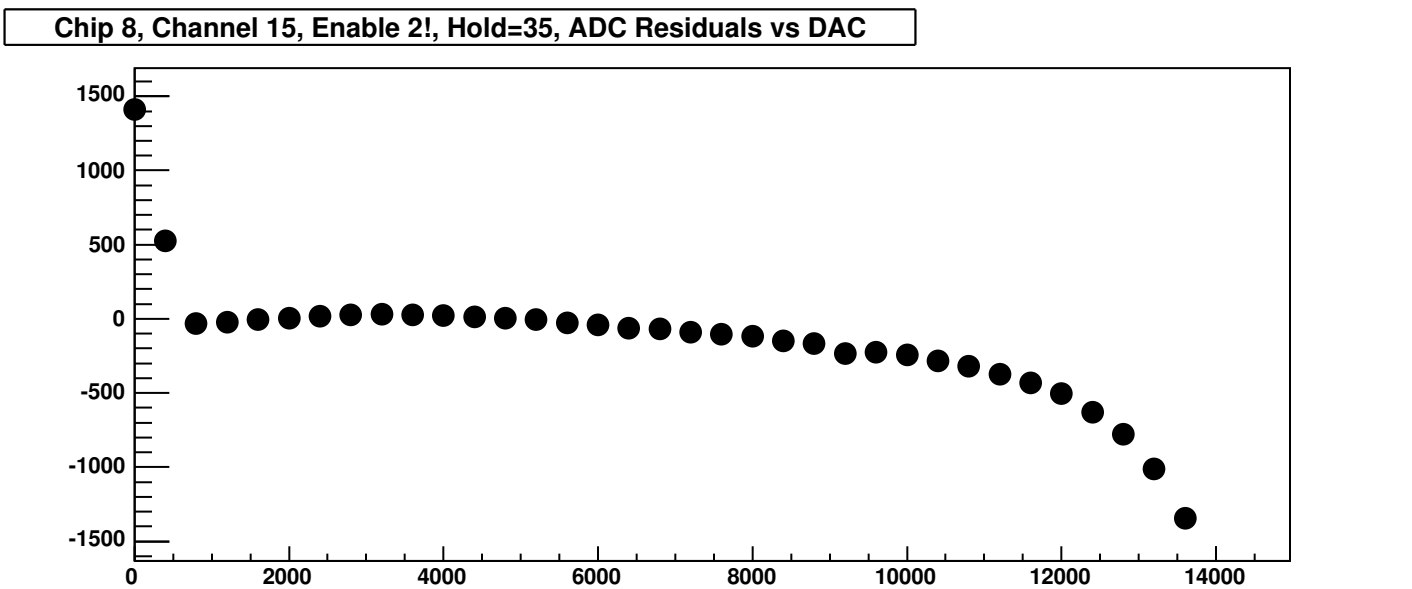

Chip 8, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.45 / 23
p0 88.88 ± 0.727
p1 0.01482 ± 0.0001095
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

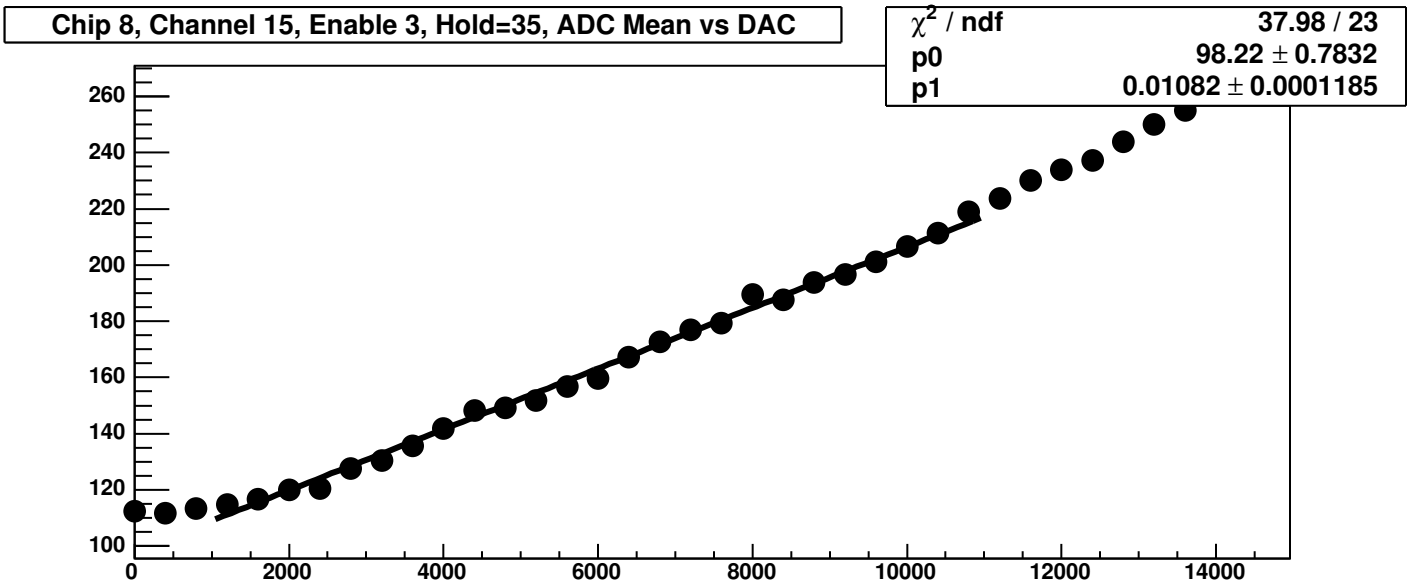

Chip 8, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

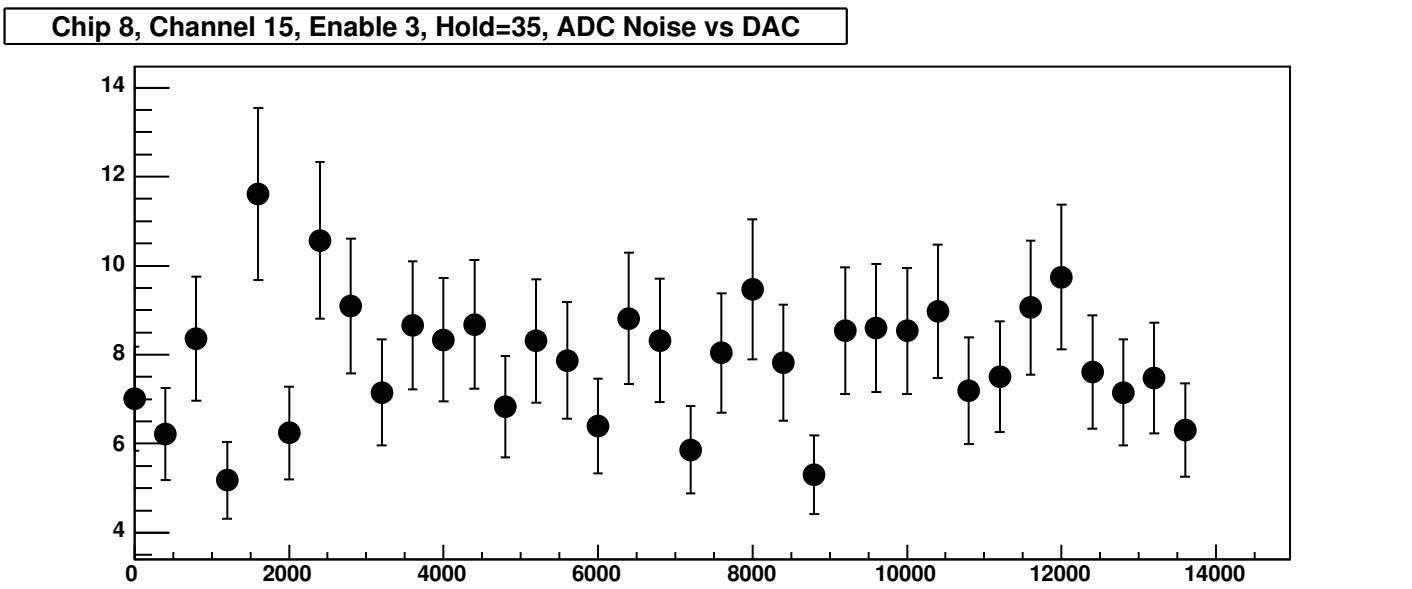

Chip 8, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

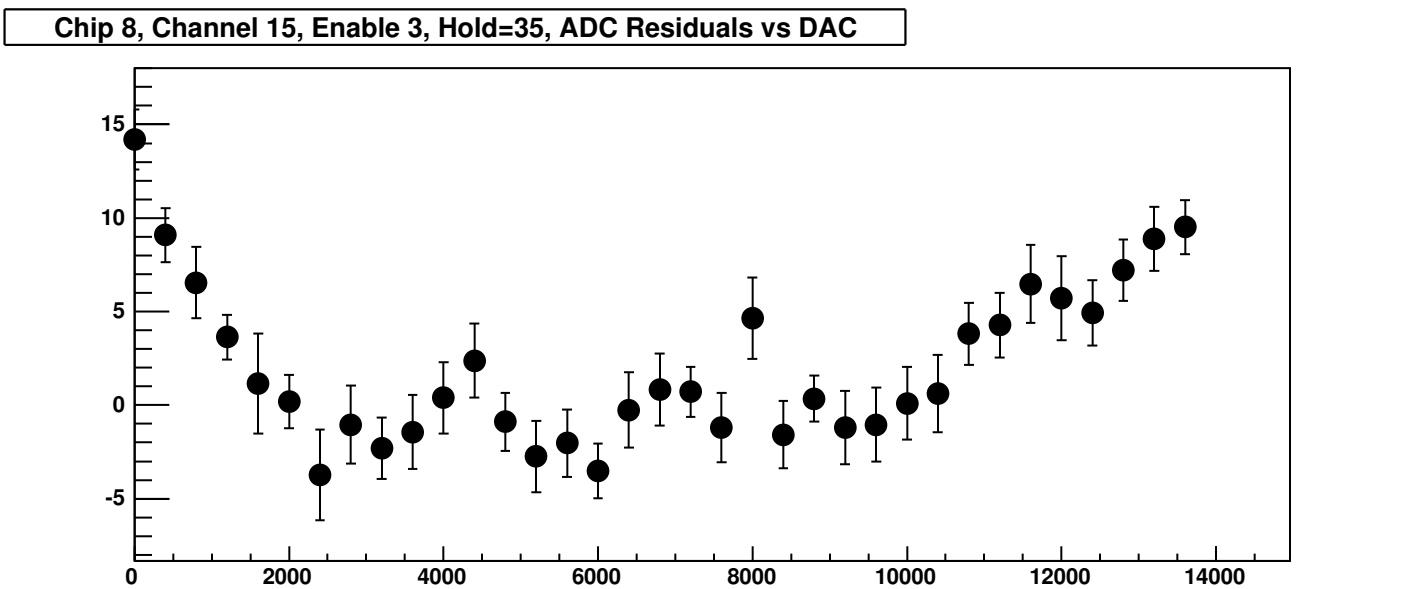

Chip 8, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 385.9 / 23
p0 143.7 ± 2.762
p1 2.254 ± 0.001319
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

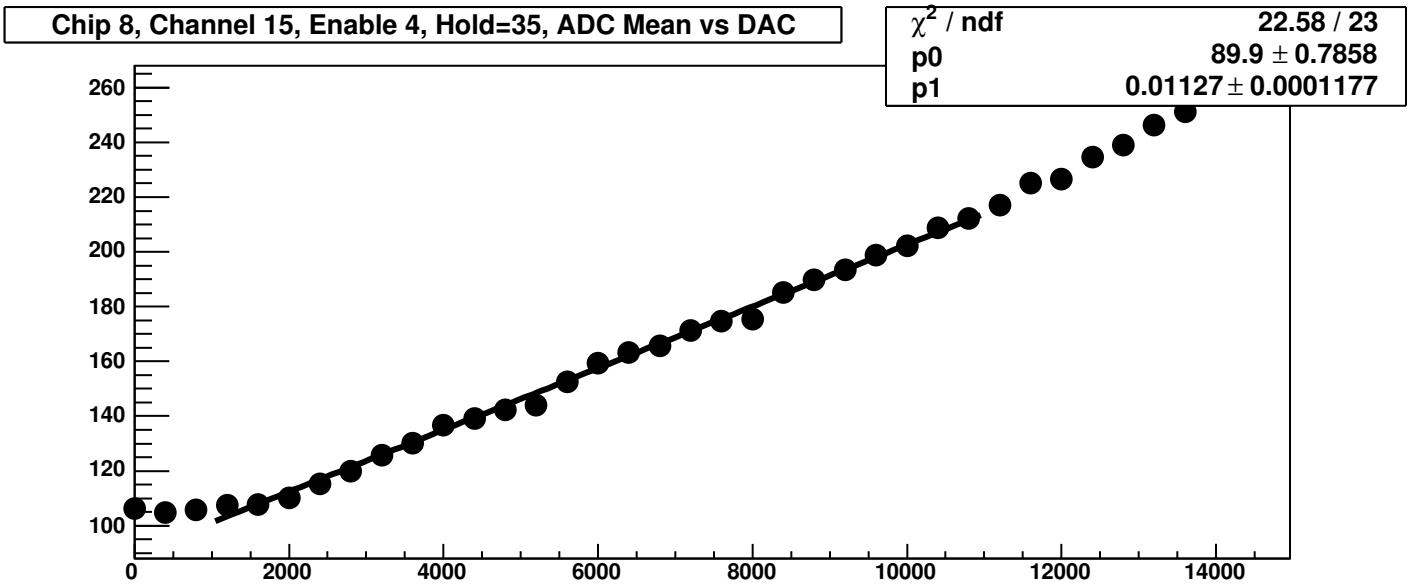

Chip 8, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

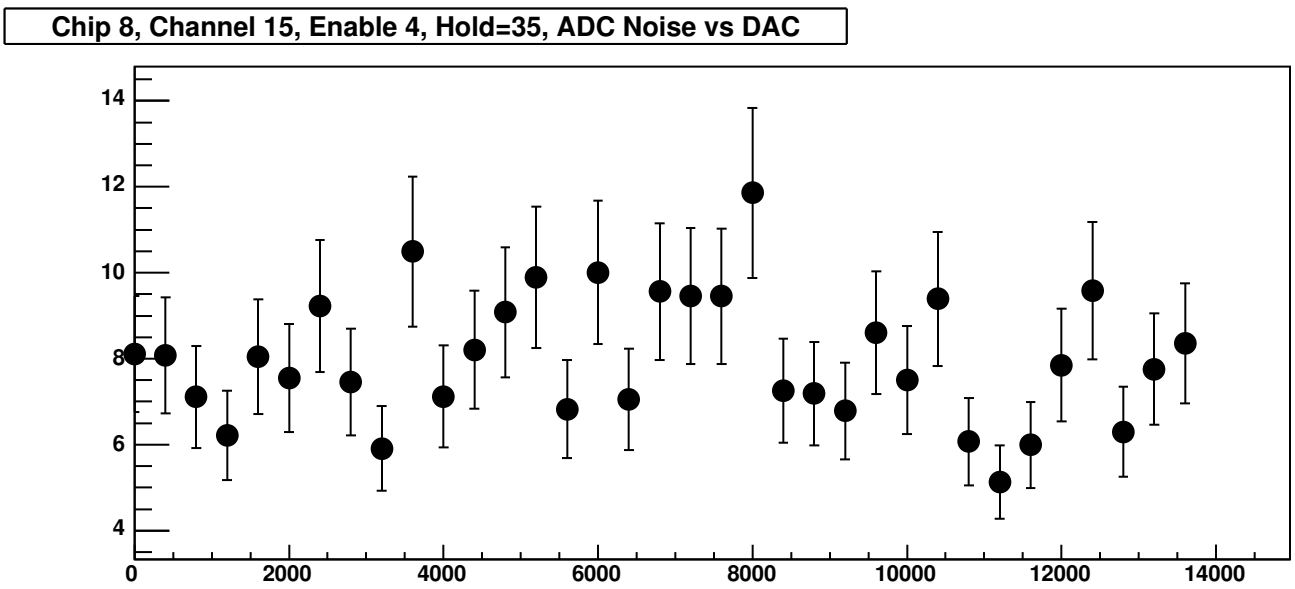

Chip 8, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

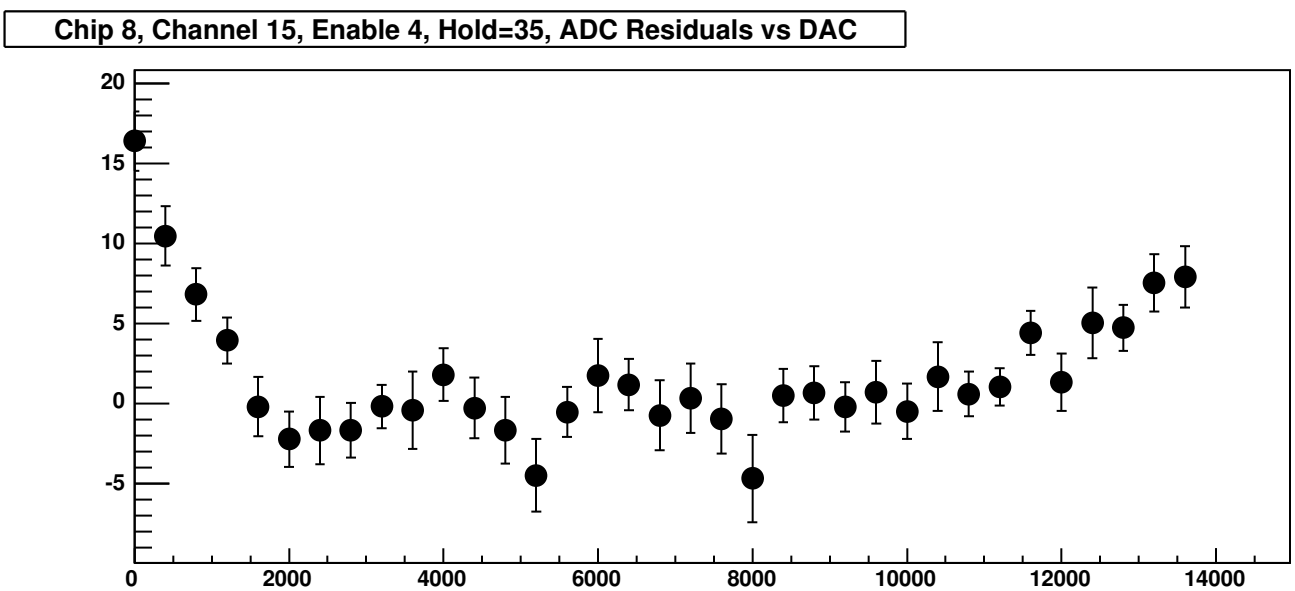

Chip 8, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 37.98 / 23
p0 98.22 ± 0.7832
p1 0.01082 ± 0.0001185
260
240
220
200
180
160
140
120
100
0
2000
4000
6000
8000
10000
12000
14000

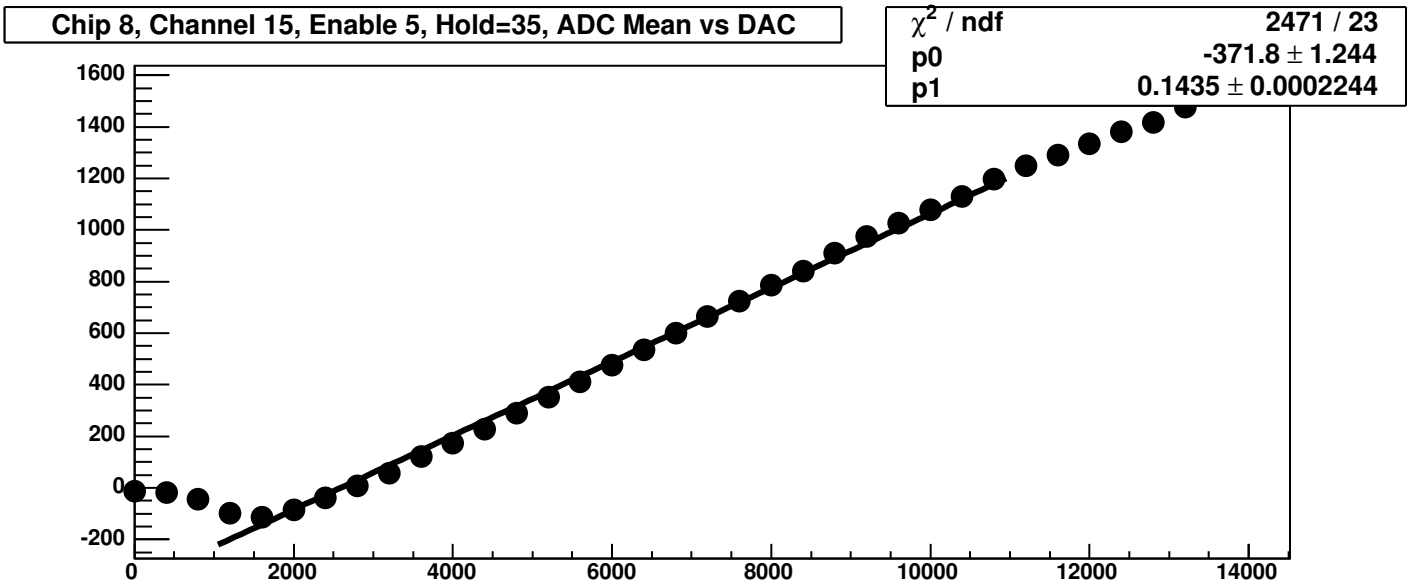

Chip 8, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

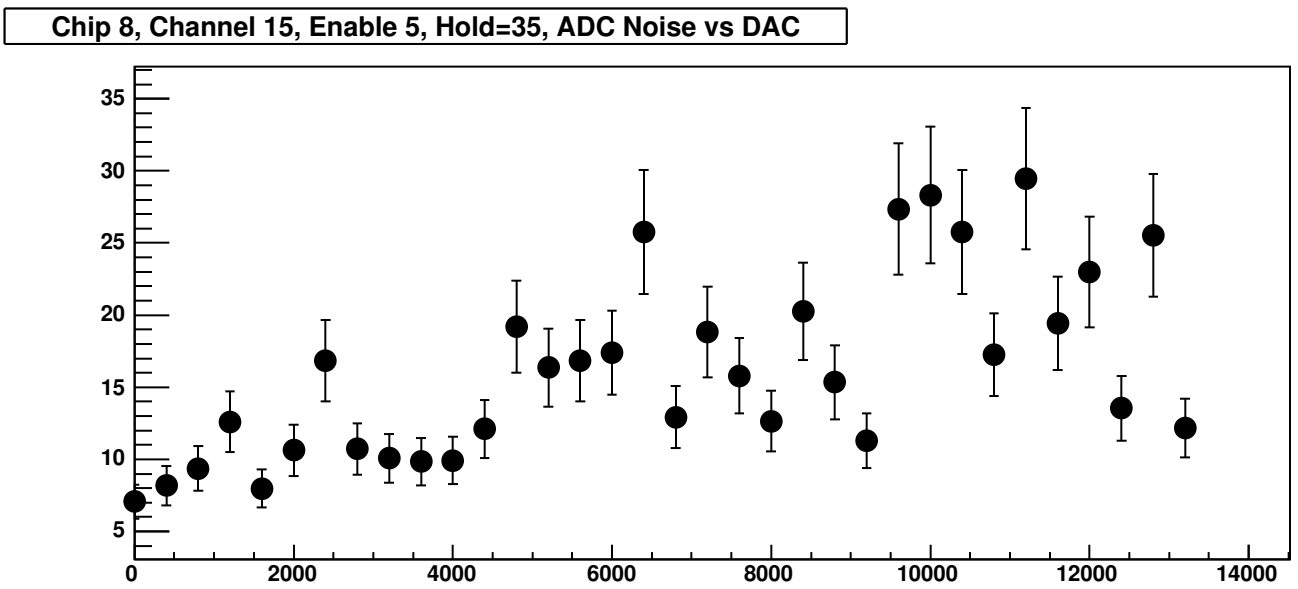

Chip 8, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

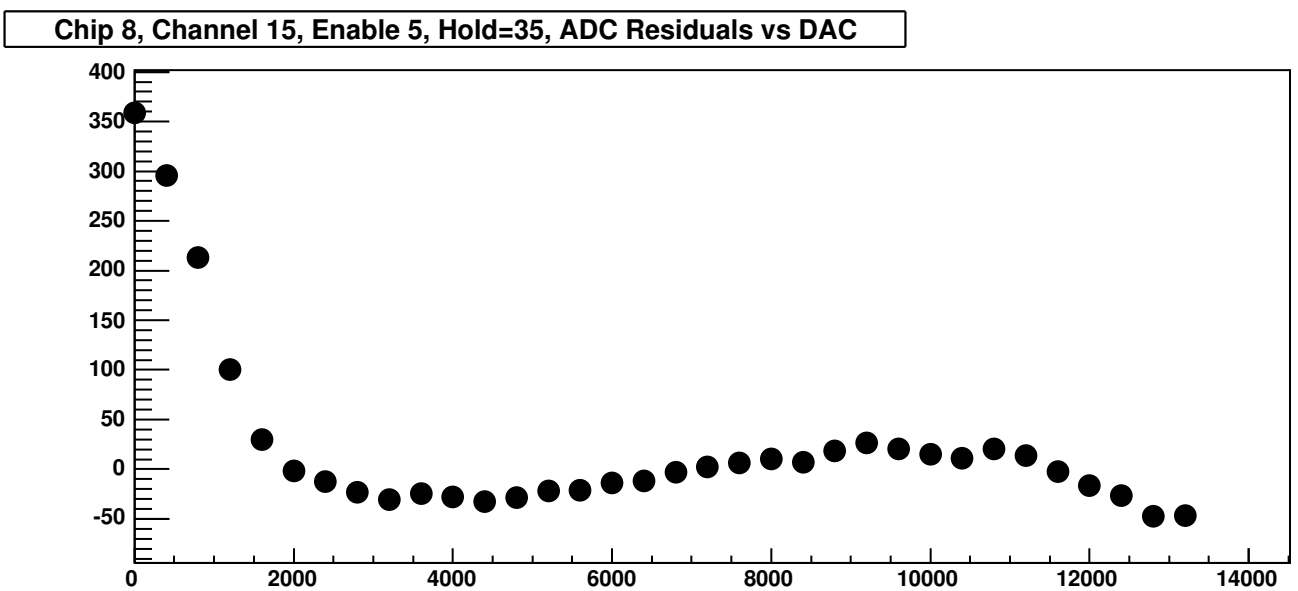

Chip 8, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 22.58 / 23
p0 89.9 ± 0.7858
p1 0.01127 ± 0.0001177
260
240
220
200
180
160
140
120
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

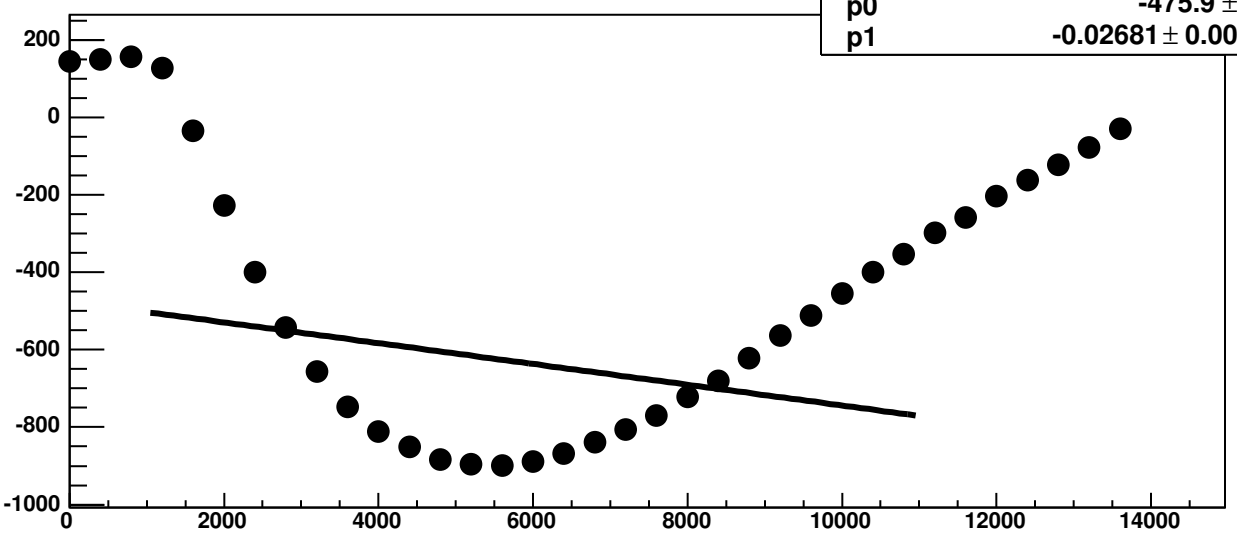

Chip 8, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2471 / 23
p0 -371.8 ± 1.244
p1 0.1435 ± 0.0002244
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

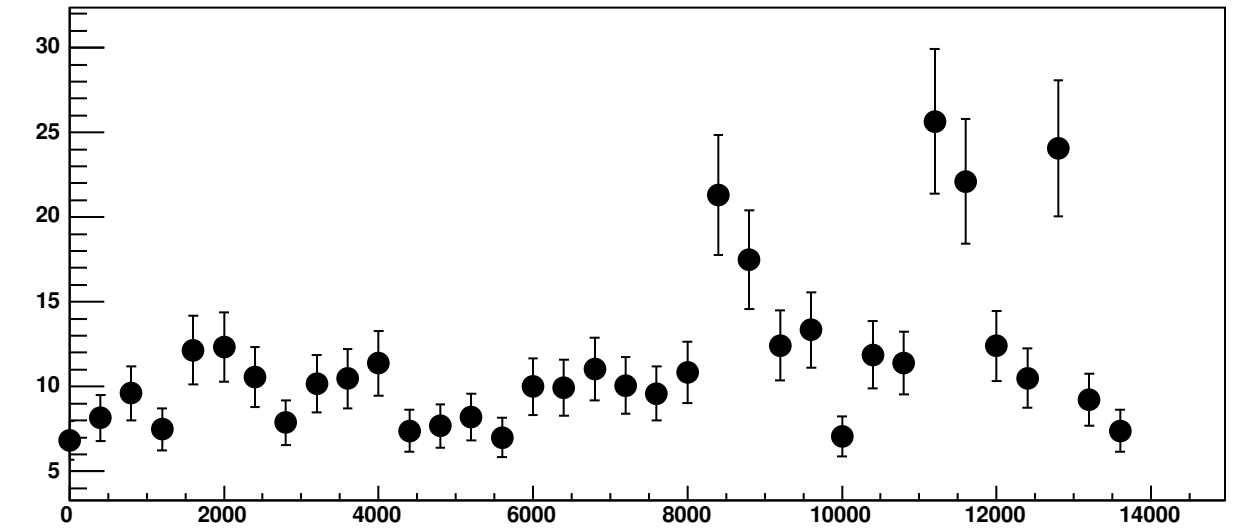

Chip 8, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC
400
350
300
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

Chip 8, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

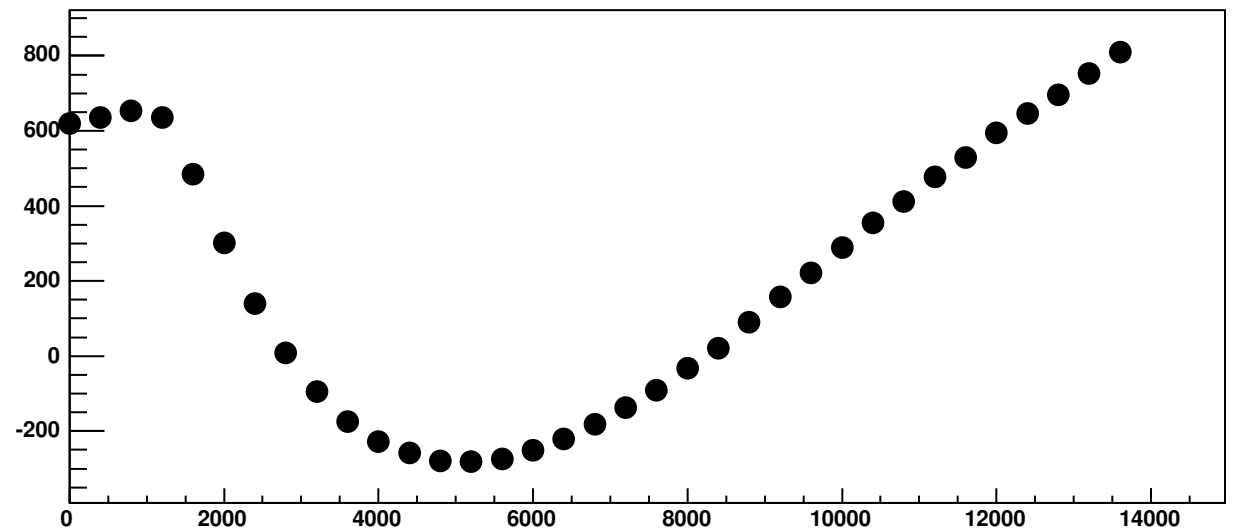
χ^2 / ndf	4.123e+05 / 23
p0	-475.9 ± 1.008
p1	-0.02681 ± 0.0001605

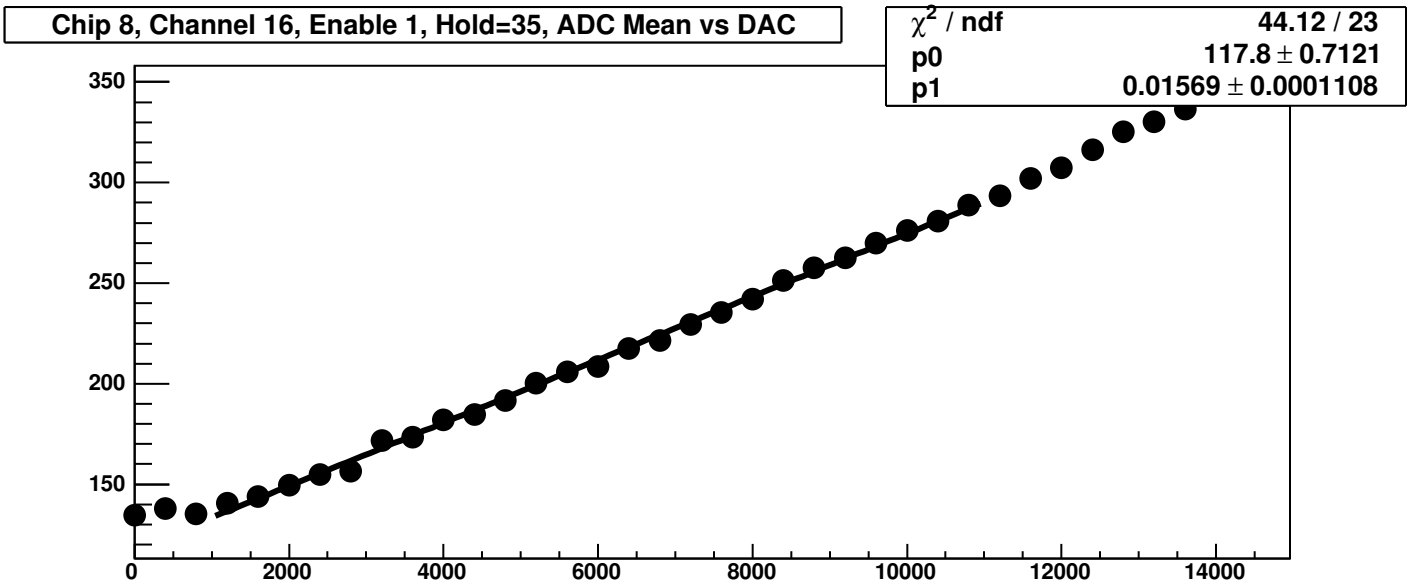


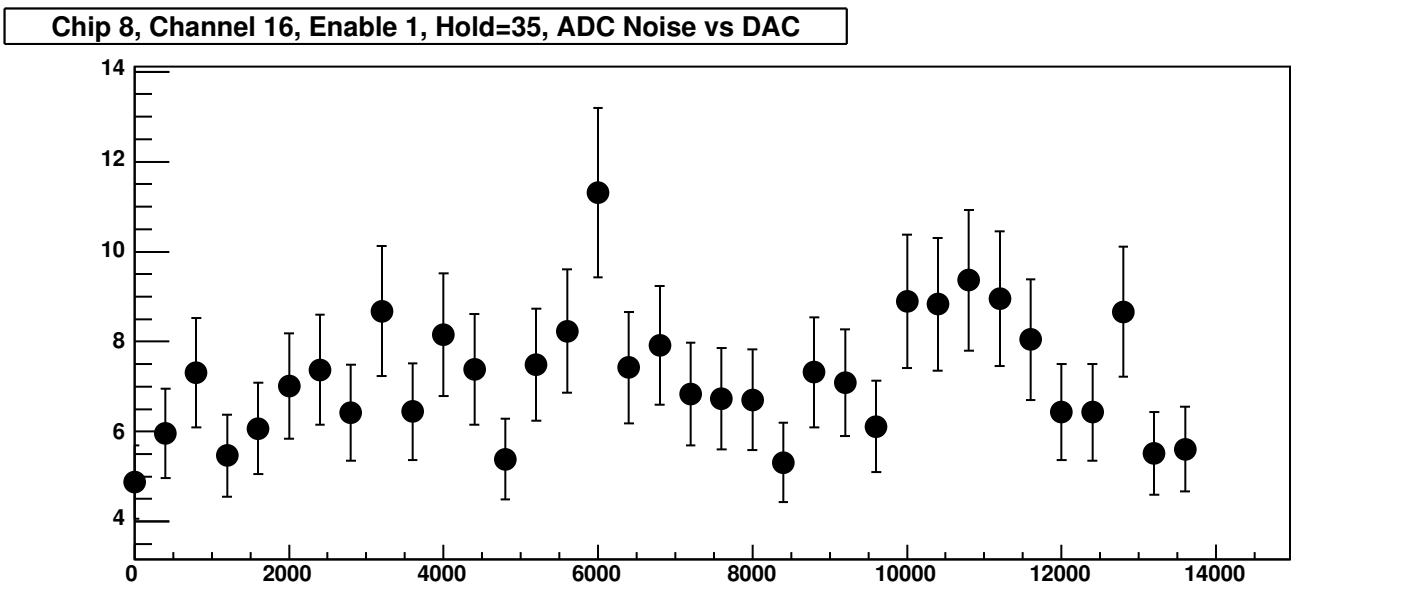
Chip 8, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC

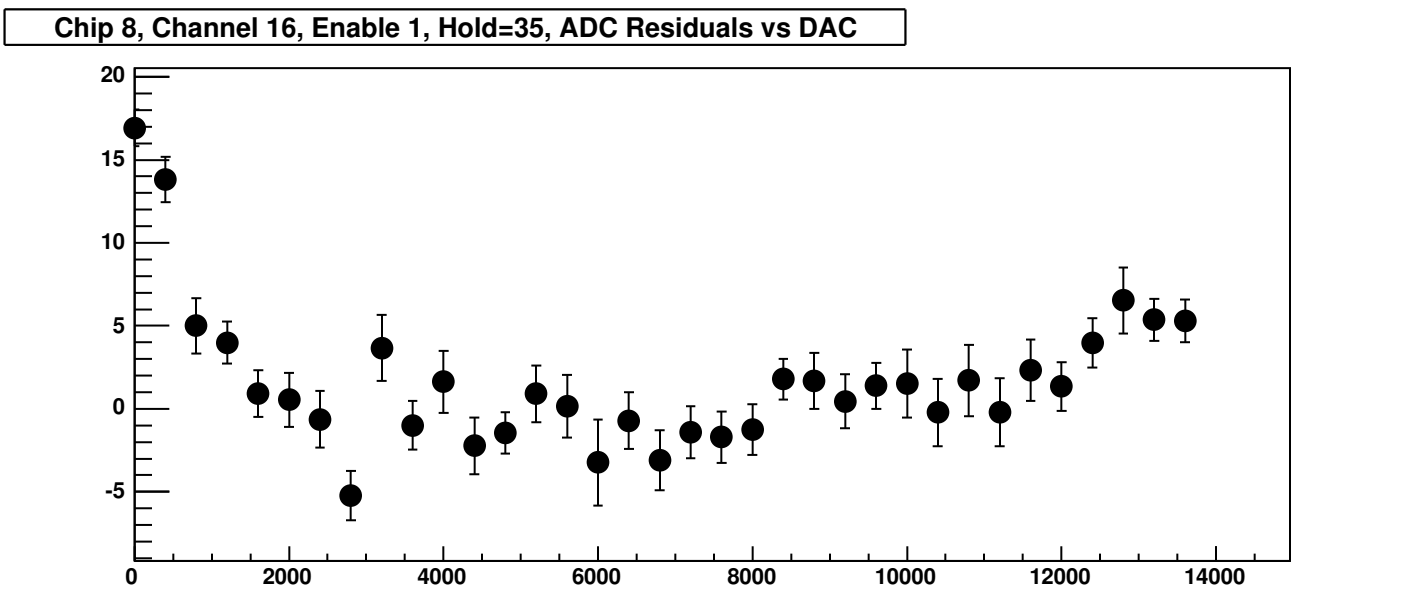


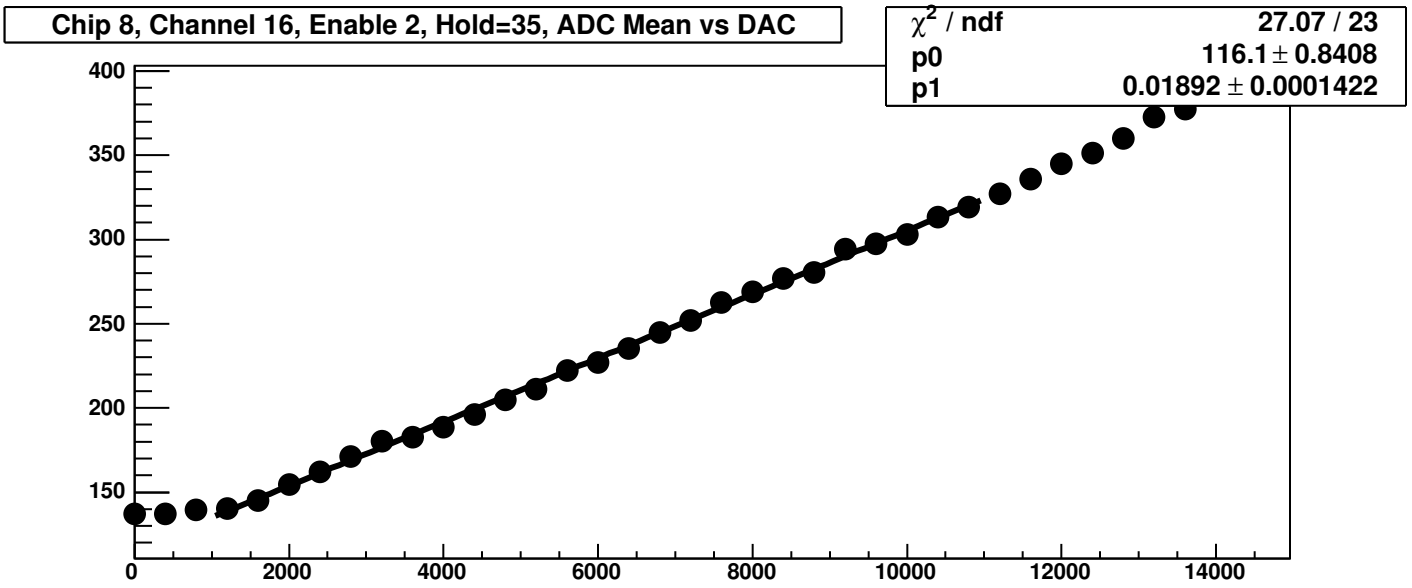
Chip 8, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

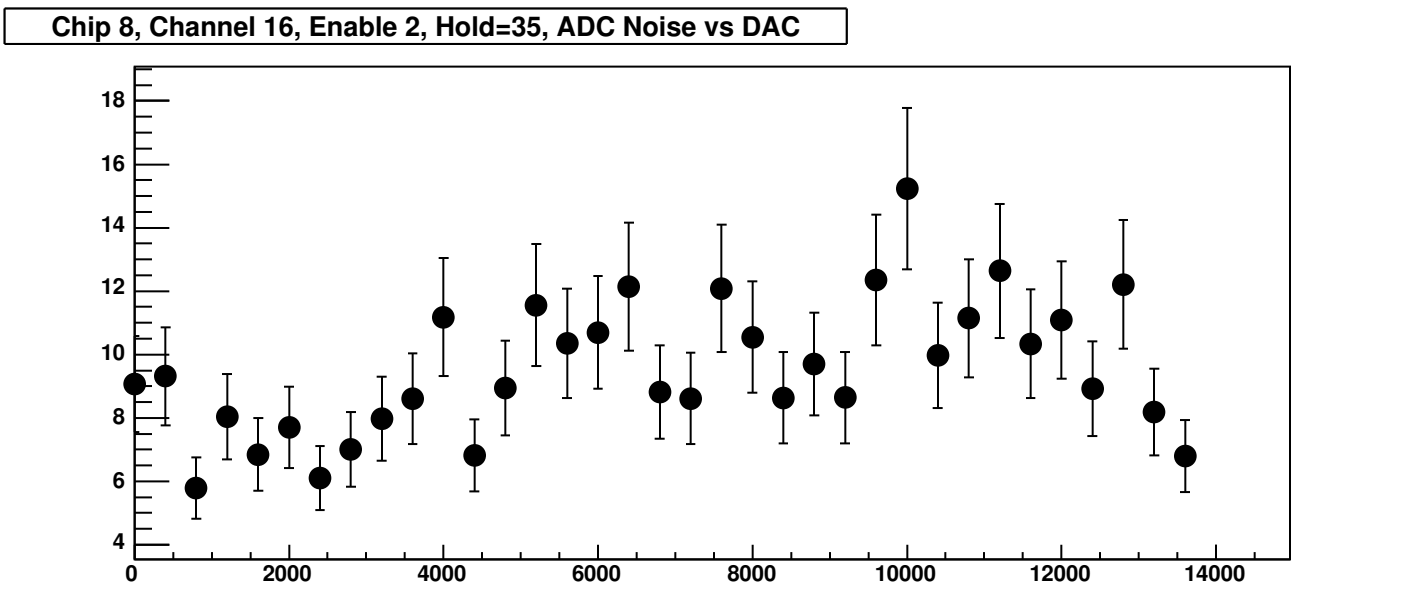


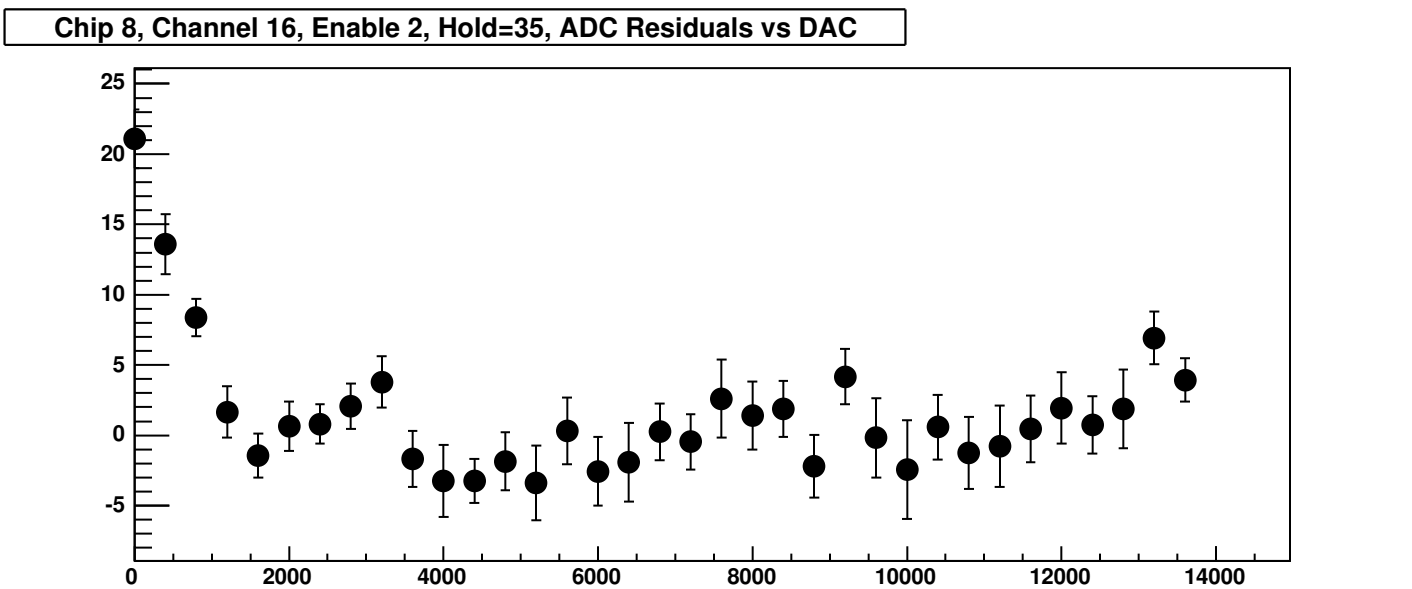

Chip 8, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 44.12 / 23
p0 117.8 ± 0.7121
p1 0.01569 ± 0.0001108
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

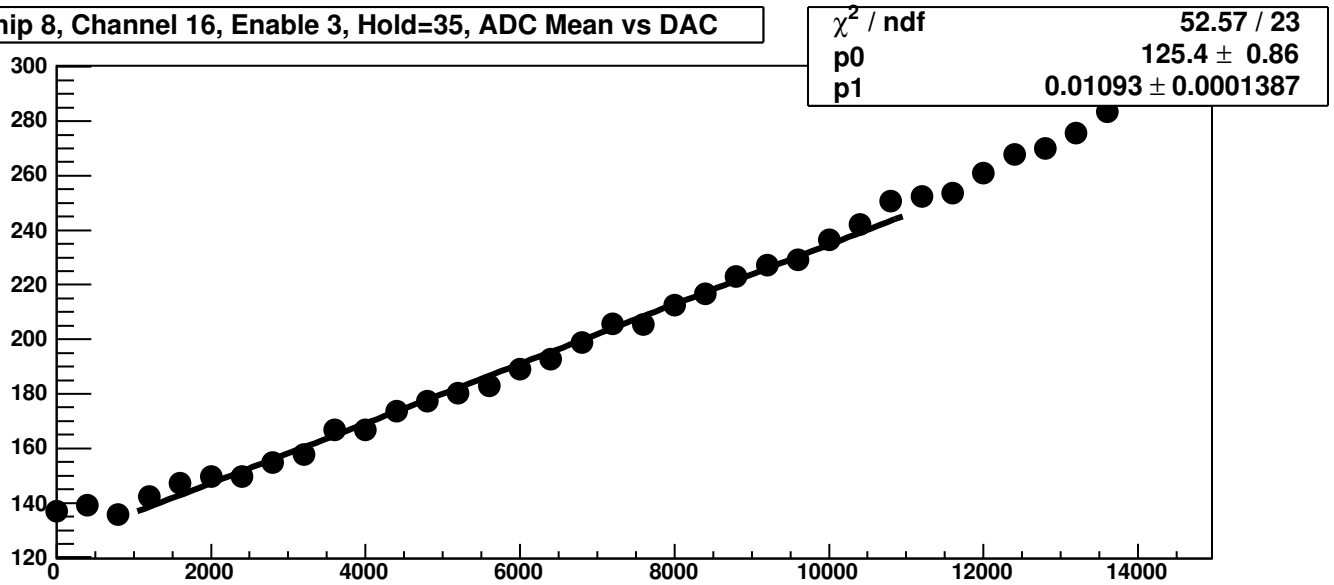

Chip 8, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 27.07 / 23
p0 116.1 ± 0.8408
p1 0.01892 ± 0.0001422
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

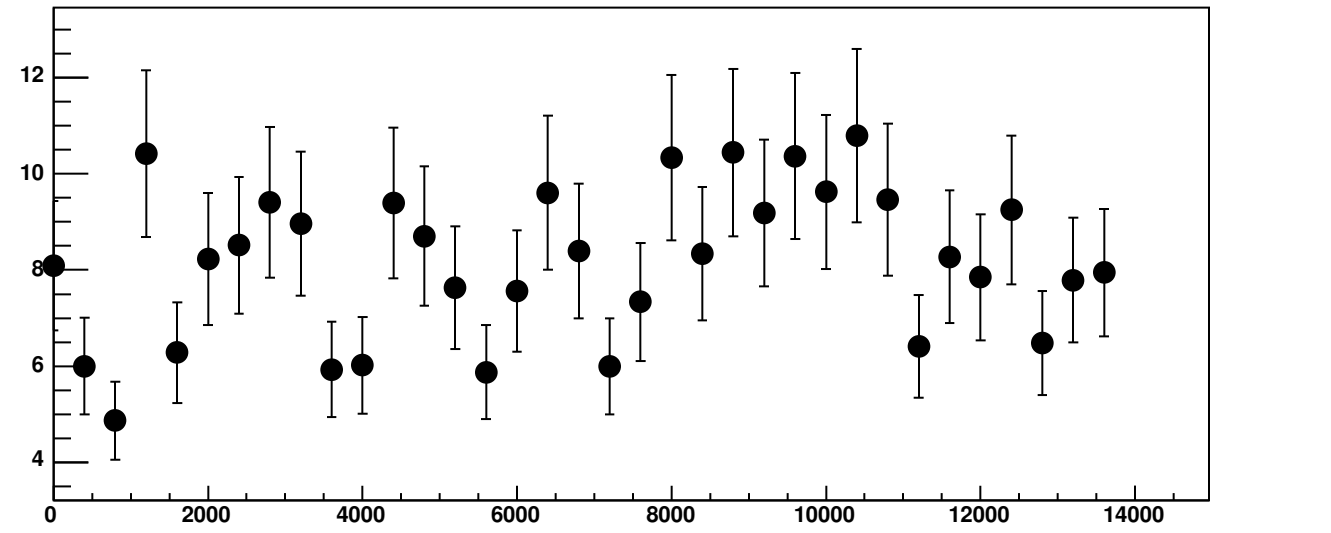

Chip 8, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

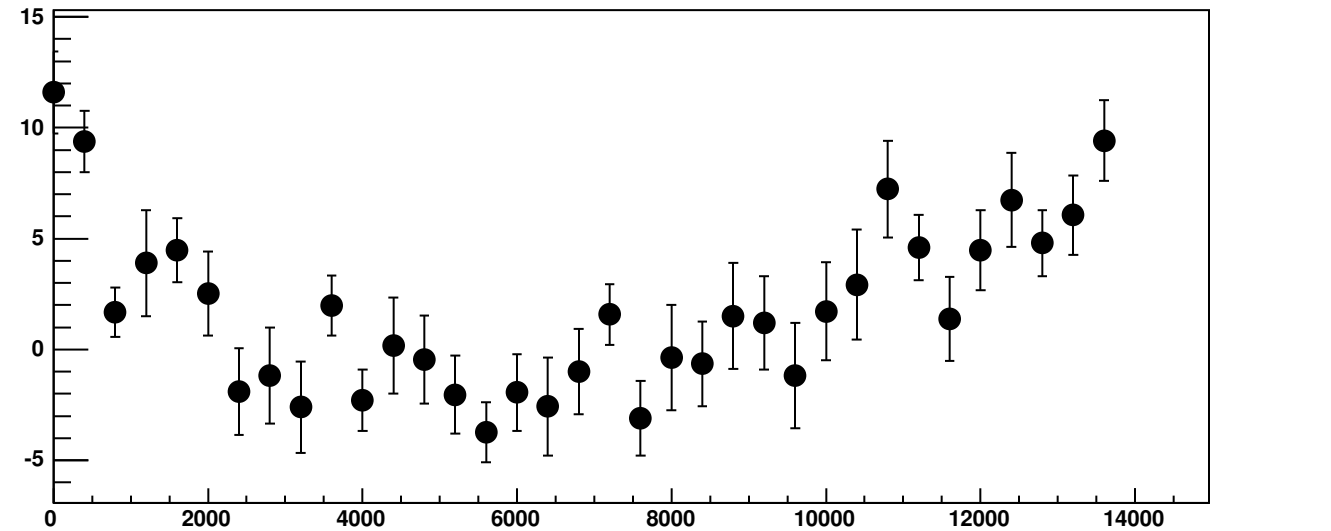
Chip 8, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

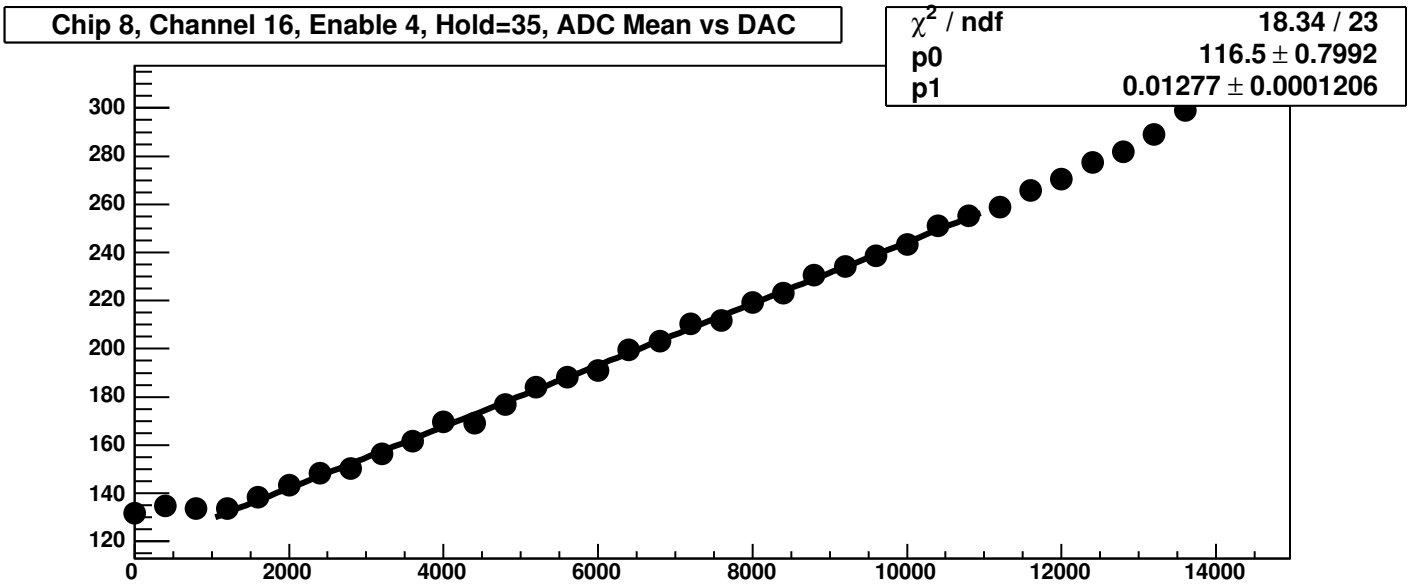


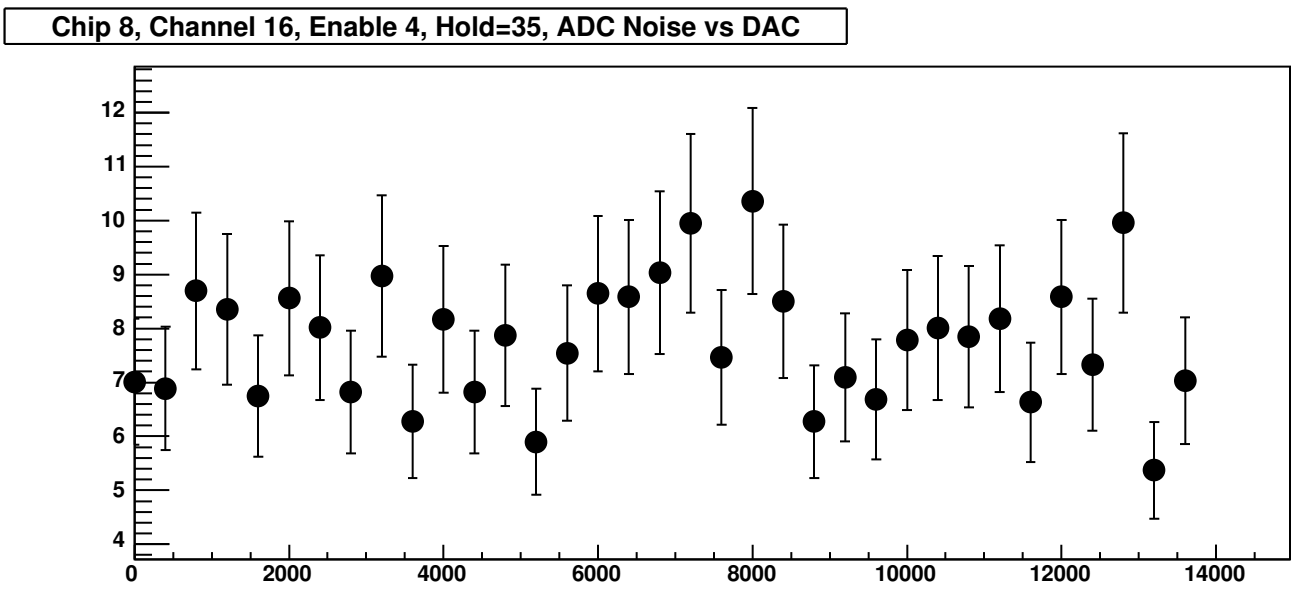
Chip 8, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC

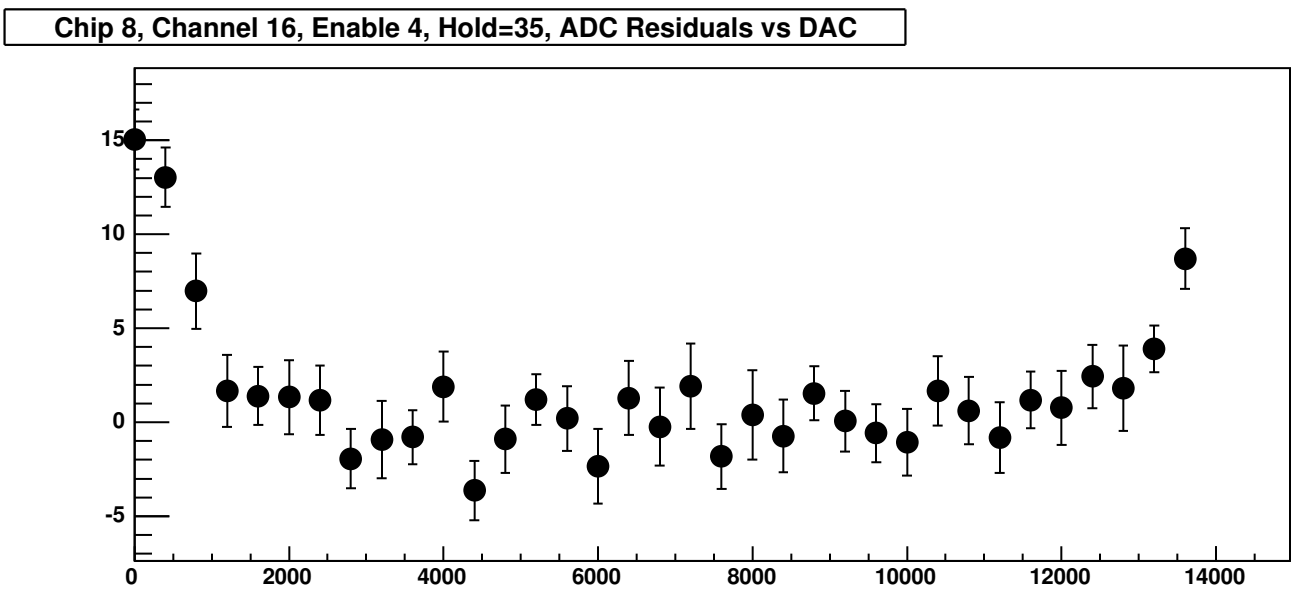


Chip 8, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

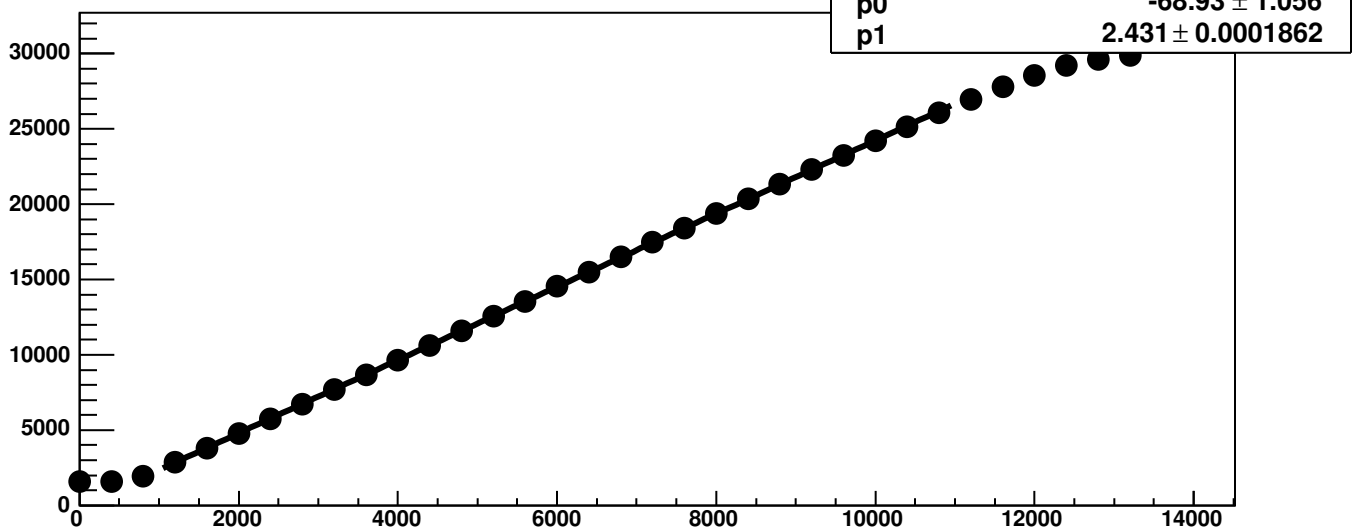



Chip 8, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 18.34 / 23
p0 116.5 ± 0.7992
p1 0.01277 ± 0.0001206
300
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

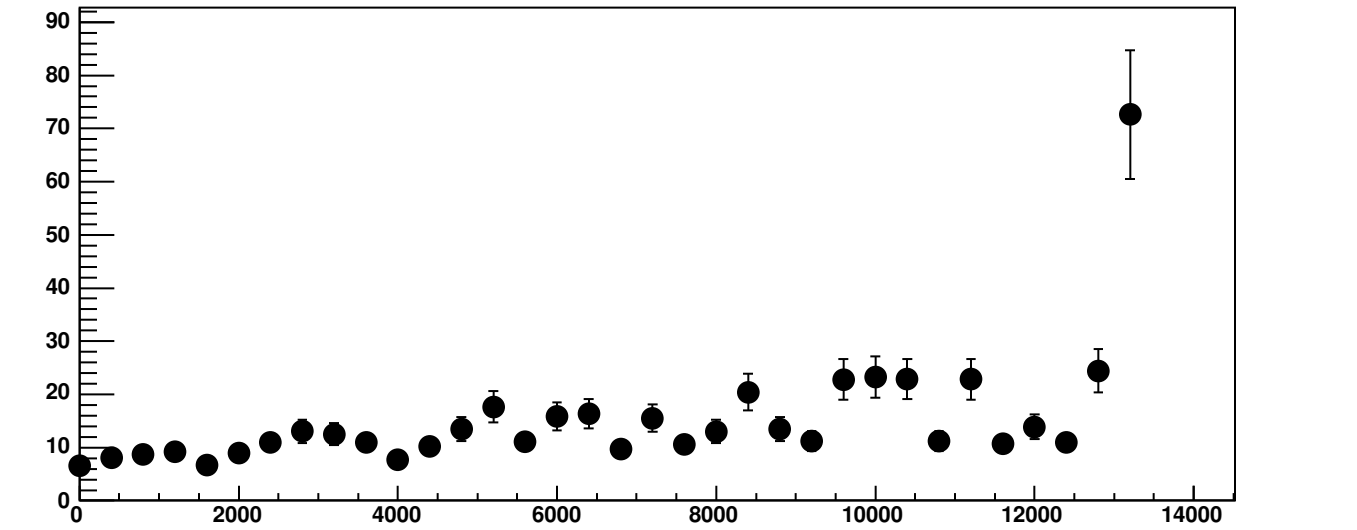

Chip 8, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

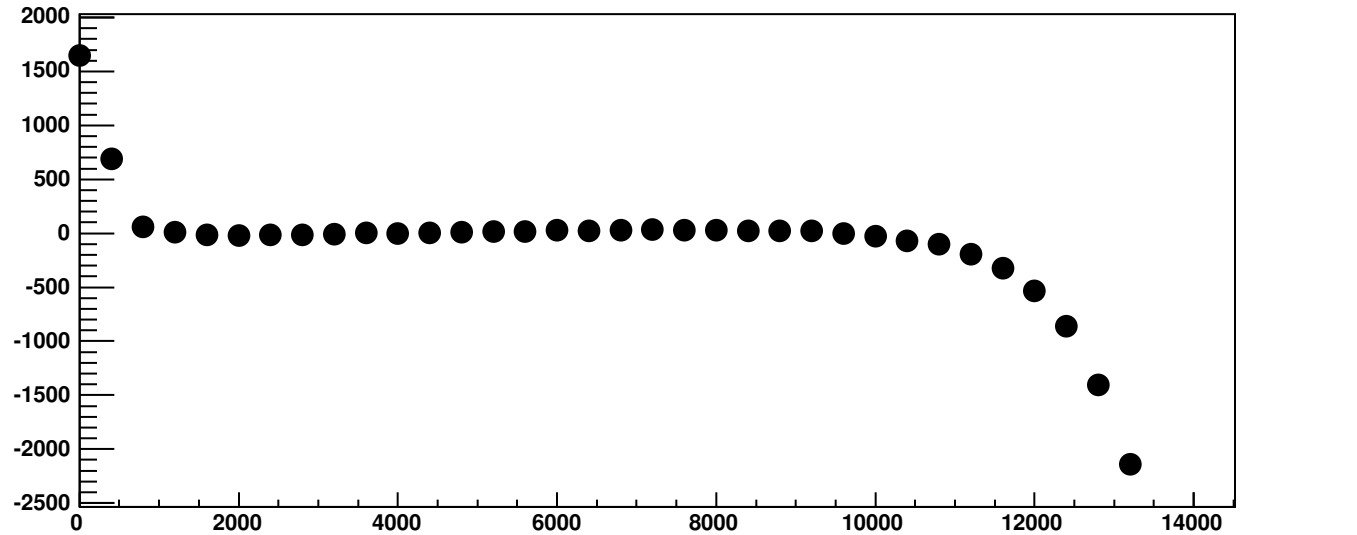
Chip 8, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC



Chip 8, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC

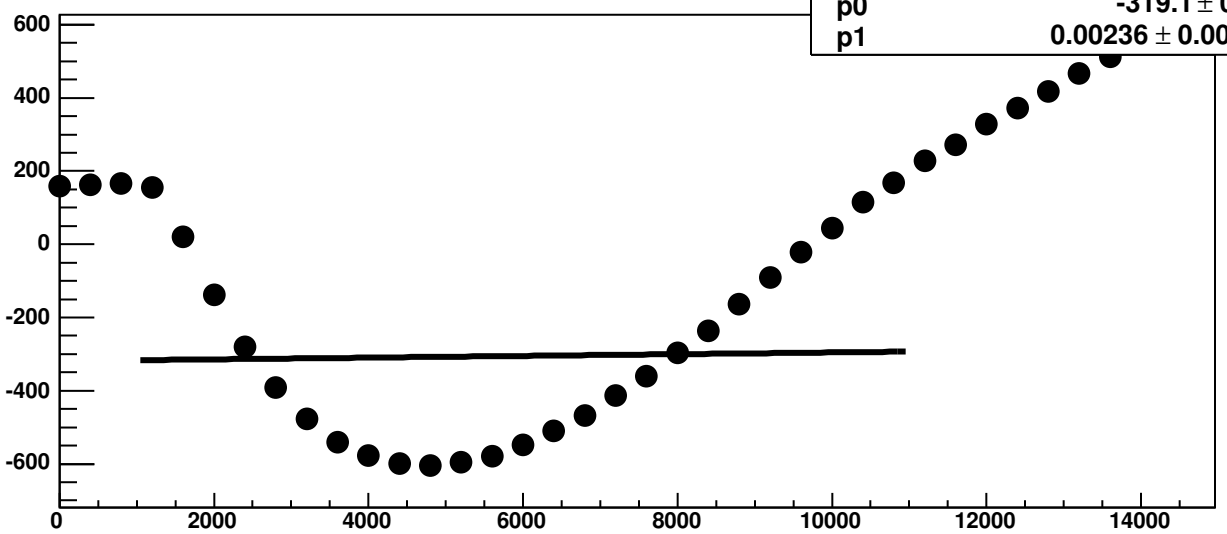


Chip 8, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC

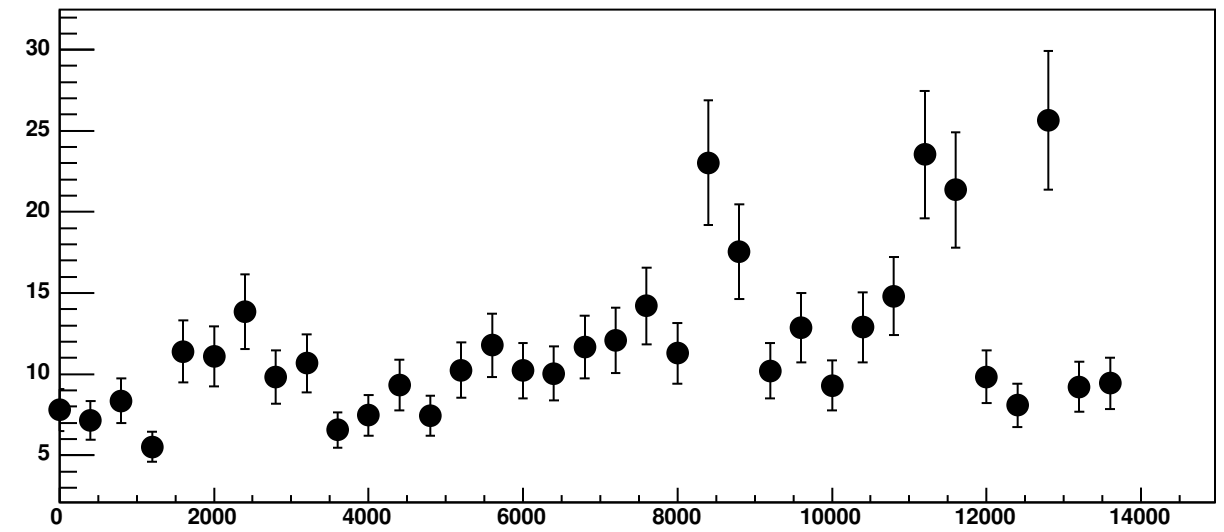


Chip 8, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

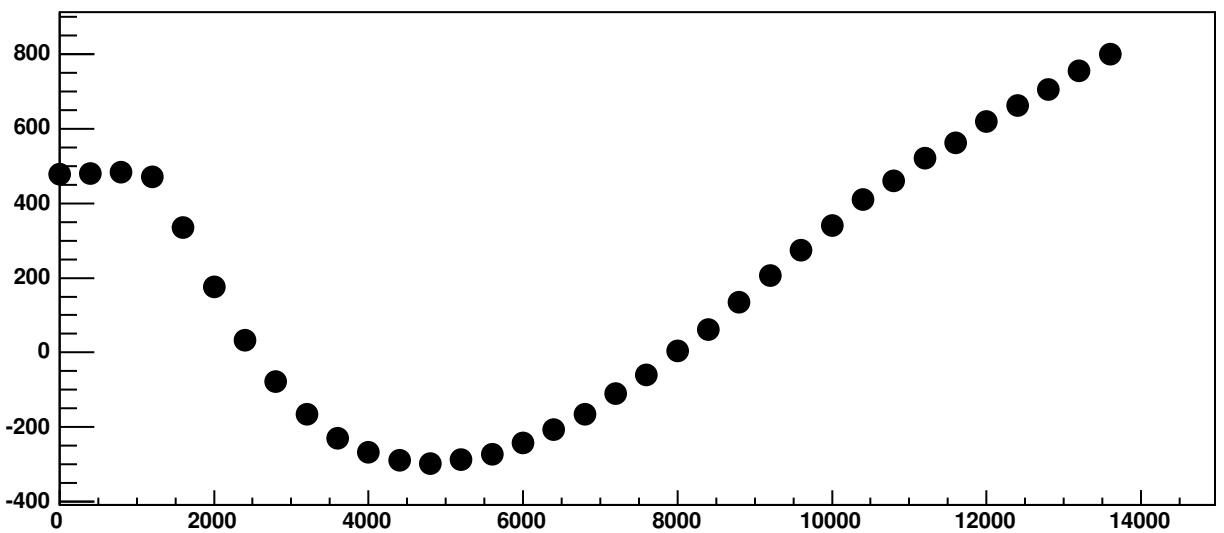
χ^2 / ndf	3.937e+05 / 23
p0	-319.1 ± 0.9378
p1	0.00236 ± 0.0001621

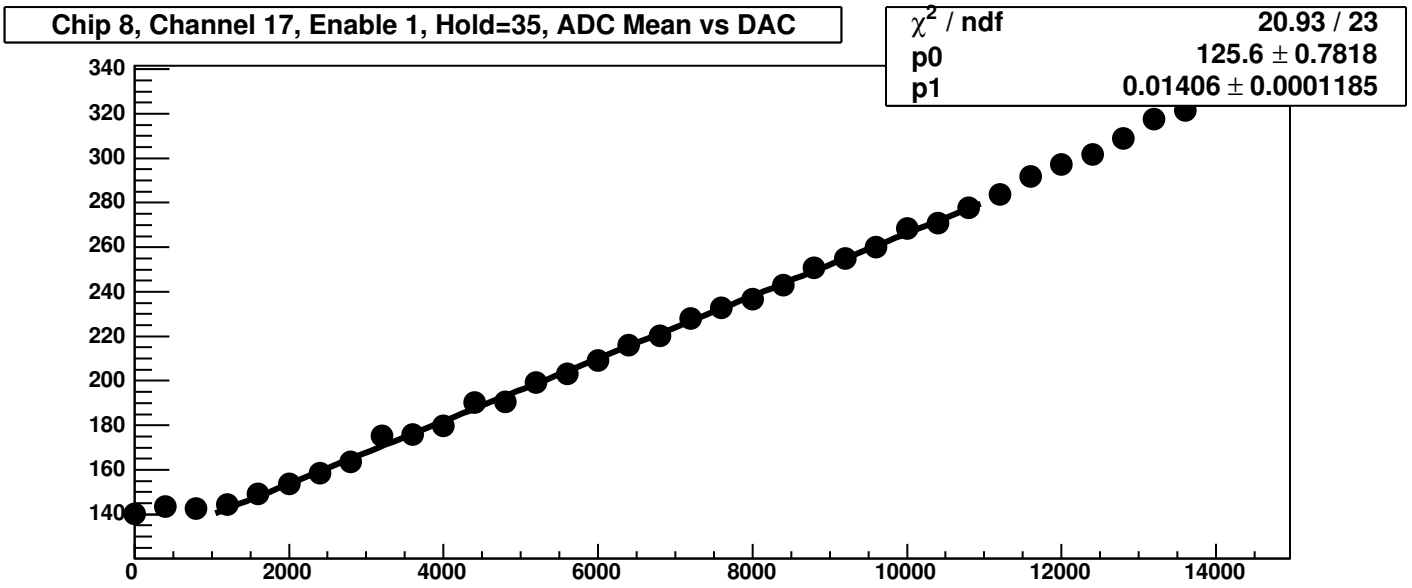


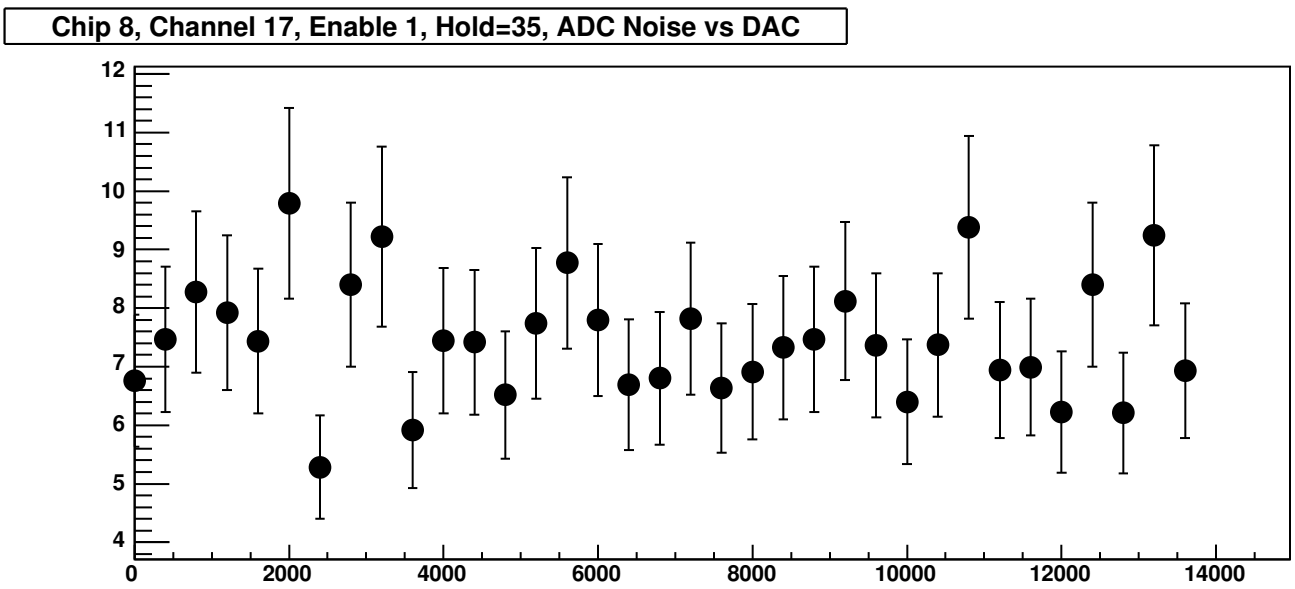
Chip 8, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC

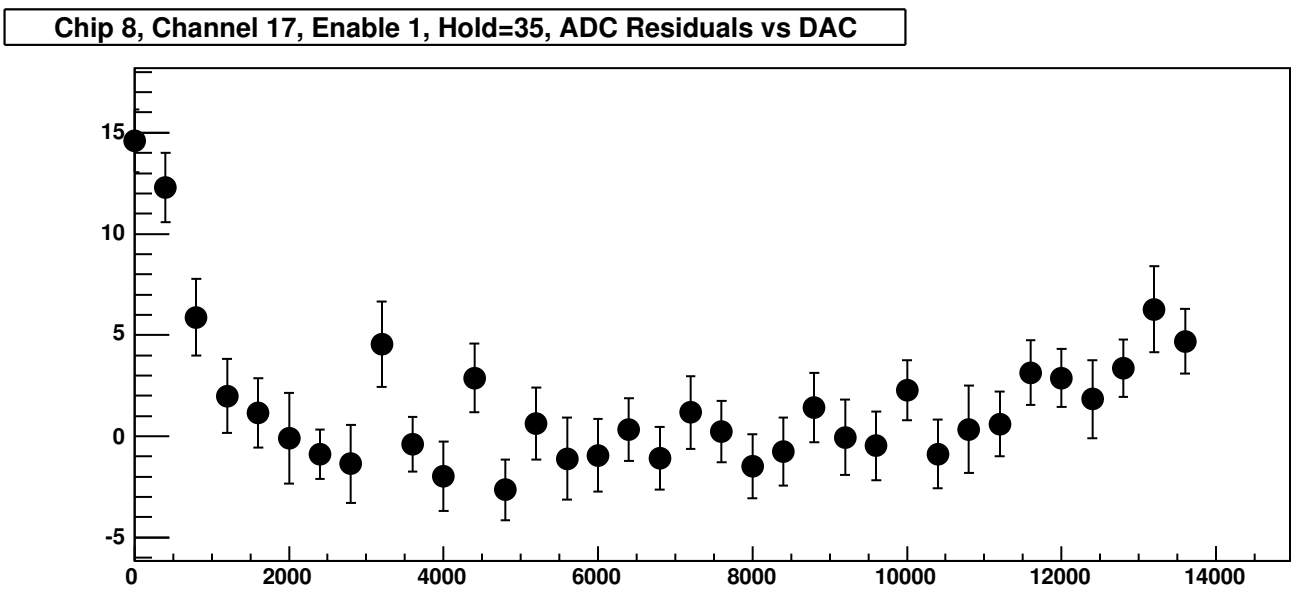


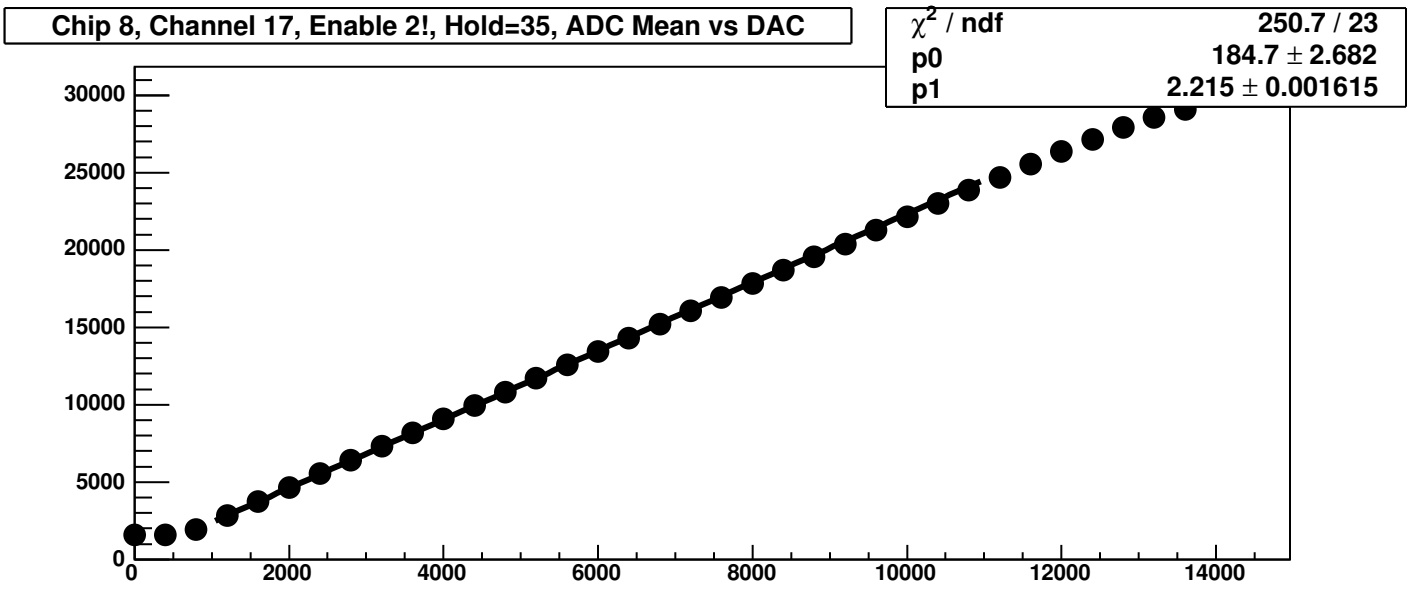
Chip 8, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

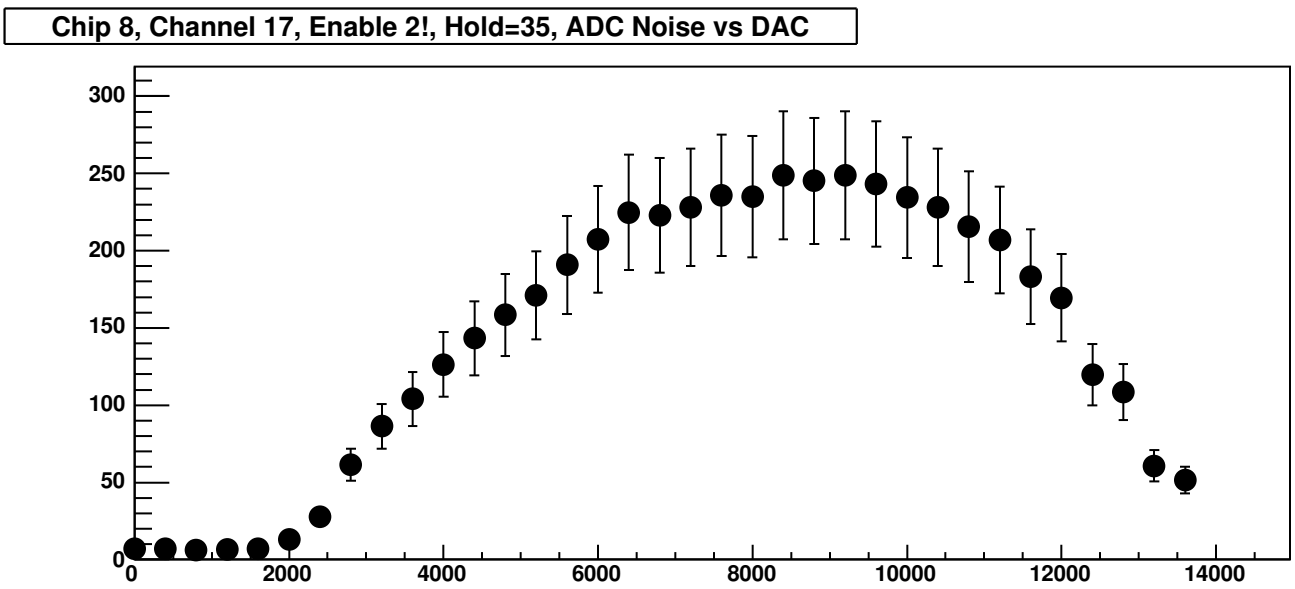


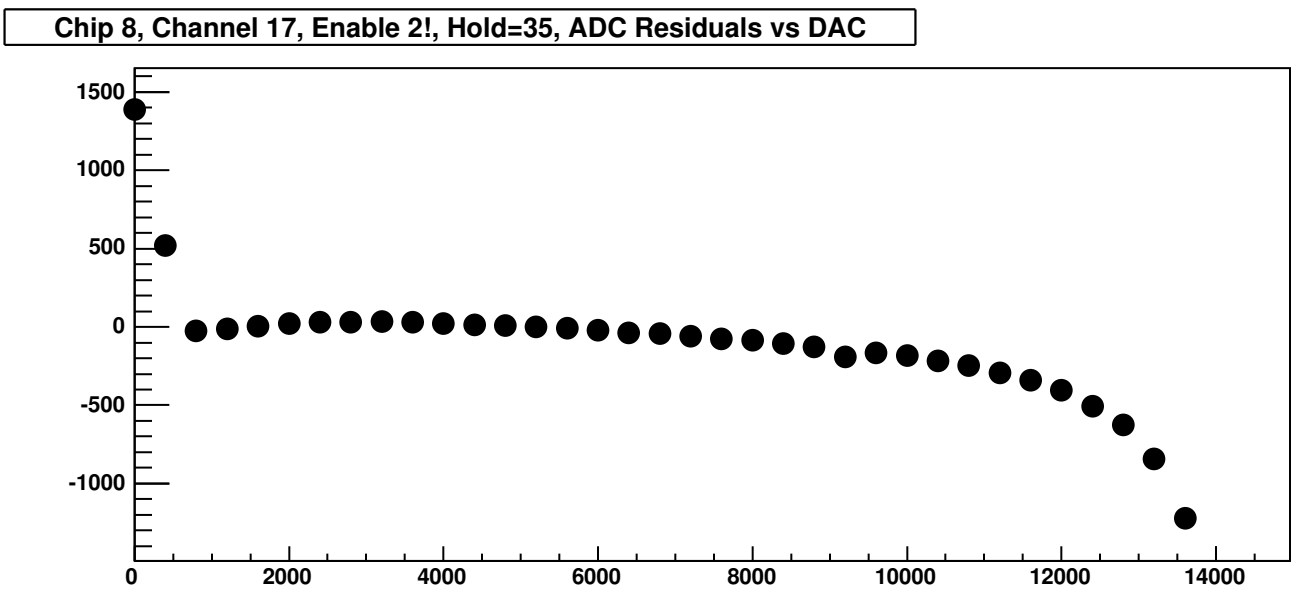

Chip 8, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 20.93 / 23
p0 125.6 ± 0.7818
p1 0.01406 ± 0.0001185
340
320
300
280
260
240
220
200
180
160
140
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

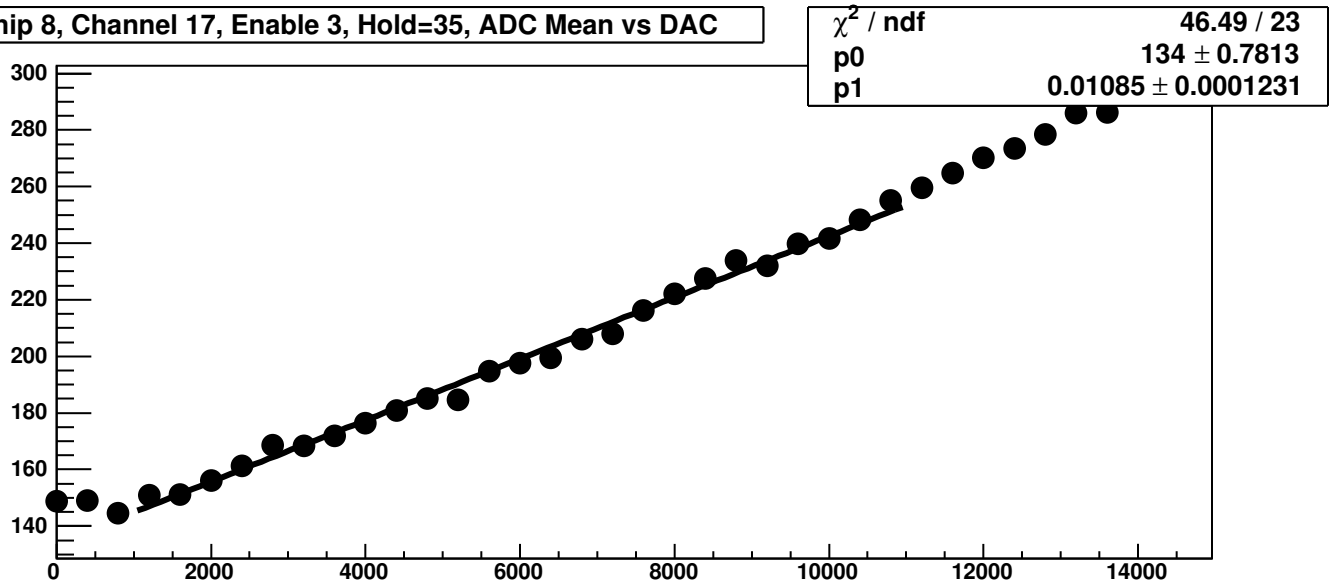

Chip 8, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 250.7 / 23
p0 184.7 ± 2.682
p1 2.215 ± 0.001615
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

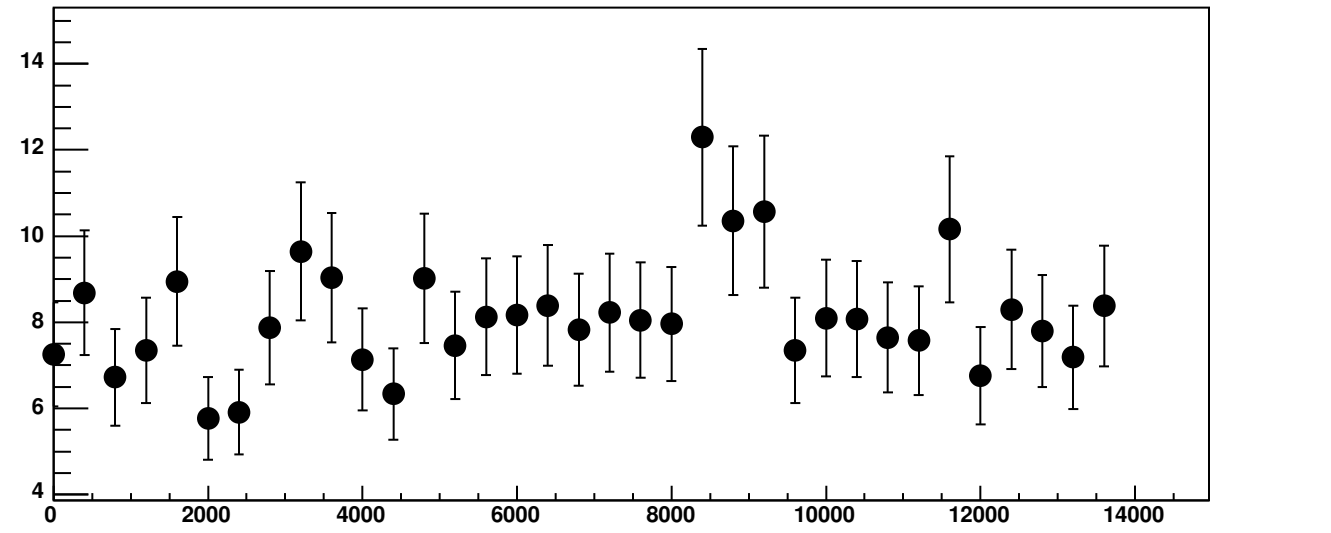

Chip 8, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

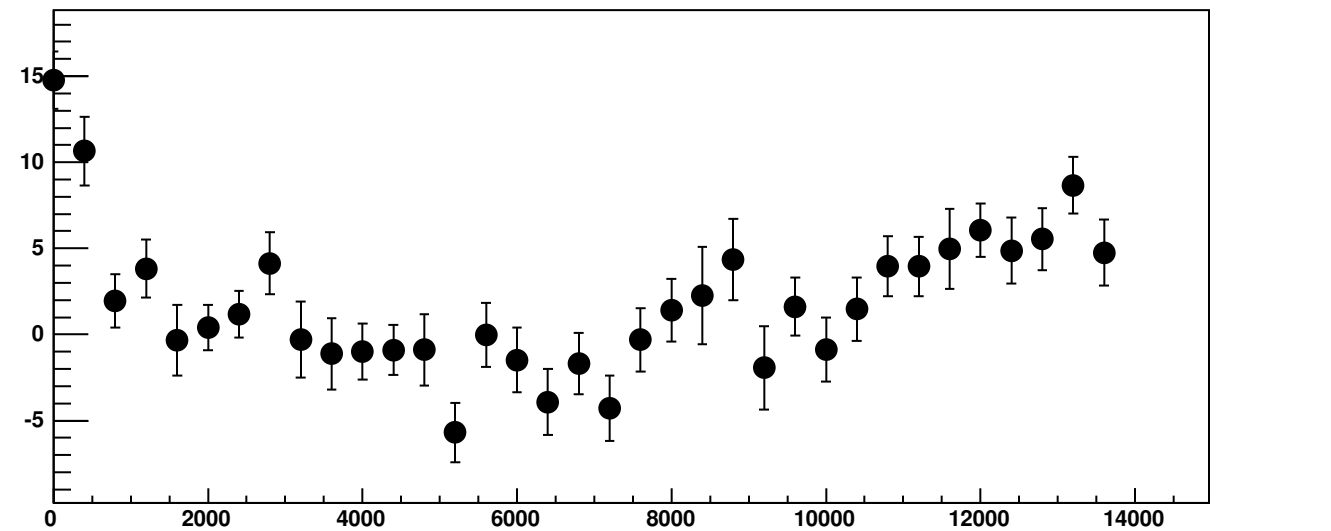
Chip 8, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

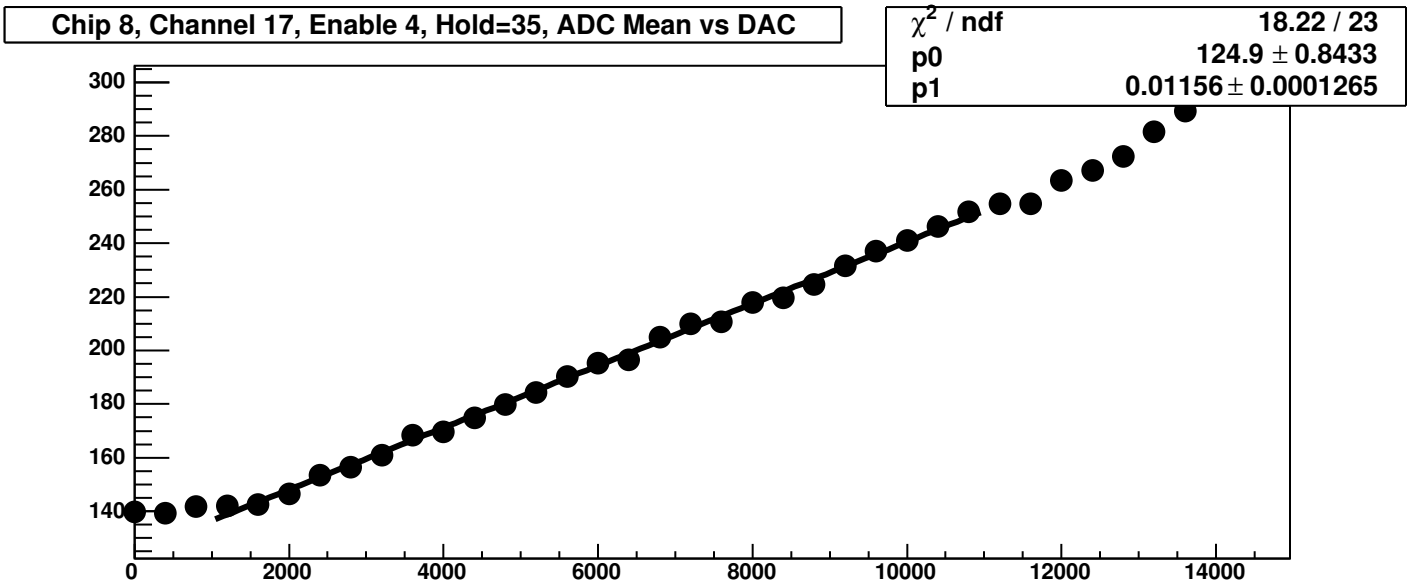


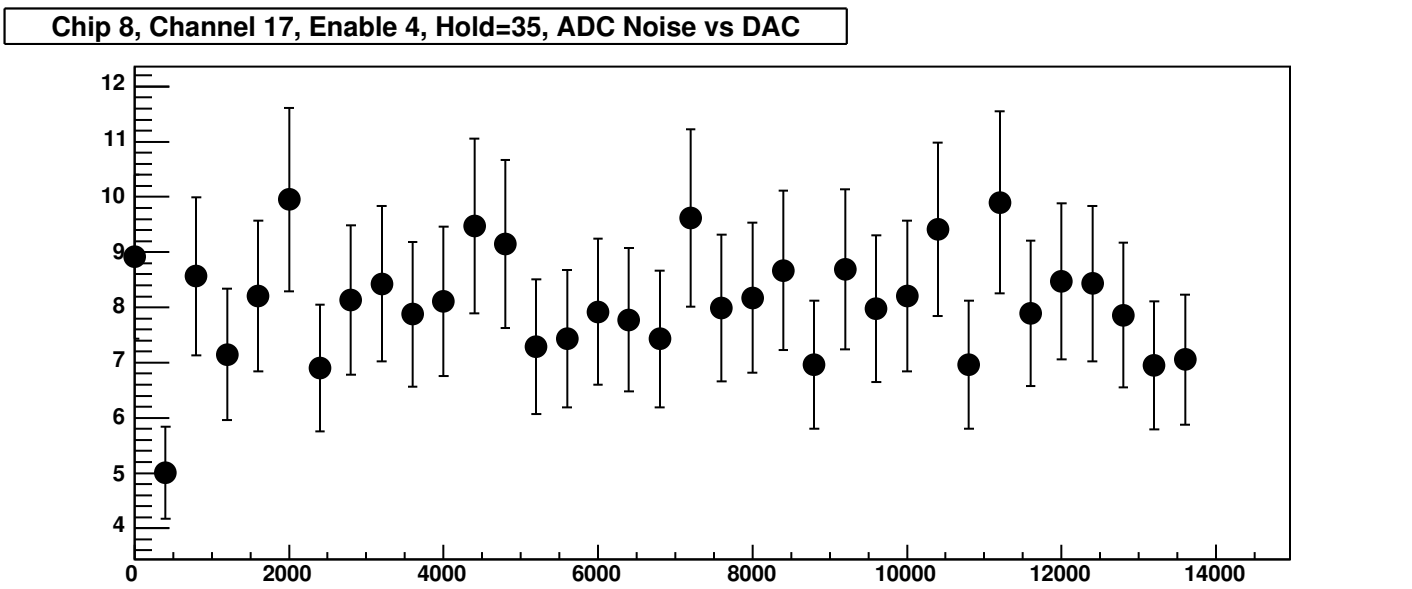
Chip 8, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC

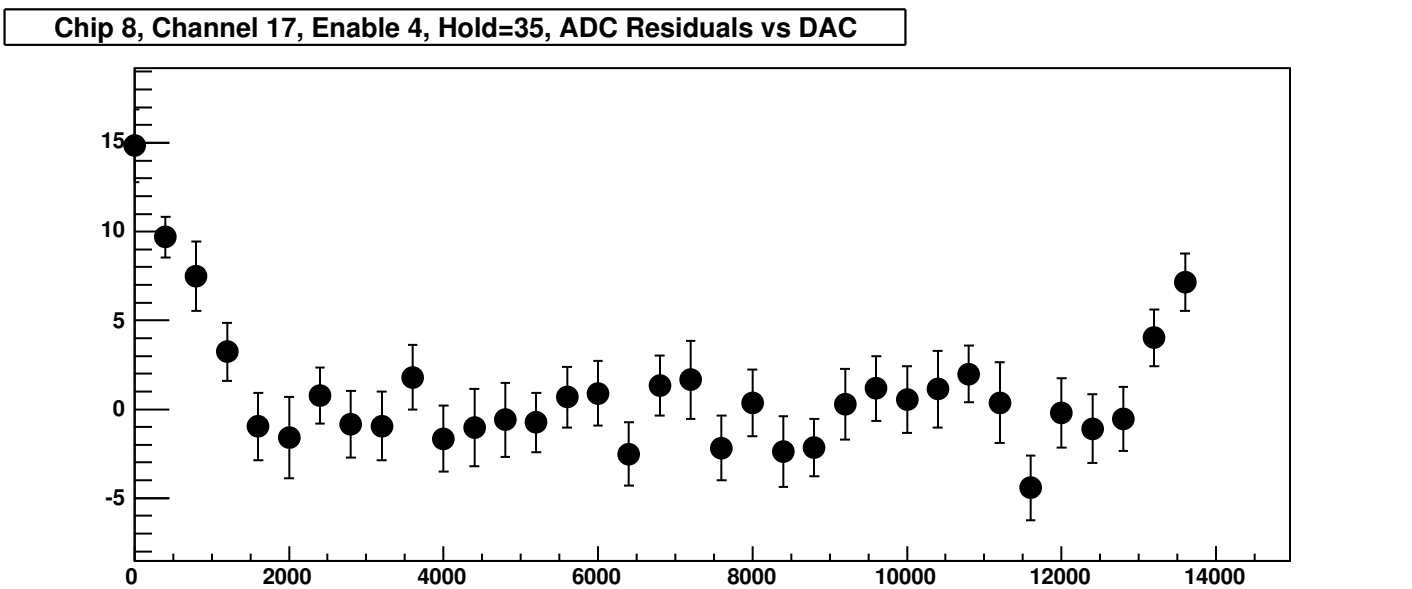


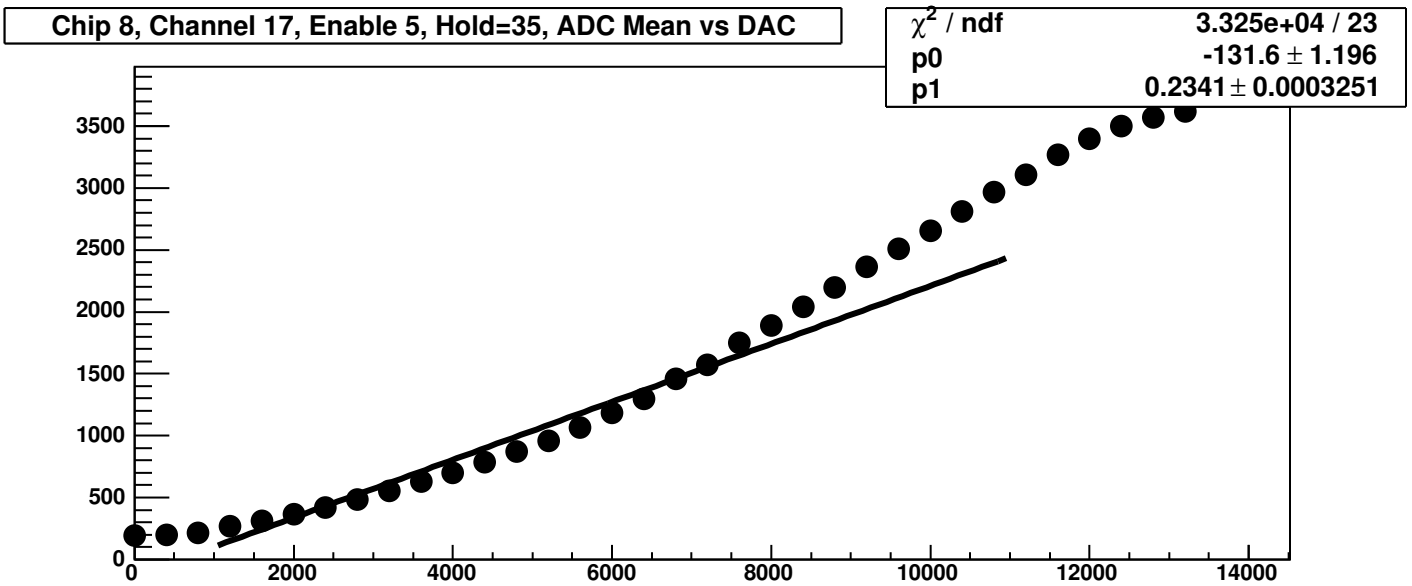
Chip 8, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

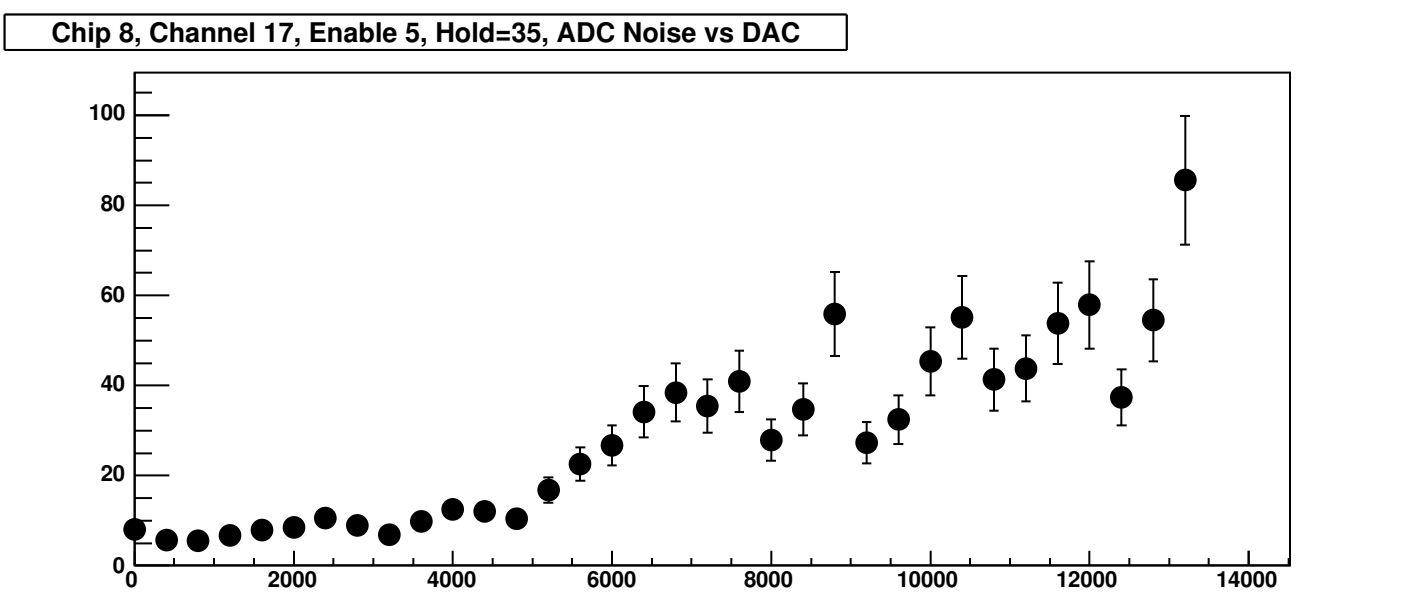


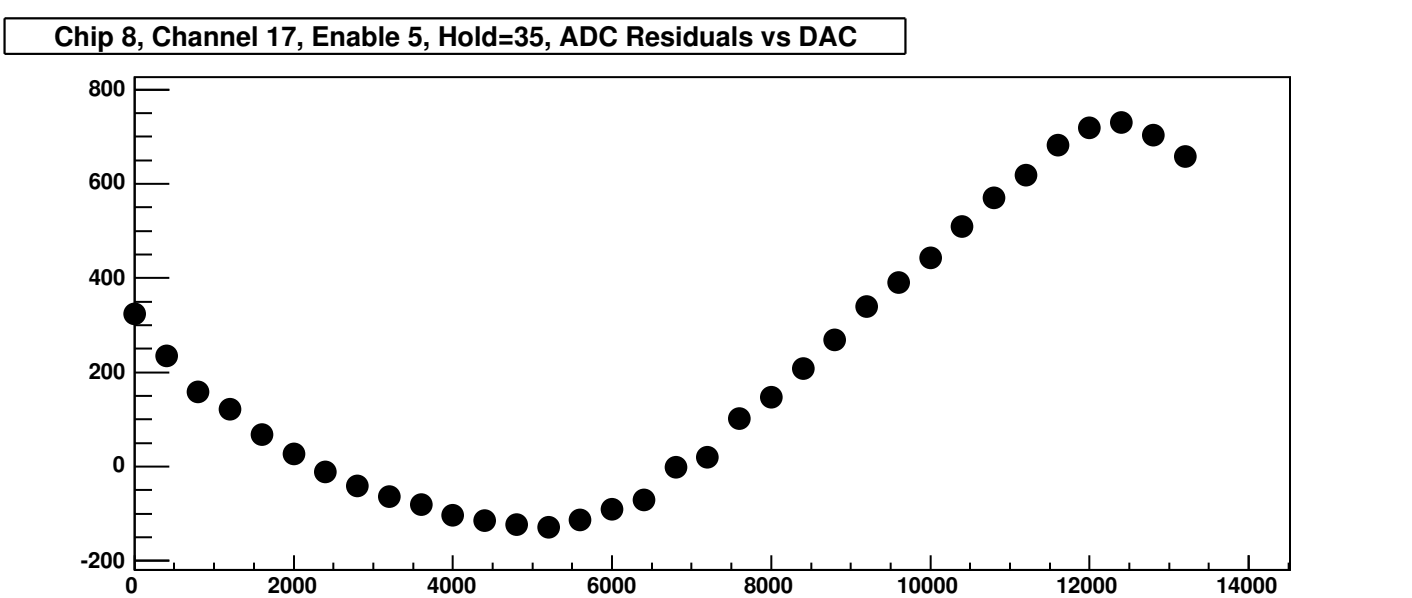

Chip 8, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 18.22 / 23
p0 124.9 ± 0.8433
p1 0.01156 ± 0.0001265
300
280
260
240
220
200
180
160
140
0
2000
4000
6000
8000
10000
12000
14000

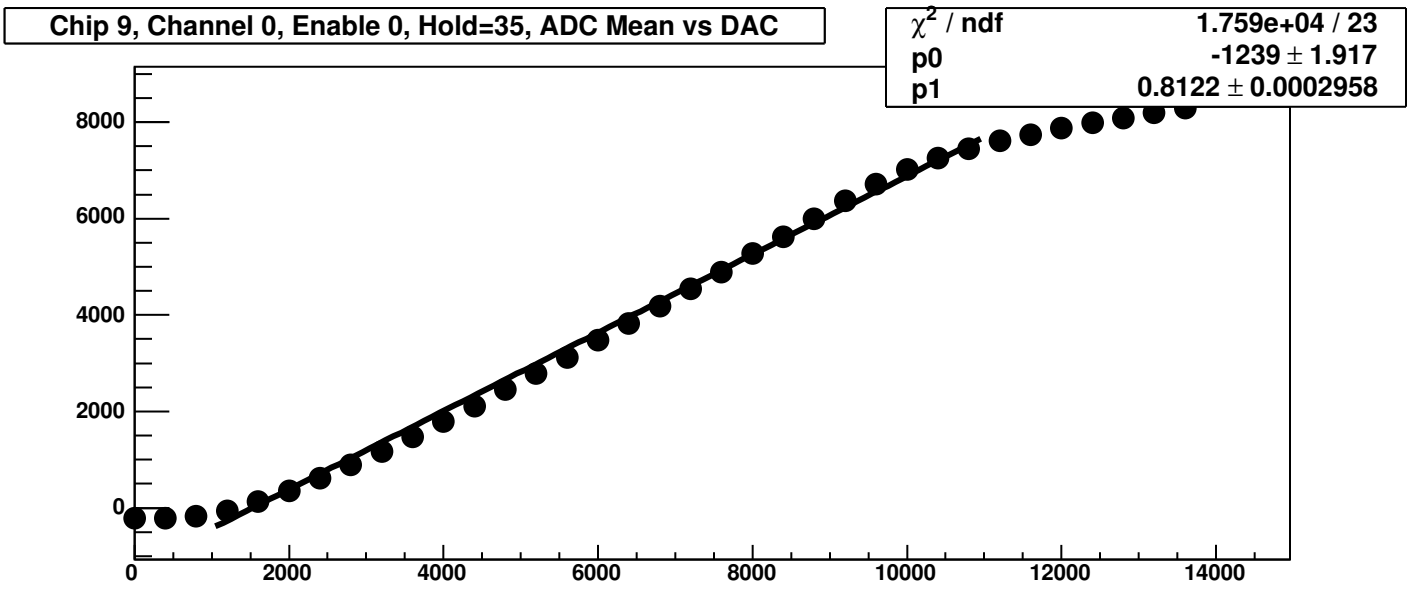

Chip 8, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

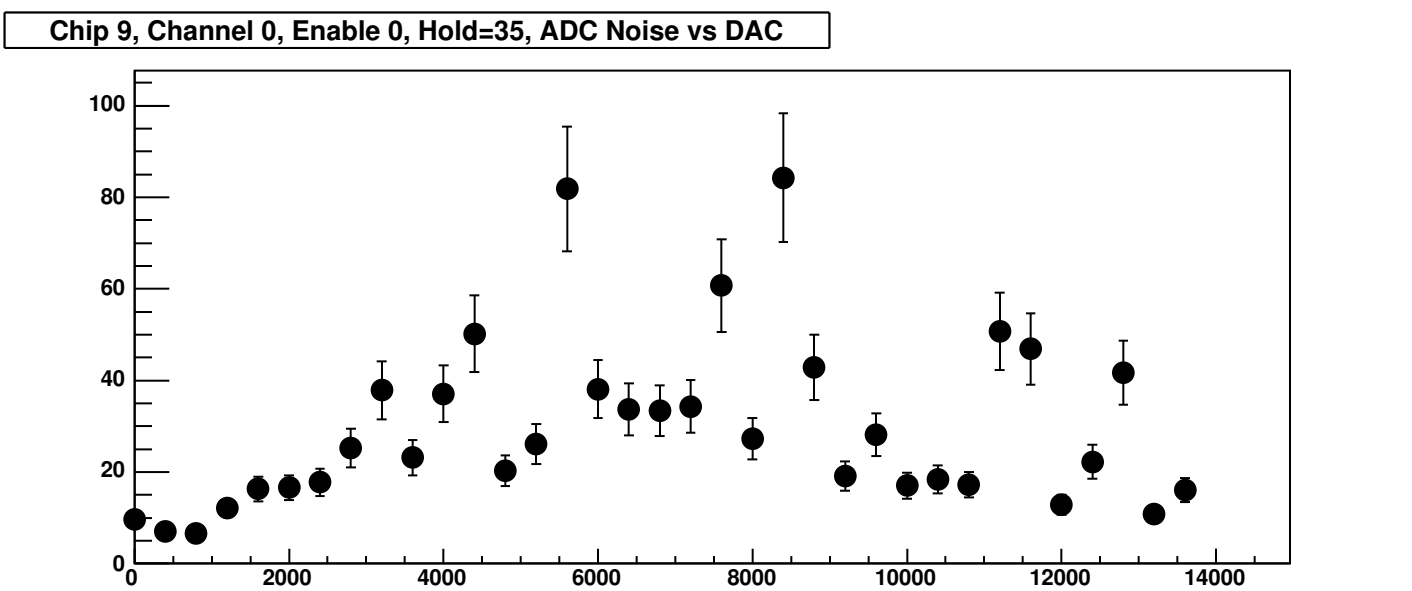

Chip 8, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

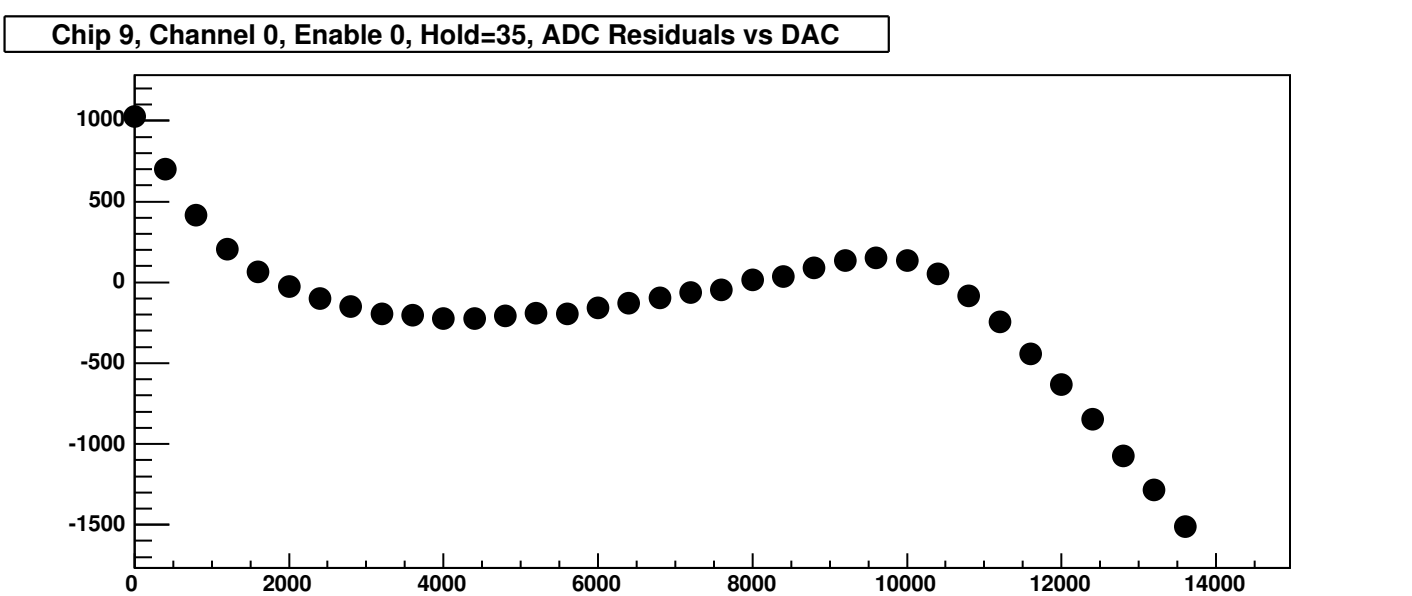

Chip 8, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.325e+04 / 23
p0 -131.6 ± 1.196
p1 0.2341 ± 0.0003251
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

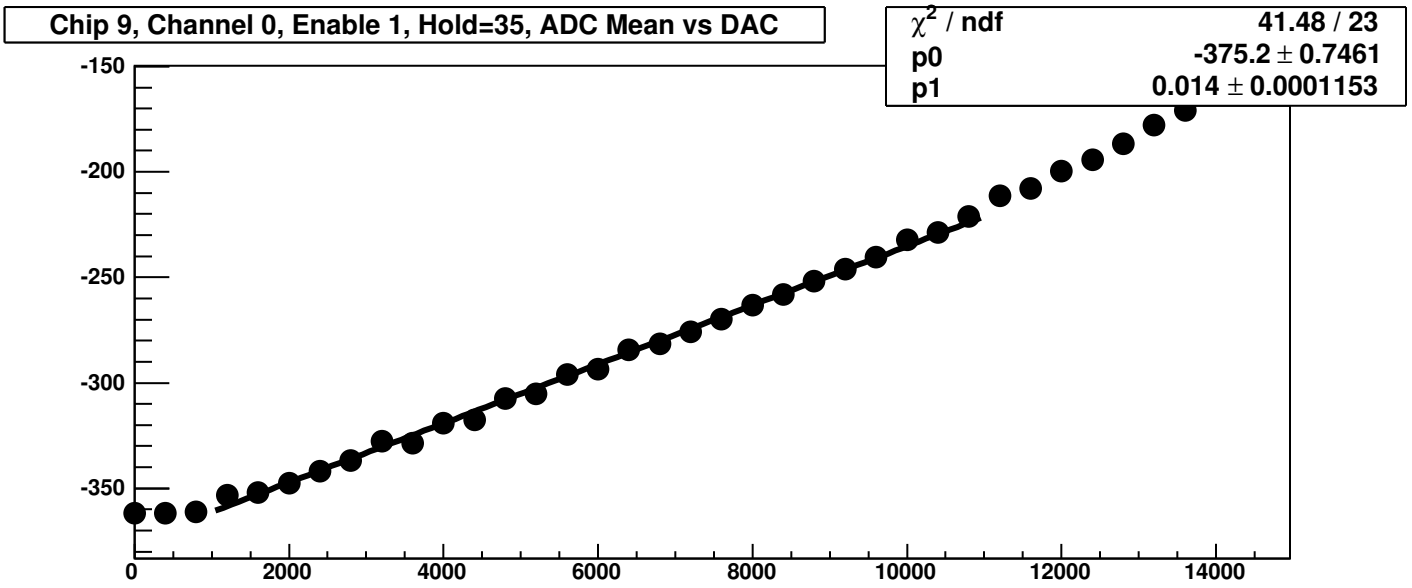

Chip 8, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

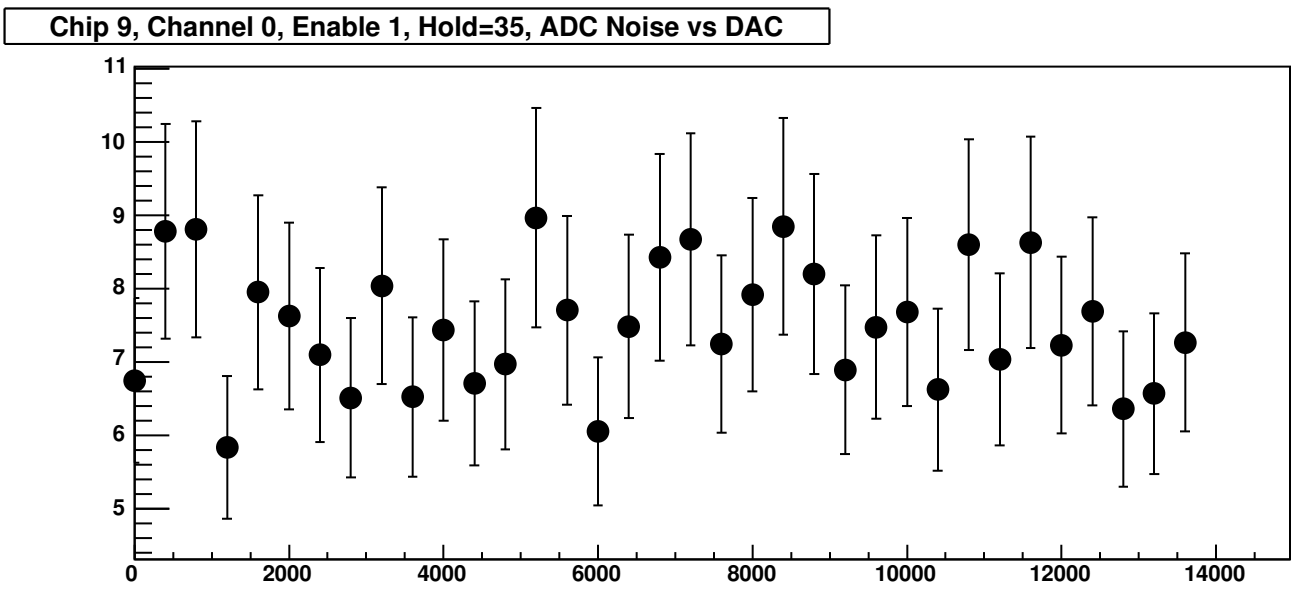

Chip 8, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

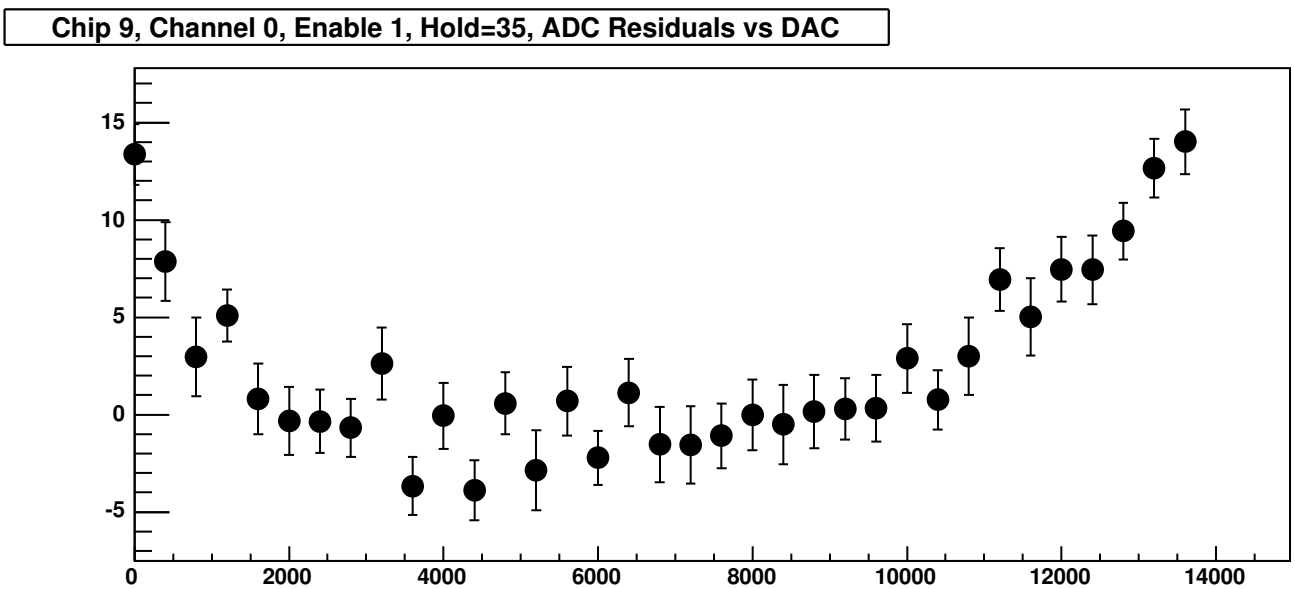

Chip 9, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.759e+04 / 23
p0 -1239 ± 1.917
p1 0.8122 ± 0.0002958
8000
6000
4000
2000
0
0
2000
4000
6000
8000
10000
12000
14000

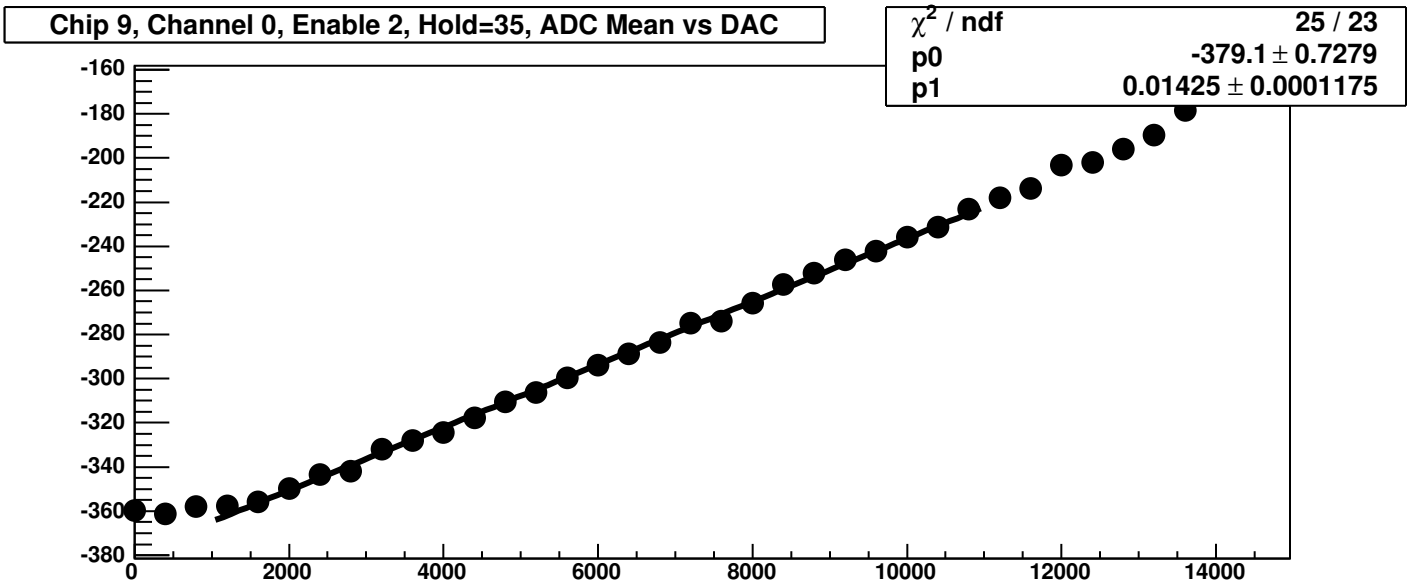

Chip 9, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

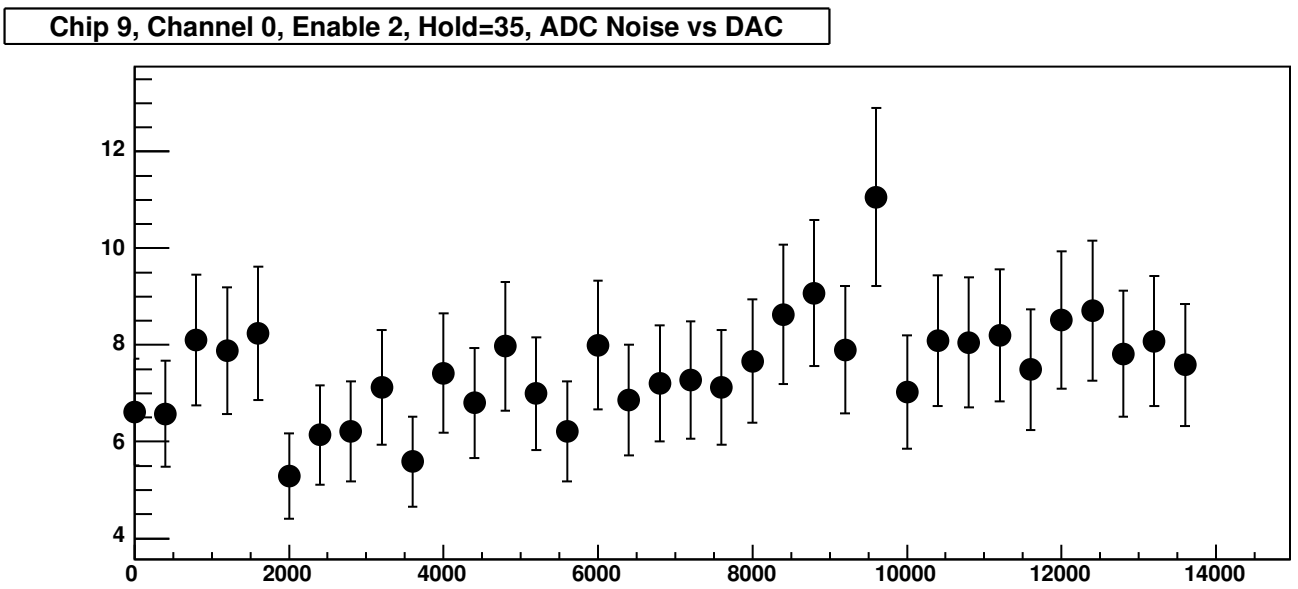

Chip 9, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

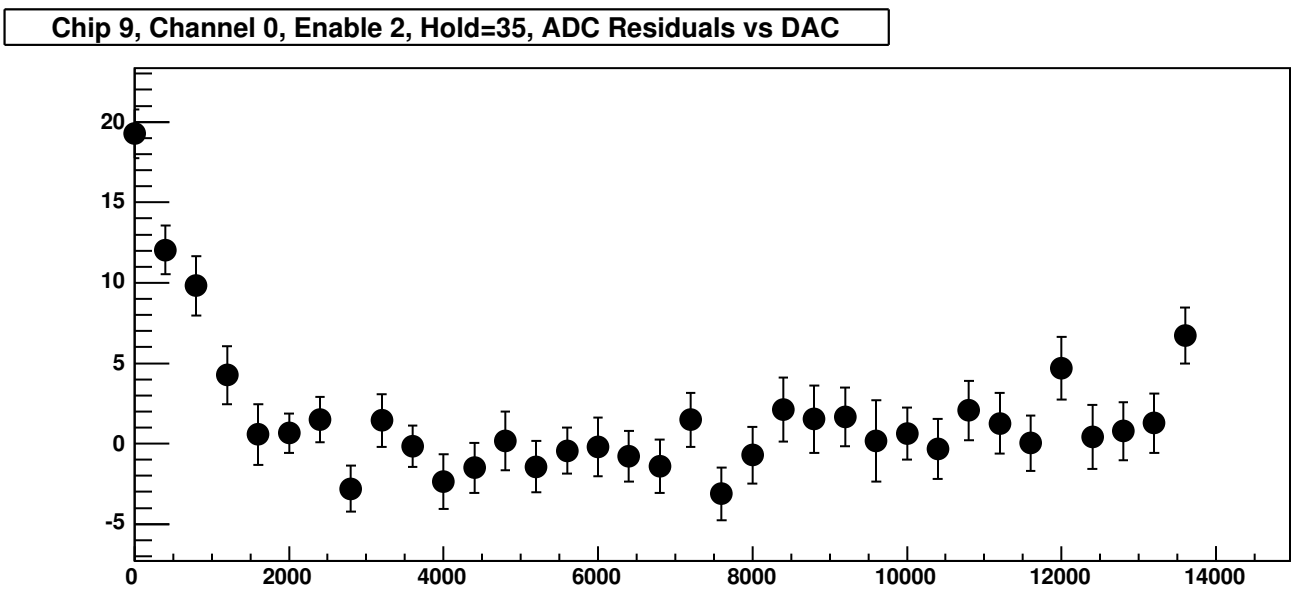

Chip 9, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 41.48 / 23
p0 -375.2 ± 0.7461
p1 0.014 ± 0.0001153
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

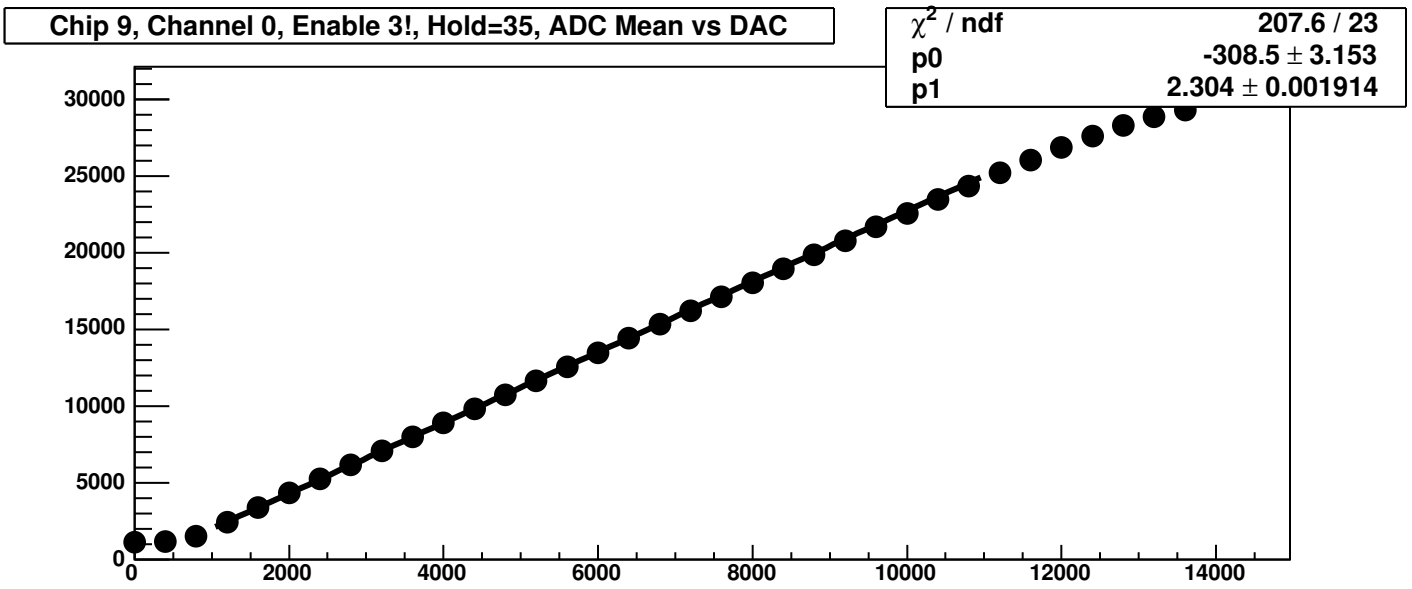

Chip 9, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

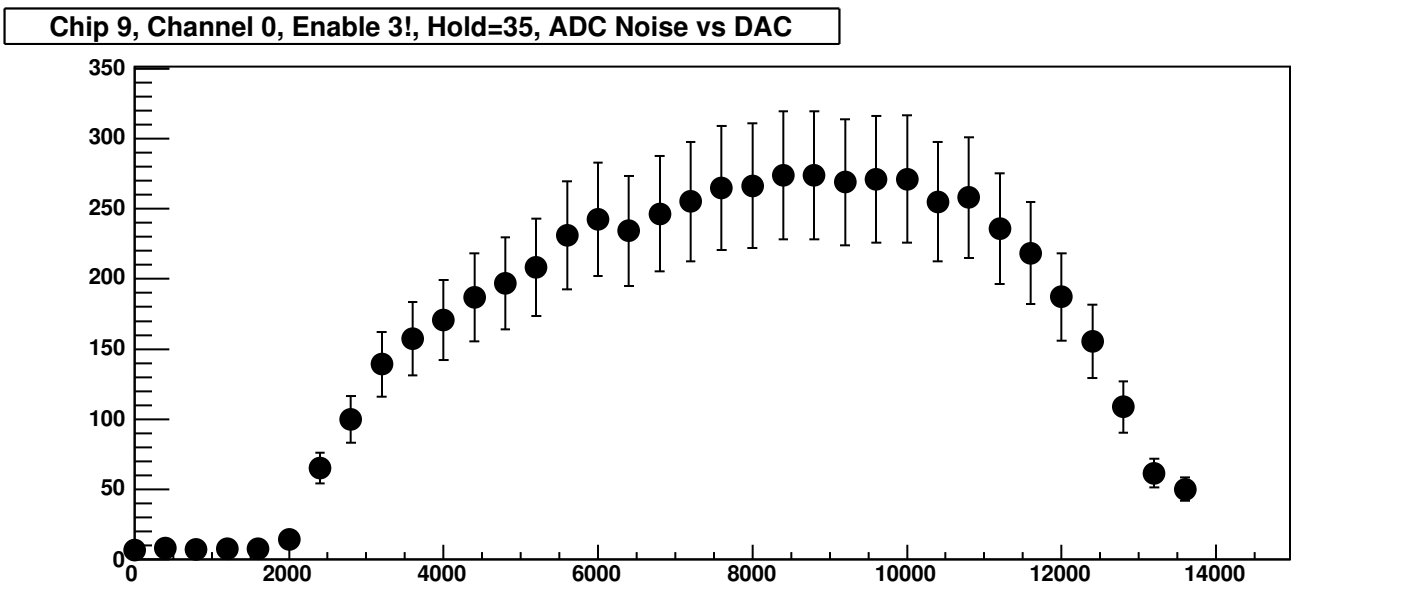

Chip 9, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

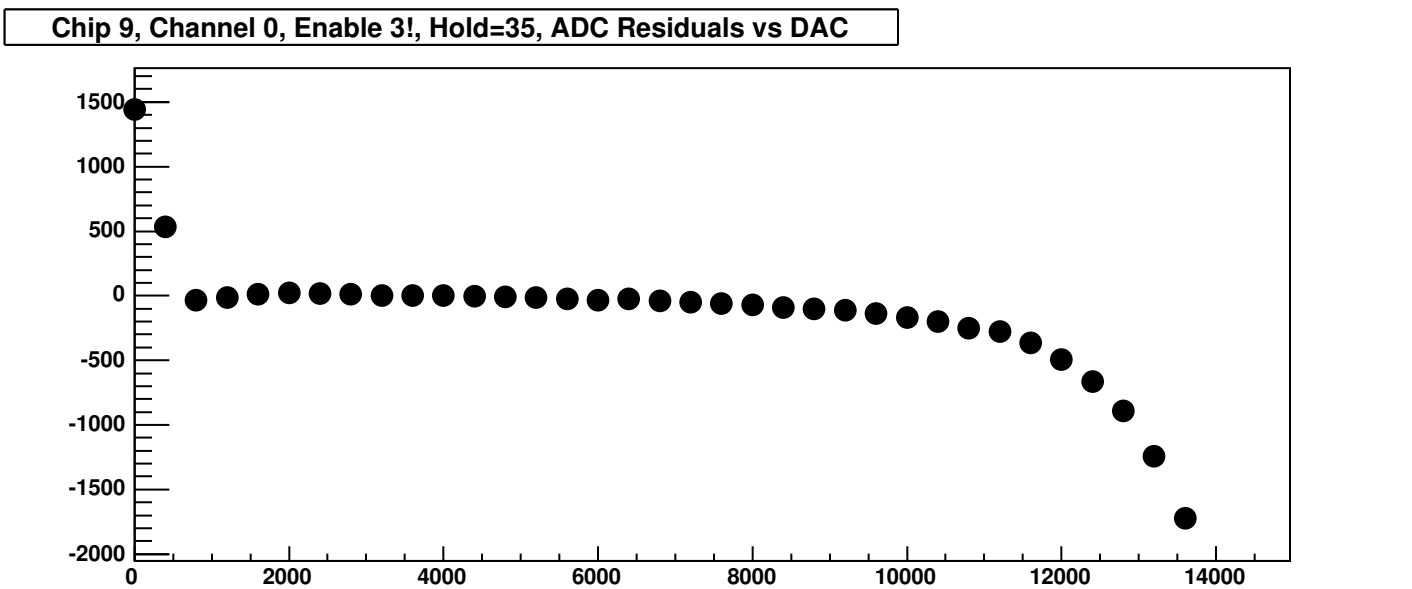

Chip 9, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 25 / 23
p0 -379.1 ± 0.7279
p1 0.01425 ± 0.0001175
-160
-180
-200
-220
-240
-260
-280
-300
-320
-340
-360
-380
0
2000
4000
6000
8000
10000
12000
14000

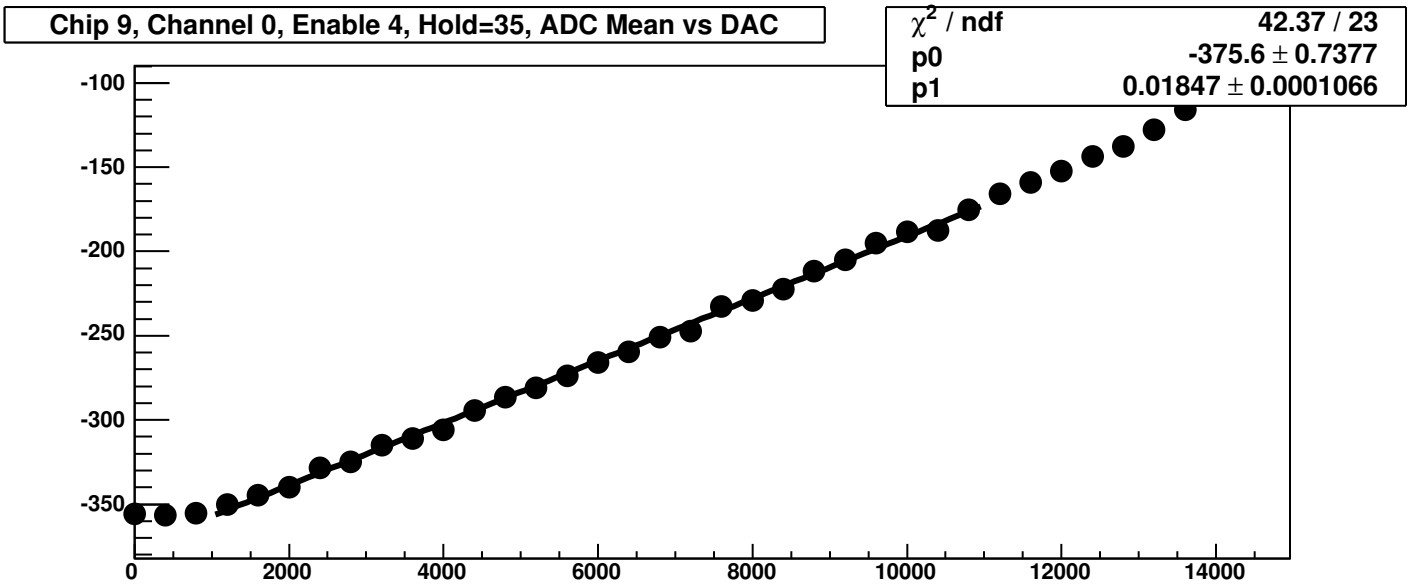

Chip 9, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

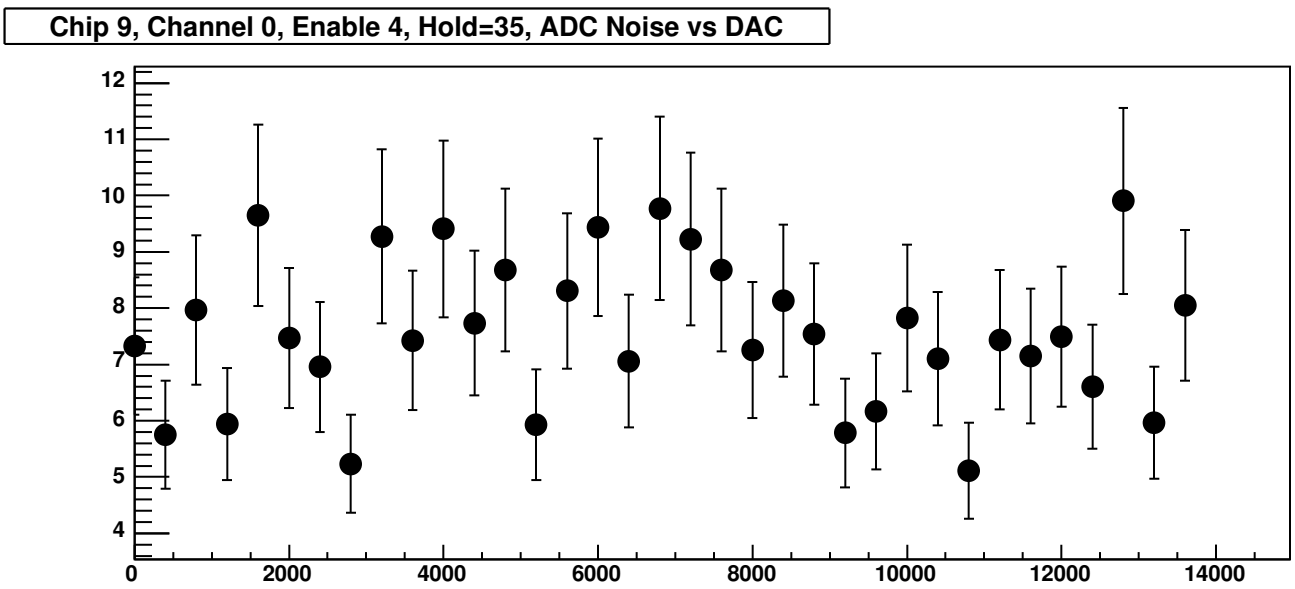

Chip 9, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

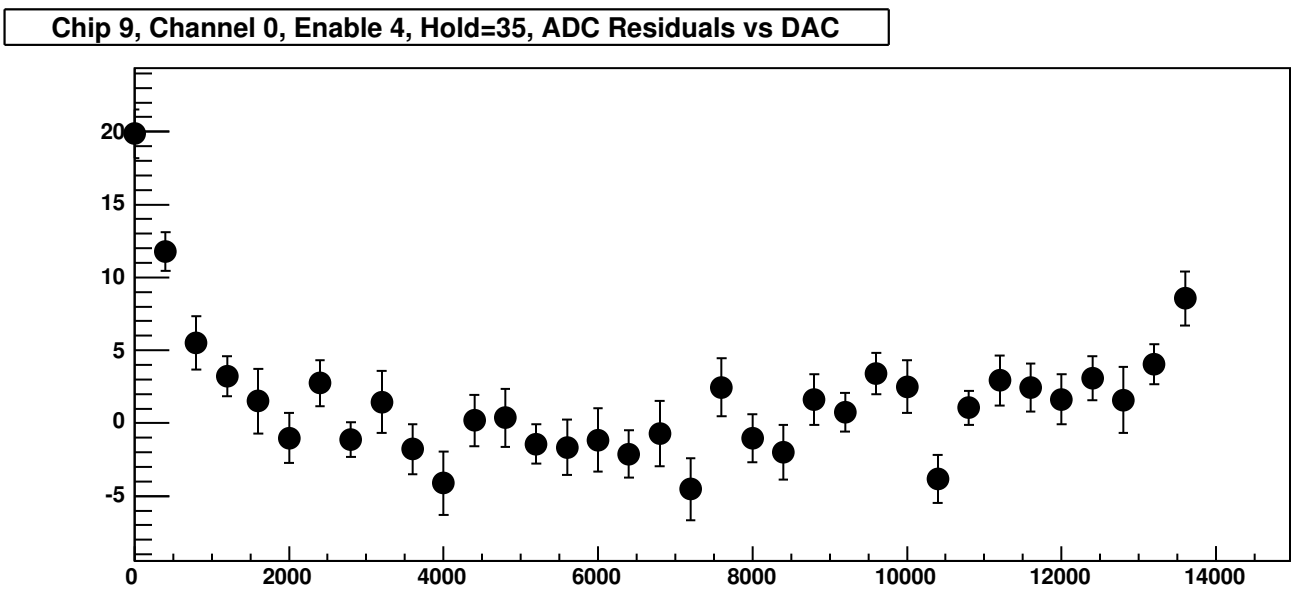

Chip 9, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 207.6 / 23
p0 -308.5 ± 3.153
p1 2.304 ± 0.001914
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

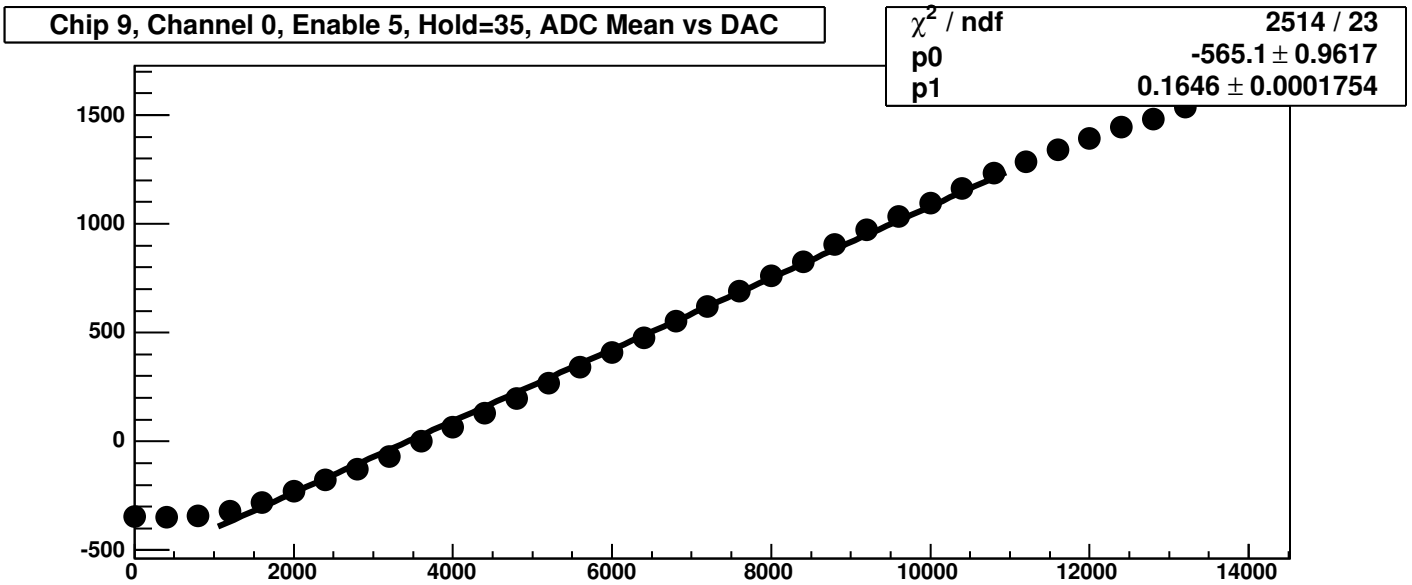

Chip 9, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

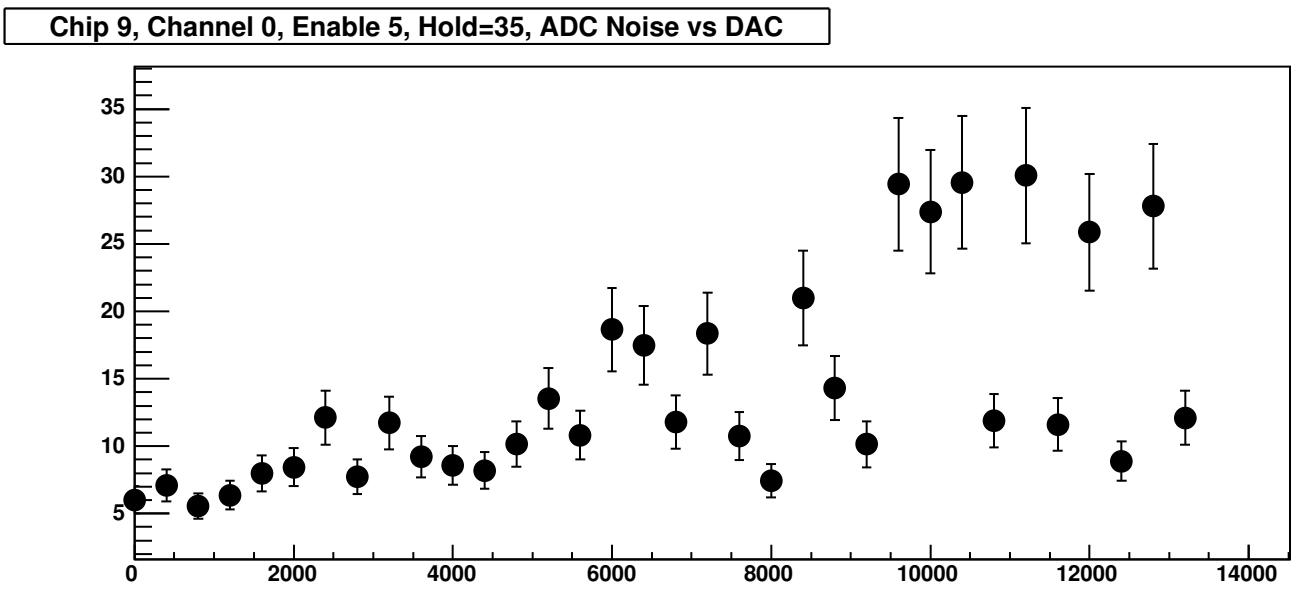

Chip 9, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

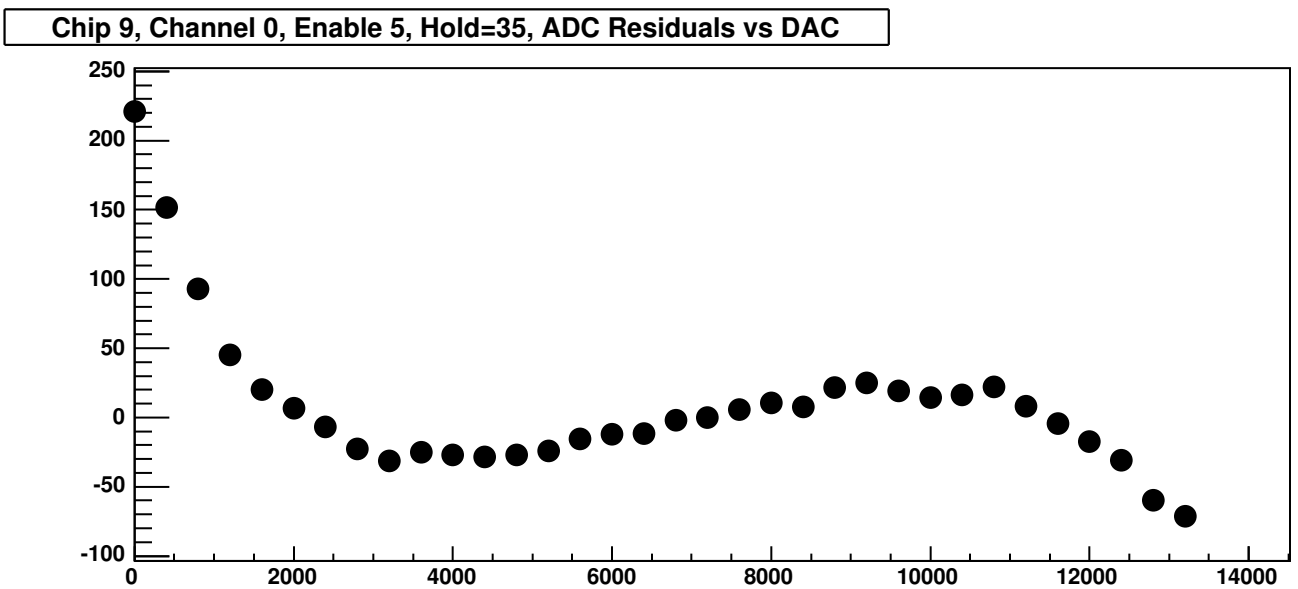

Chip 9, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 42.37 / 23
p0 -375.6 ± 0.7377
p1 0.01847 ± 0.0001066
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

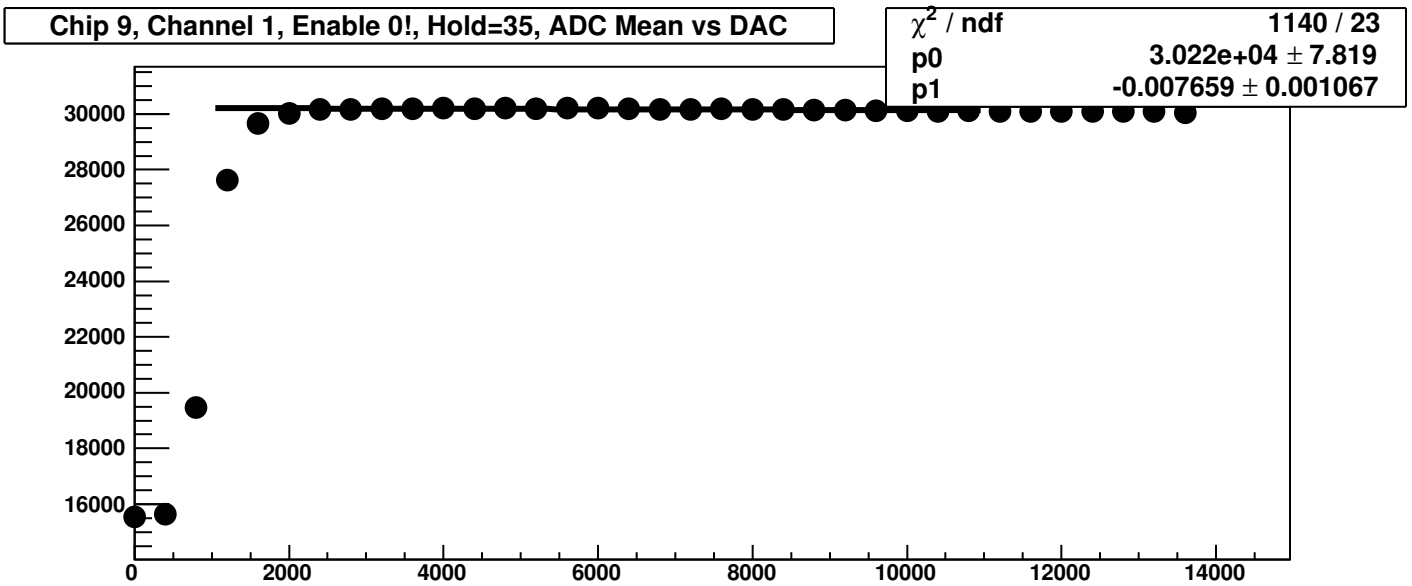

Chip 9, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

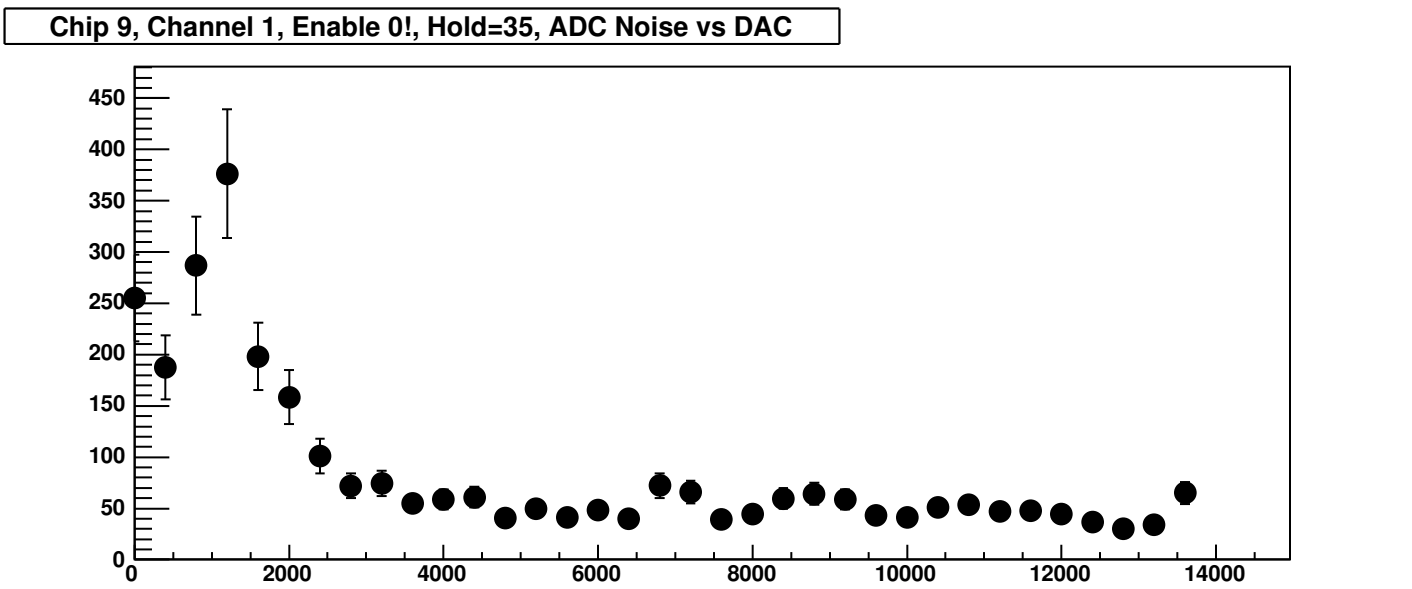

Chip 9, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

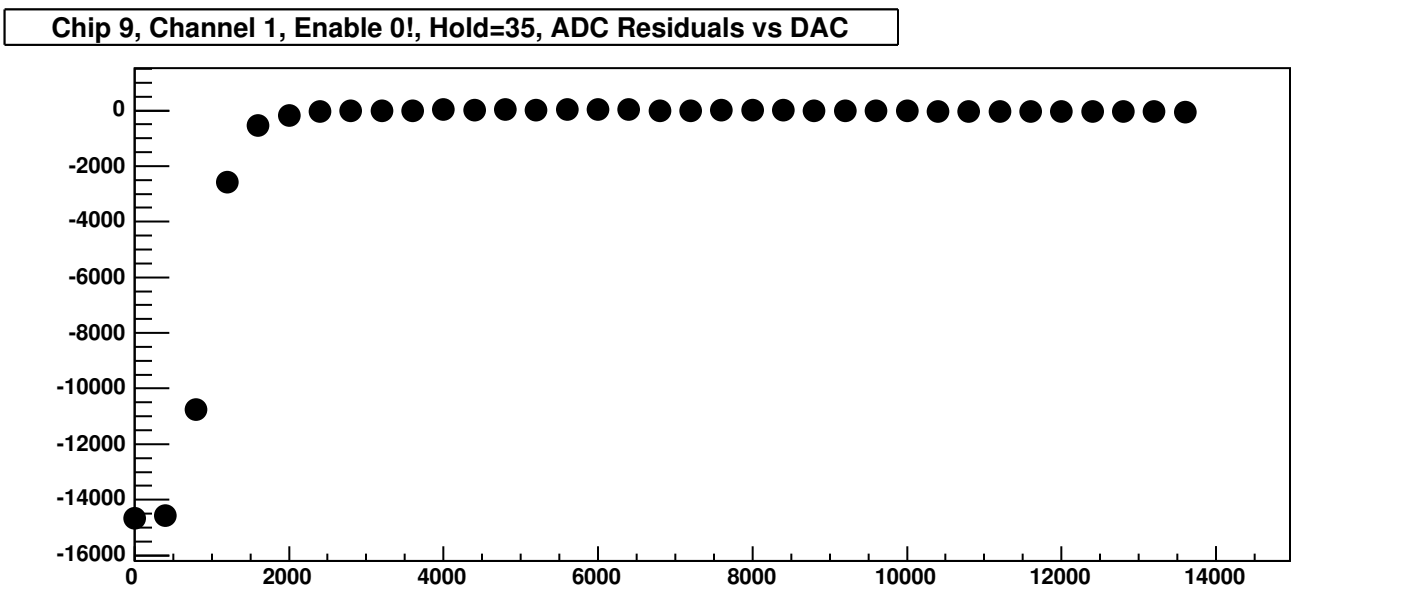

Chip 9, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2514 / 23
p0 -565.1 ± 0.9617
p1 0.1646 ± 0.0001754
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

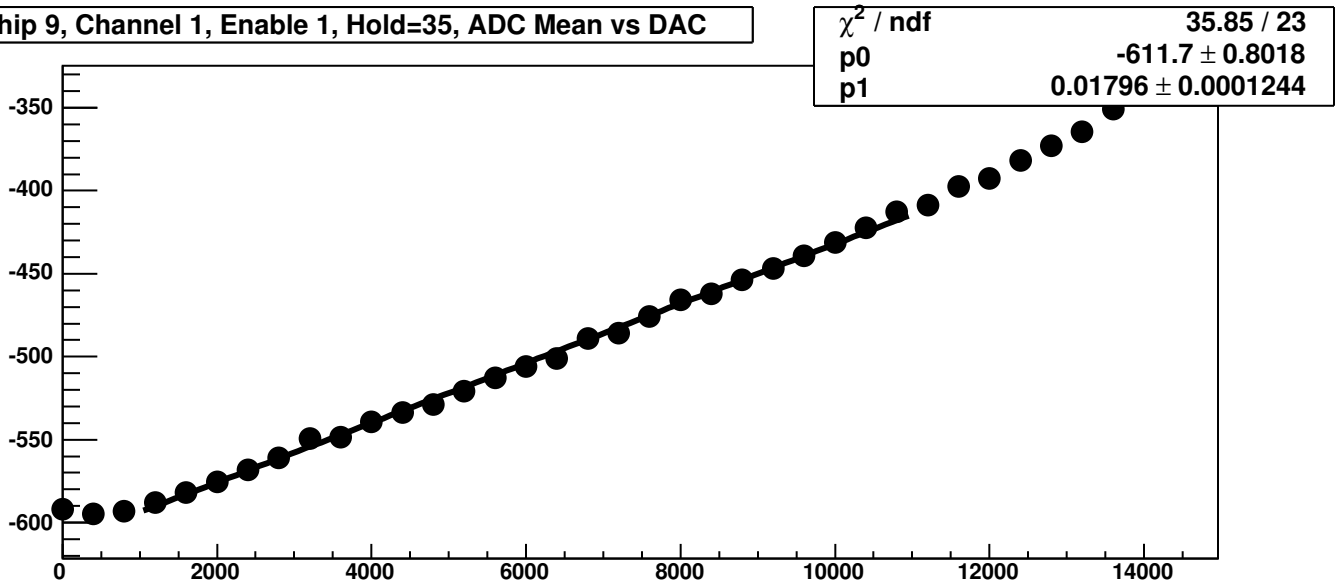

Chip 9, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1140 / 23
p0 3.022e+04 ± 7.819
p1 -0.007659 ± 0.001067
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

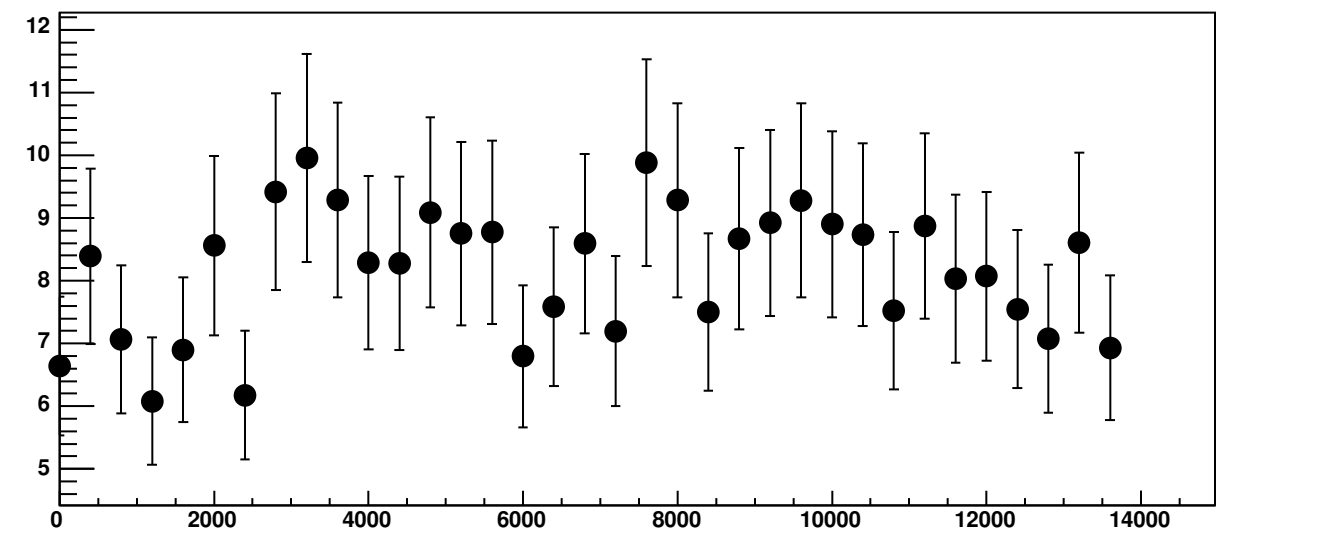

Chip 9, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

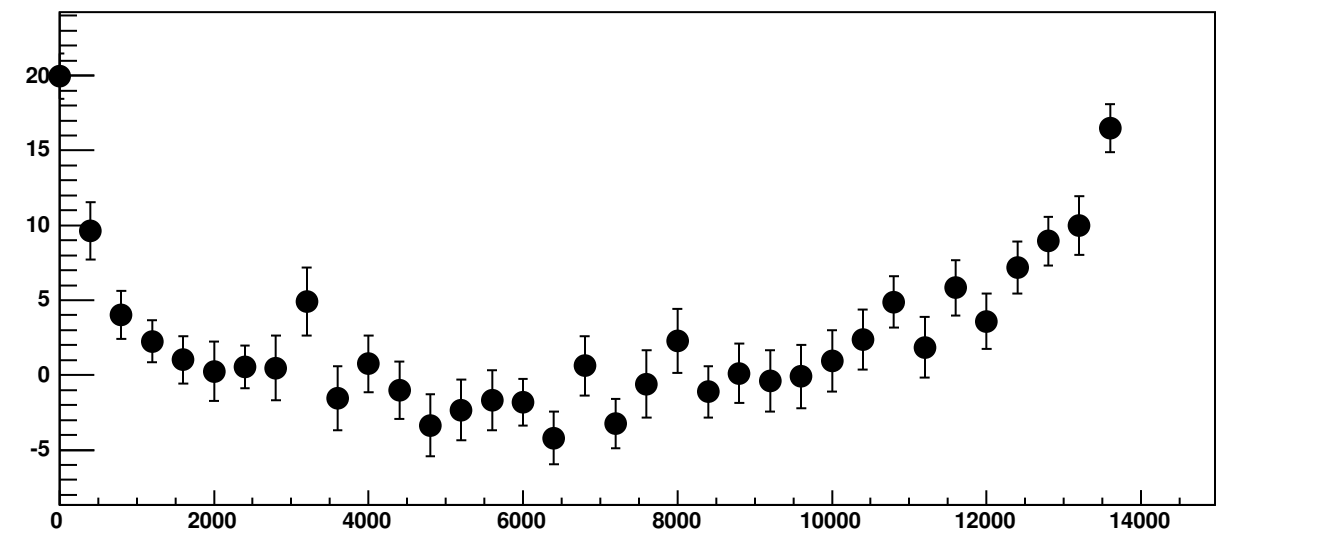
Chip 9, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

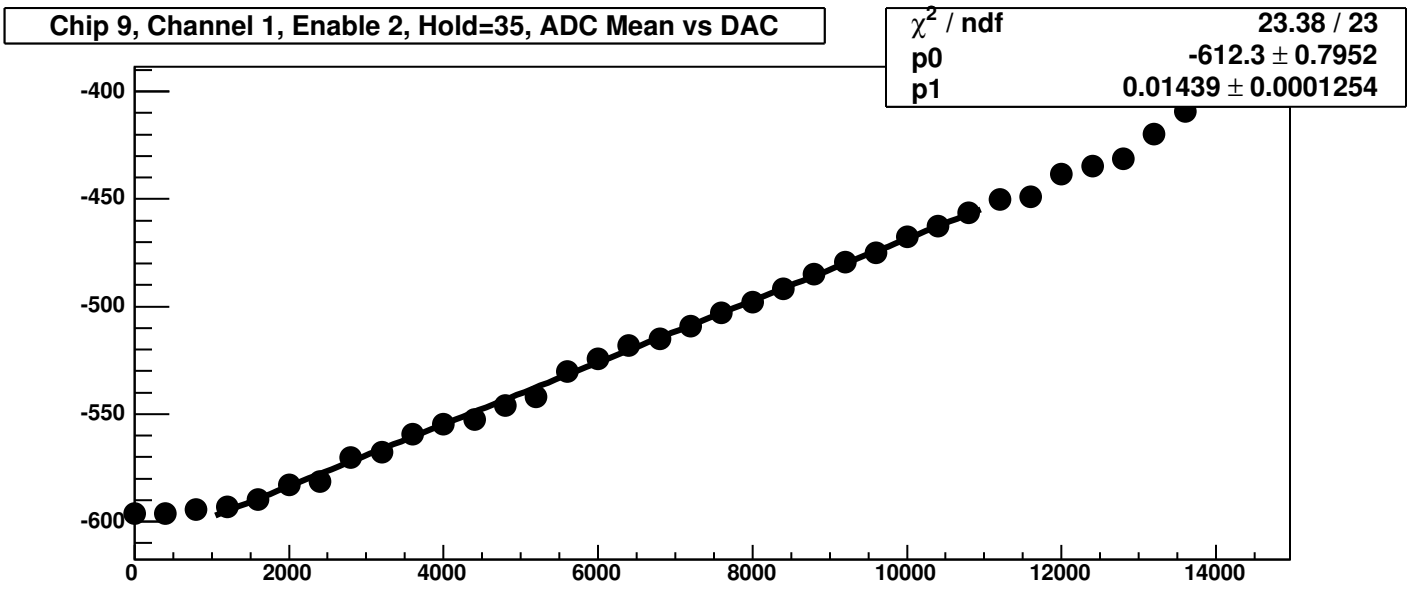


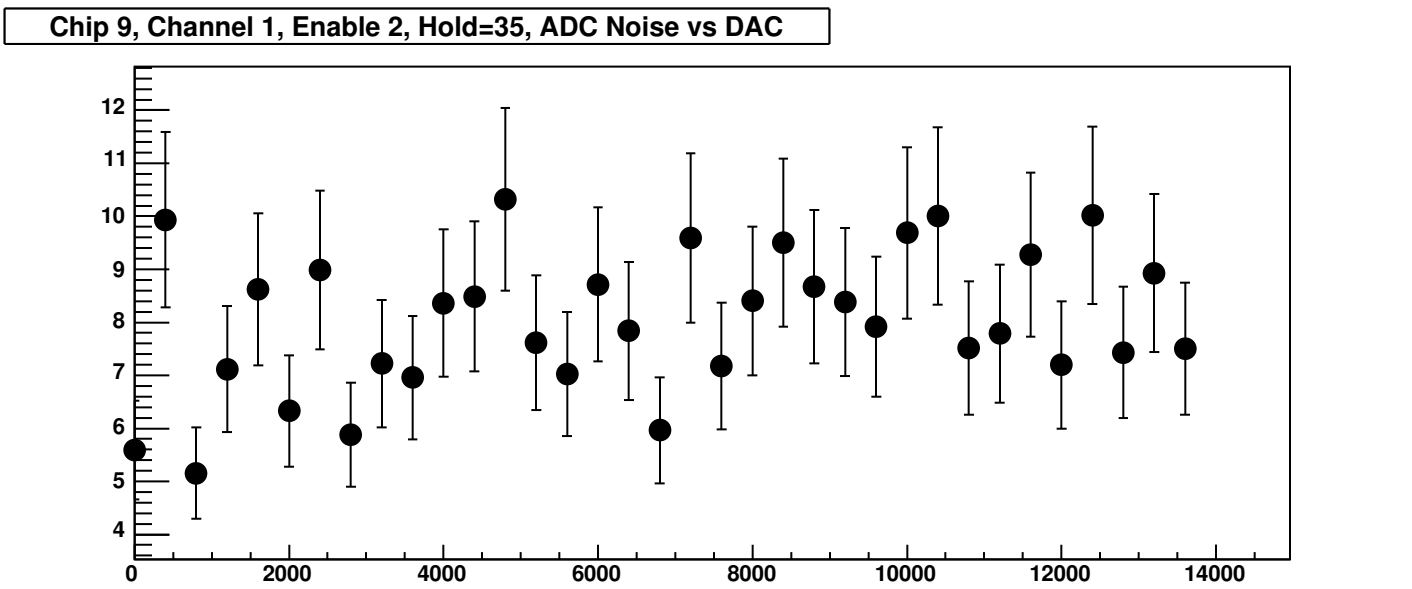
Chip 9, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC

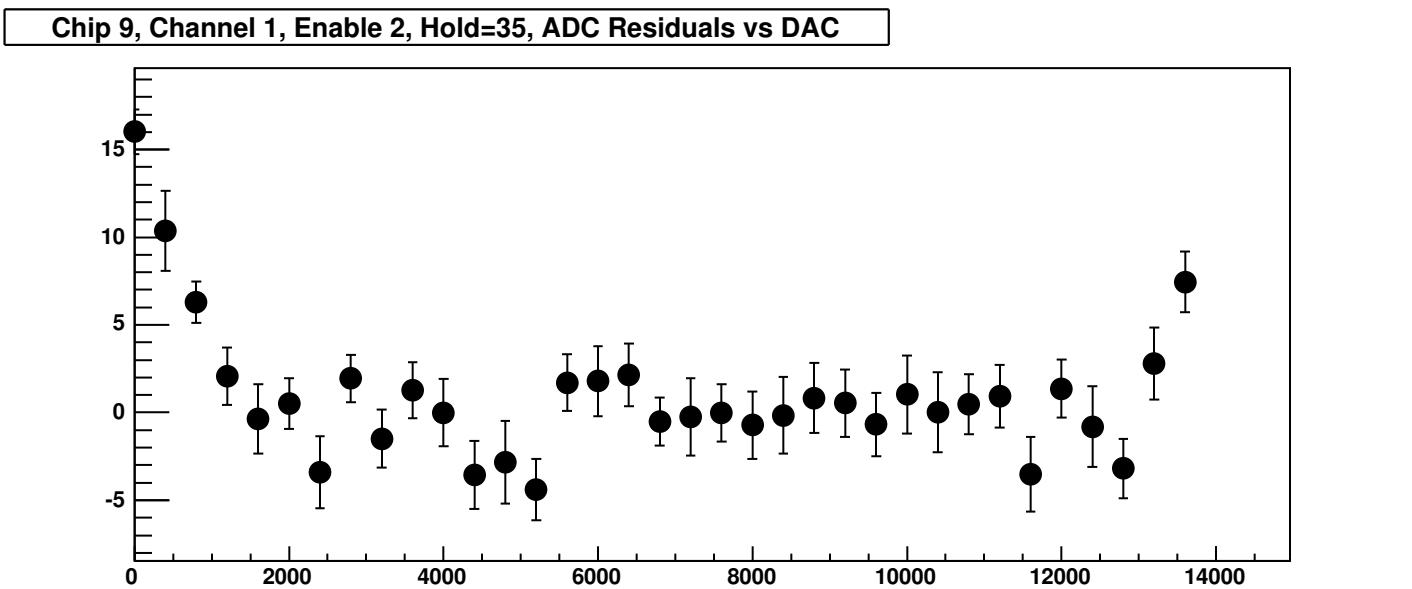


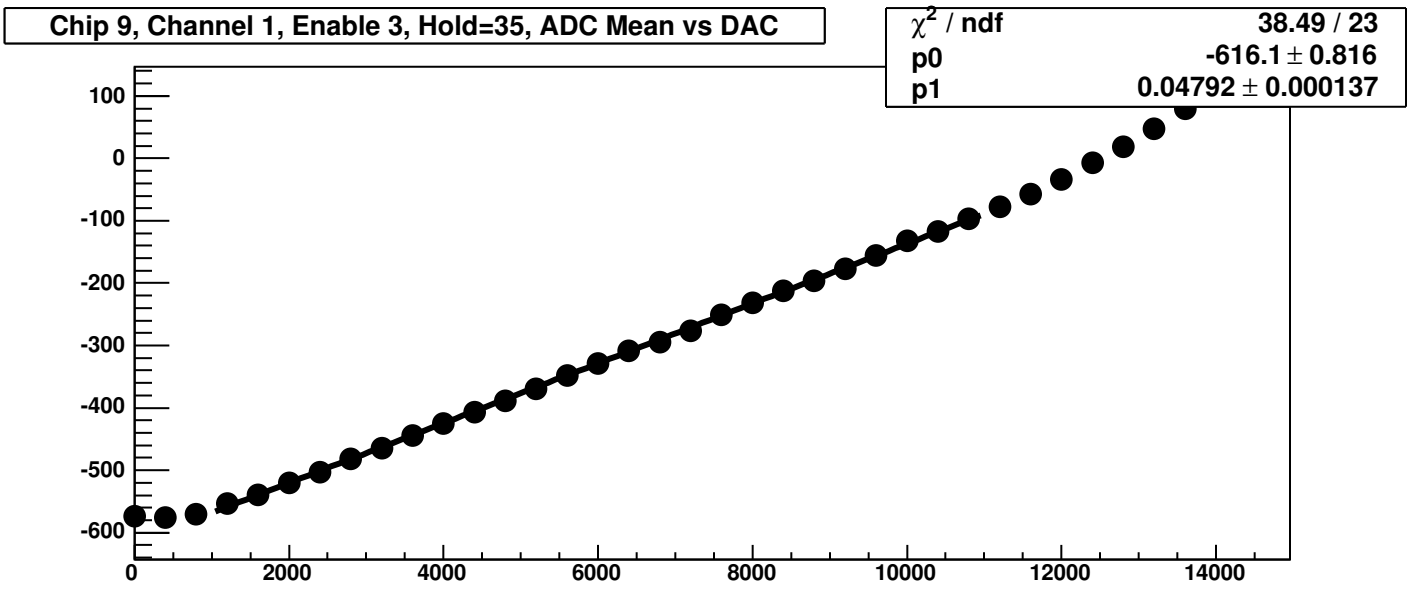
Chip 9, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

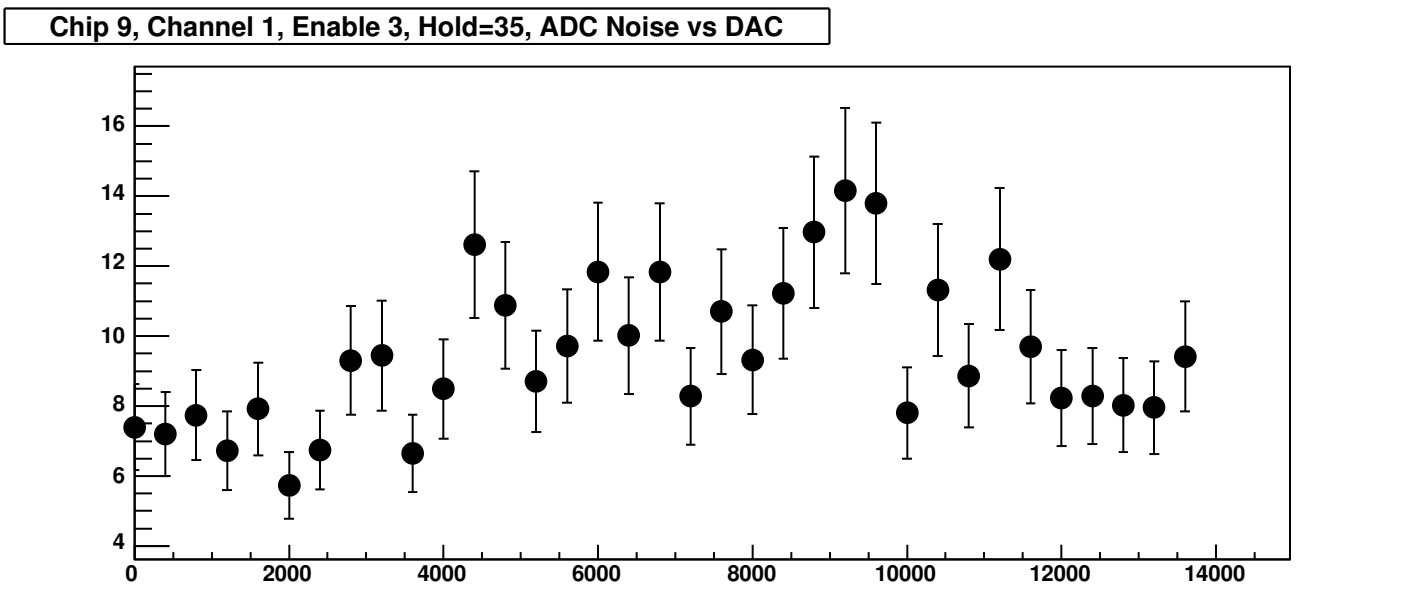


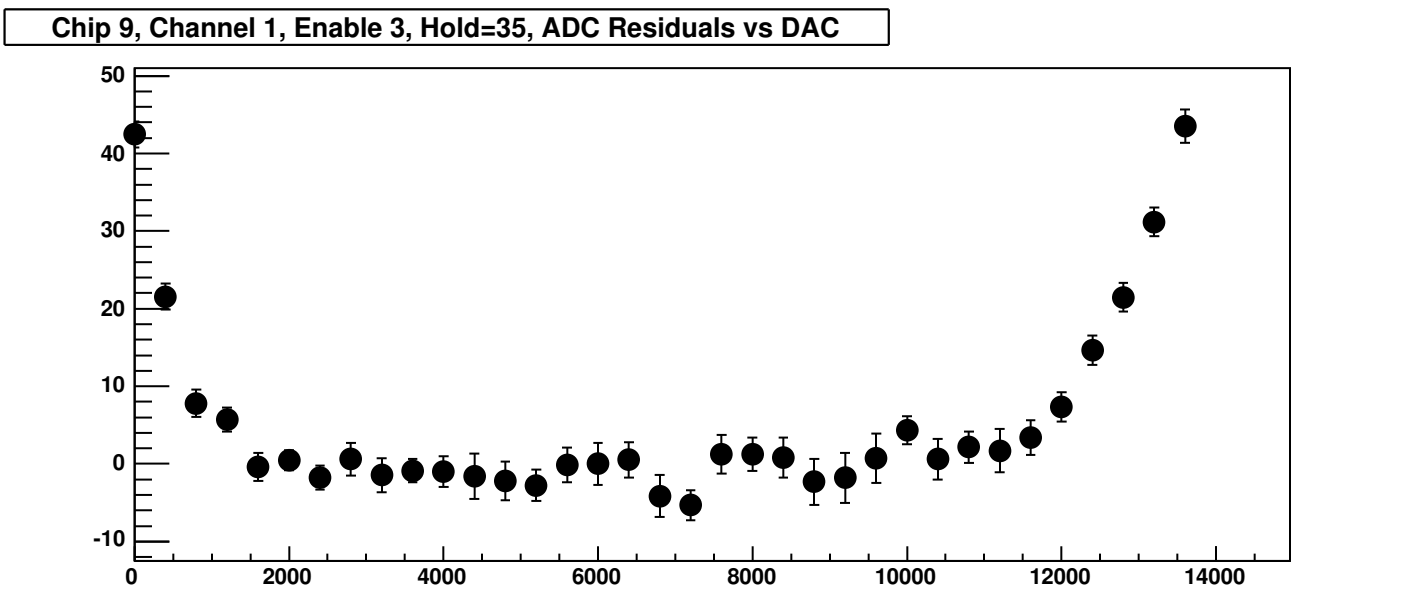

Chip 9, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 23.38 / 23
p0 -612.3 ± 0.7952
p1 0.01439 ± 0.0001254
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

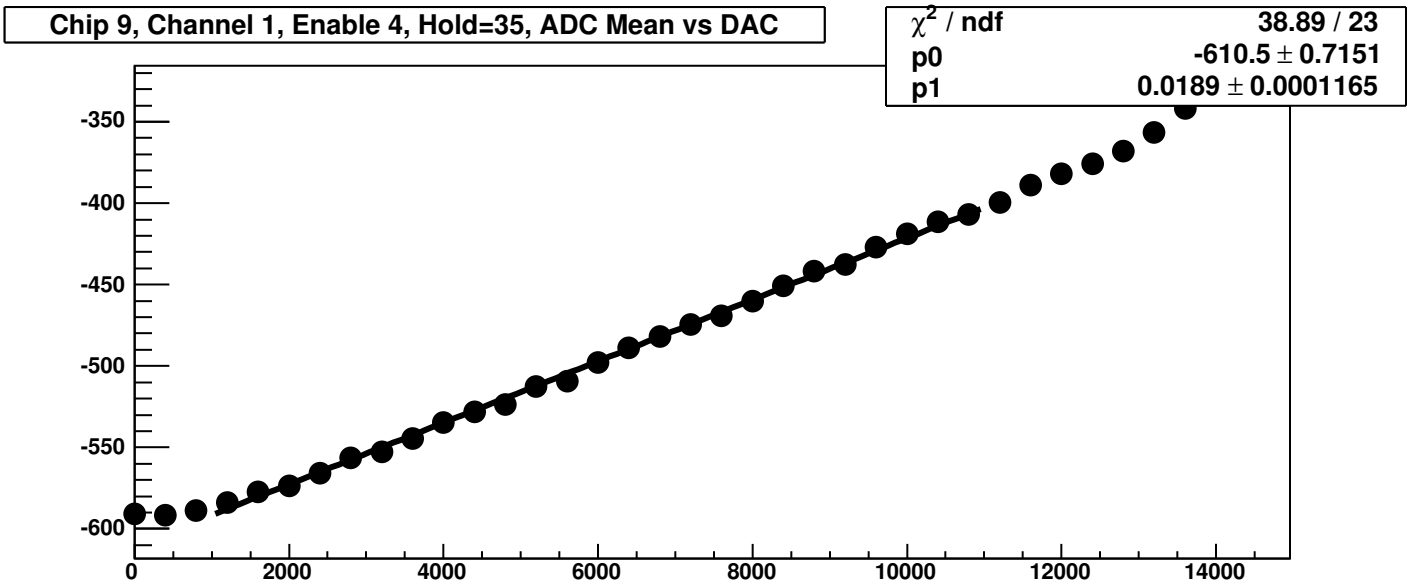

Chip 9, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

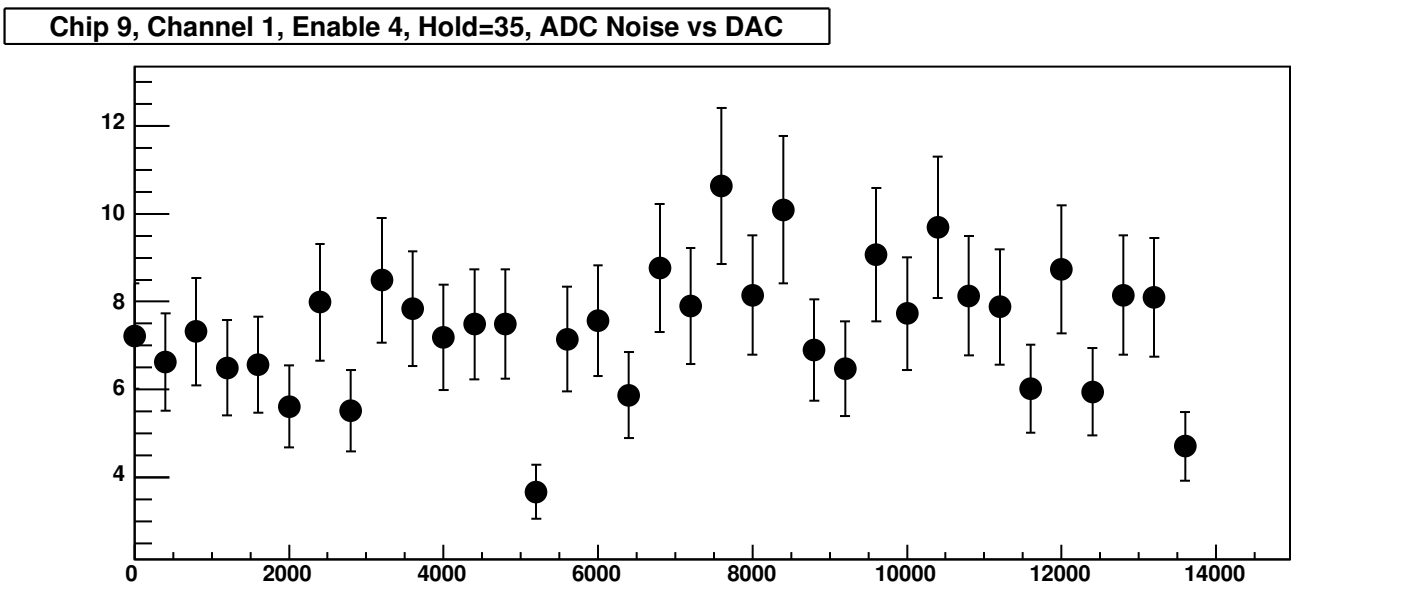

Chip 9, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

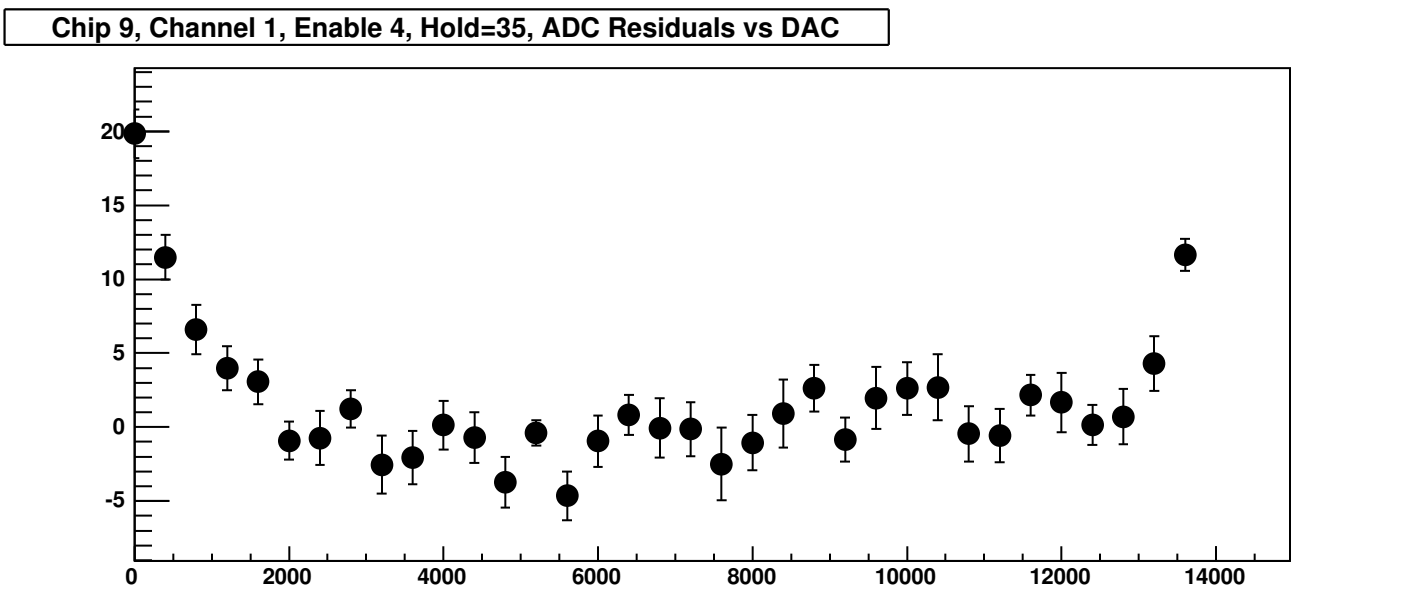

Chip 9, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.49 / 23
p0 -616.1 ± 0.816
p1 0.04792 ± 0.000137
100
0
-100
-200
-300
-400
-500
-600
0
2000
4000
6000
8000
10000
12000
14000

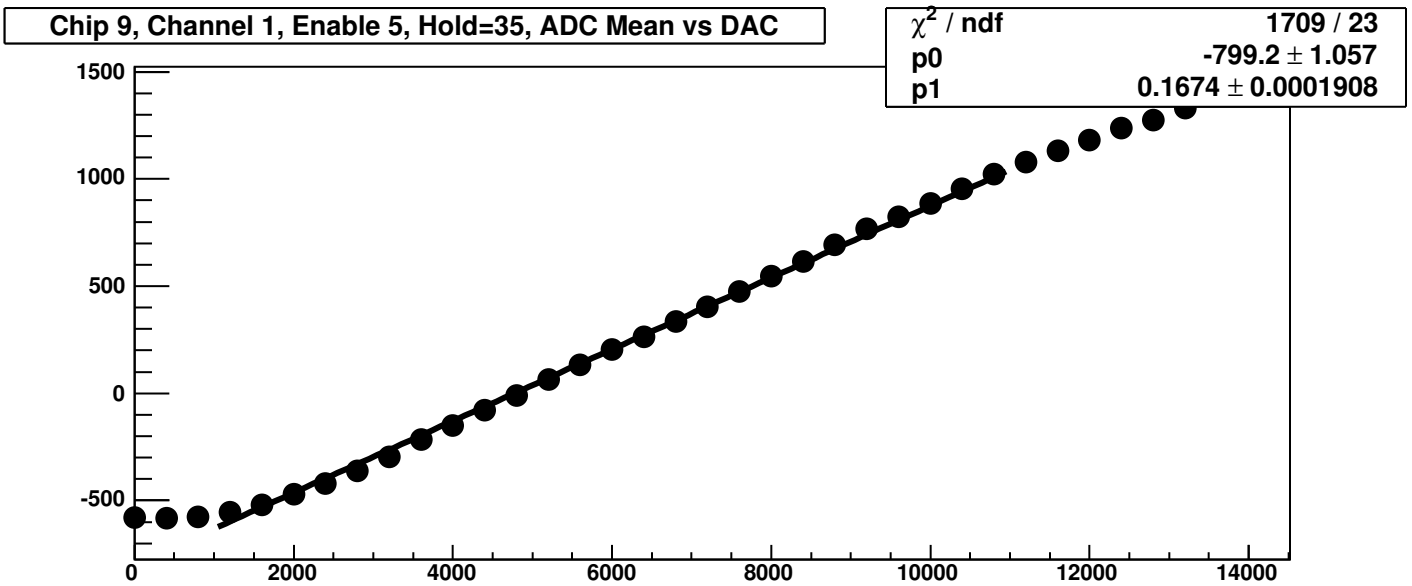

Chip 9, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

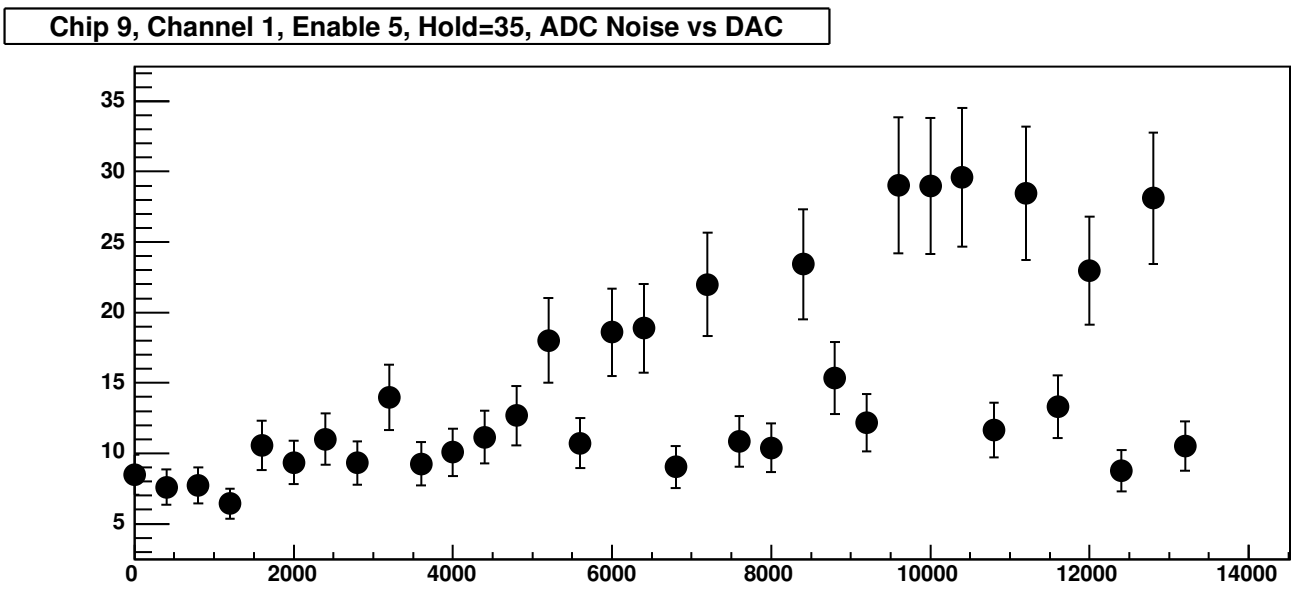

Chip 9, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
50
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000

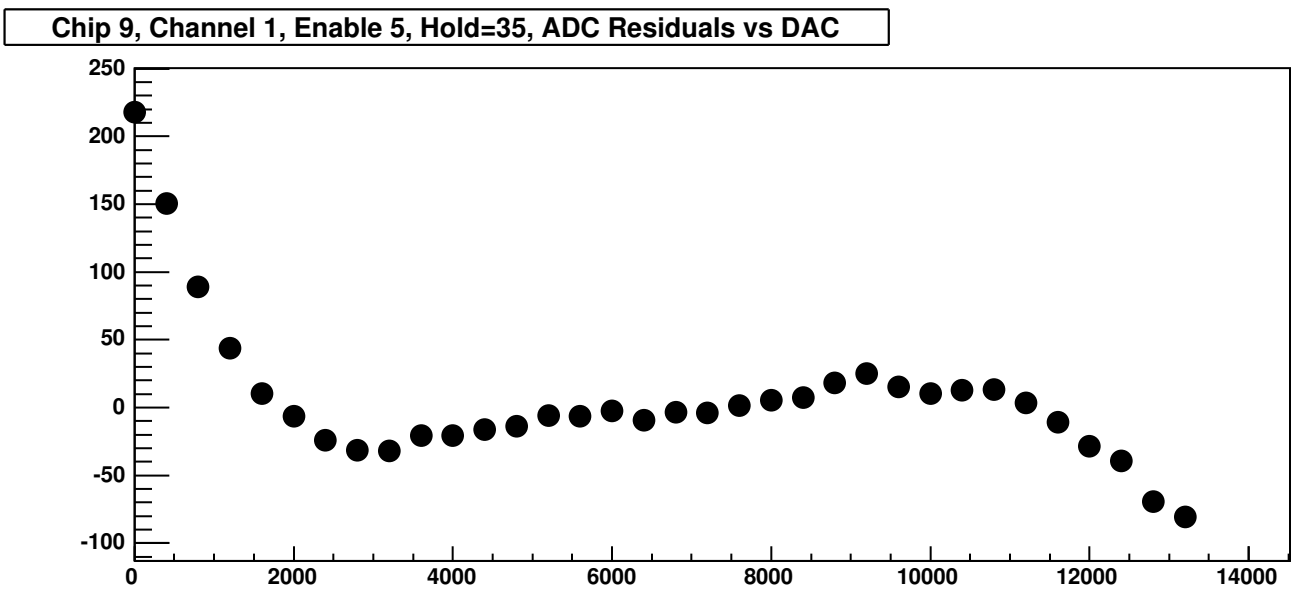

Chip 9, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 38.89 / 23
p0 -610.5 ± 0.7151
p1 0.0189 ± 0.0001165
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

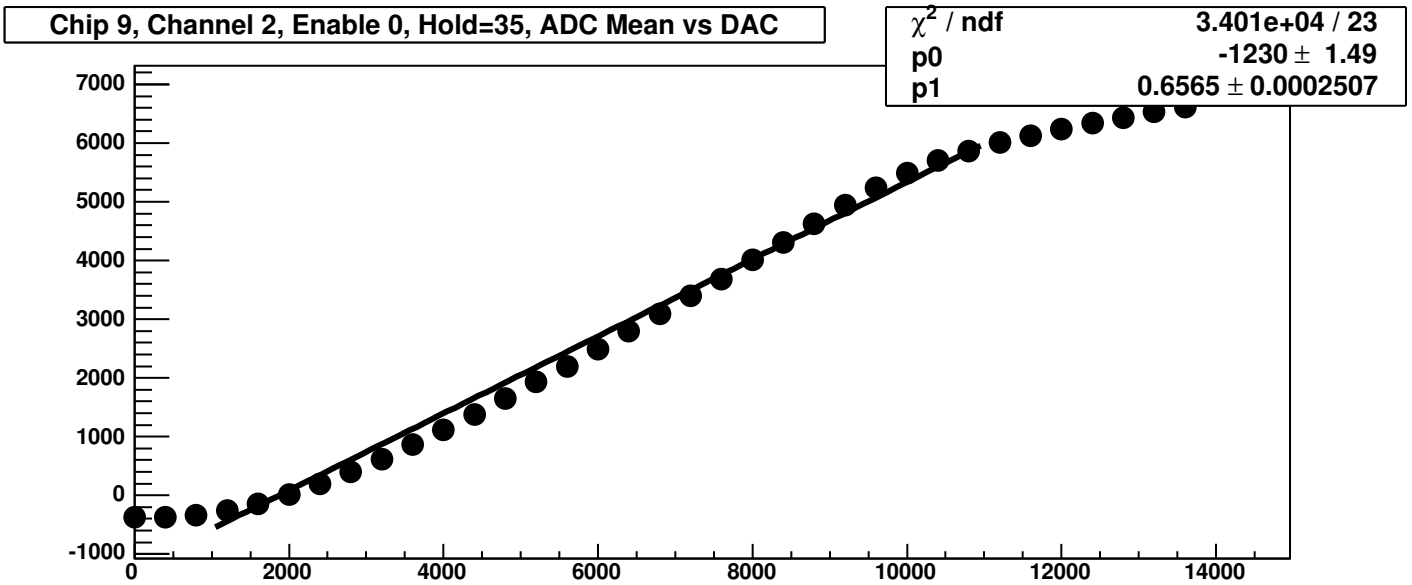

Chip 9, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

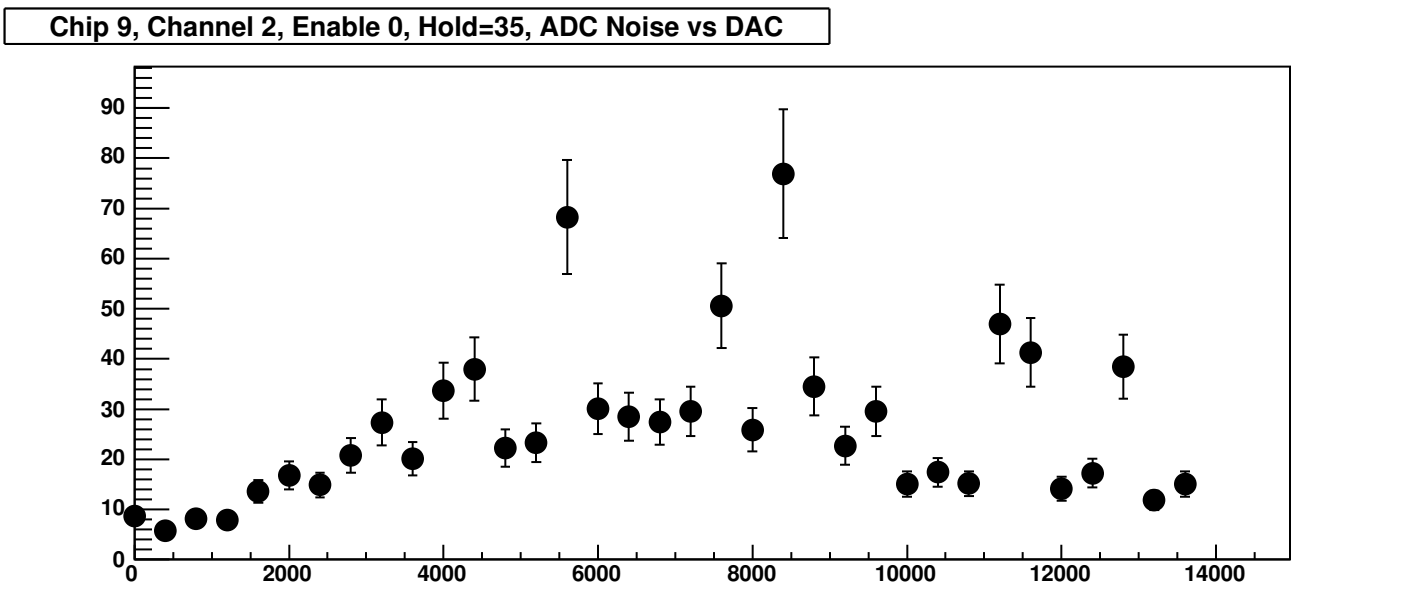

Chip 9, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

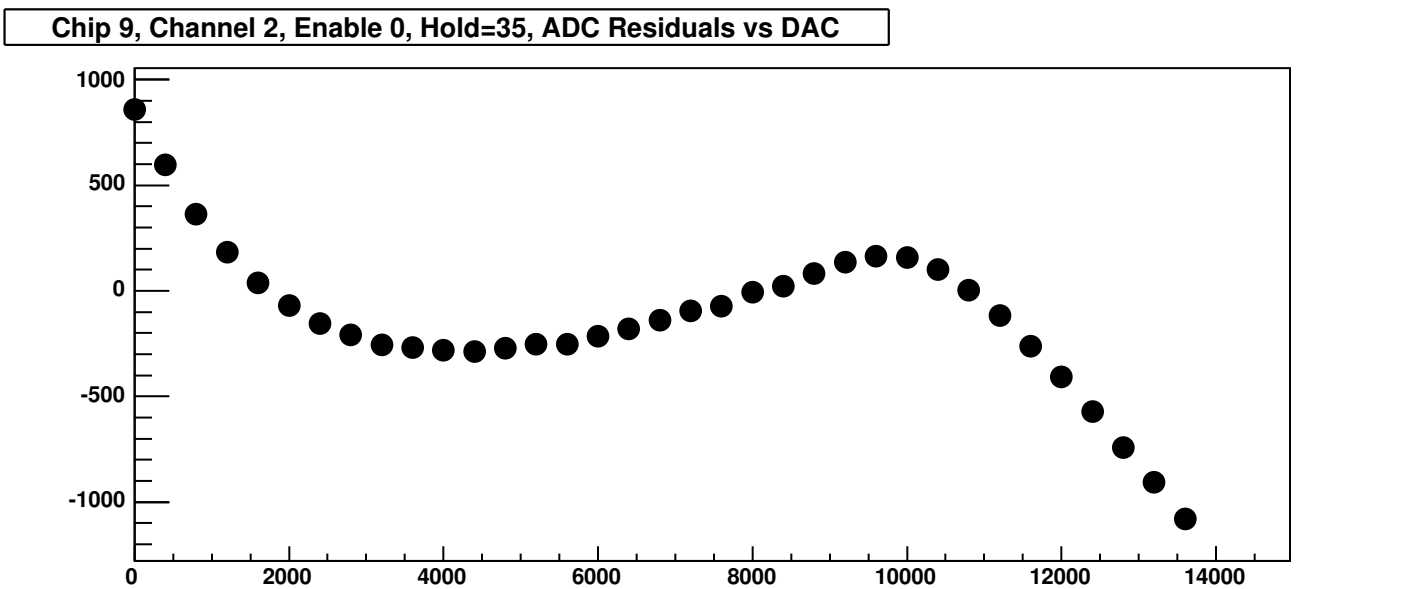

Chip 9, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1709 / 23
p0 -799.2 ± 1.057
p1 0.1674 ± 0.0001908
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

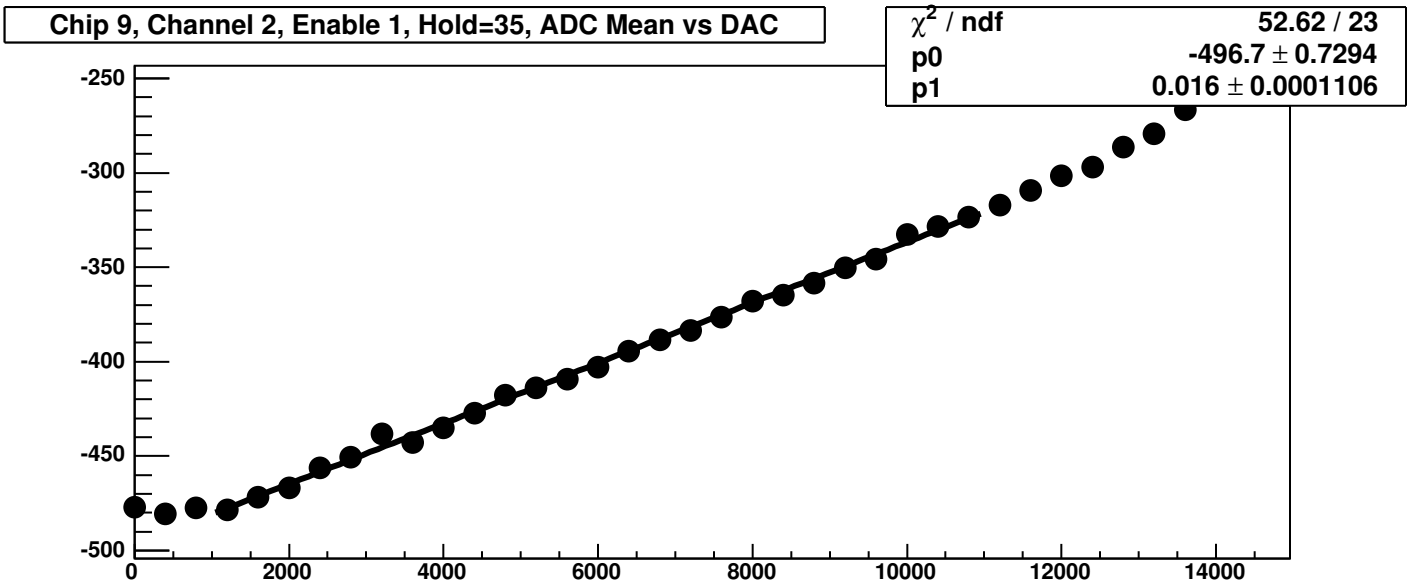

Chip 9, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

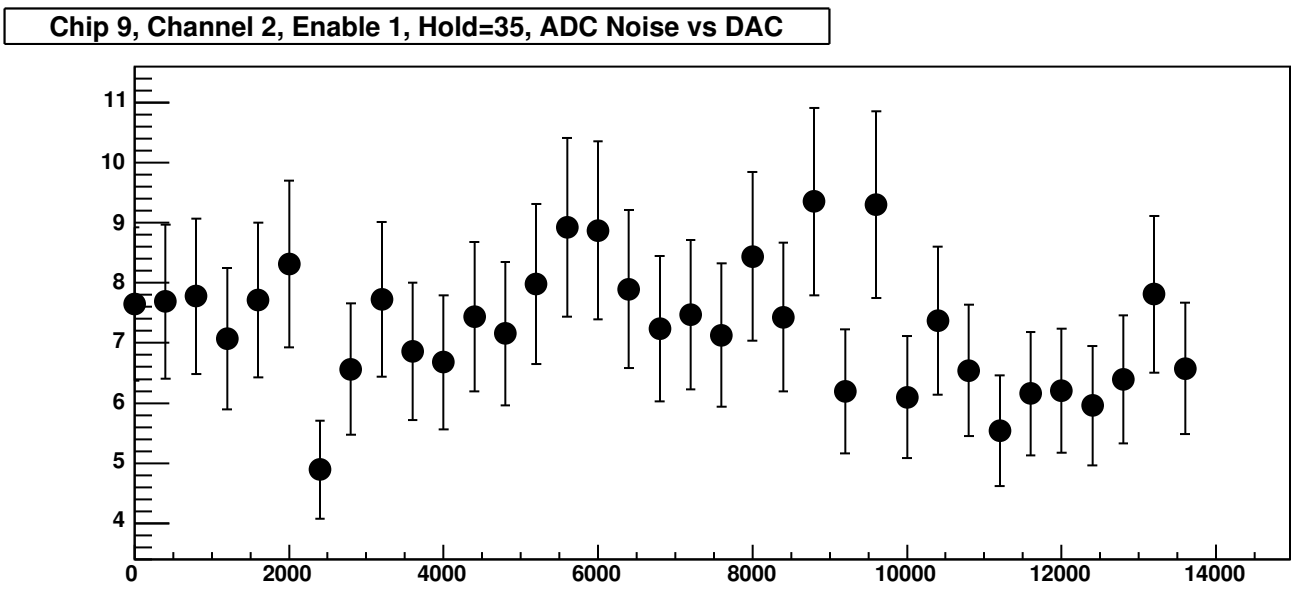

Chip 9, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

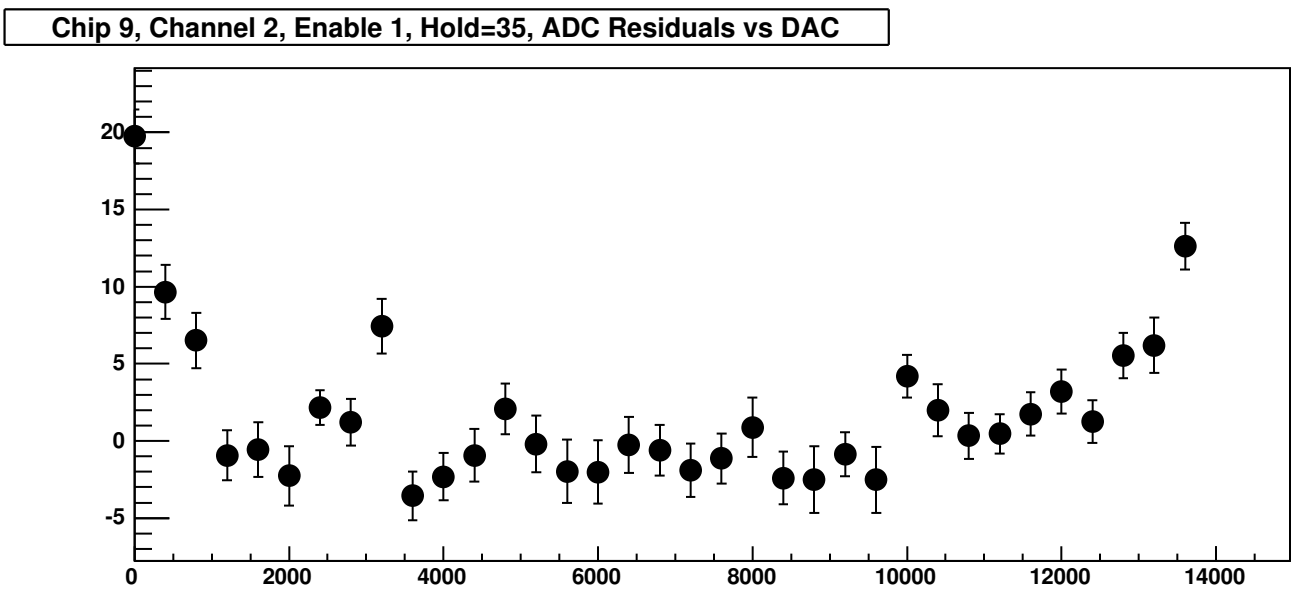

Chip 9, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.401e+04 / 23
p0 -1230 ± 1.49
p1 0.6565 ± 0.0002507
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

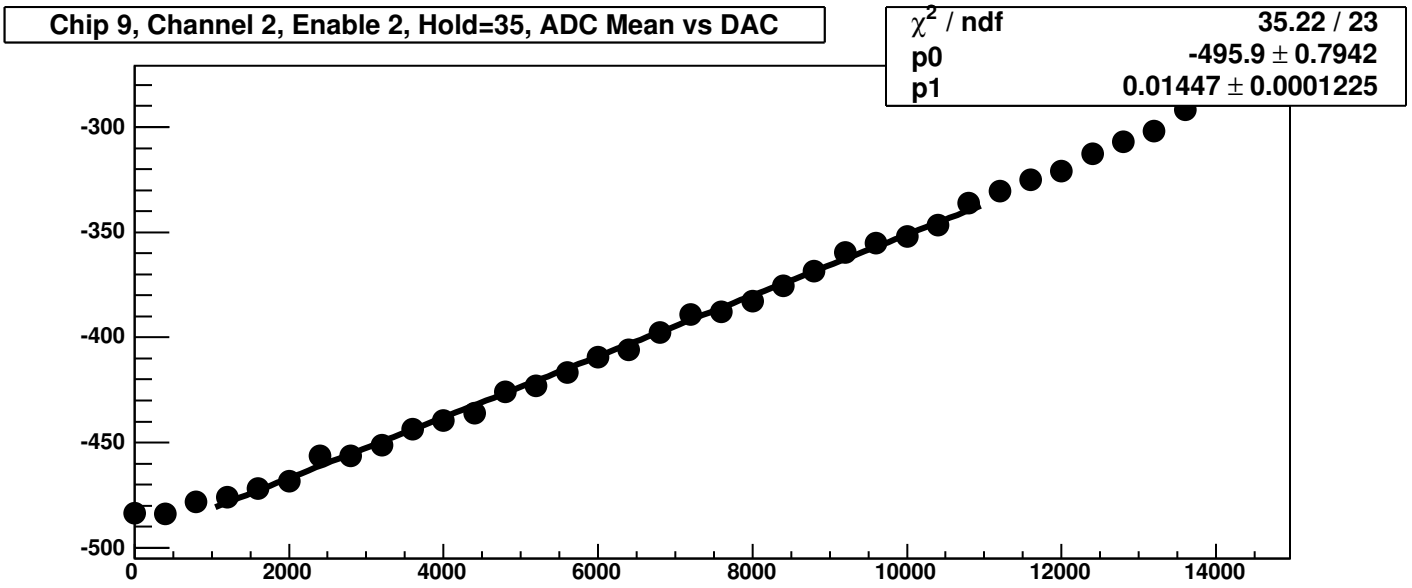

Chip 9, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

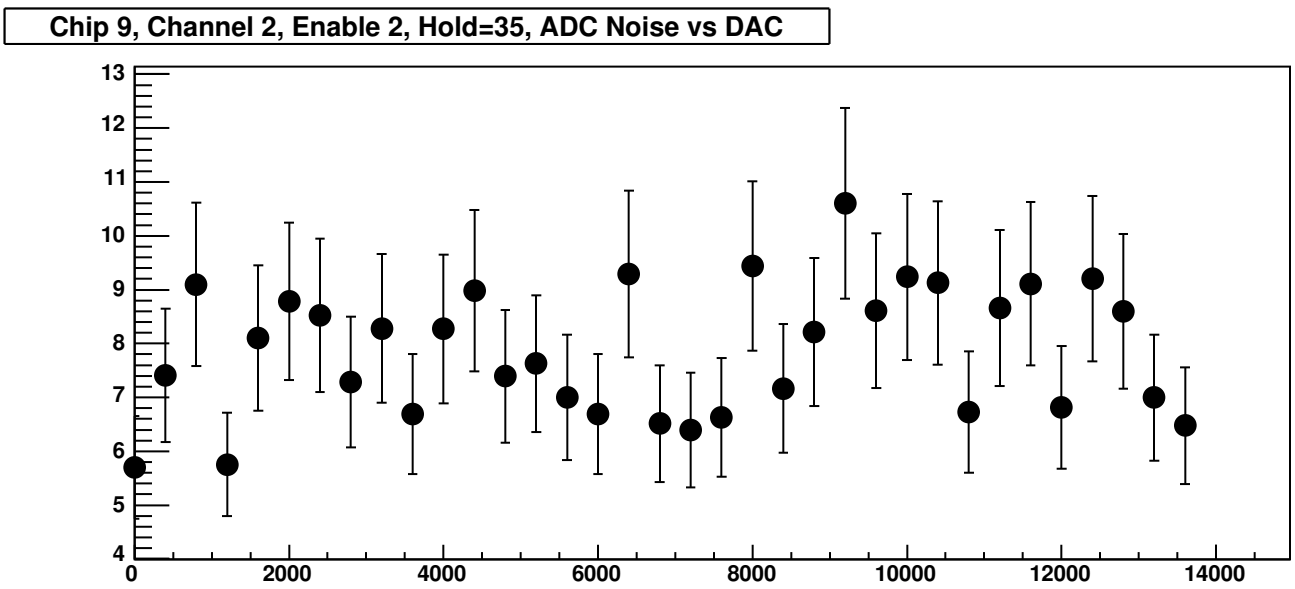

Chip 9, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

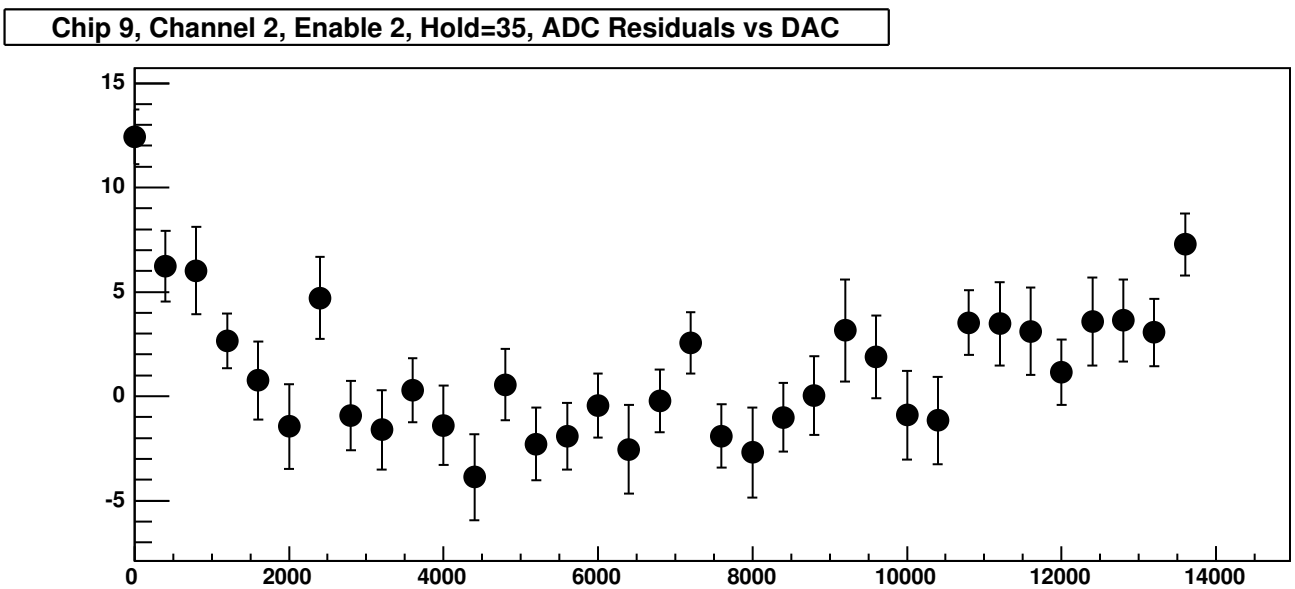

Chip 9, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 52.62 / 23
p0 -496.7 ± 0.7294
p1 0.016 ± 0.0001106
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

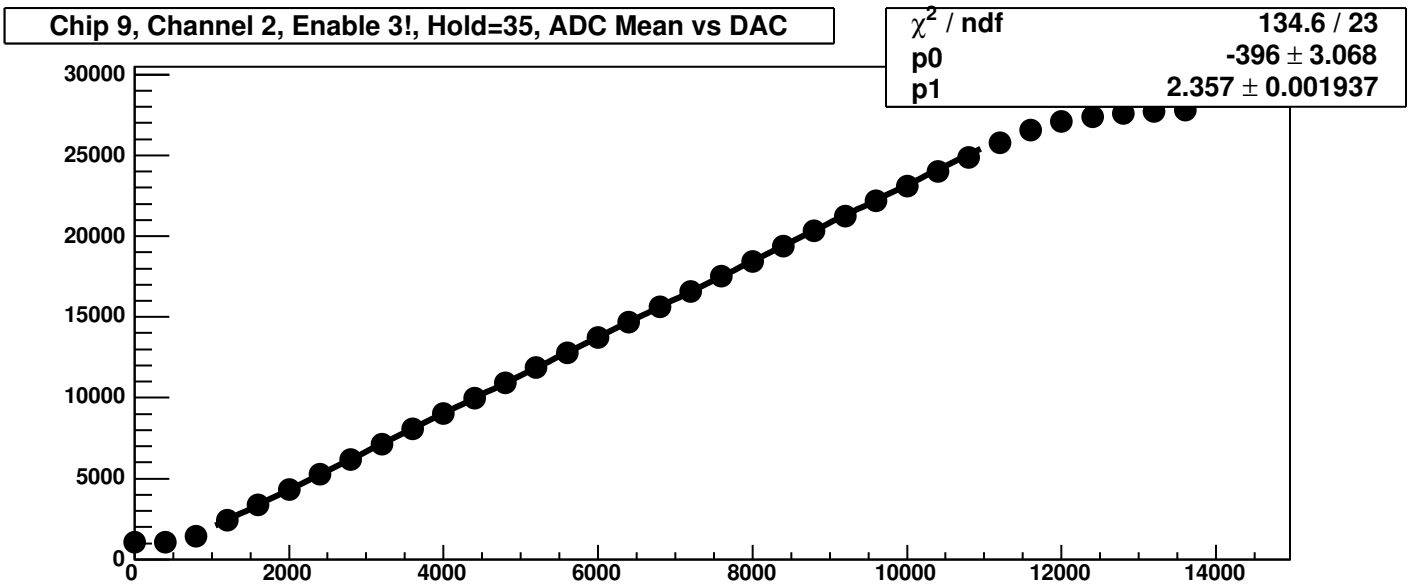

Chip 9, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

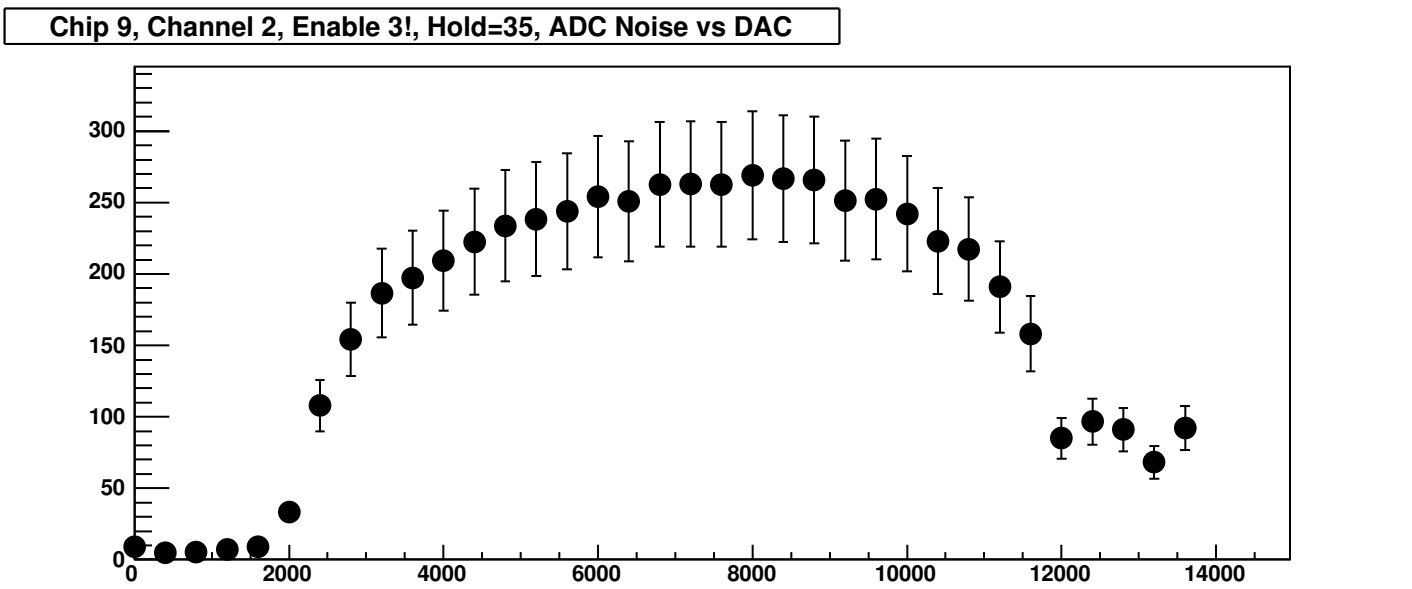

Chip 9, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

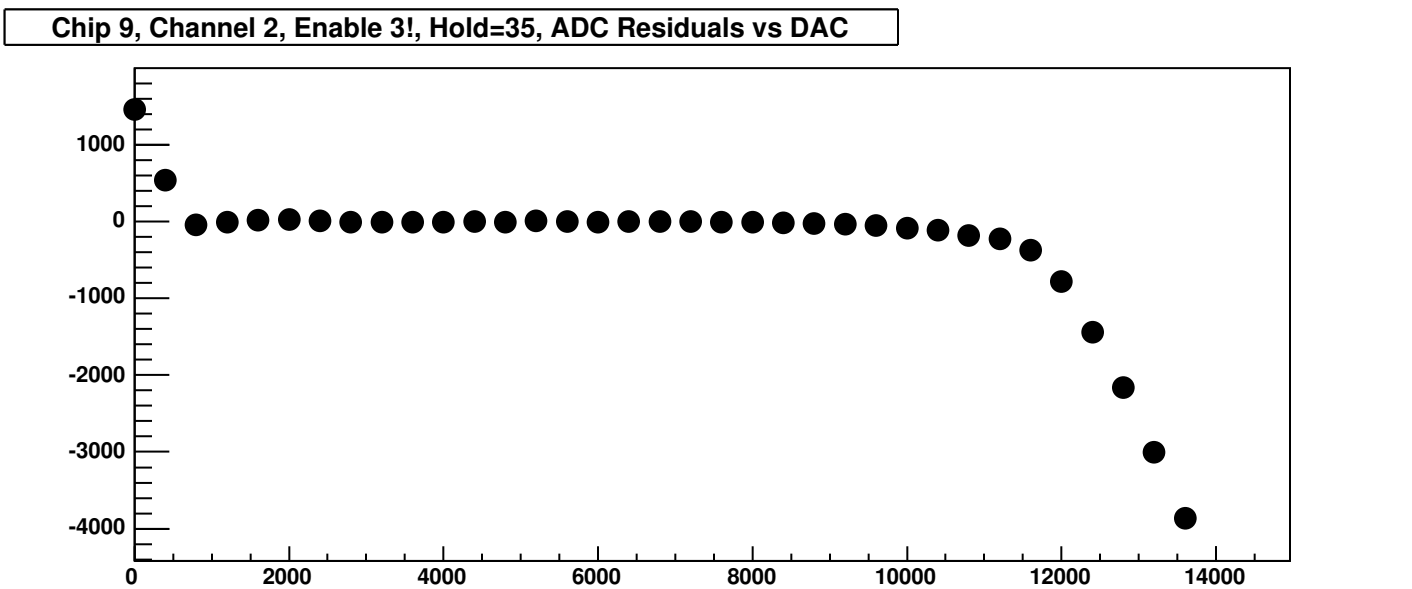

Chip 9, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 35.22 / 23
p0 -495.9 ± 0.7942
p1 0.01447 ± 0.0001225
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

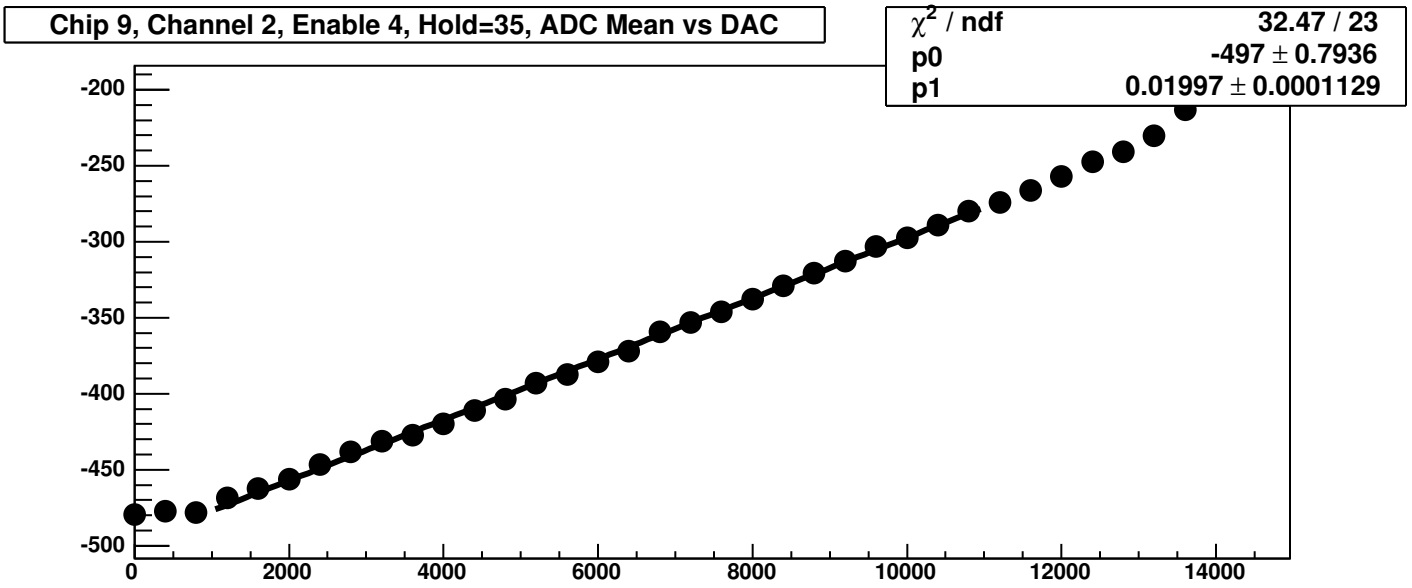

Chip 9, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

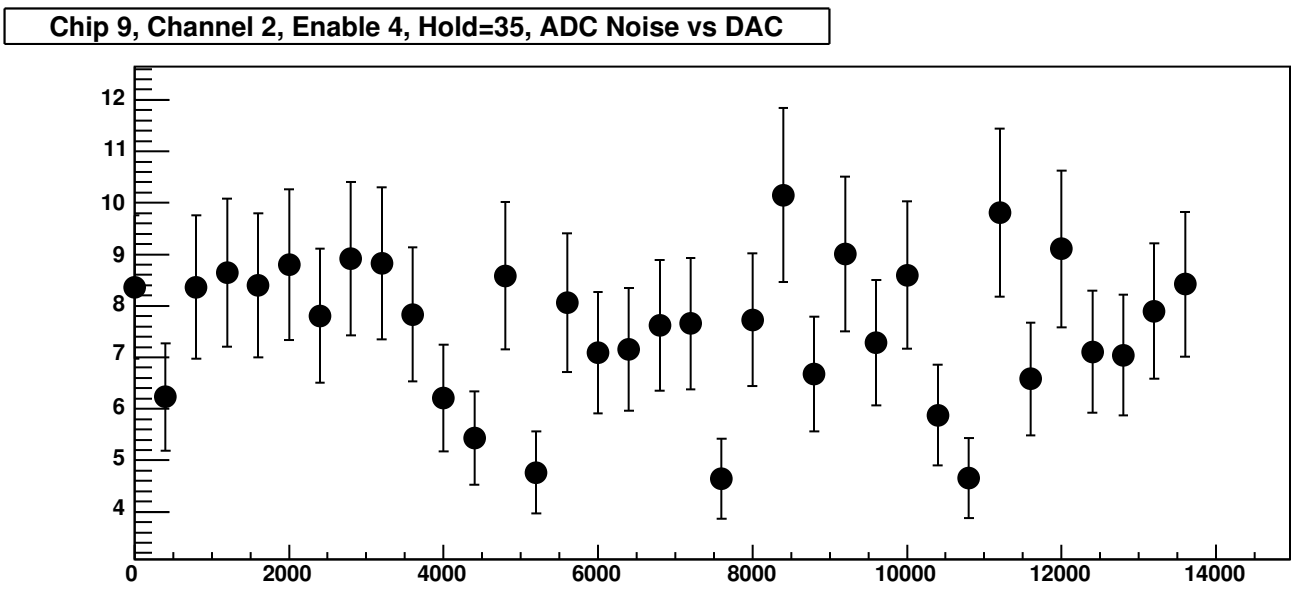

Chip 9, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

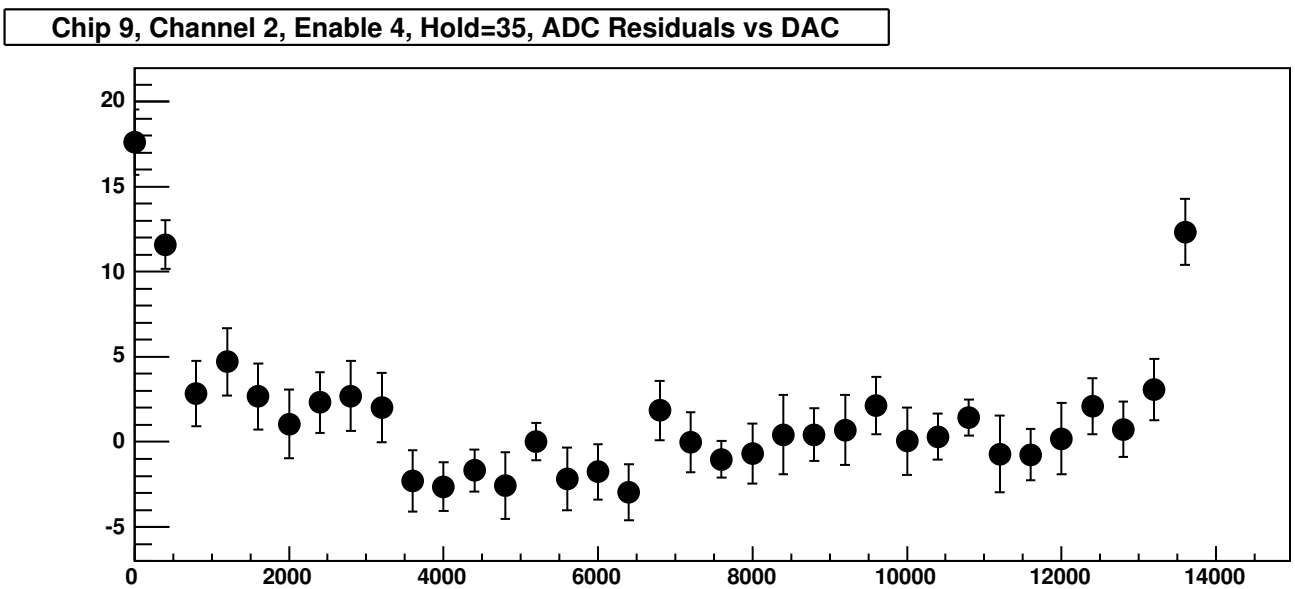

Chip 9, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 134.6 / 23
p0 -396 ± 3.068
p1 2.357 ± 0.001937
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

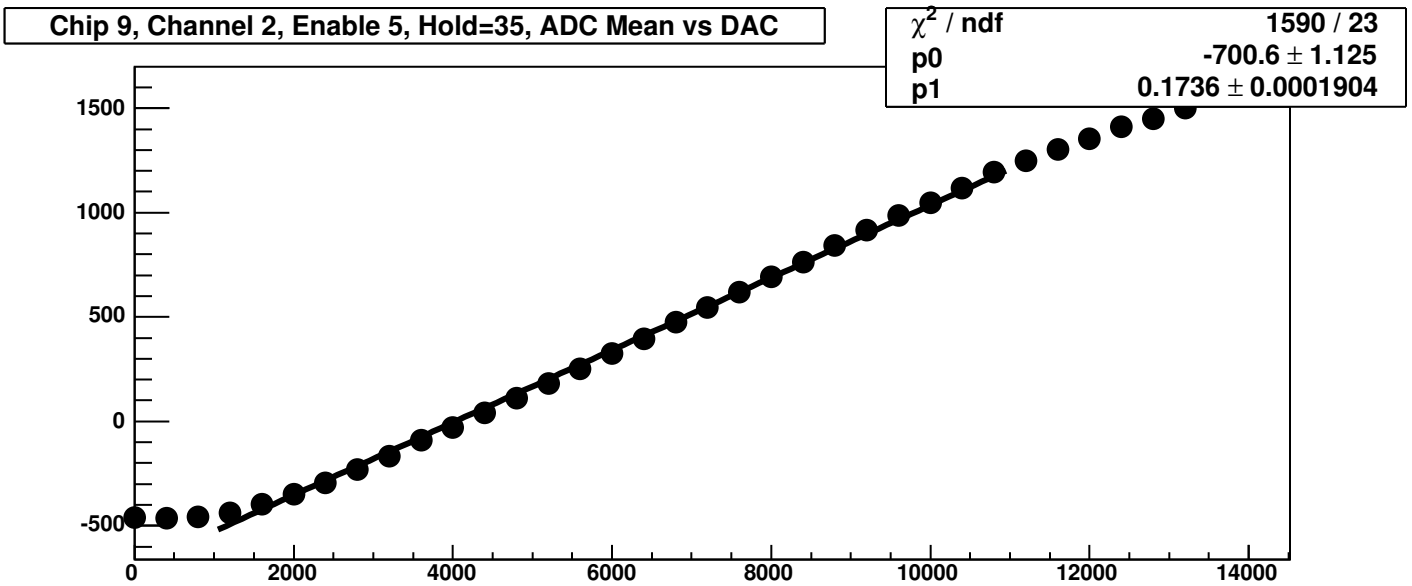

Chip 9, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

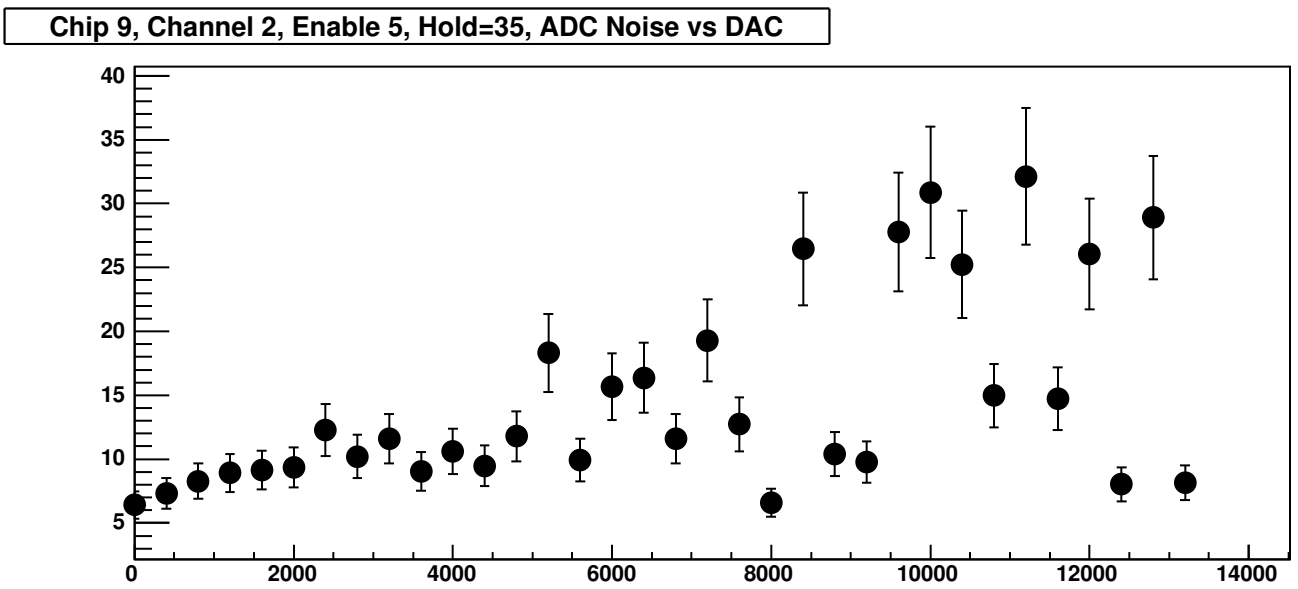

Chip 9, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

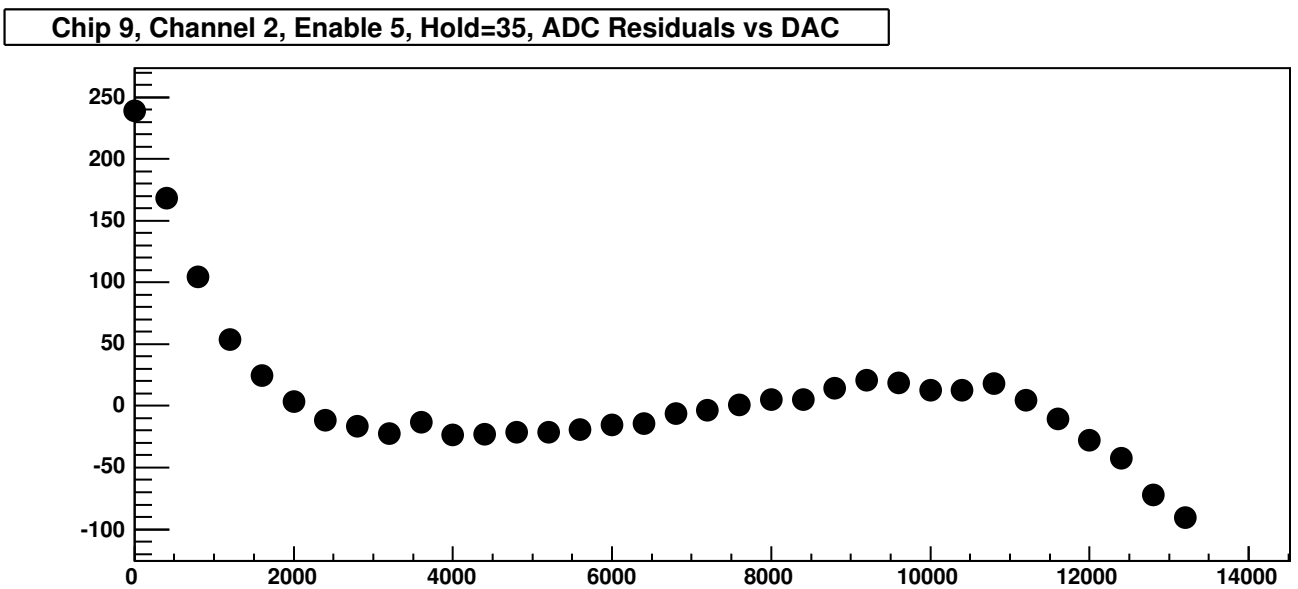

Chip 9, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 32.47 / 23
p0 -497 ± 0.7936
p1 0.01997 ± 0.0001129
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

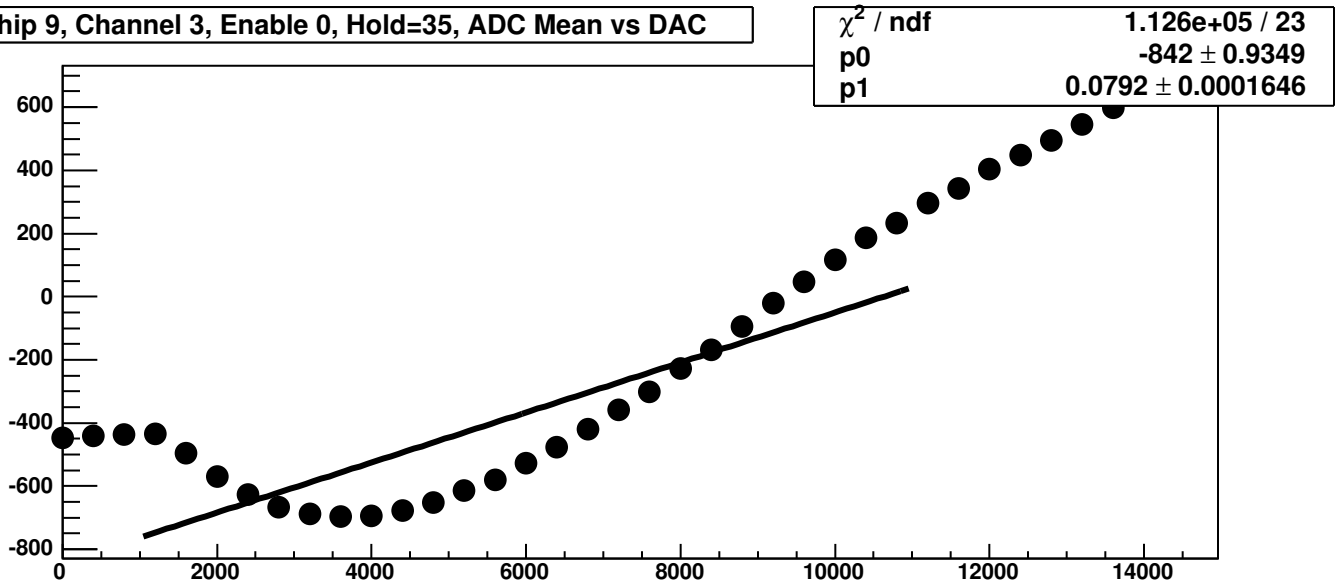

Chip 9, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1590 / 23
p0 -700.6 ± 1.125
p1 0.1736 ± 0.0001904
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

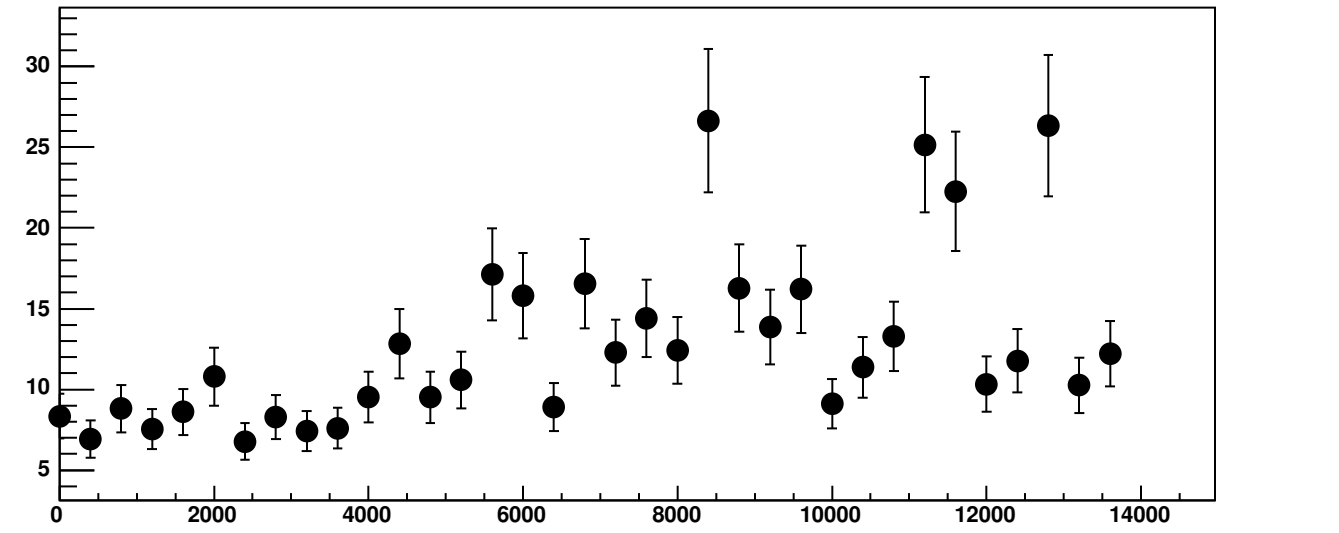

Chip 9, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

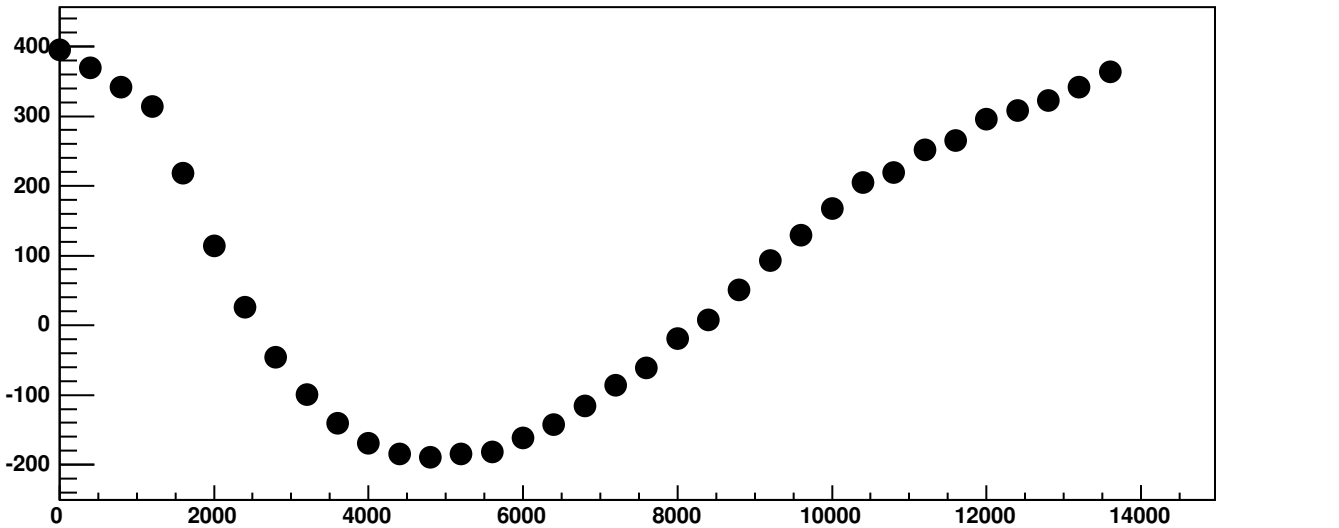
Chip 9, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

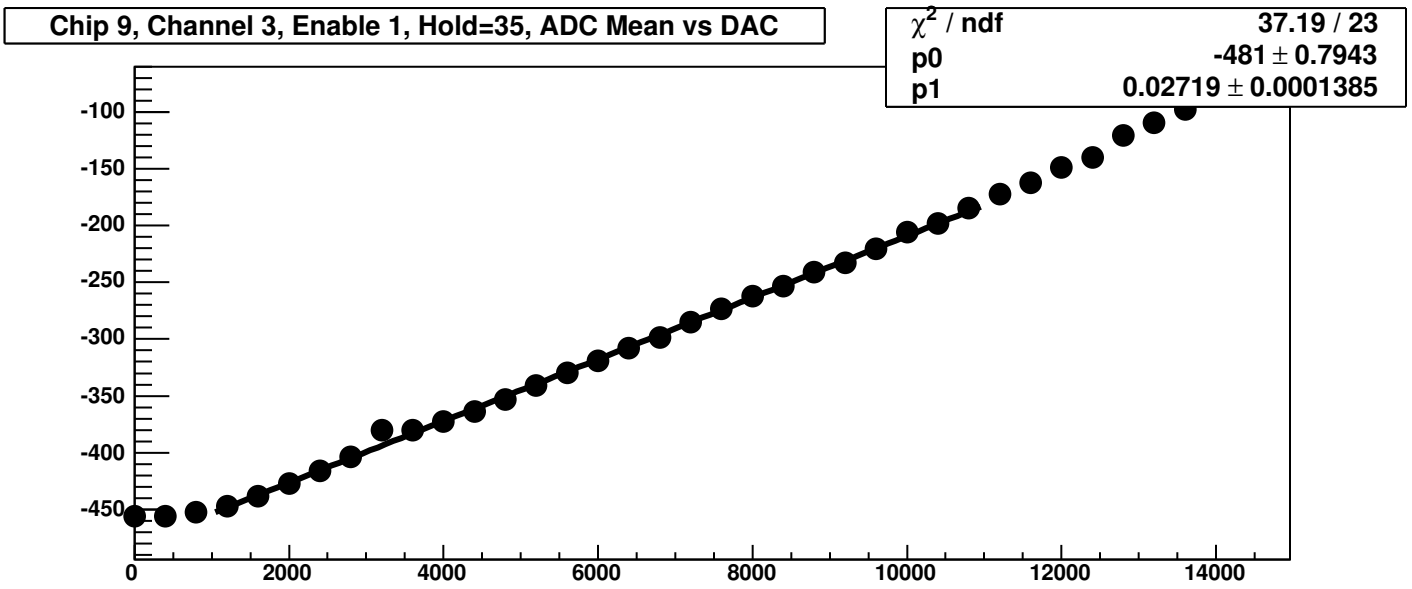


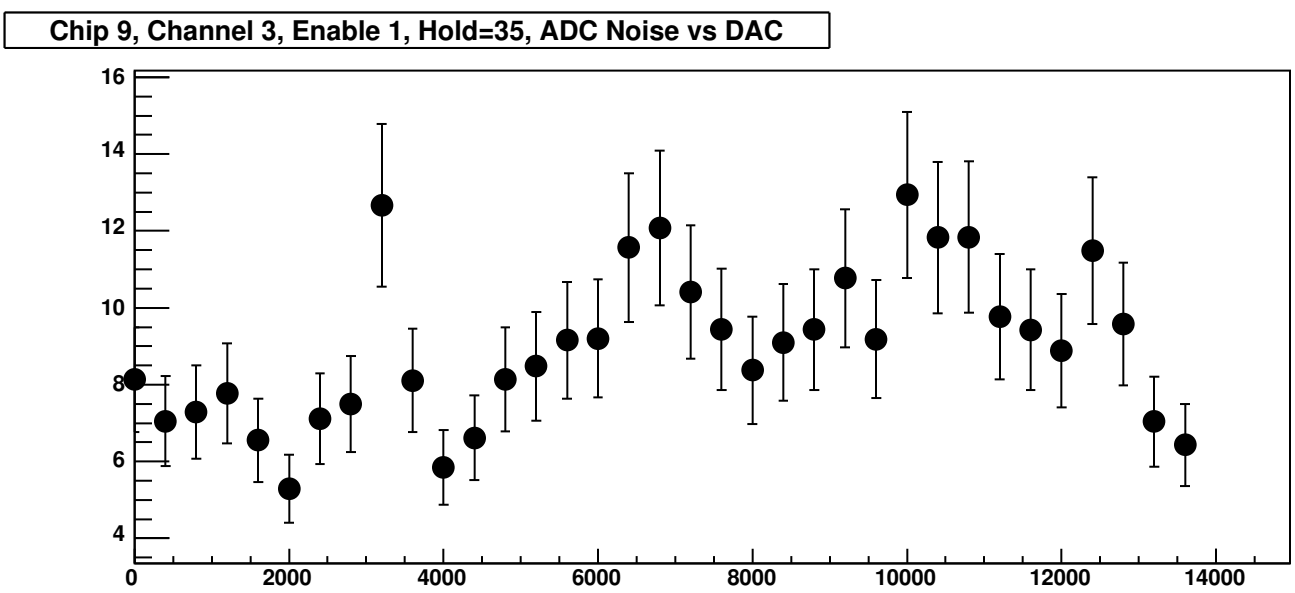
Chip 9, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

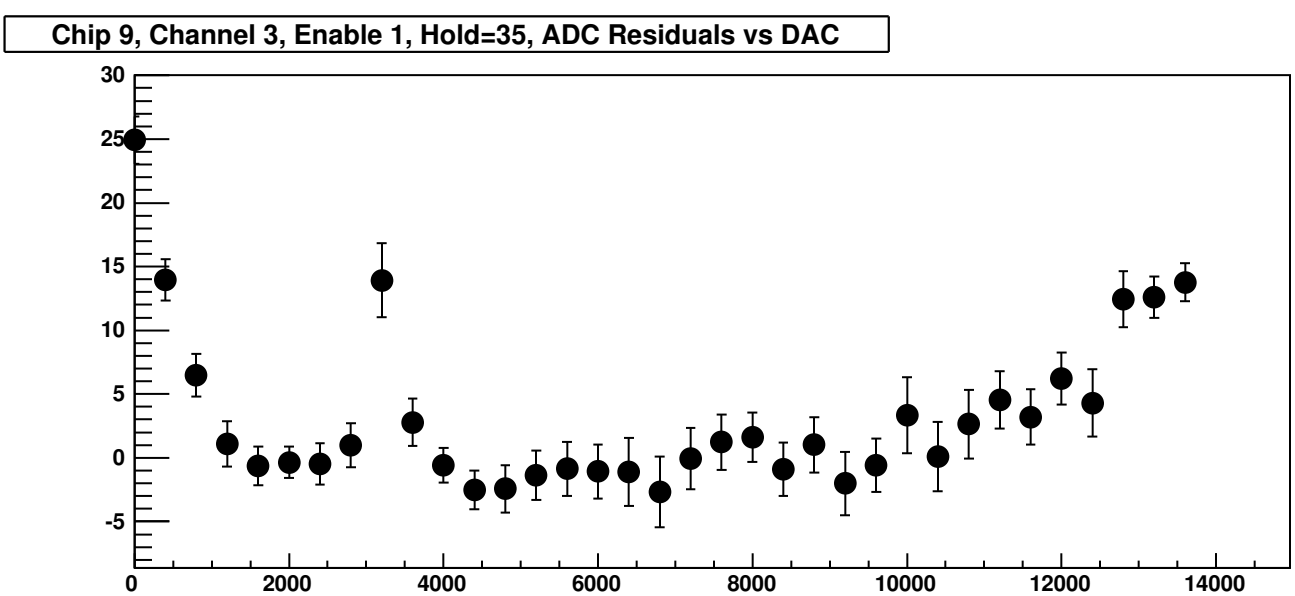


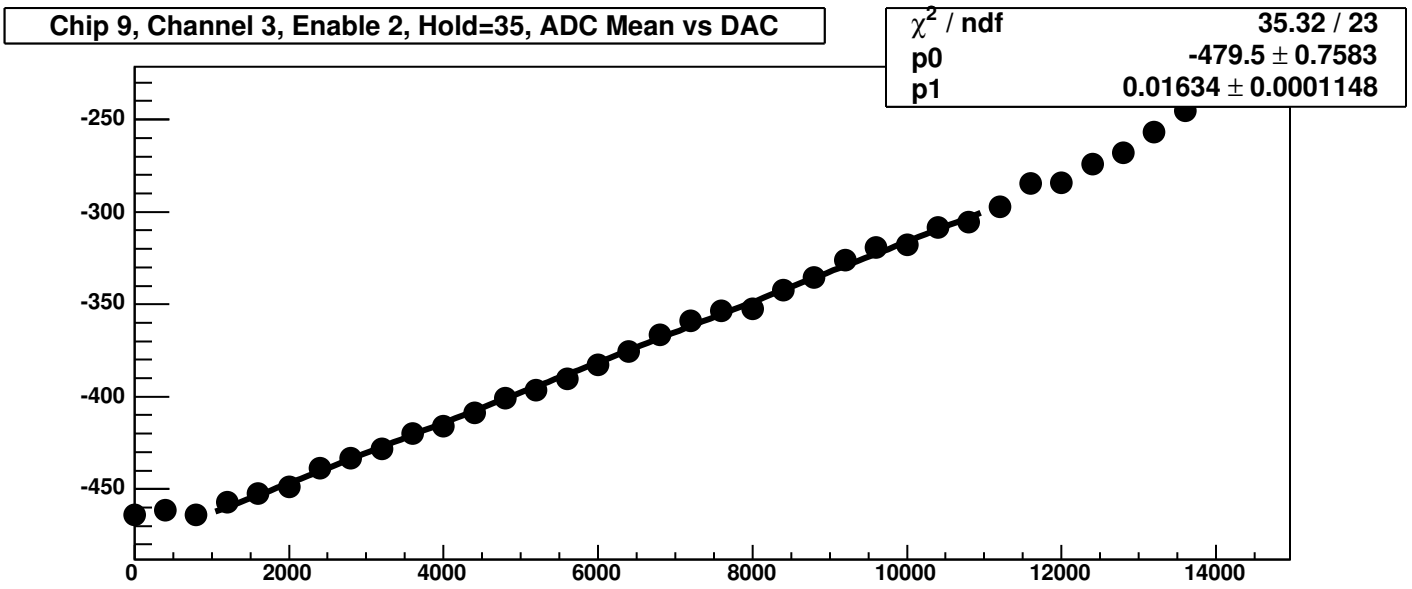
Chip 9, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

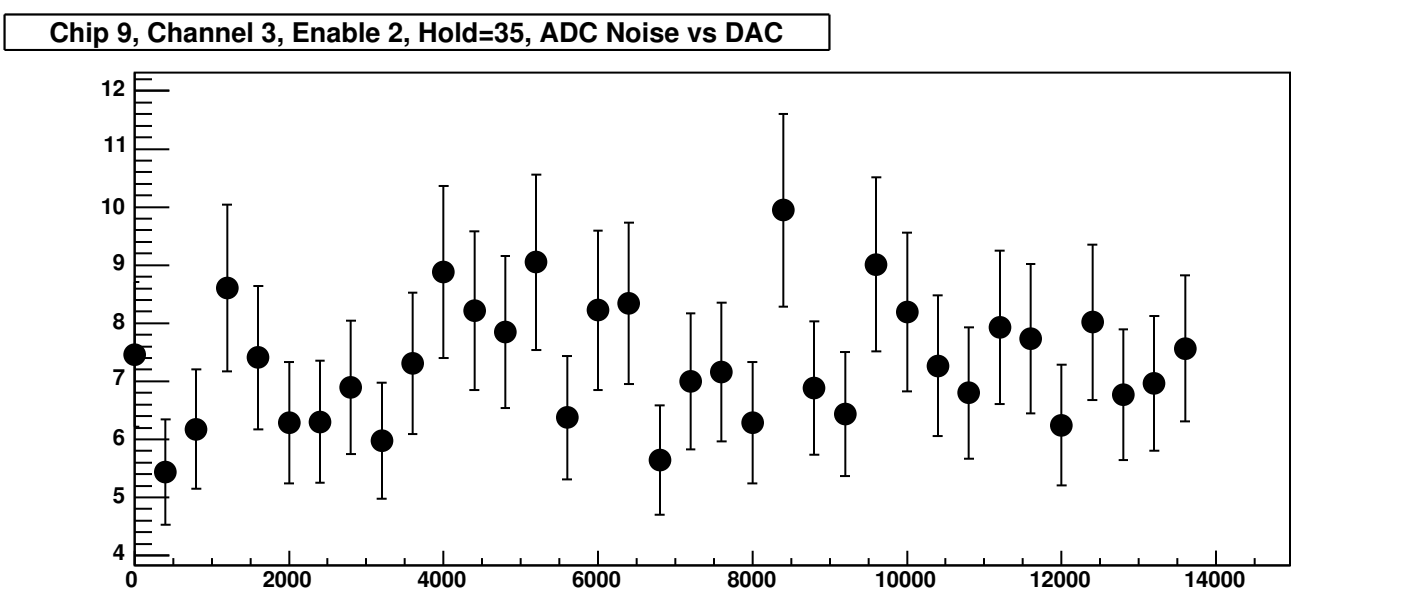


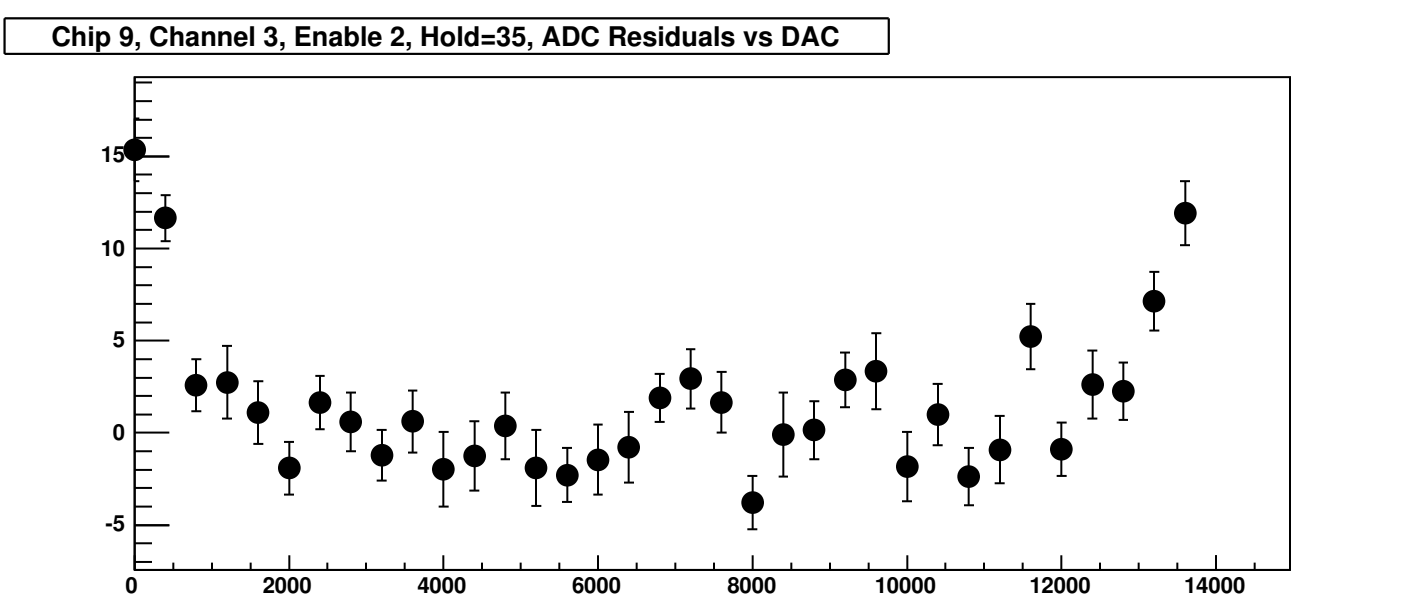

Chip 9, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 37.19 / 23
p0 -481 ± 0.7943
p1 0.02719 ± 0.0001385
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

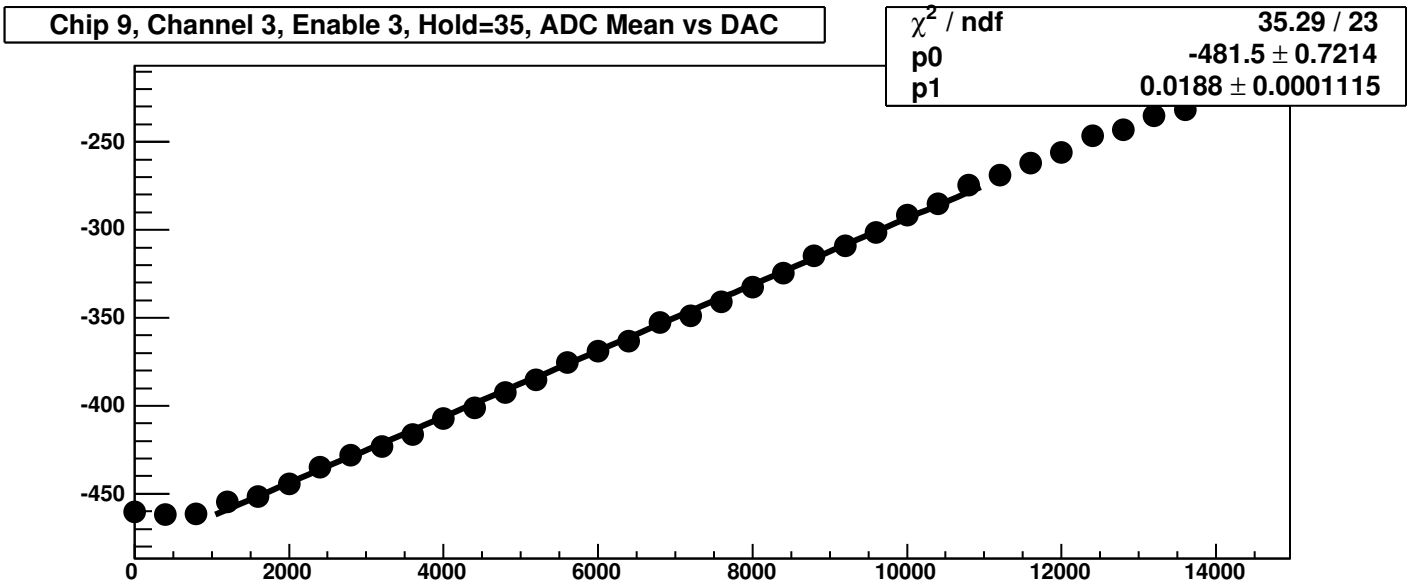

Chip 9, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

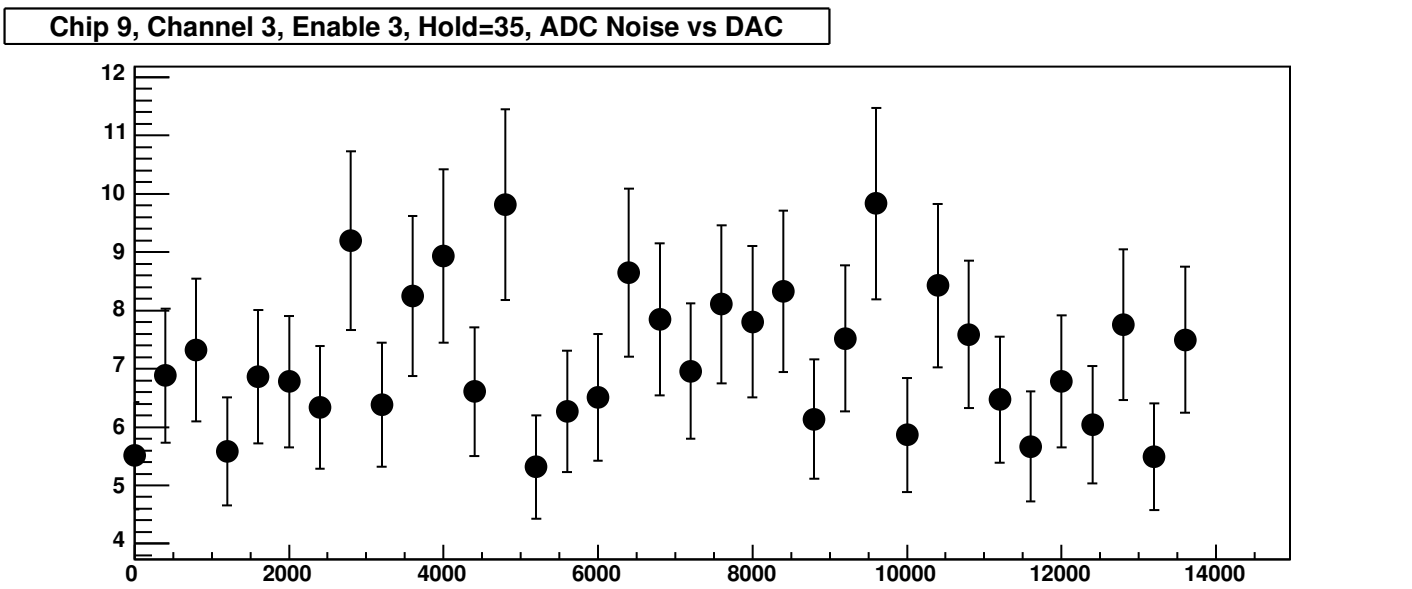

Chip 9, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

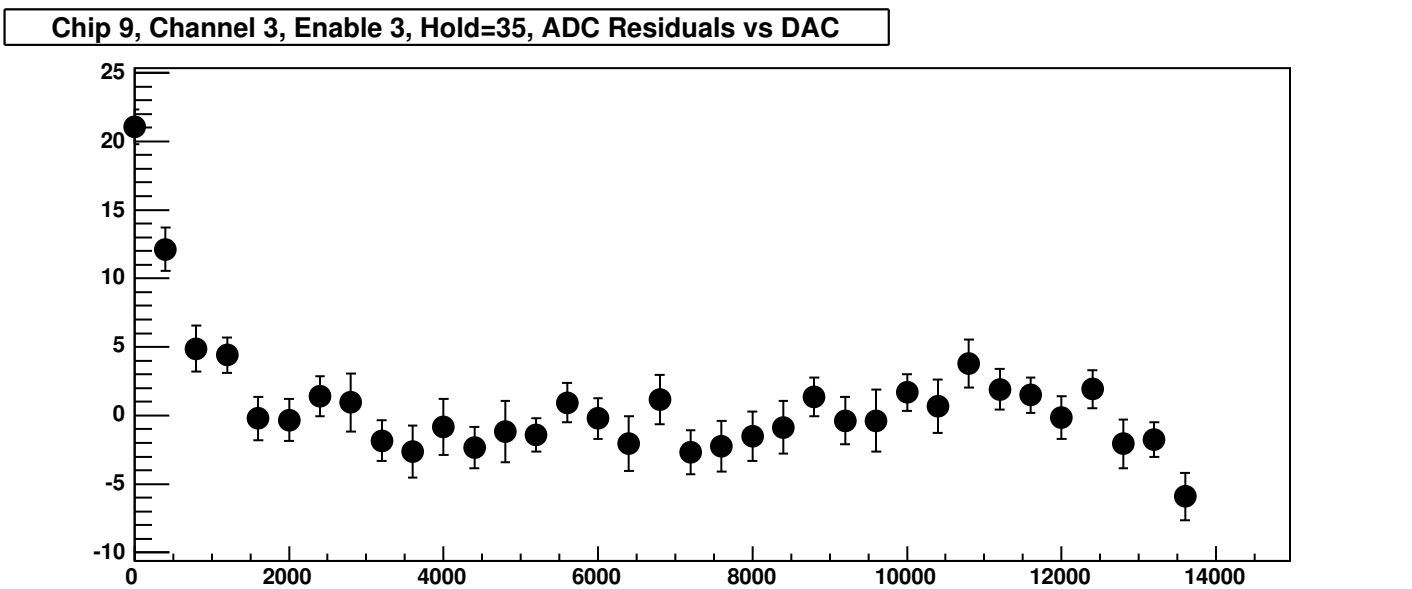

Chip 9, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 35.32 / 23
p0 -479.5 ± 0.7583
p1 0.01634 ± 0.0001148
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

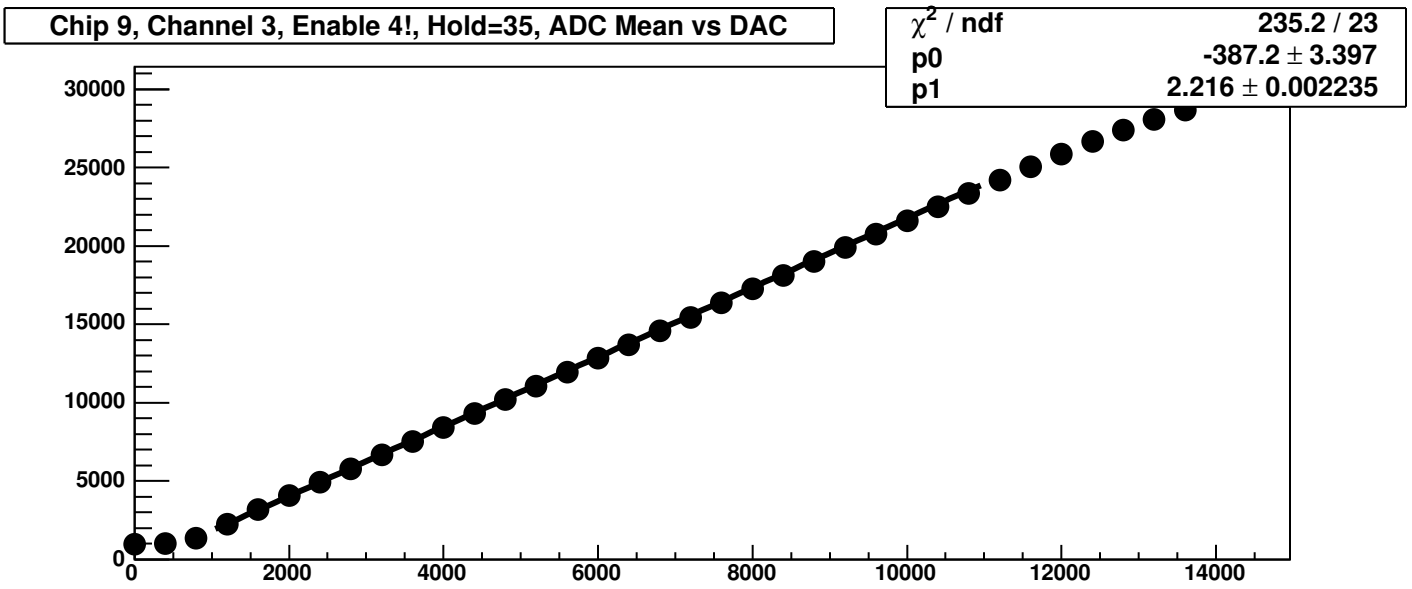

Chip 9, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

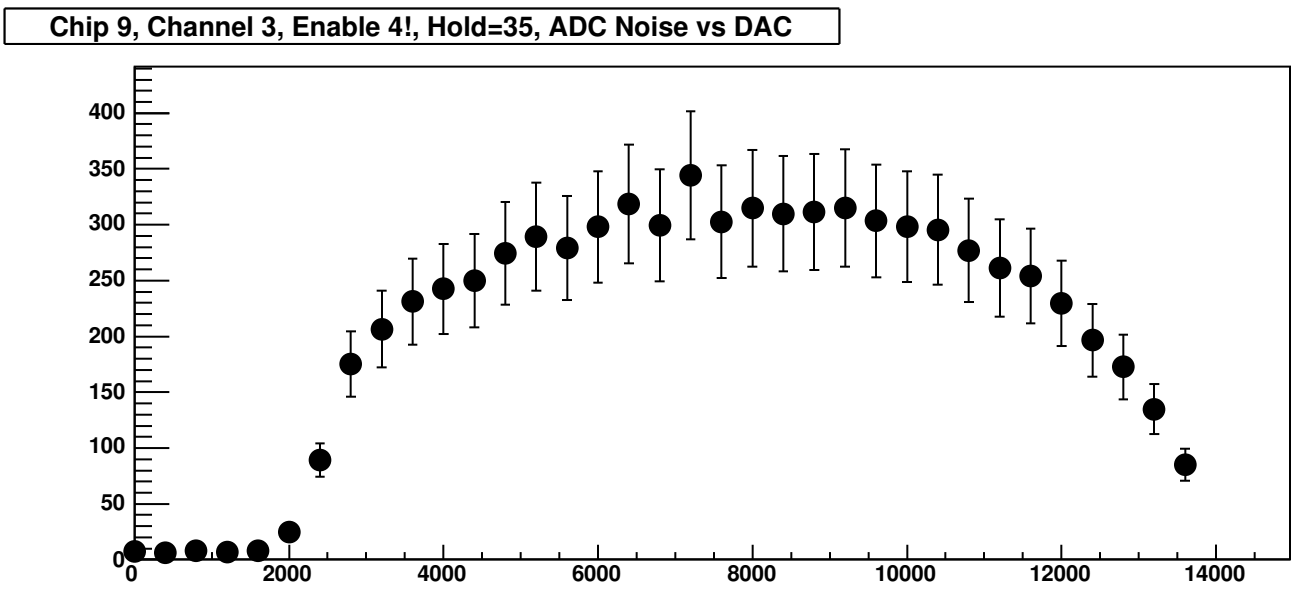

Chip 9, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

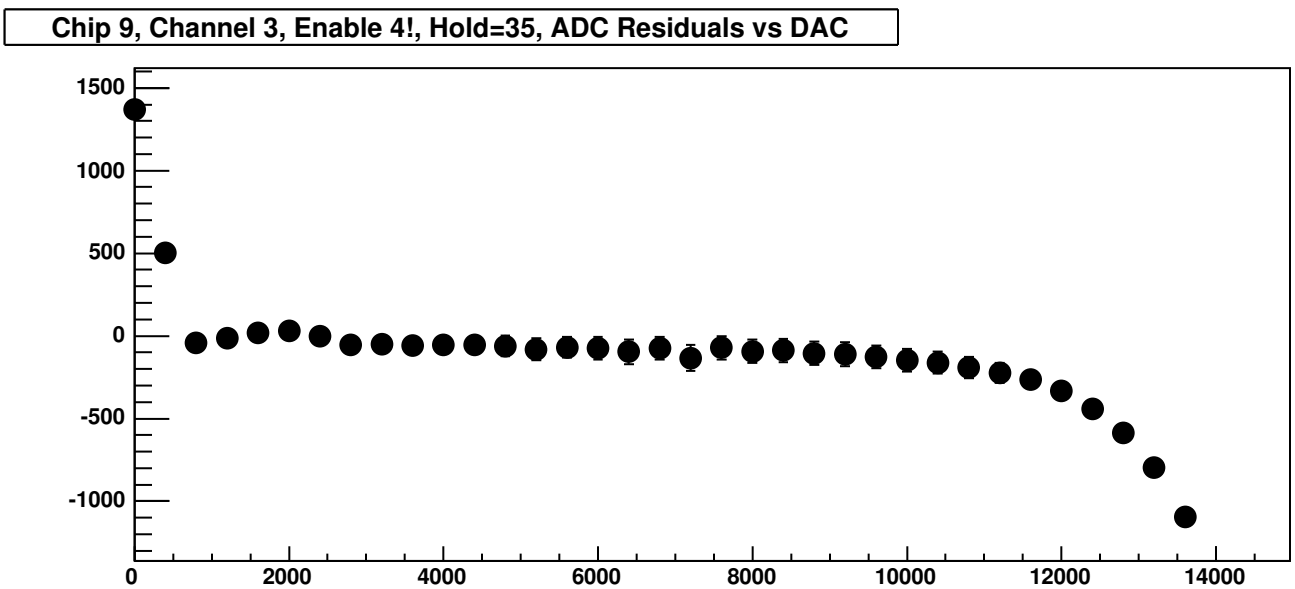

Chip 9, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 35.29 / 23
p0 -481.5 ± 0.7214
p1 0.0188 ± 0.0001115
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

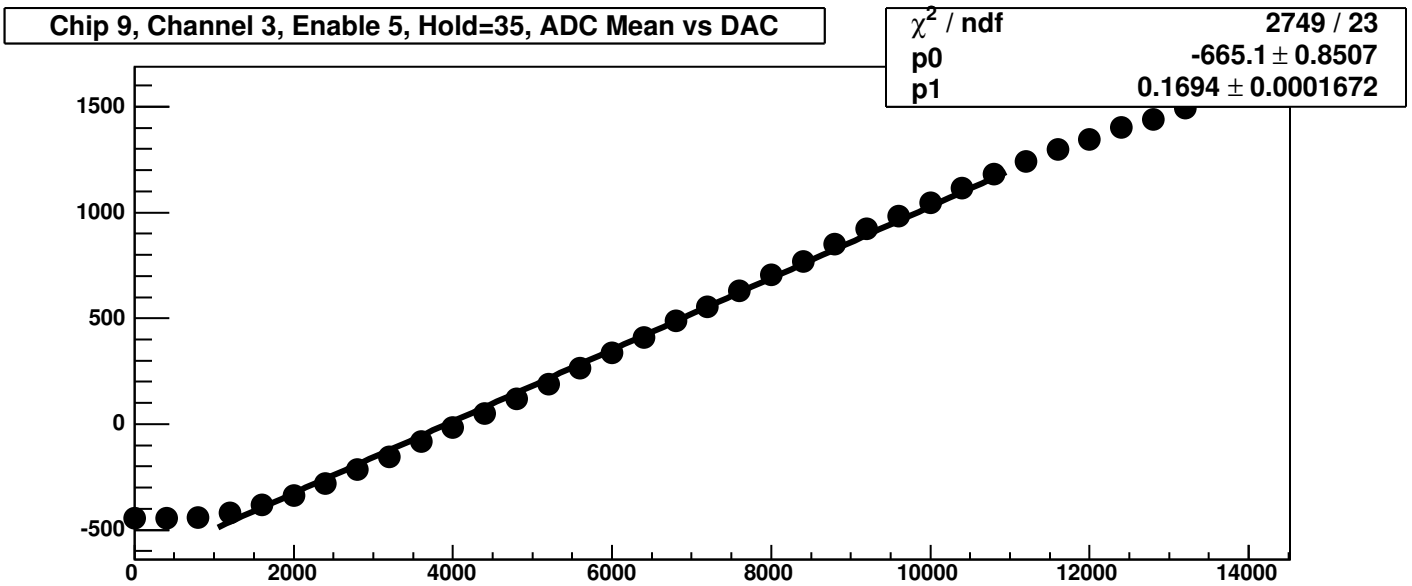

Chip 9, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

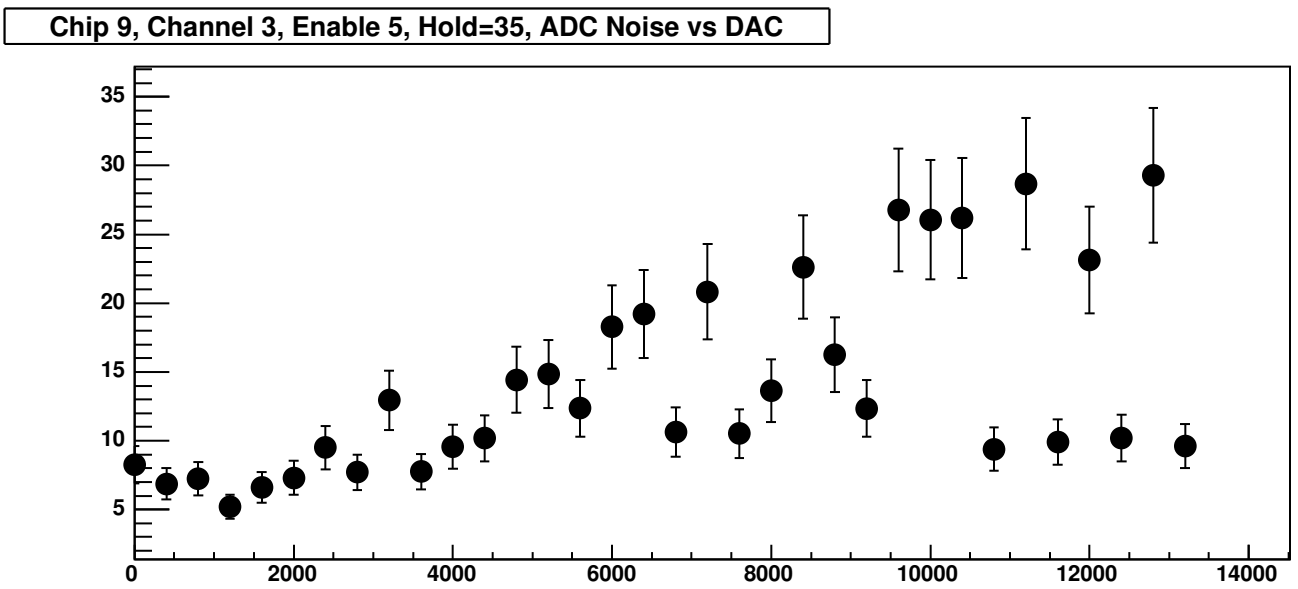

Chip 9, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

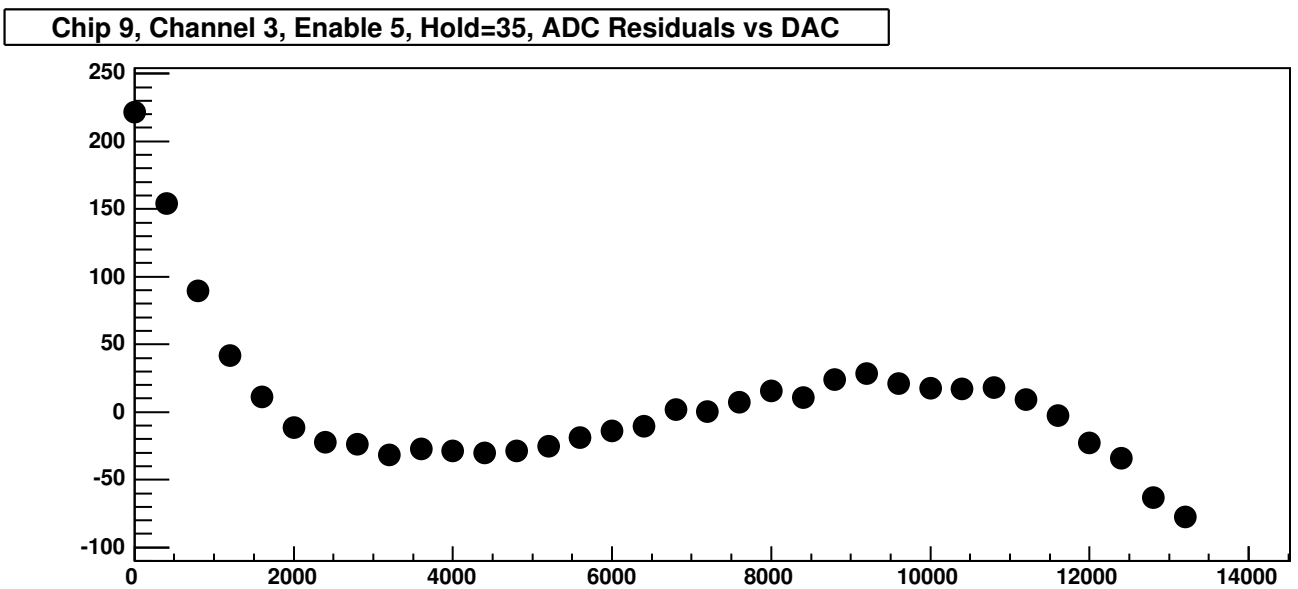

Chip 9, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 235.2 / 23
p0 -387.2 ± 3.397
p1 2.216 ± 0.002235
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

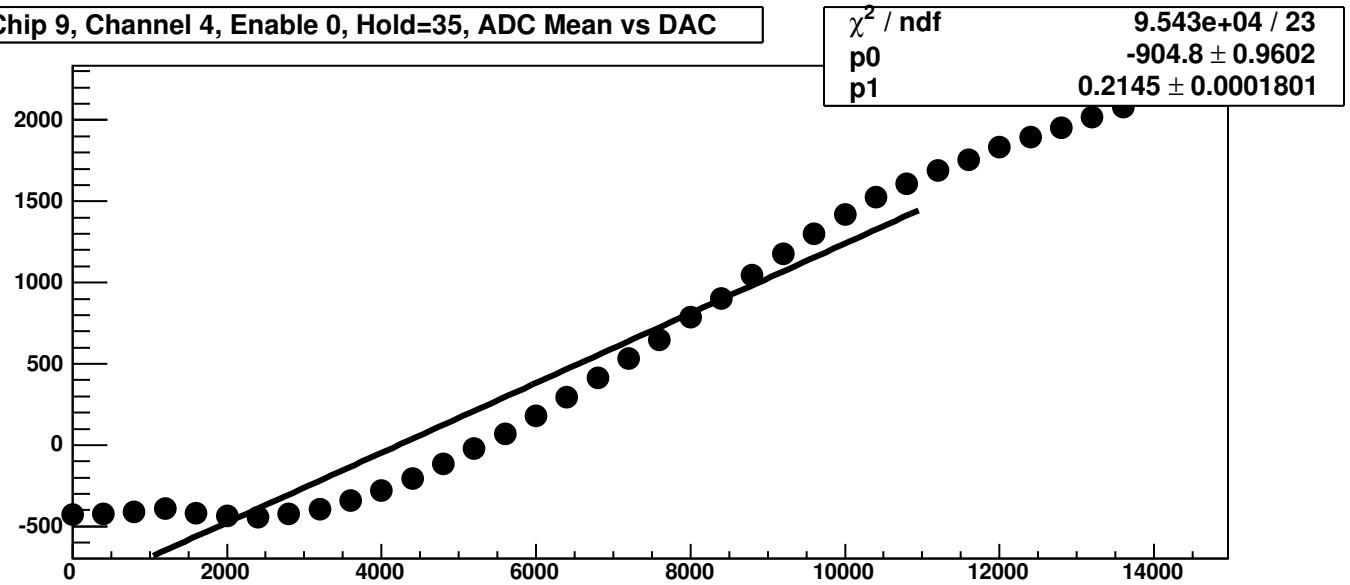

Chip 9, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2749 / 23
p0 -665.1 ± 0.8507
p1 0.1694 ± 0.0001672
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

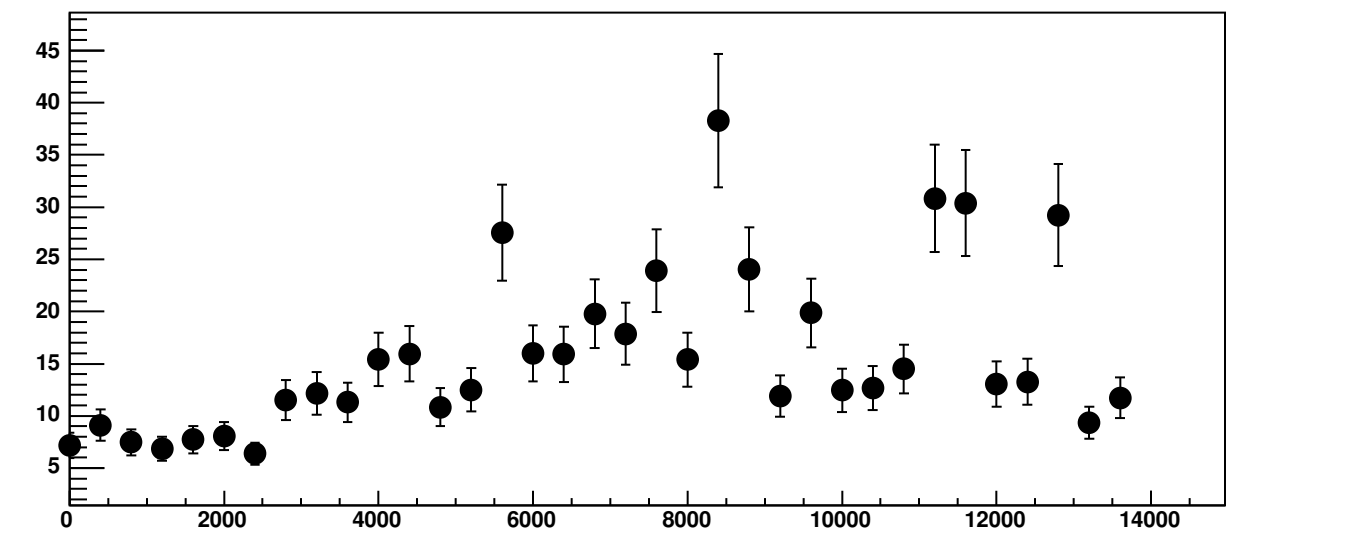

Chip 9, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

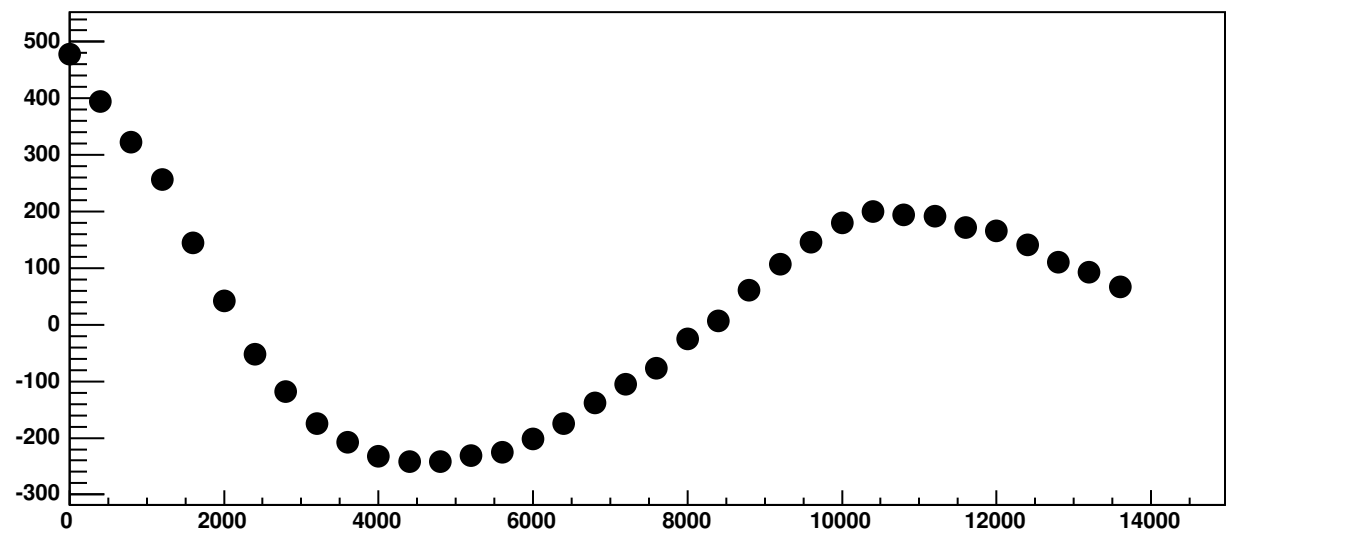
Chip 9, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

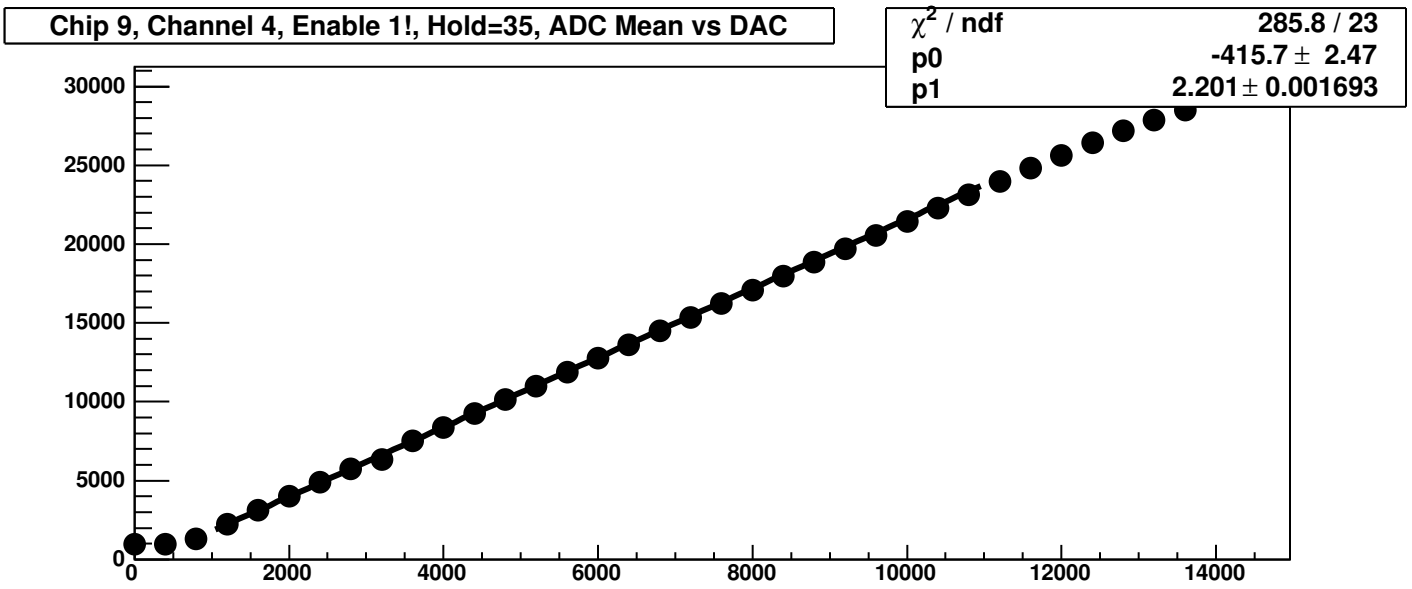


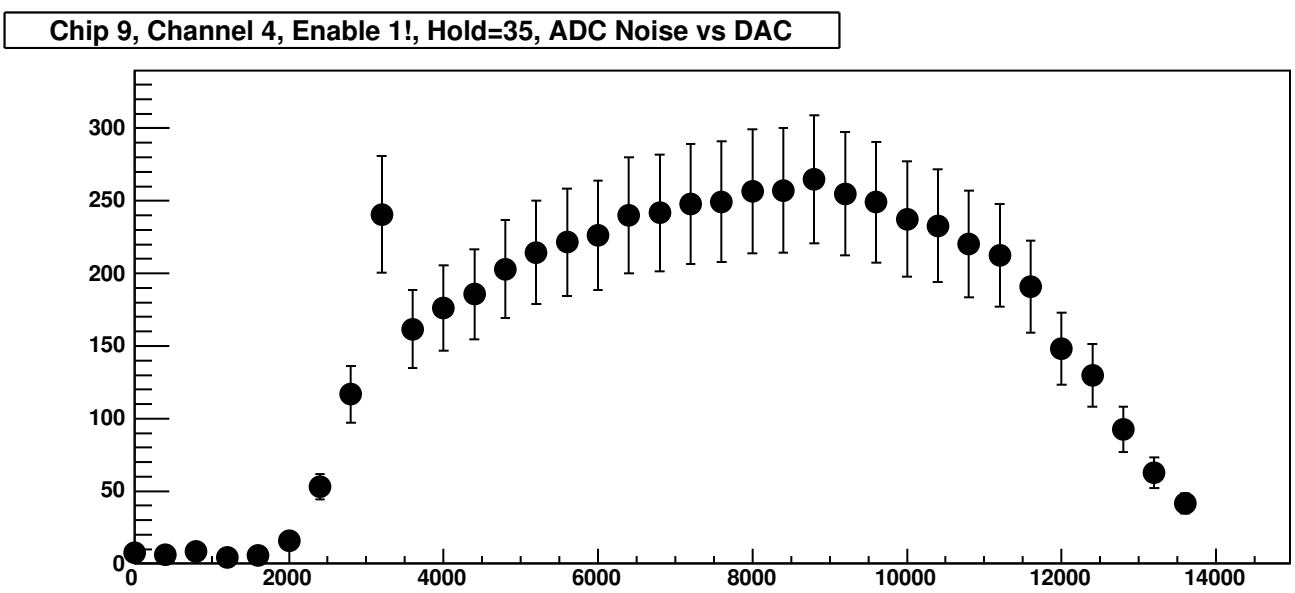
Chip 9, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC

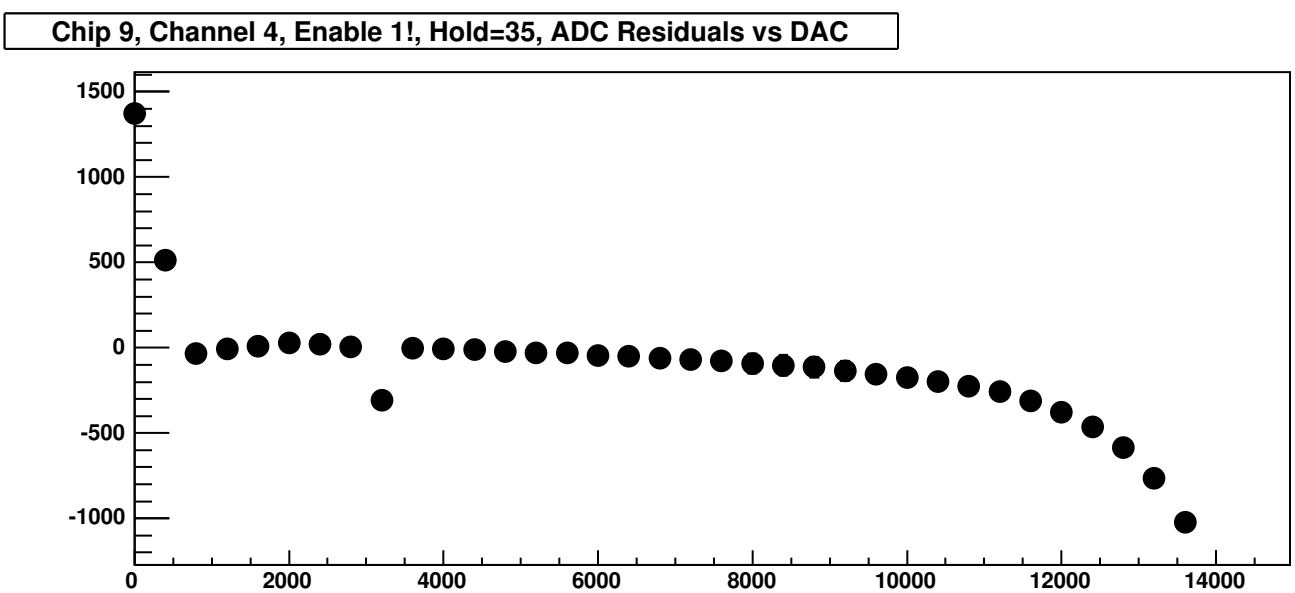


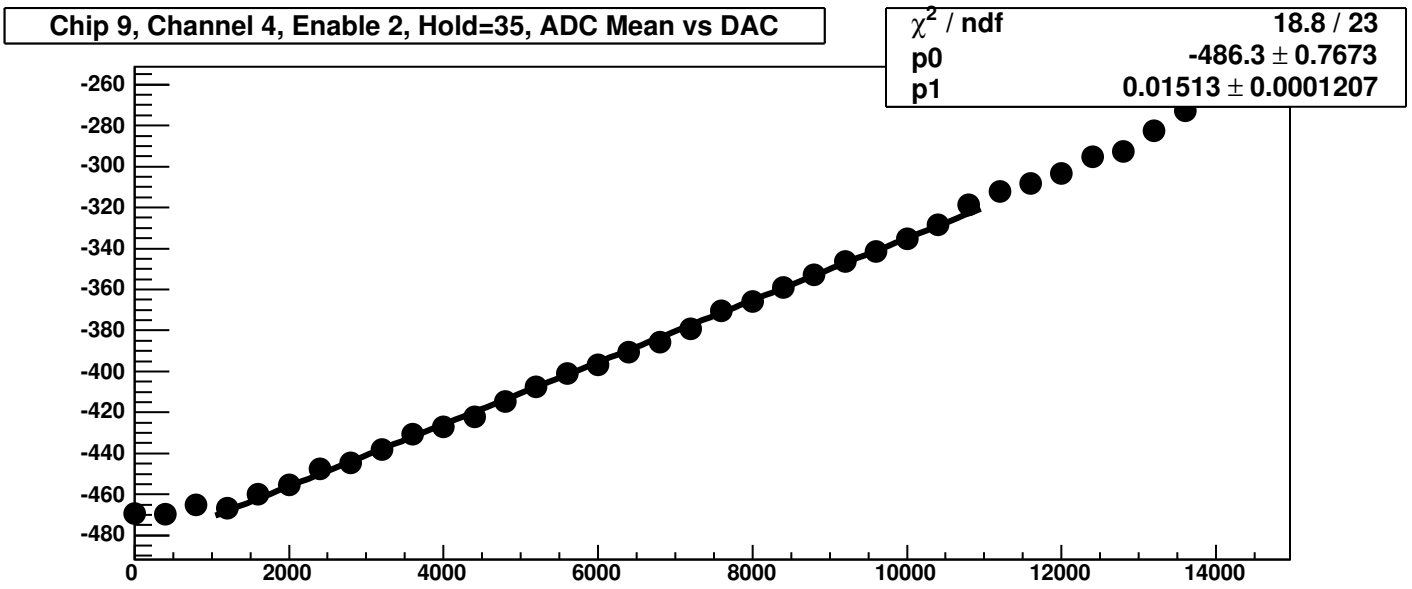
Chip 9, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

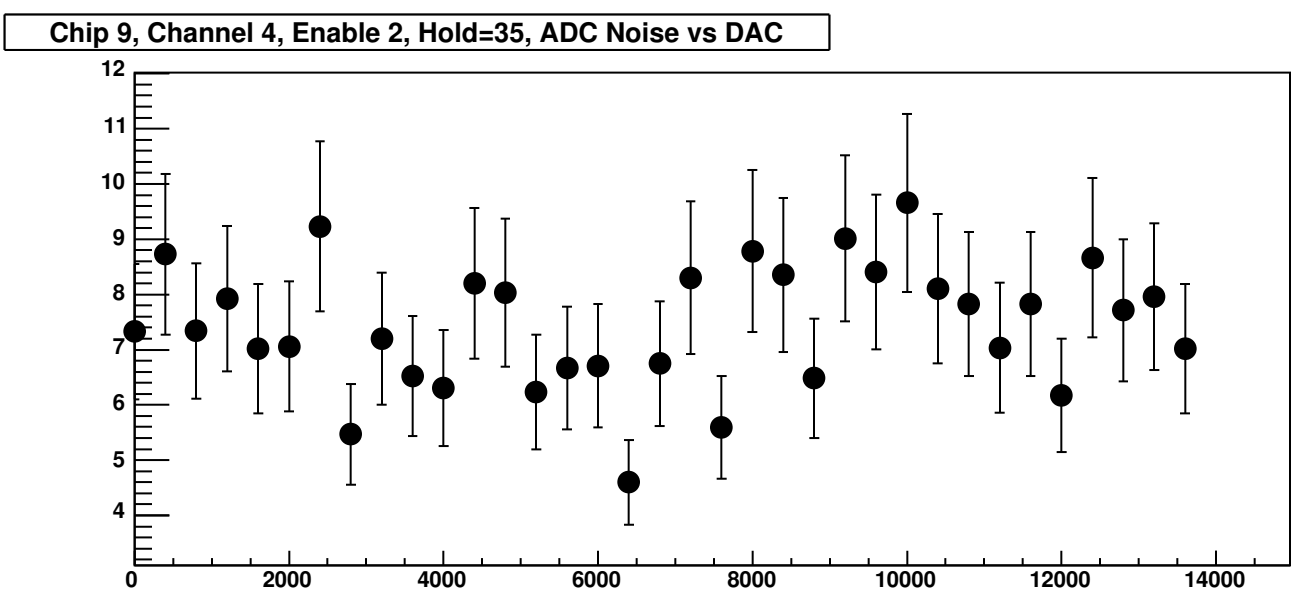


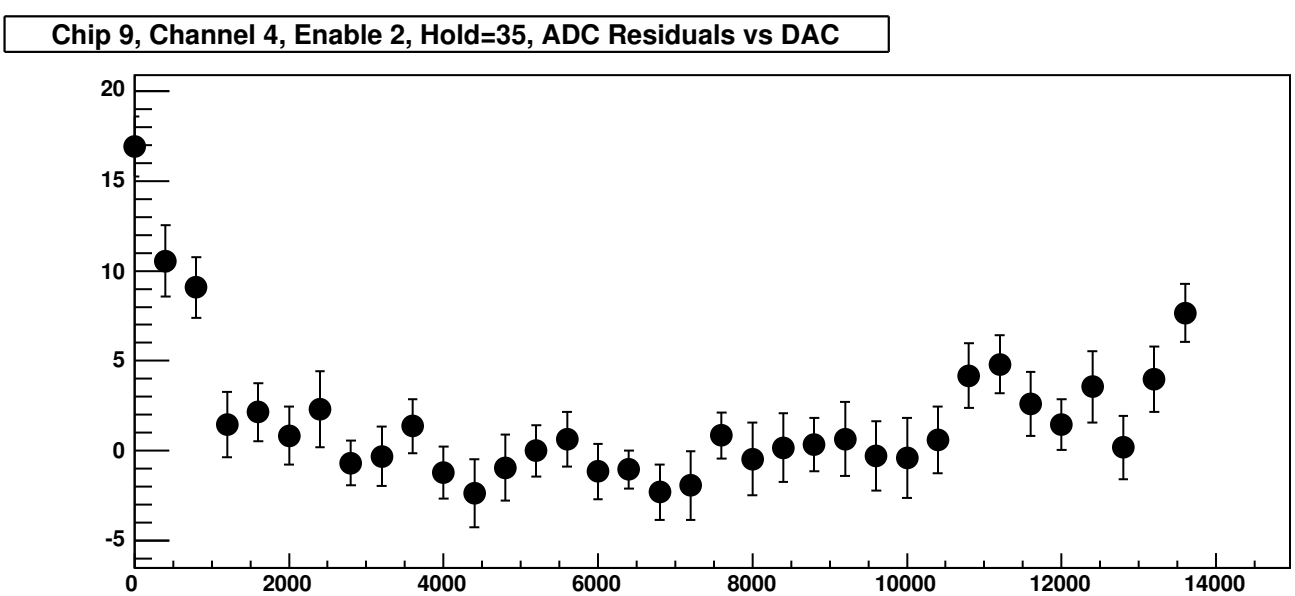

Chip 9, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 285.8 / 23
p0 -415.7 ± 2.47
p1 2.201 ± 0.001693
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

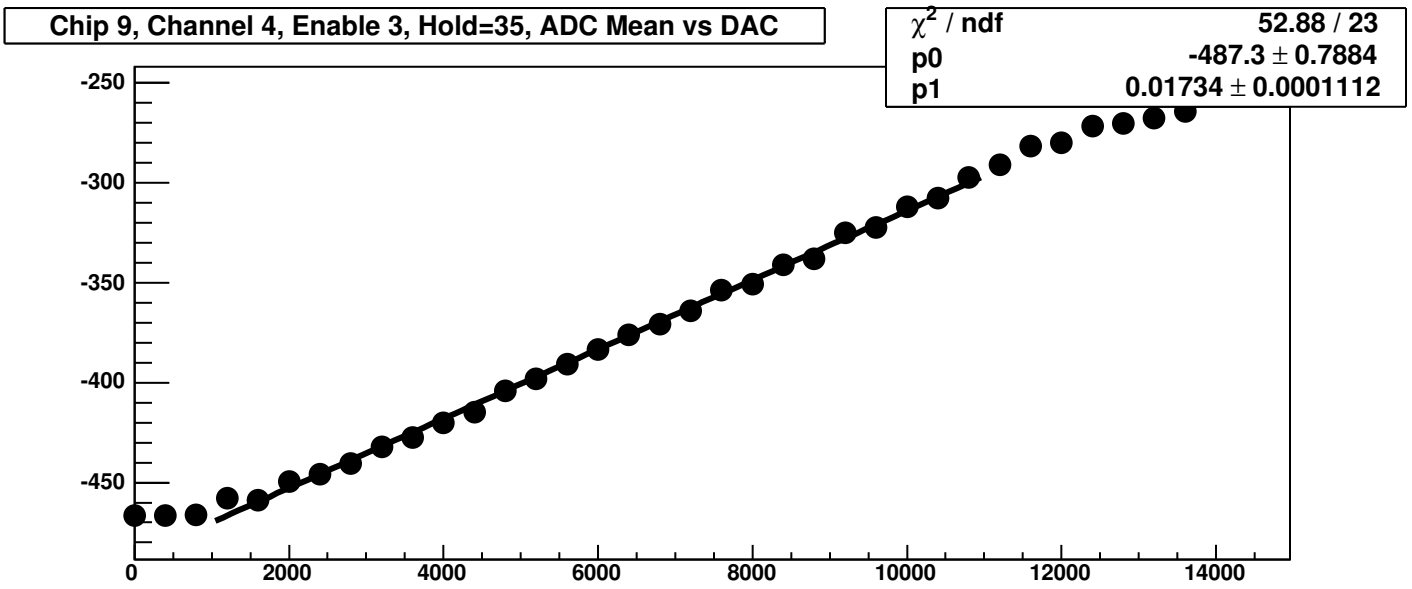

Chip 9, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

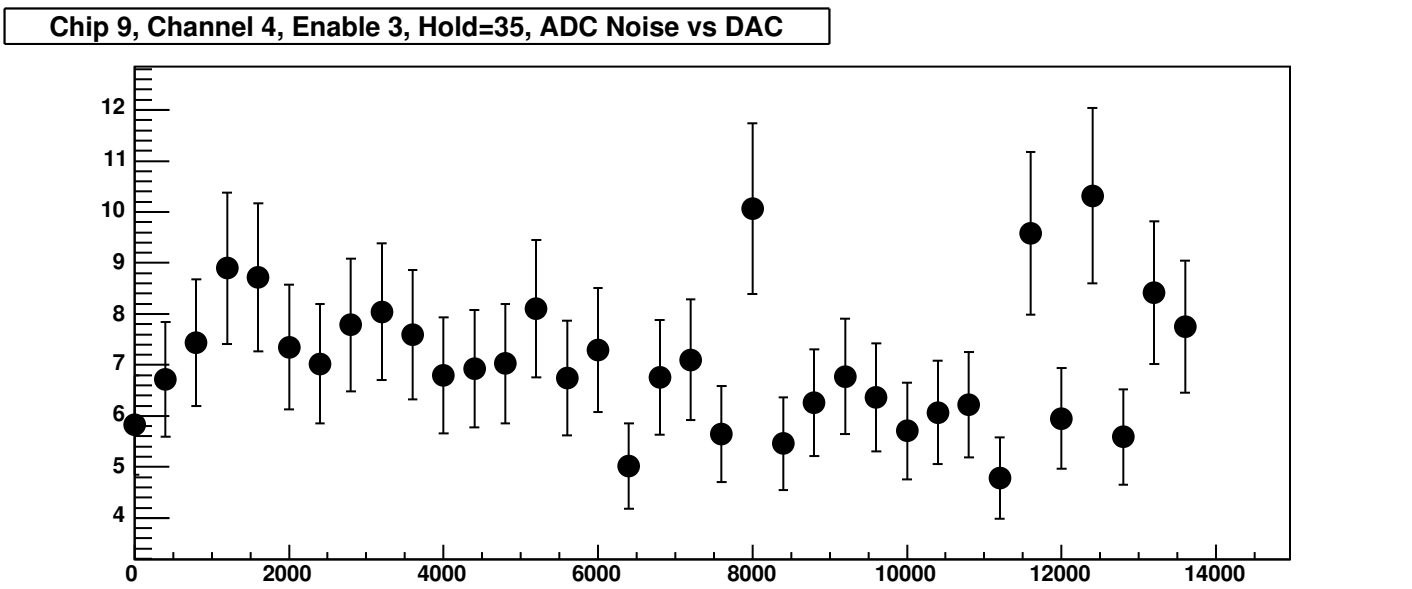

Chip 9, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

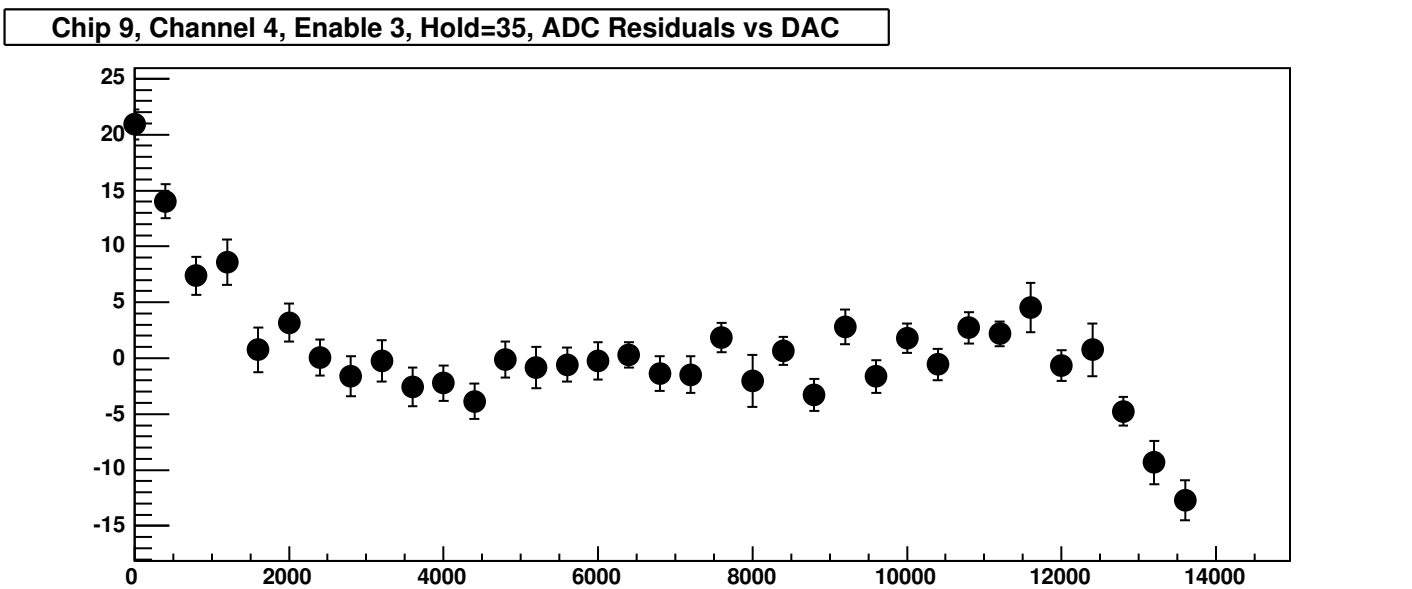

Chip 9, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 18.8 / 23
p0 -486.3 ± 0.7673
p1 0.01513 ± 0.0001207
-260
-280
-300
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
2000
4000
6000
8000
10000
12000
14000

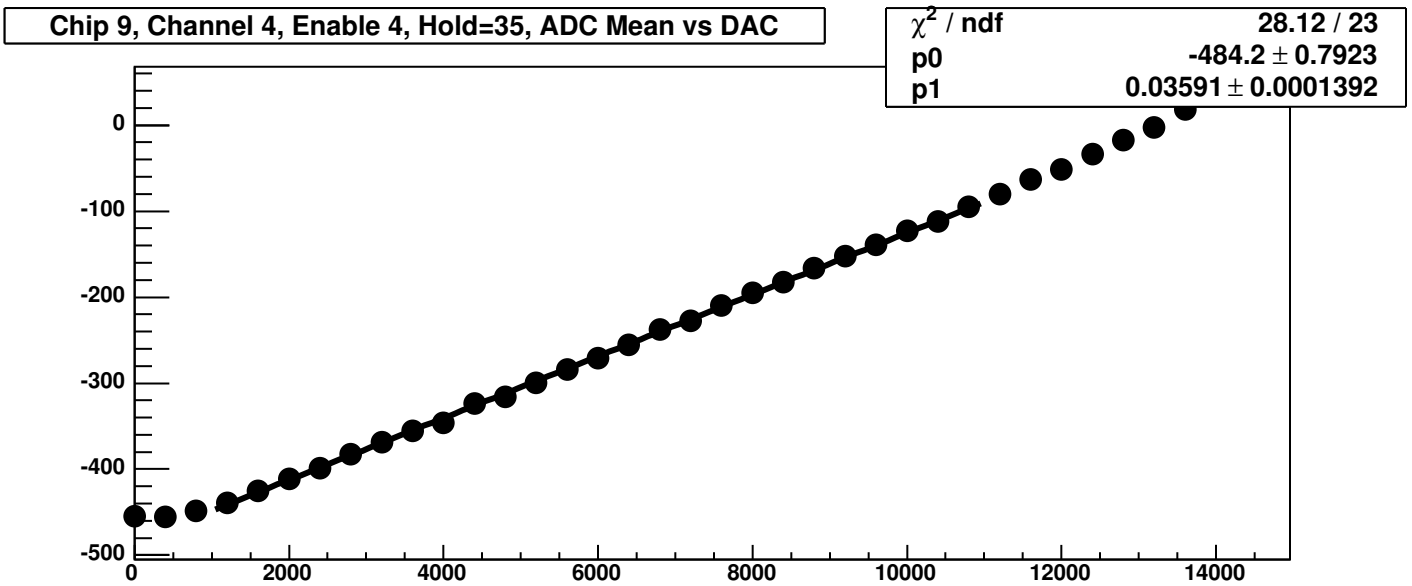

Chip 9, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

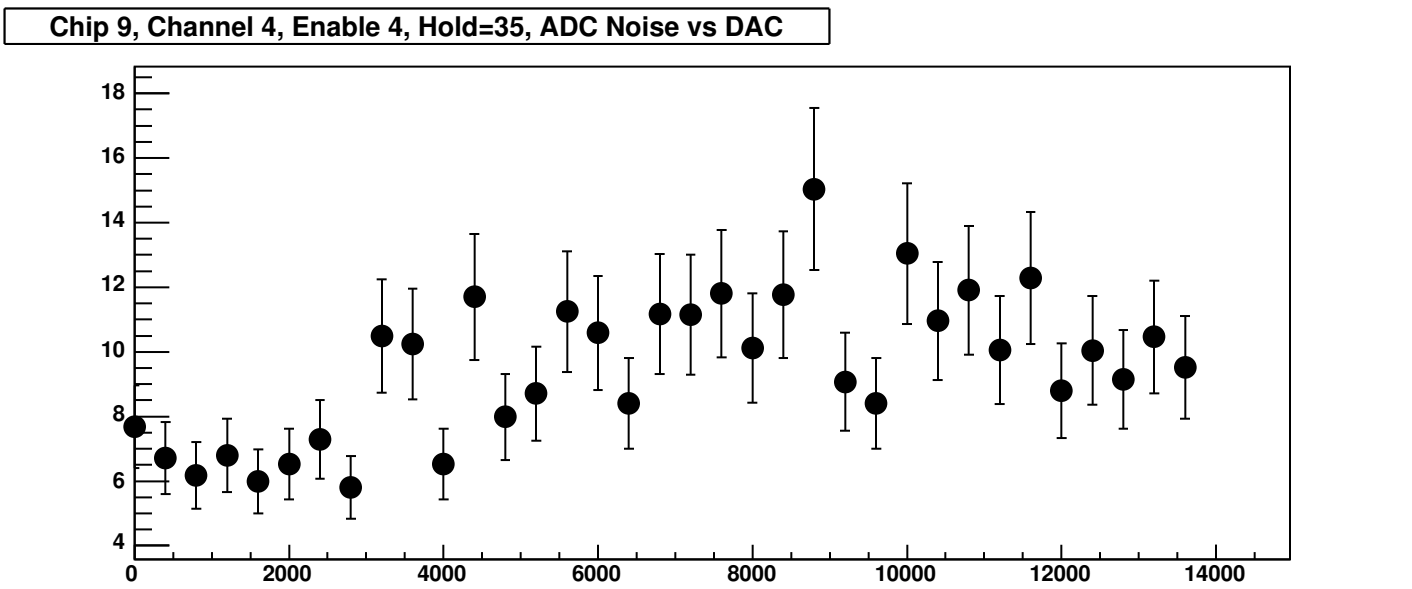

Chip 9, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

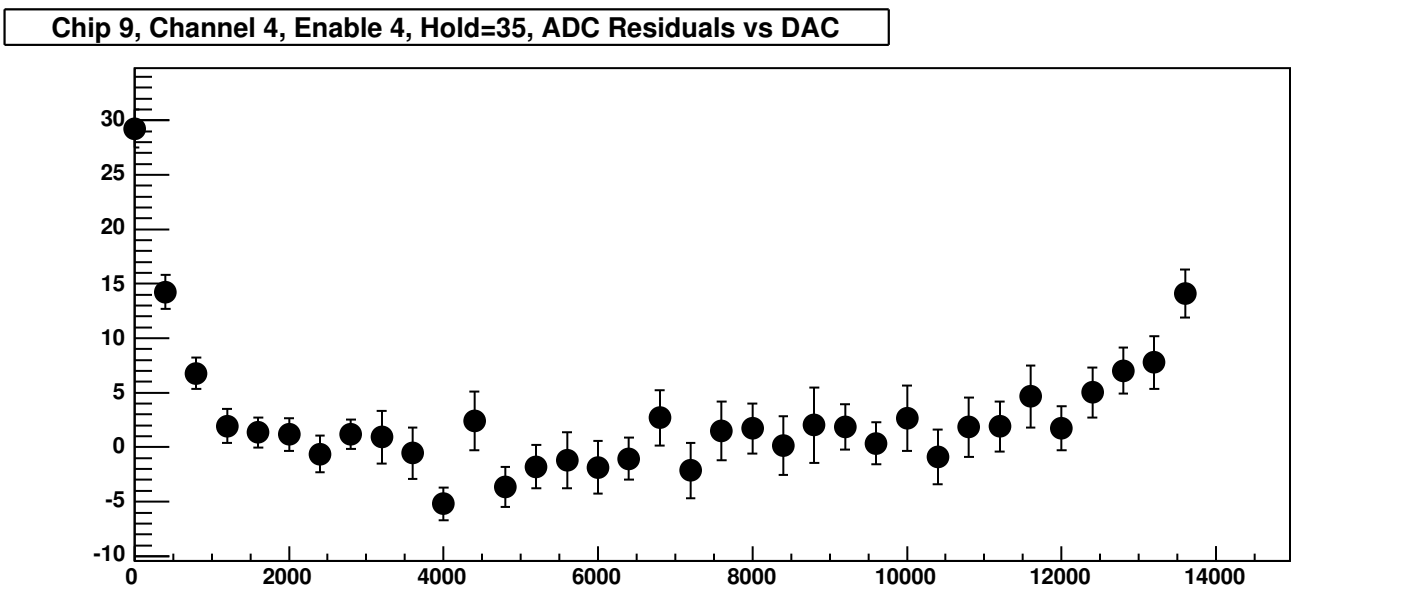

Chip 9, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 52.88 / 23
p0 -487.3 ± 0.7884
p1 0.01734 ± 0.0001112
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

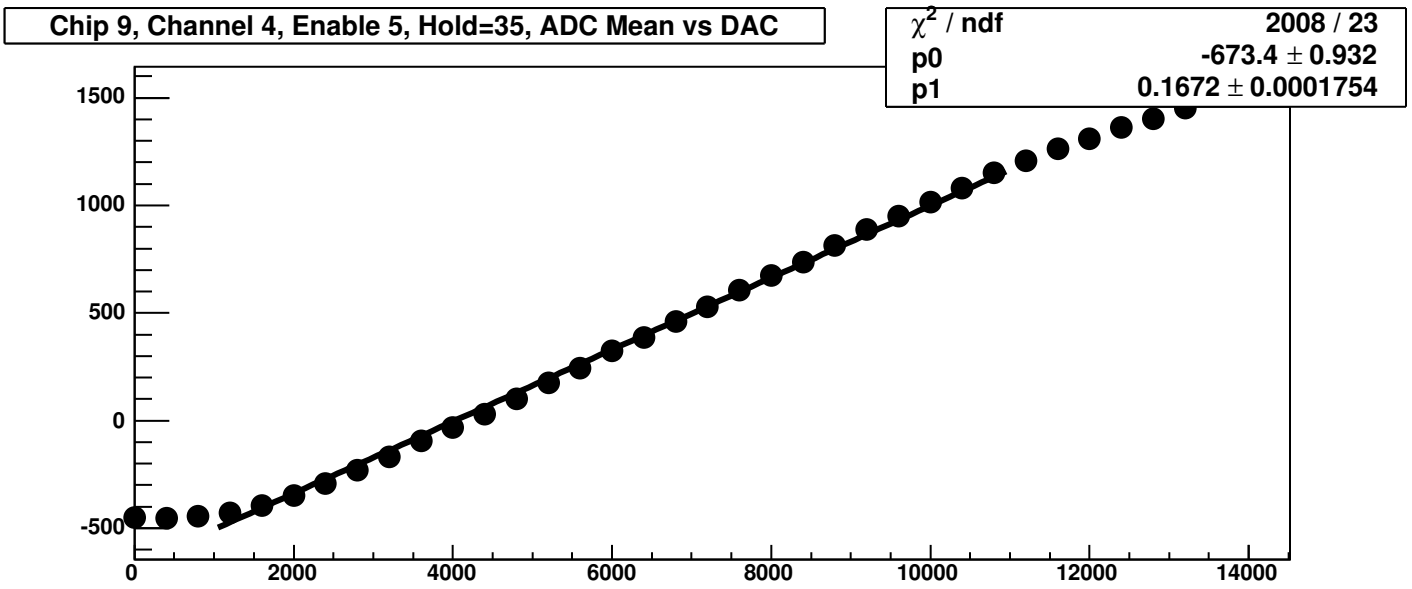

Chip 9, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

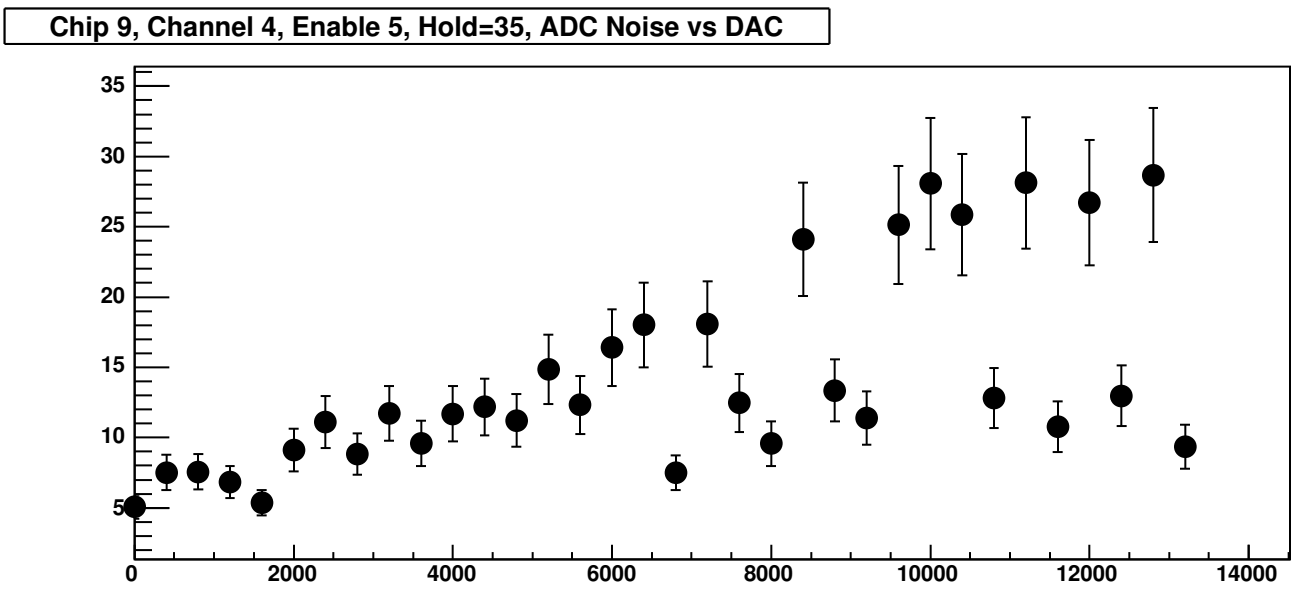

Chip 9, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

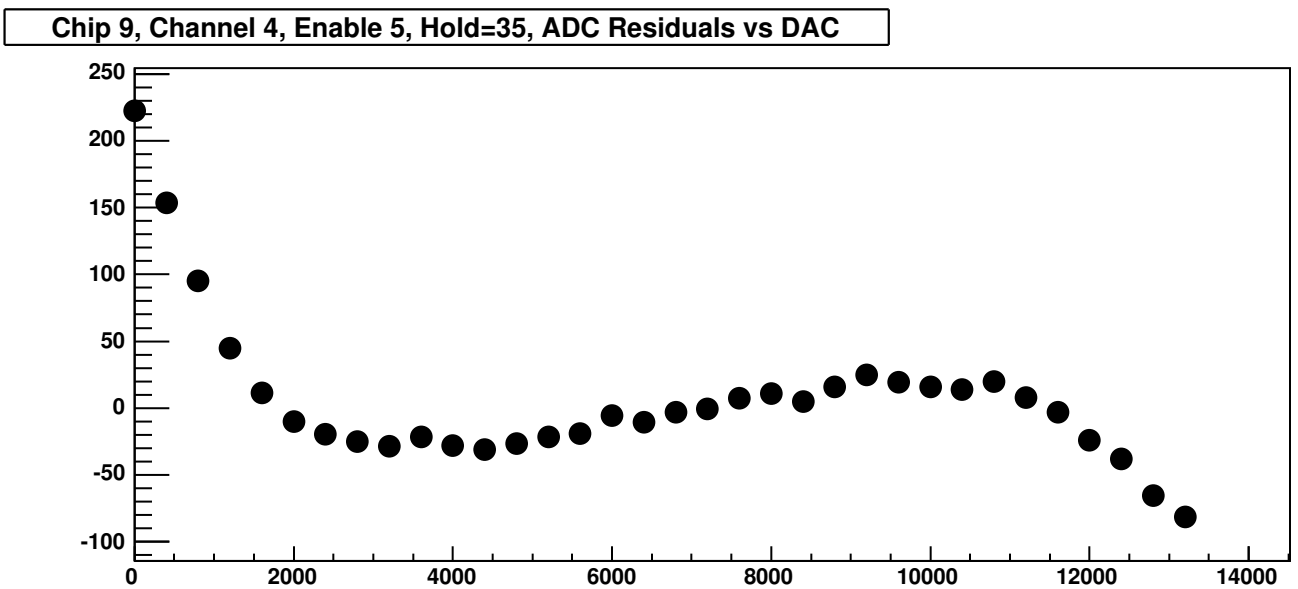

Chip 9, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 28.12 / 23
p0 -484.2 ± 0.7923
p1 0.03591 ± 0.0001392
0
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000

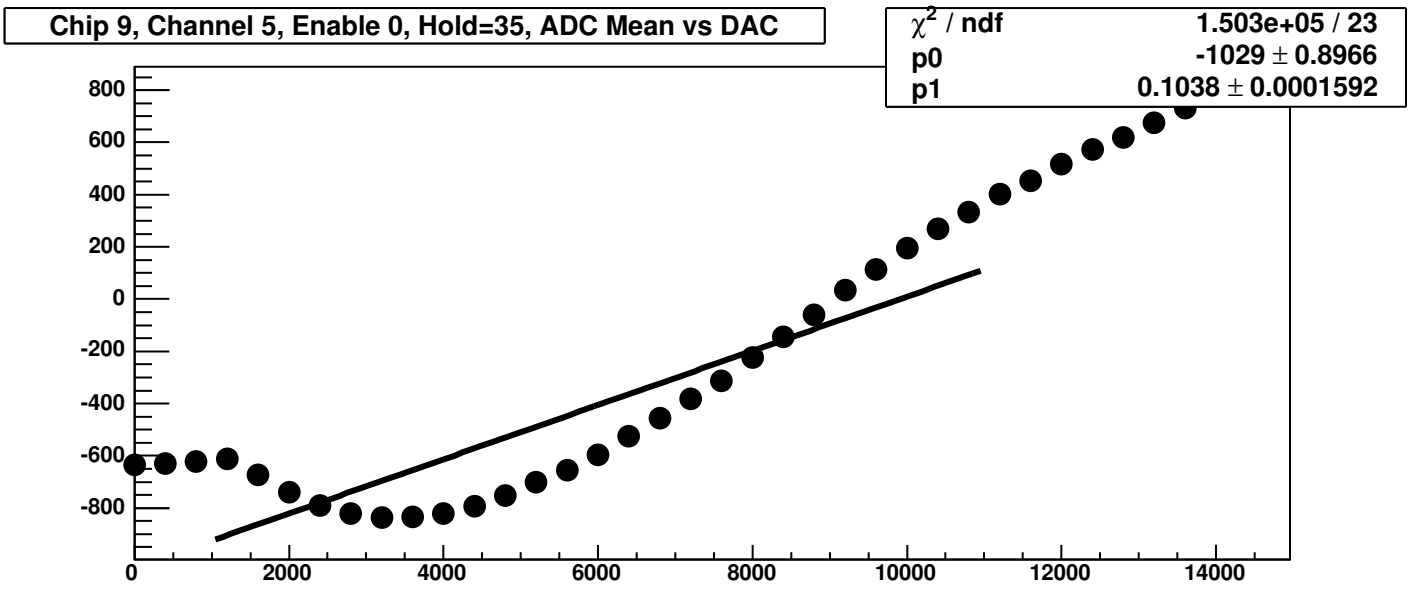

Chip 9, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

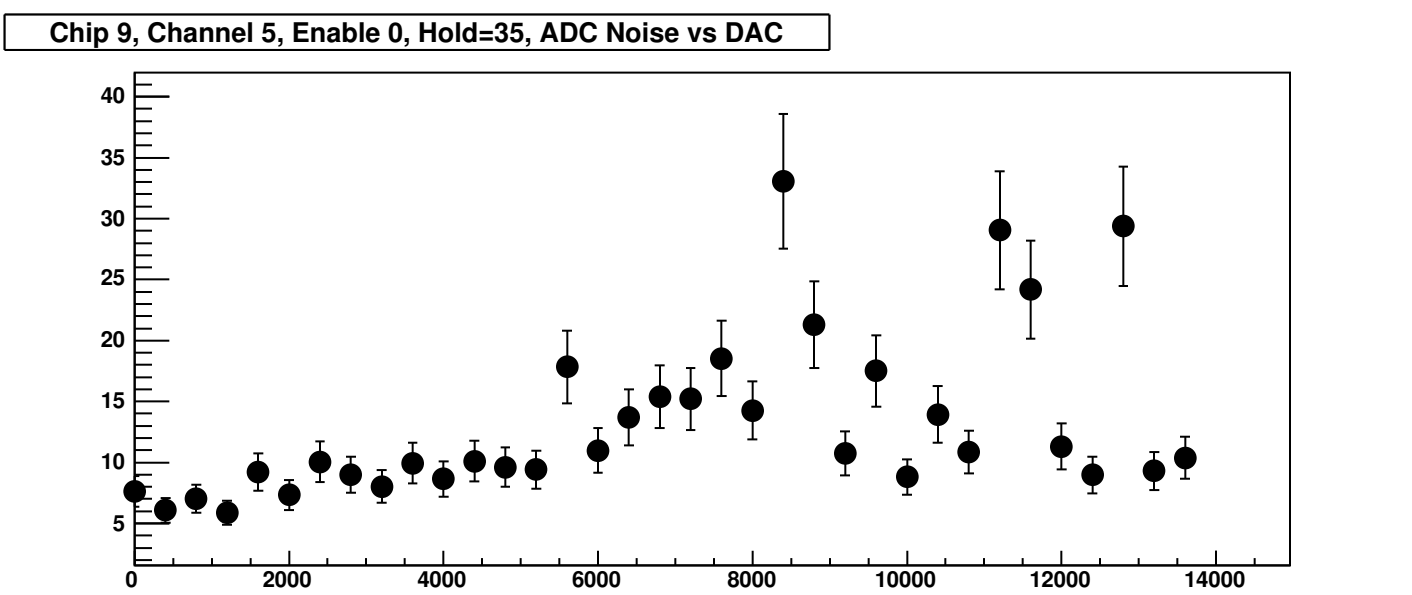

Chip 9, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

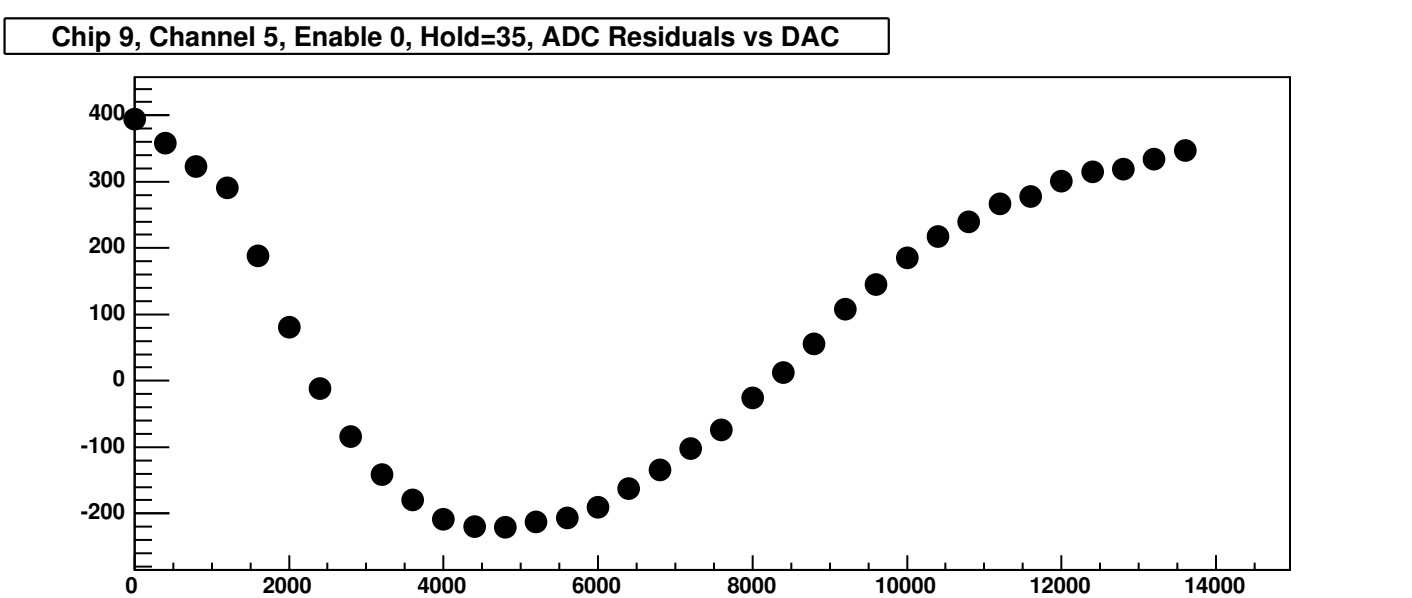

Chip 9, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2008 / 23
p0 -673.4 ± 0.932
p1 0.1672 ± 0.0001754
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

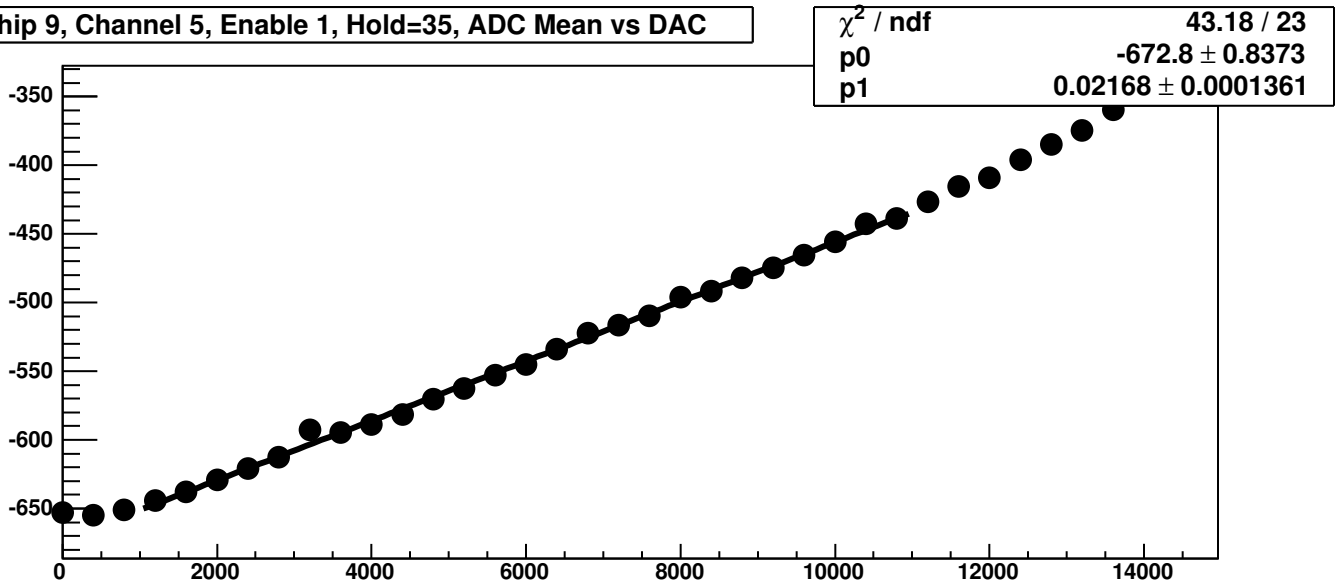

Chip 9, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.503e+05 / 23
p0 -1029 ± 0.8966
p1 0.1038 ± 0.0001592
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

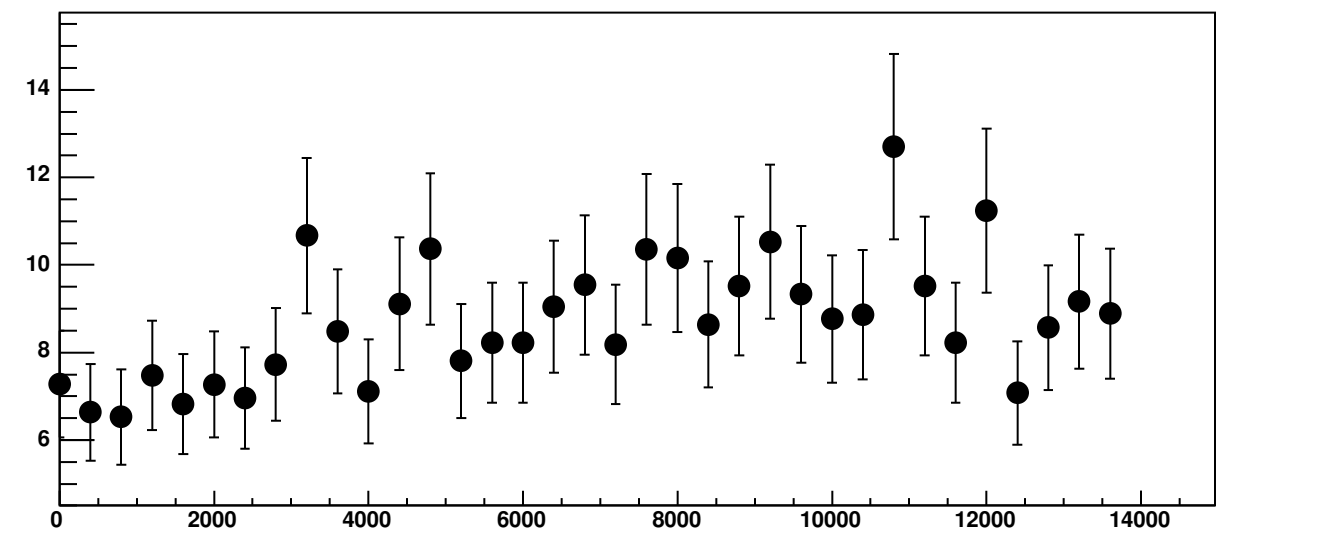

Chip 9, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

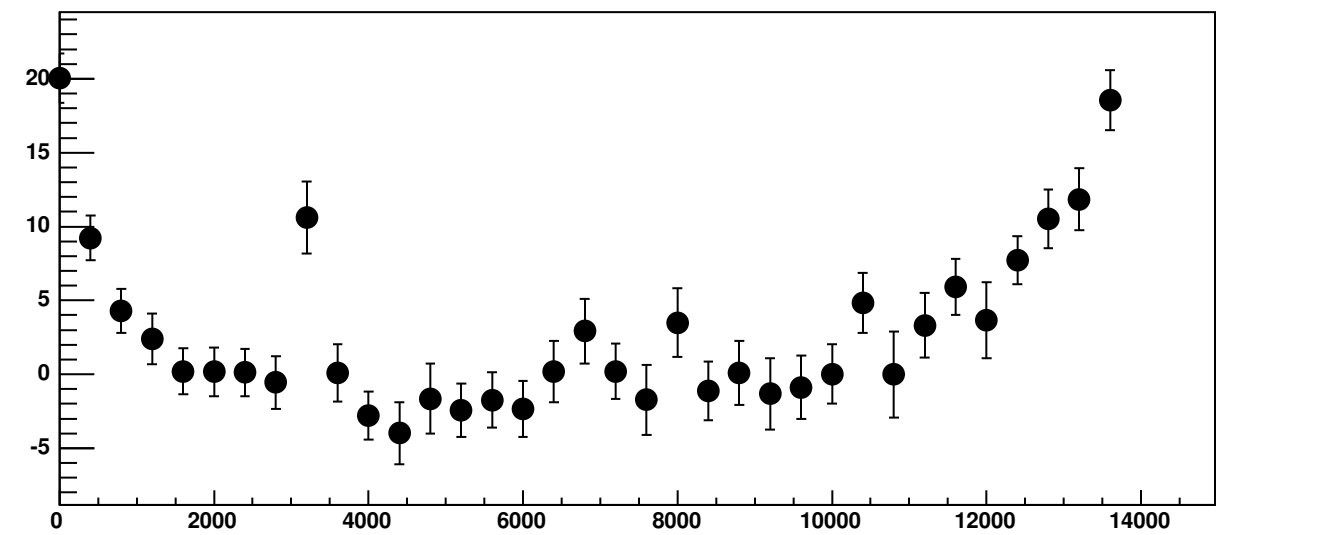
Chip 9, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC

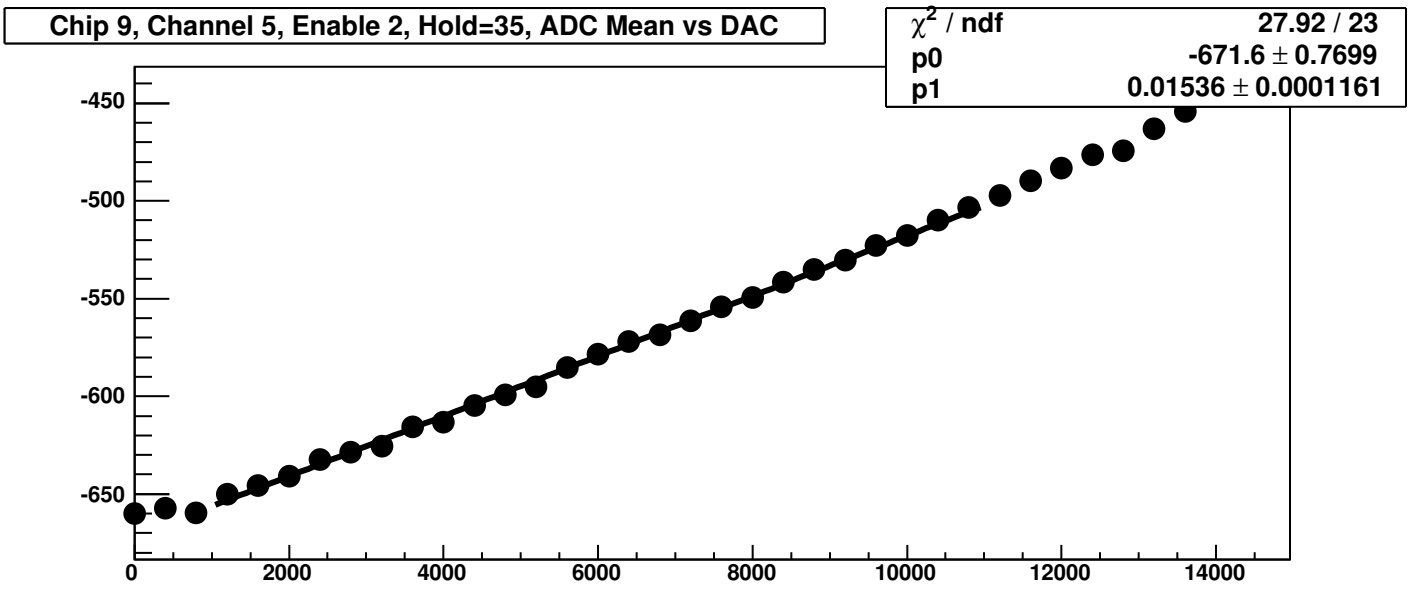


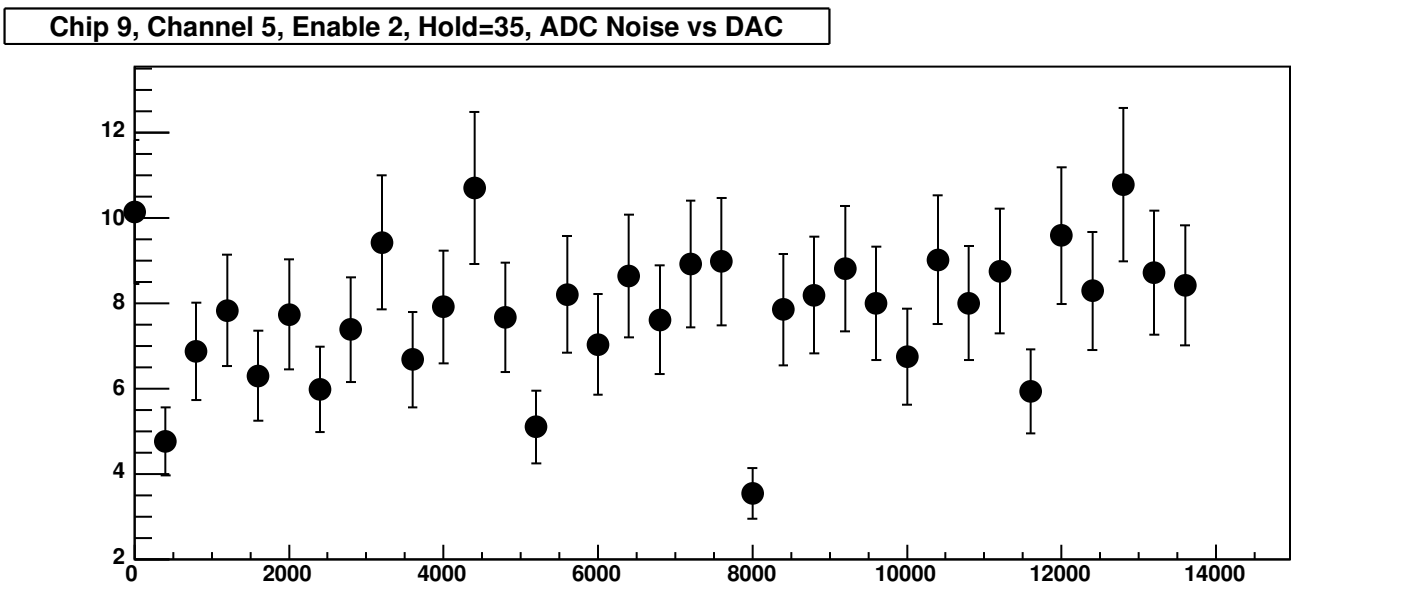
Chip 9, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC

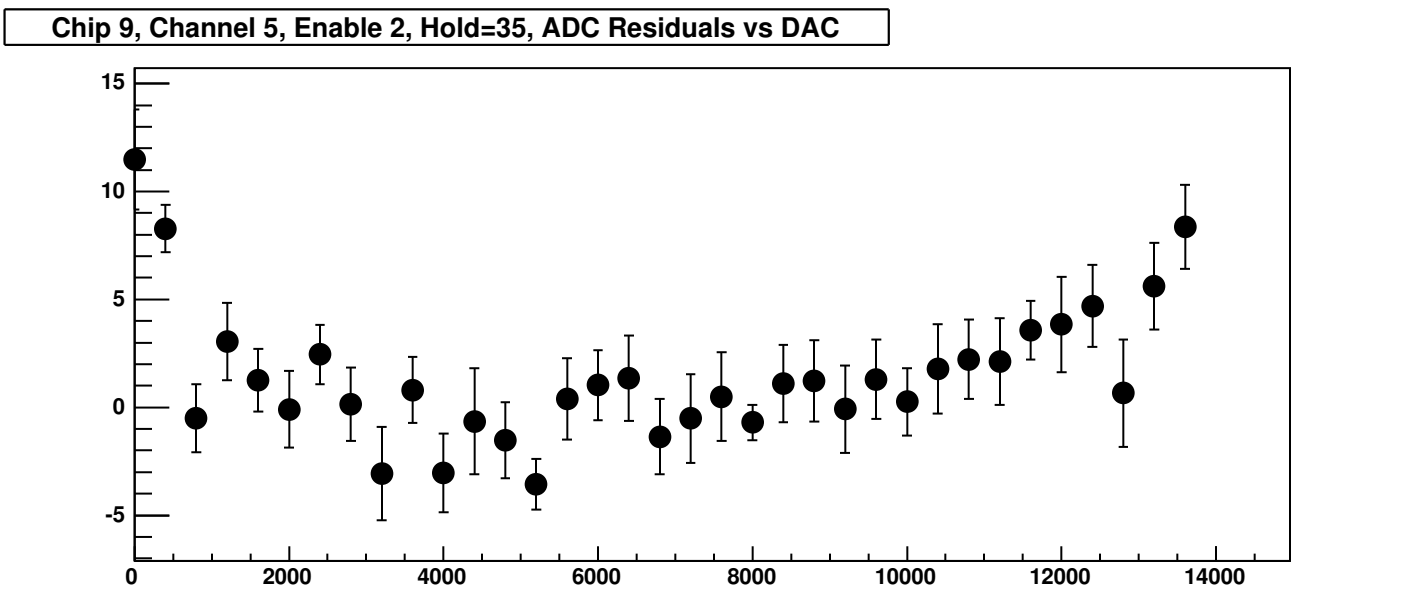


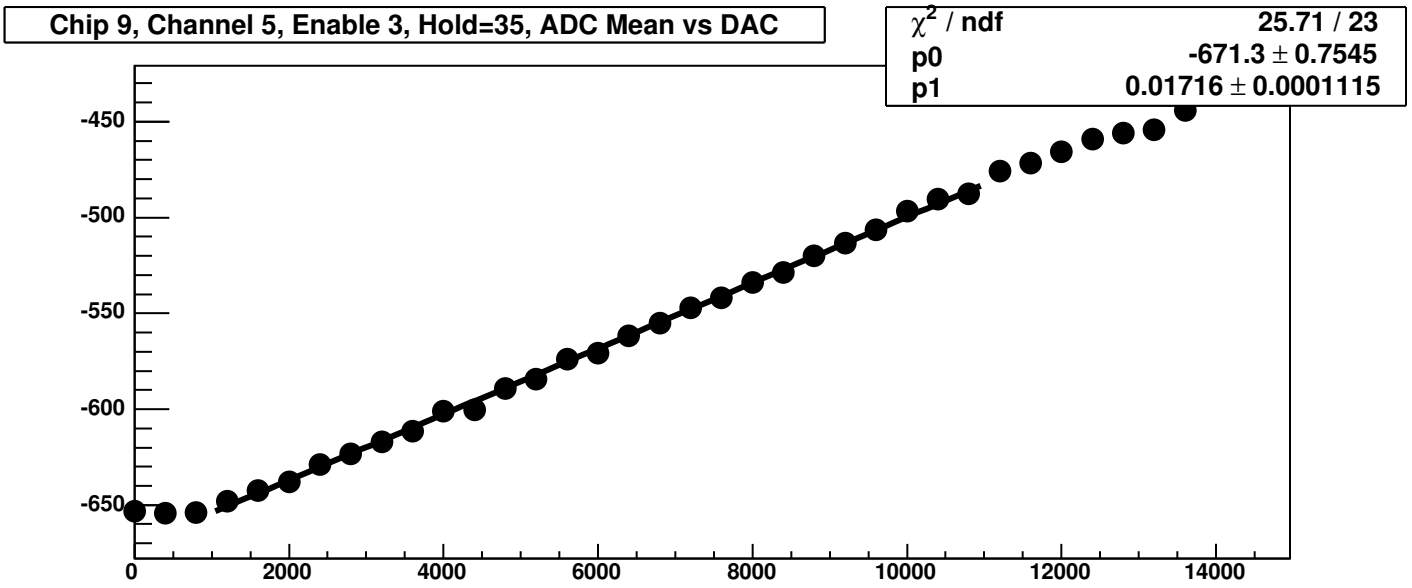
Chip 9, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC

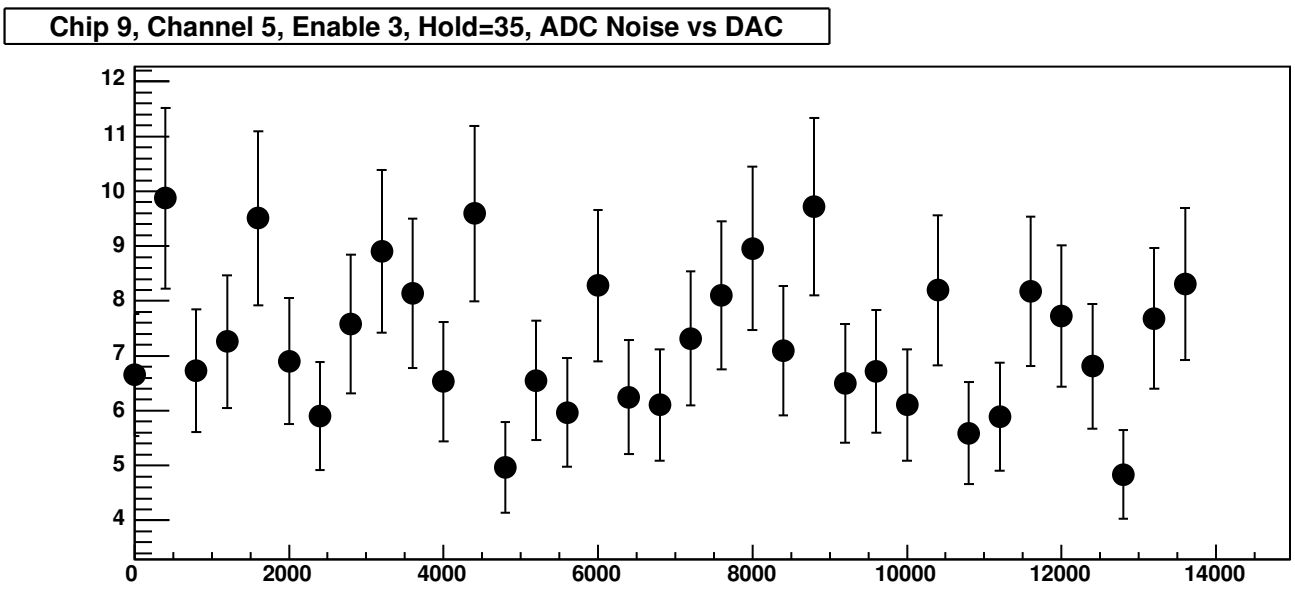


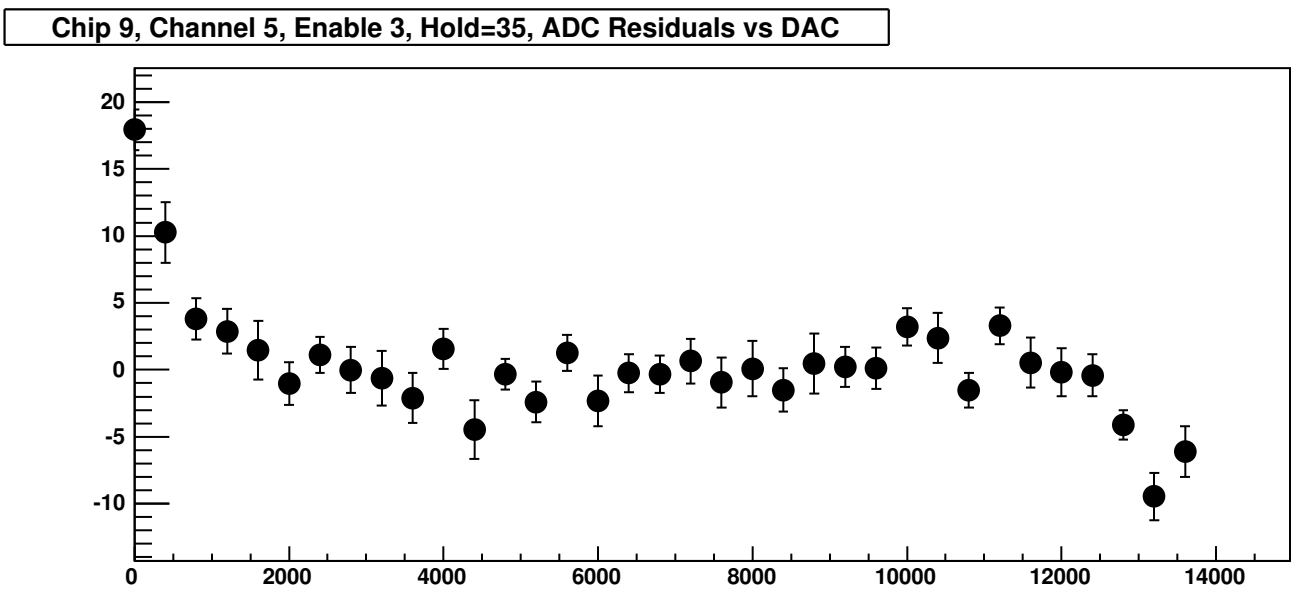

Chip 9, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 27.92 / 23
p0 -671.6 ± 0.7699
p1 0.01536 ± 0.0001161
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

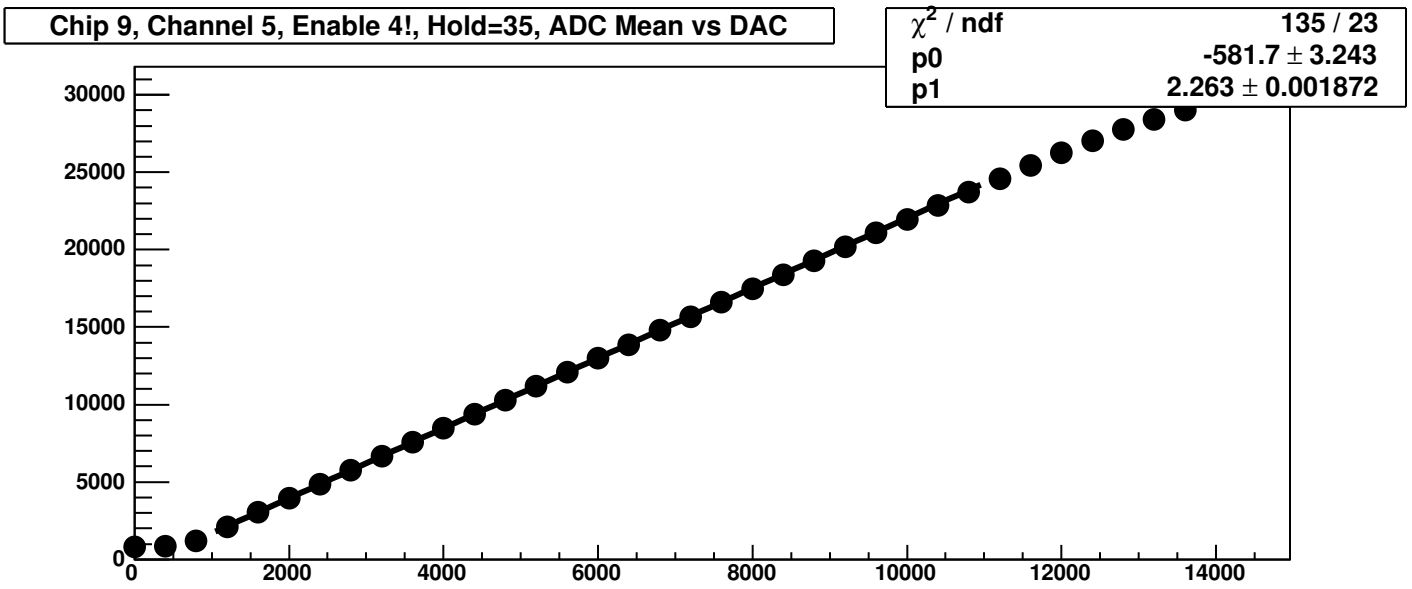

Chip 9, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

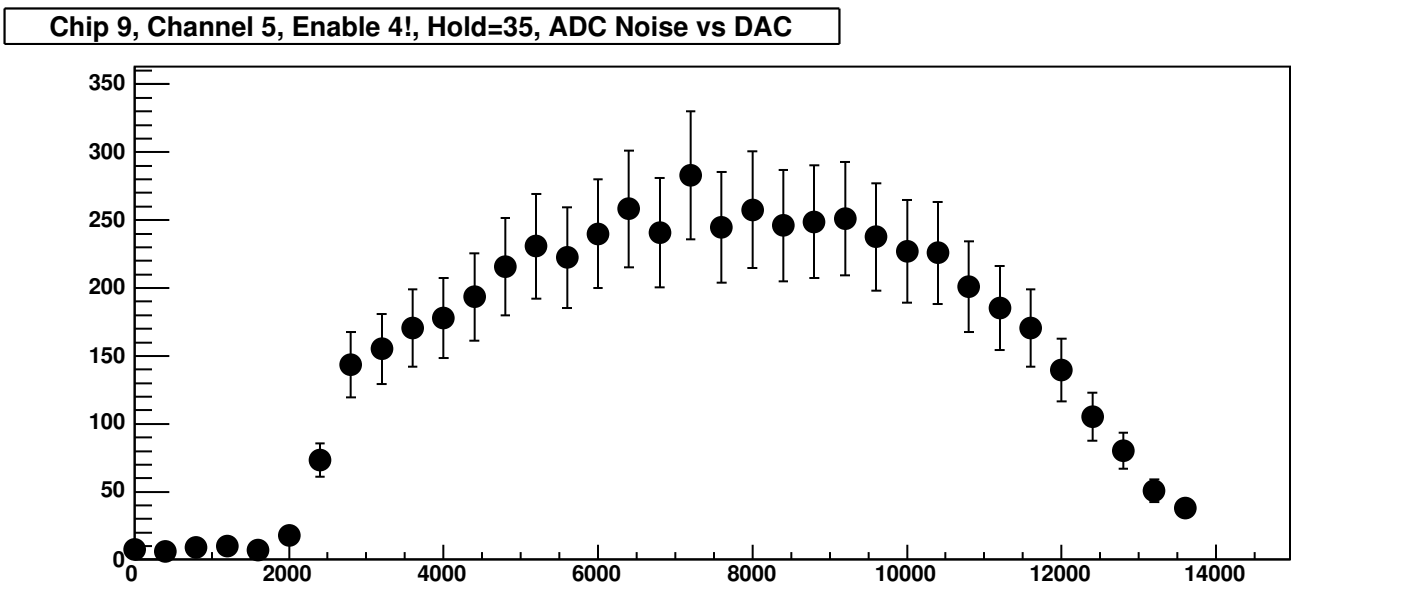

Chip 9, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

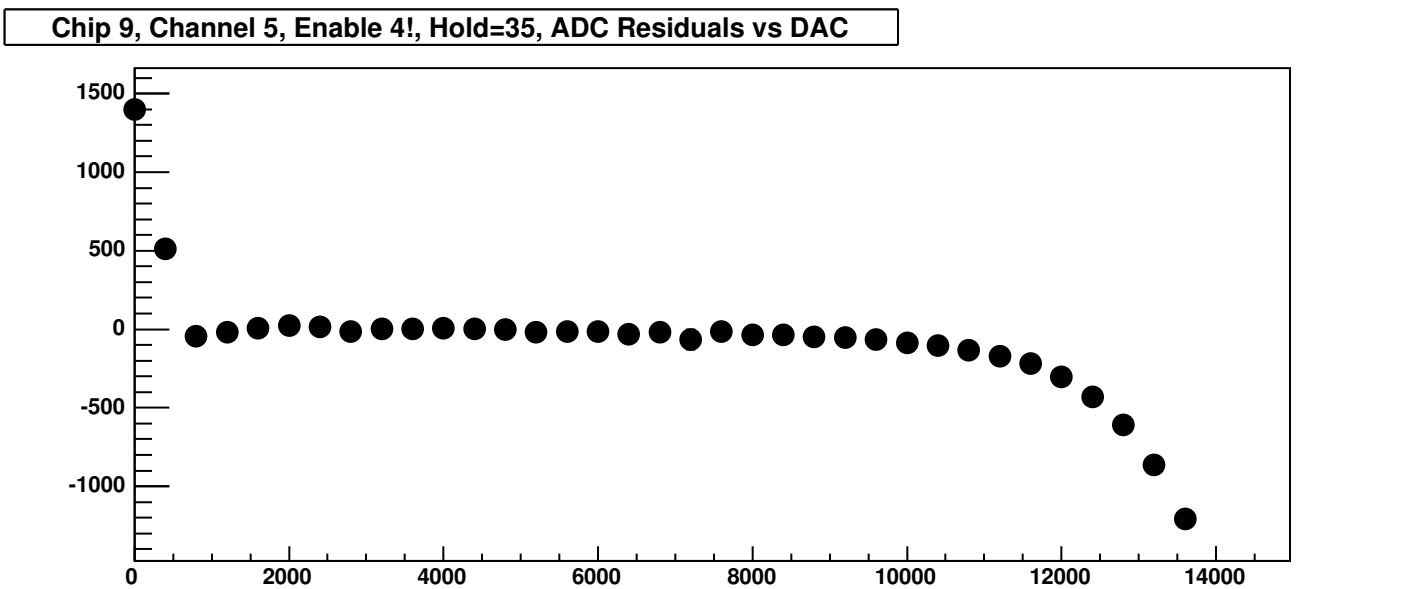

Chip 9, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.71 / 23
p0 -671.3 ± 0.7545
p1 0.01716 ± 0.0001115
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

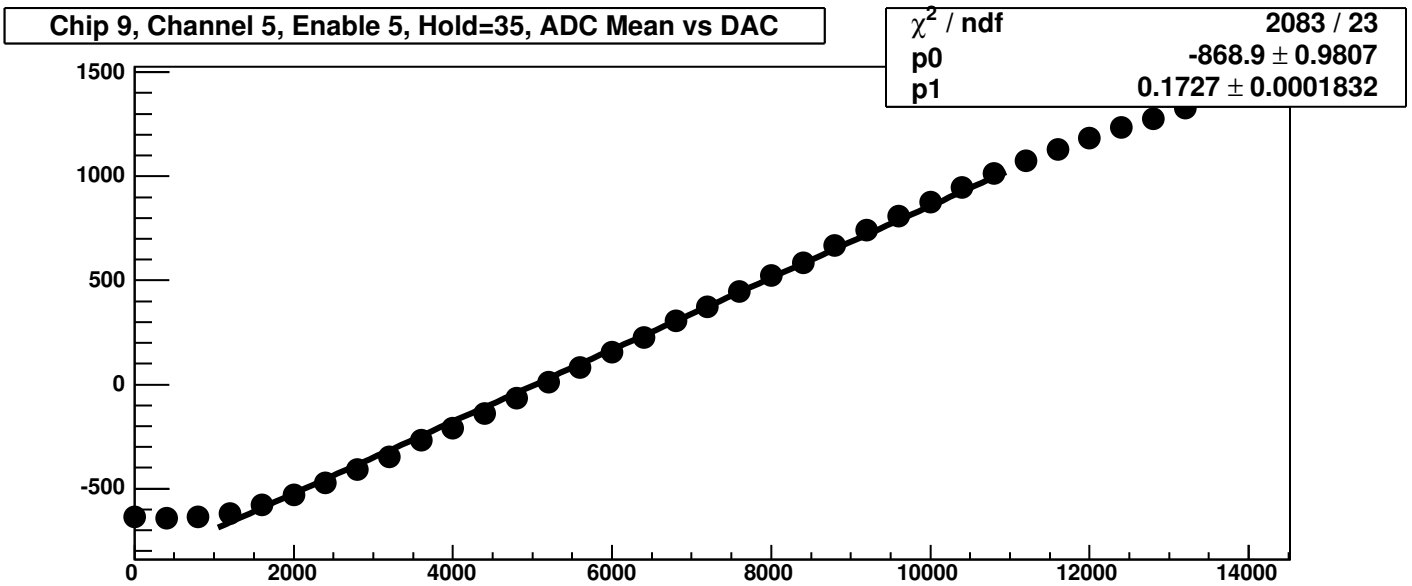

Chip 9, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

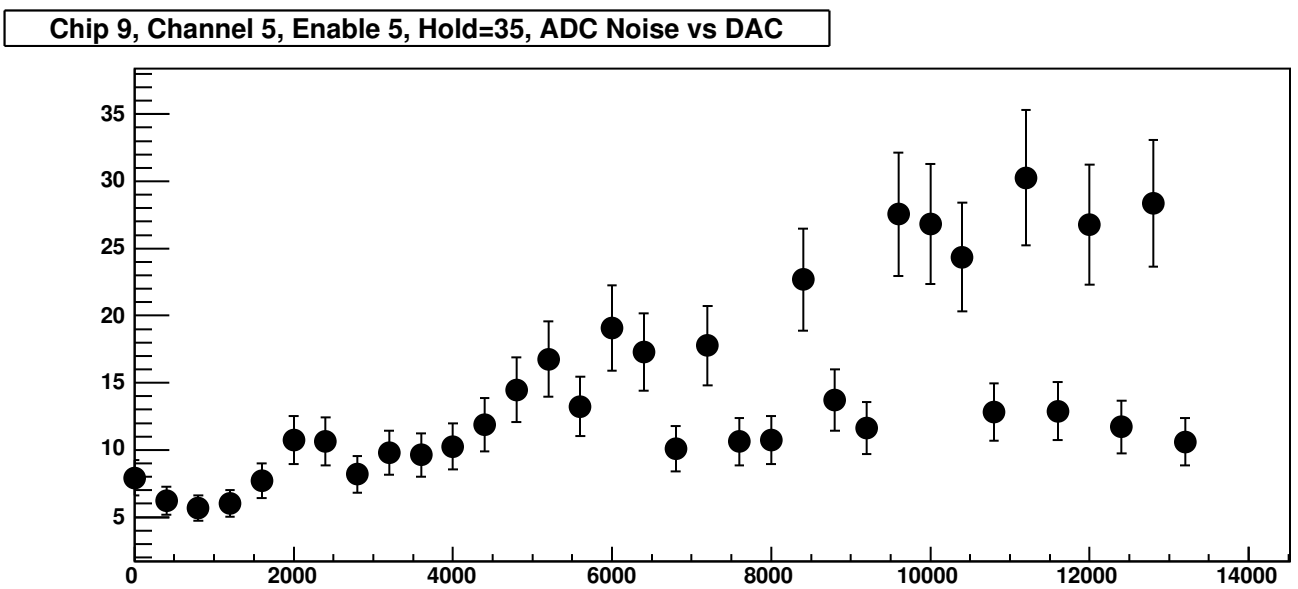

Chip 9, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

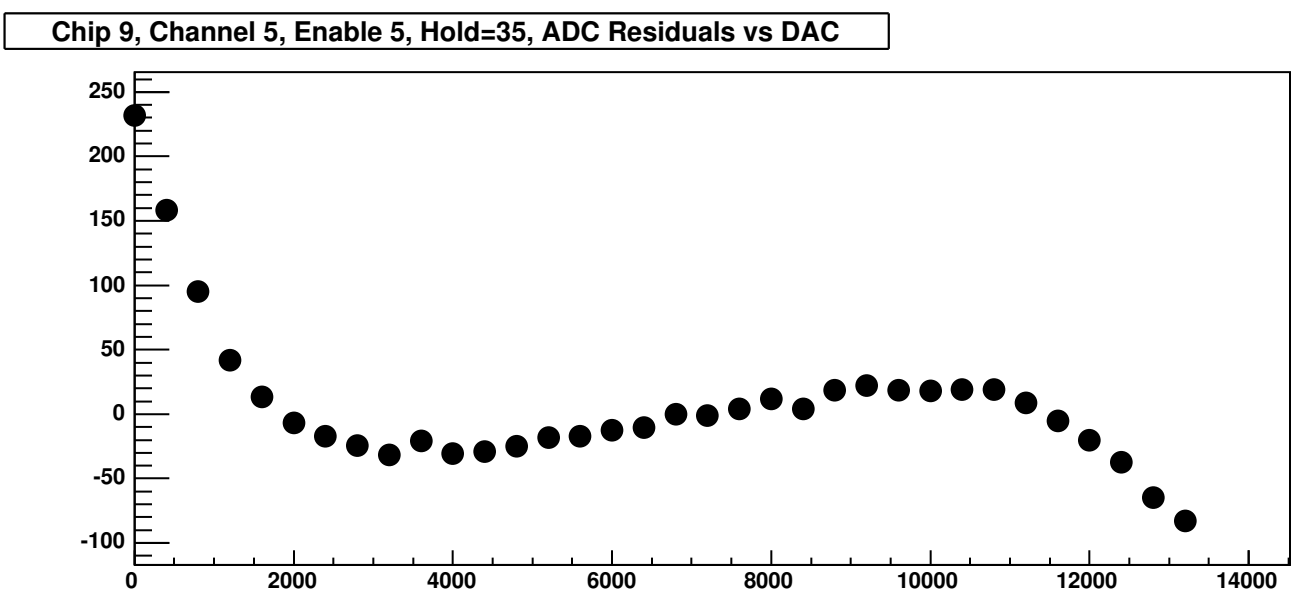

Chip 9, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 135 / 23
p0 -581.7 ± 3.243
p1 2.263 ± 0.001872
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

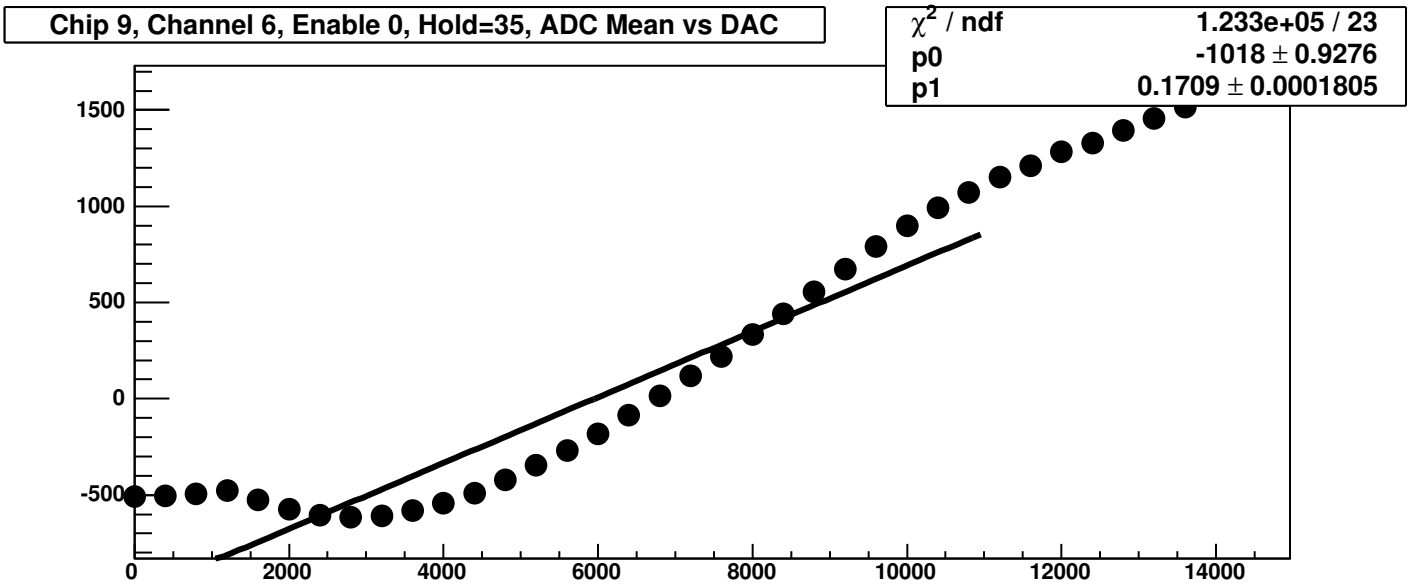

Chip 9, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

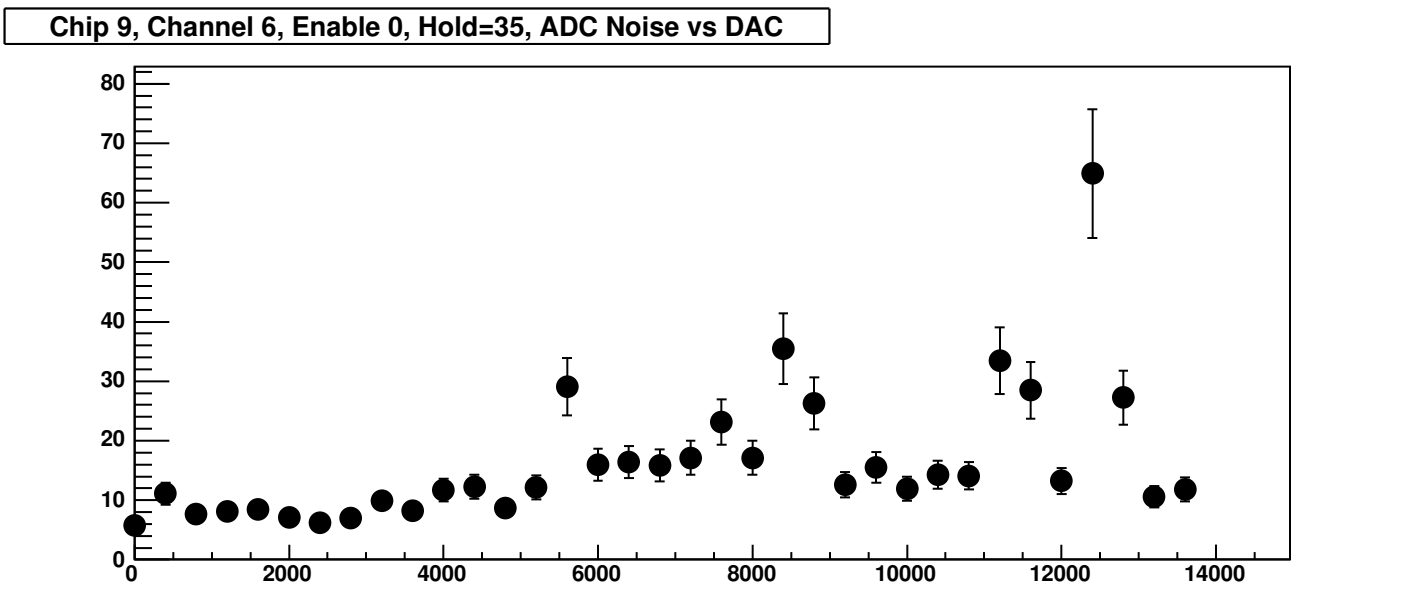

Chip 9, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

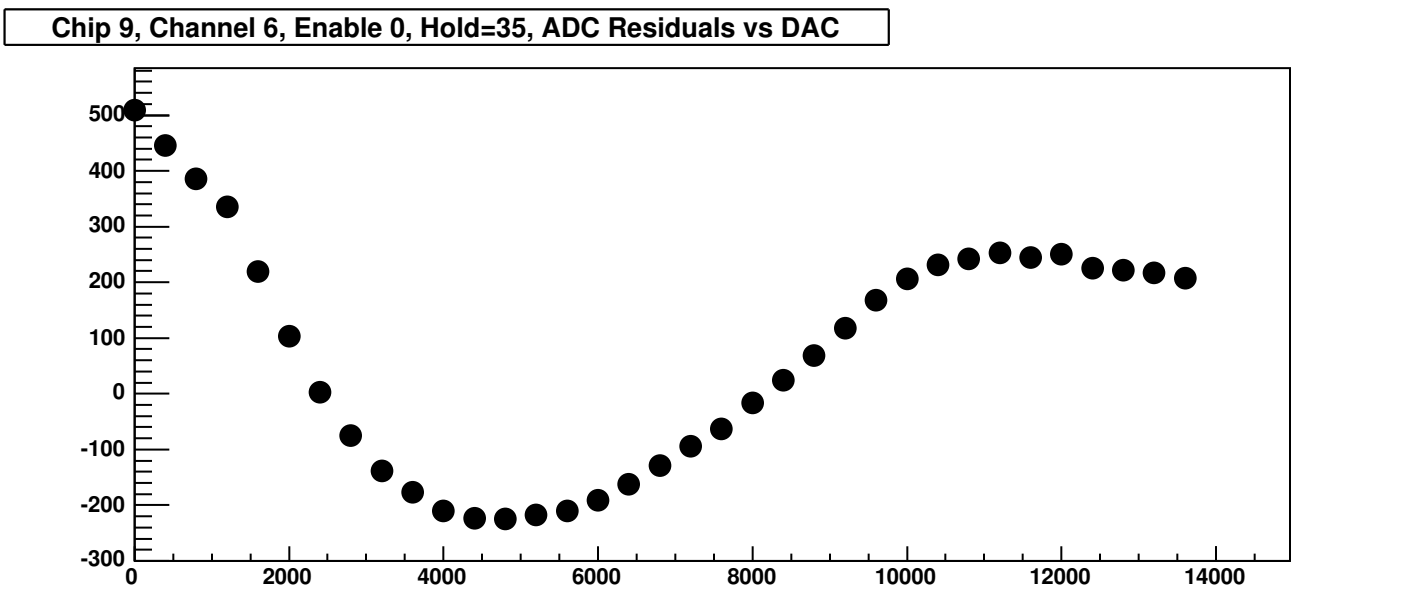

Chip 9, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2083 / 23
p0 -868.9 ± 0.9807
p1 0.1727 ± 0.0001832
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

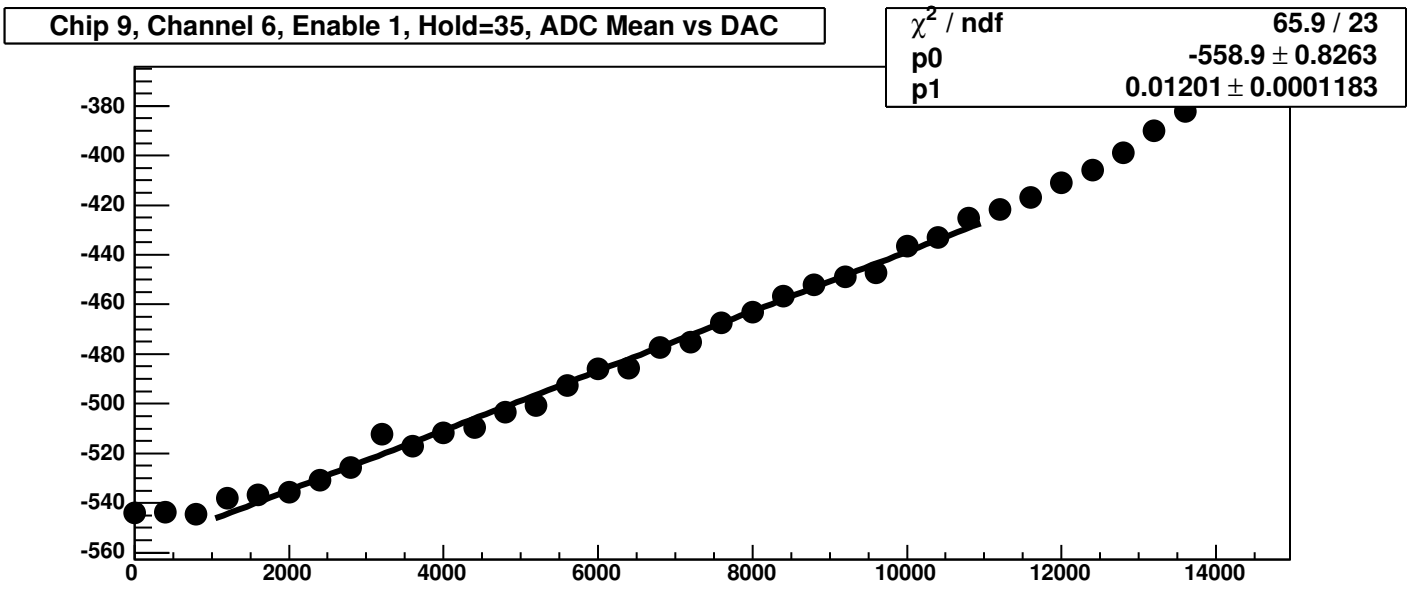

Chip 9, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

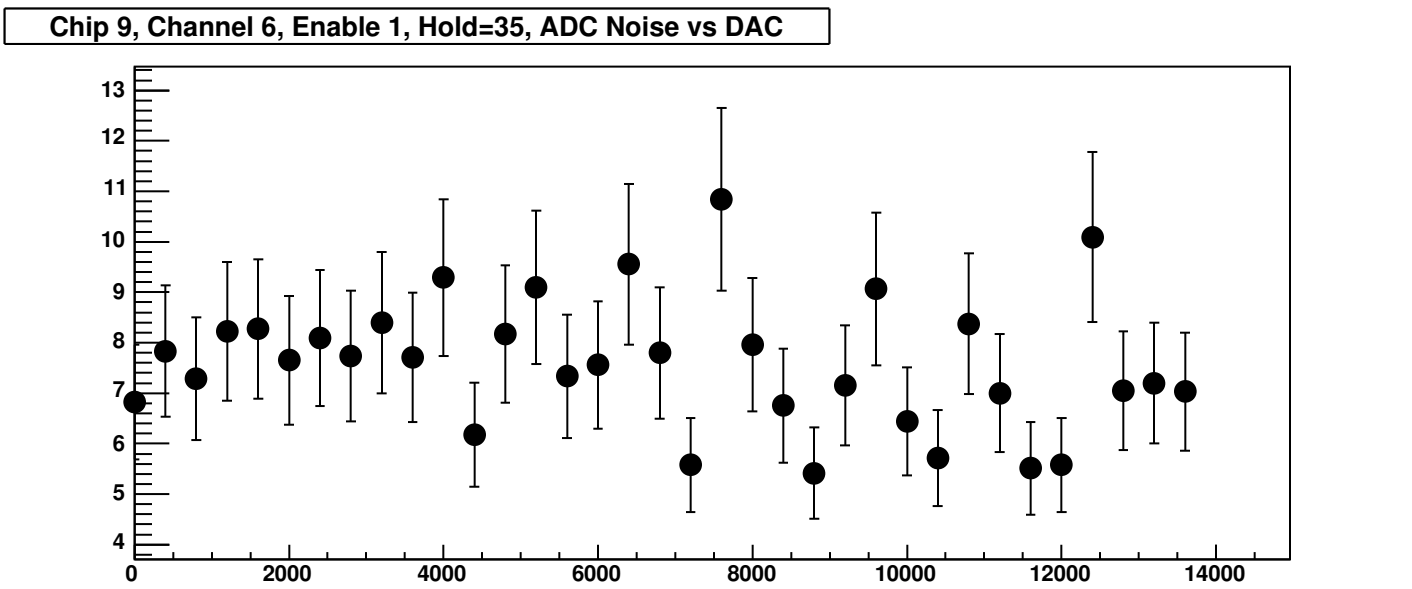

Chip 9, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

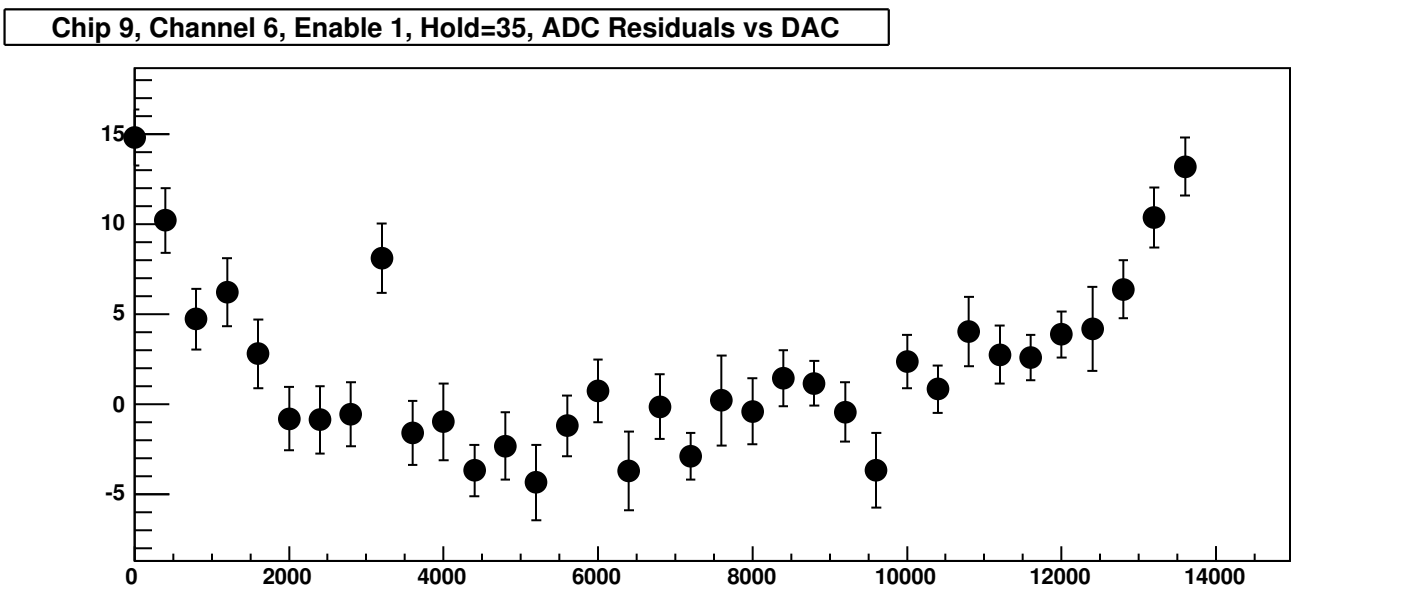

Chip 9, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.233e+05 / 23
p0 -1018 ± 0.9276
p1 0.1709 ± 0.0001805
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

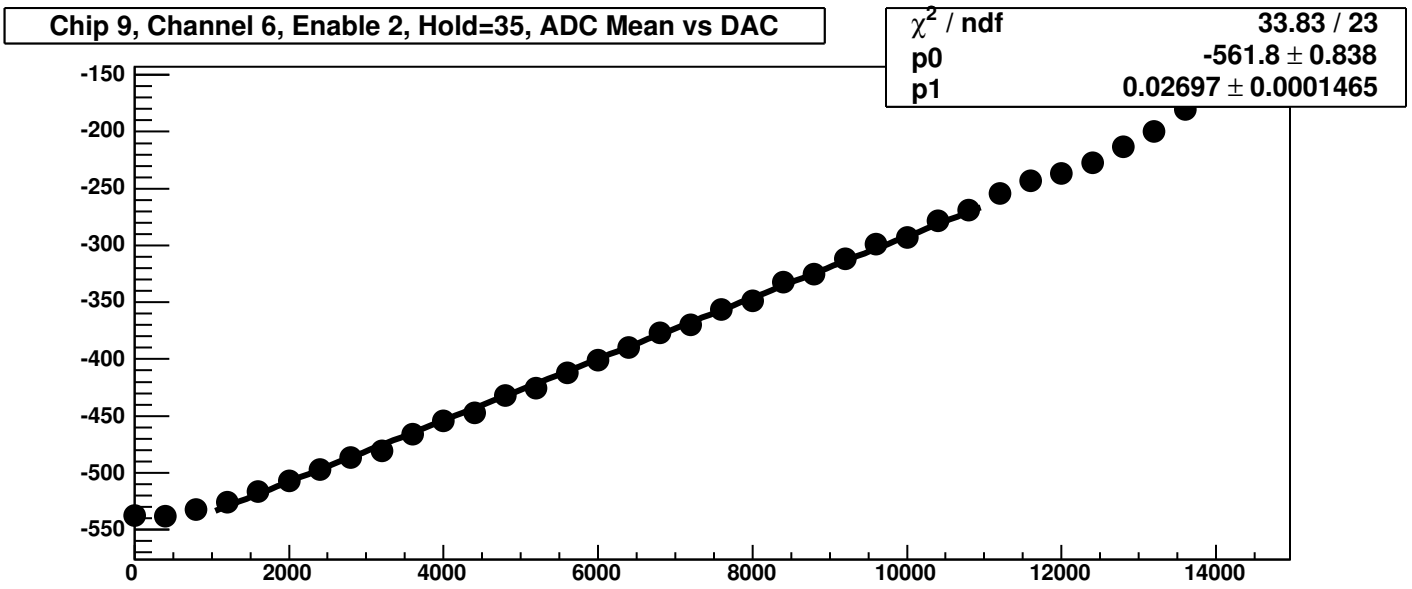

Chip 9, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

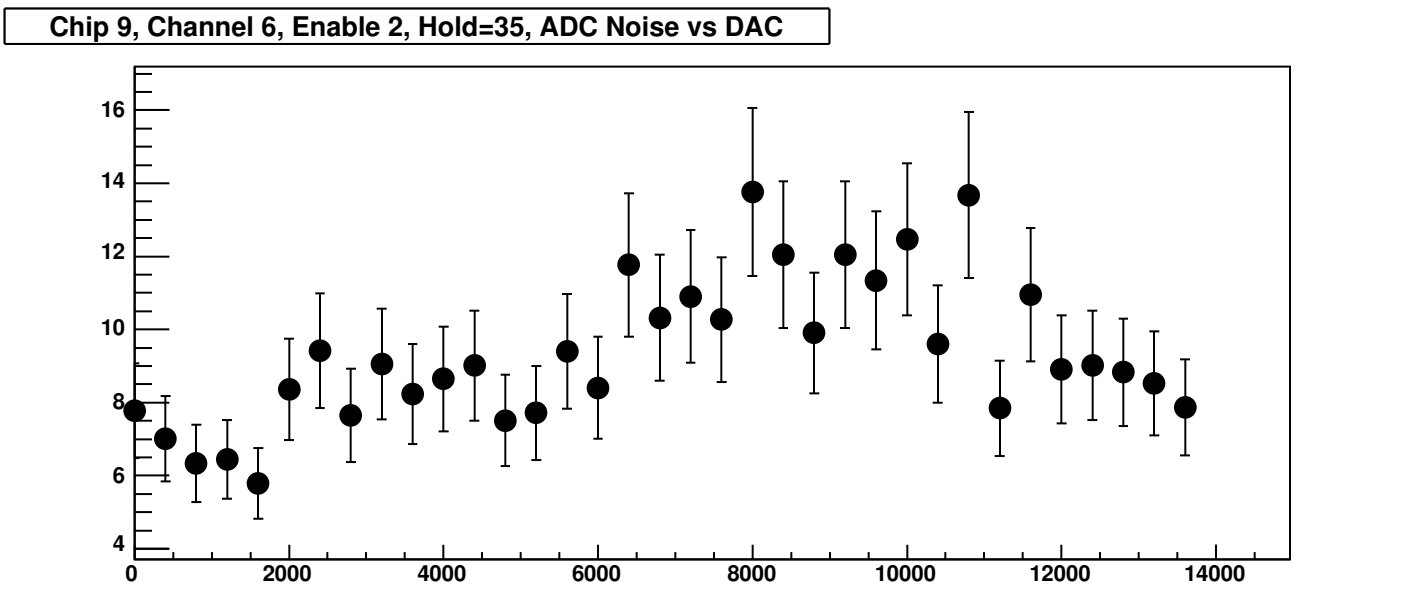

Chip 9, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

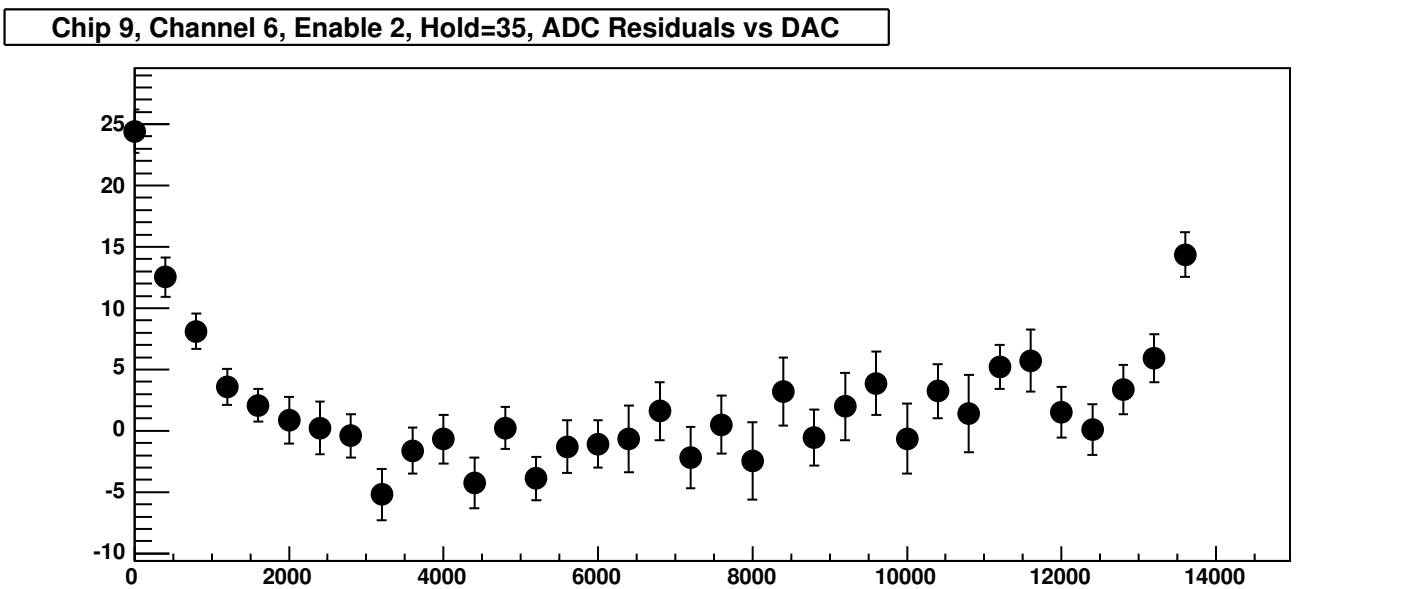

Chip 9, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 65.9 / 23
p0 -558.9 ± 0.8263
p1 0.01201 ± 0.0001183
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
0
2000
4000
6000
8000
10000
12000
14000

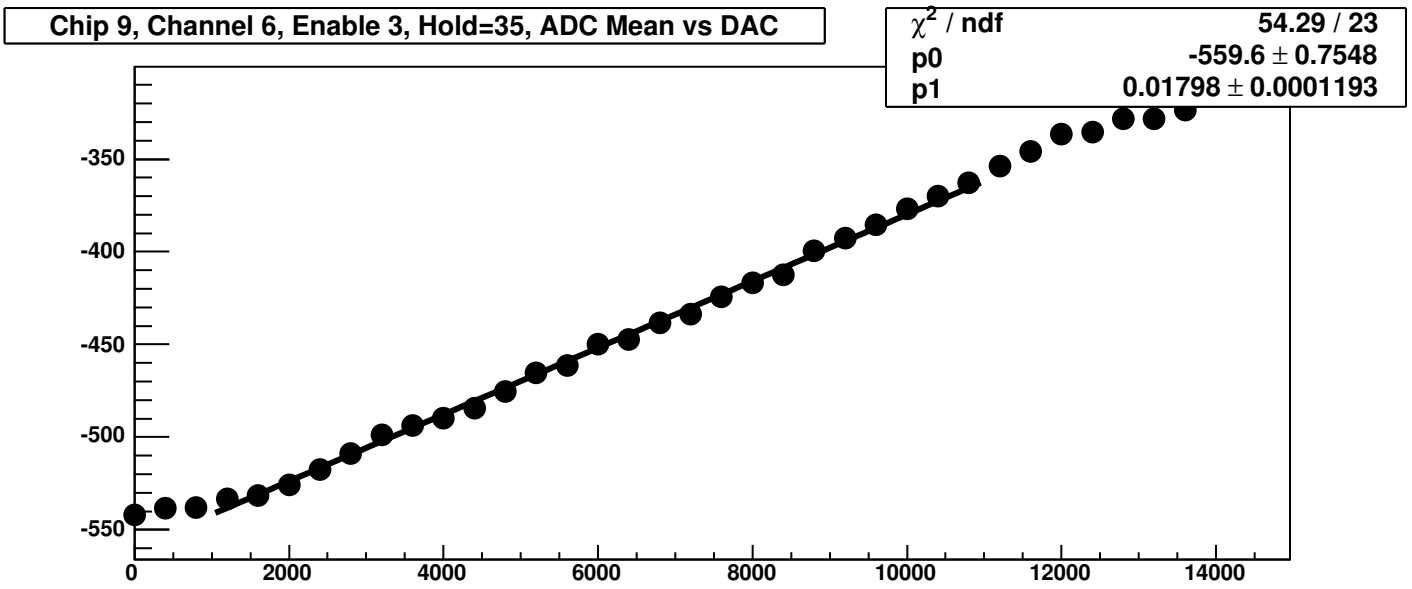

Chip 9, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

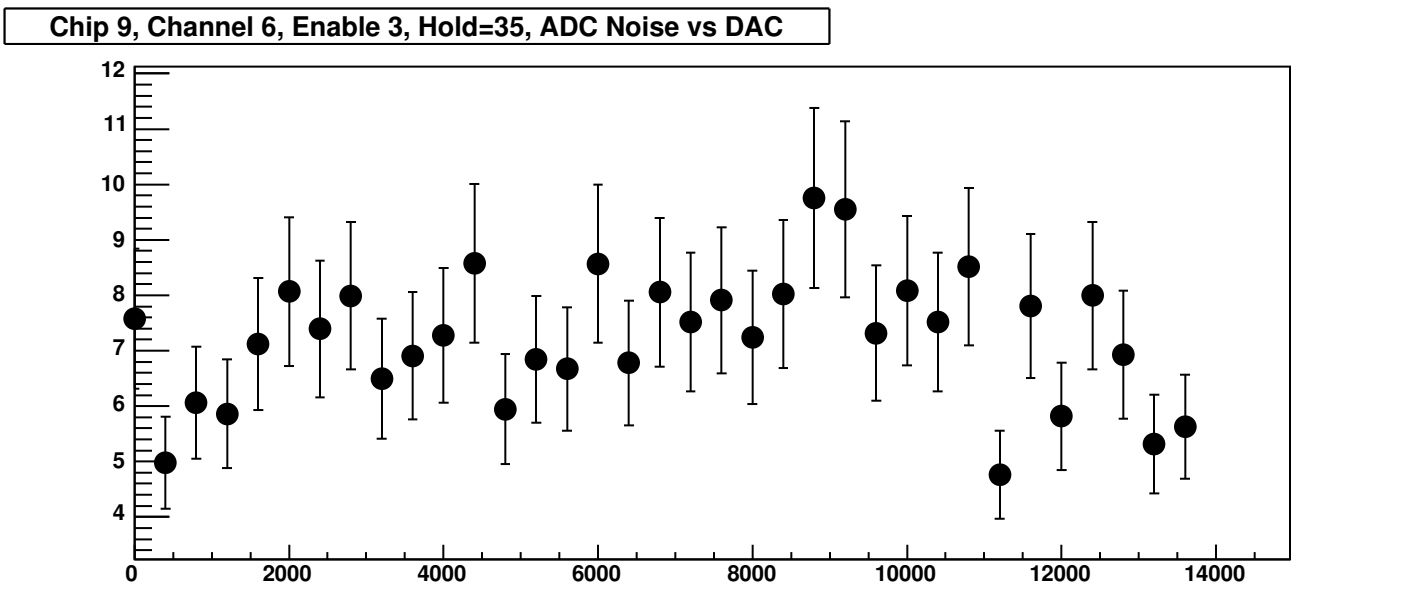

Chip 9, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

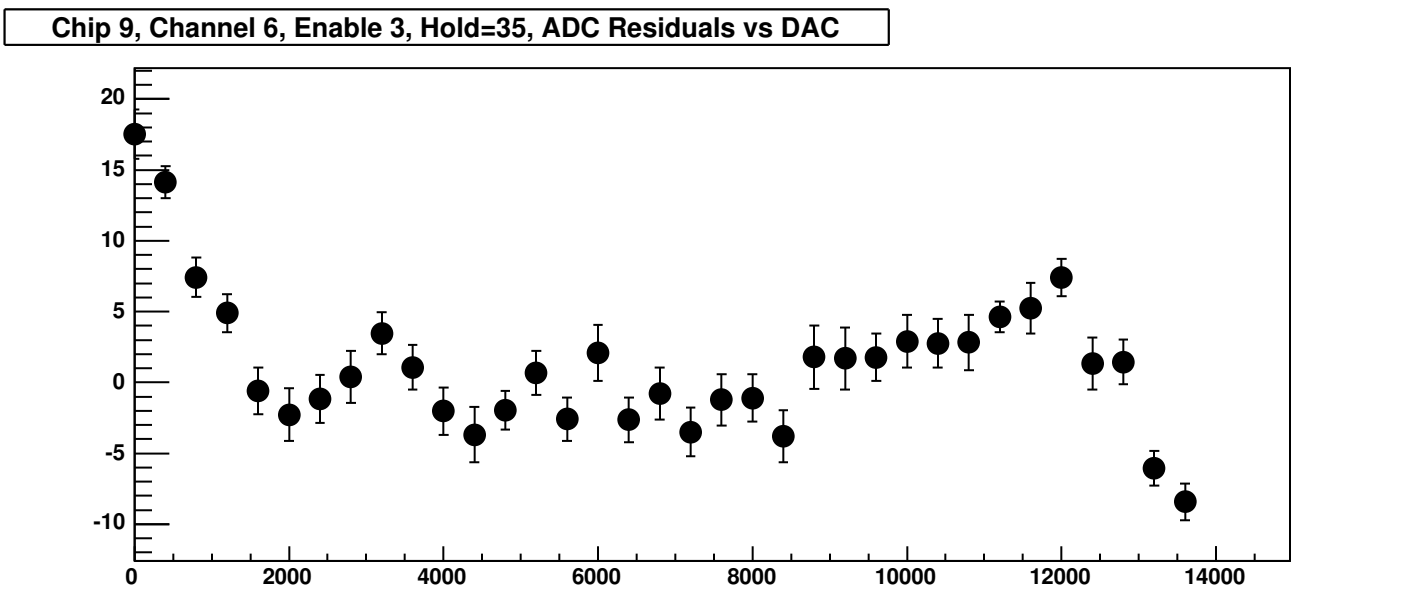

Chip 9, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 33.83 / 23
p0 -561.8 ± 0.838
p1 0.02697 ± 0.0001465
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

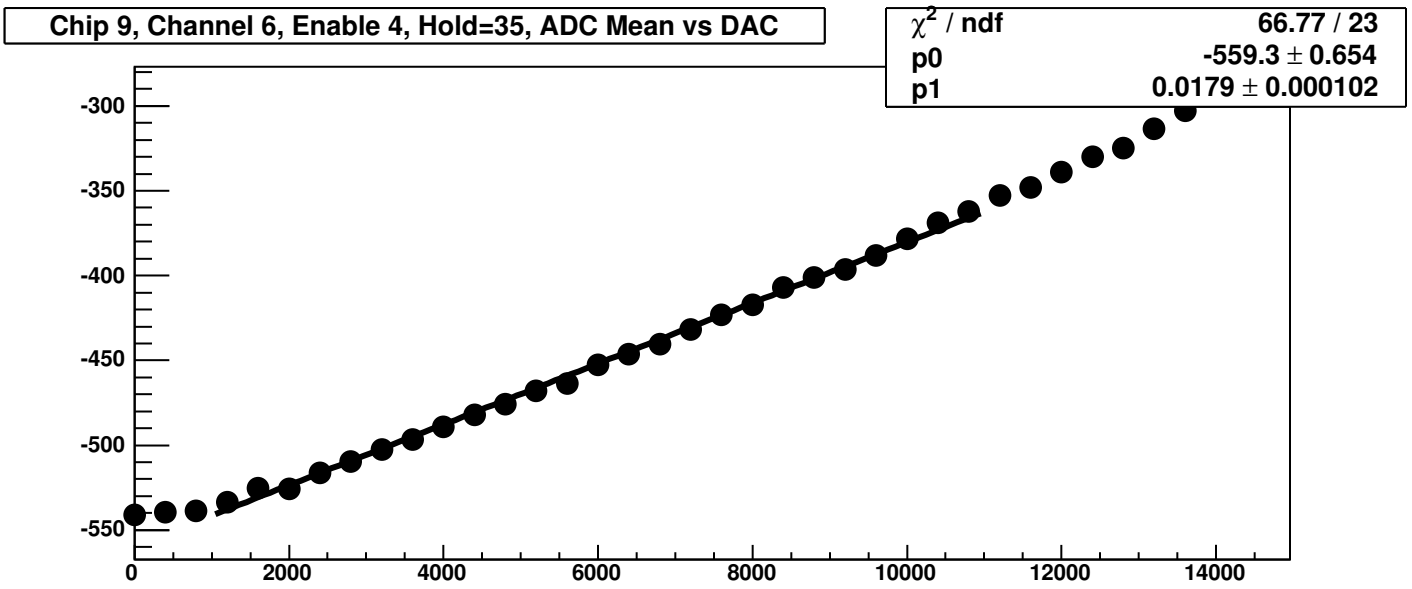

Chip 9, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

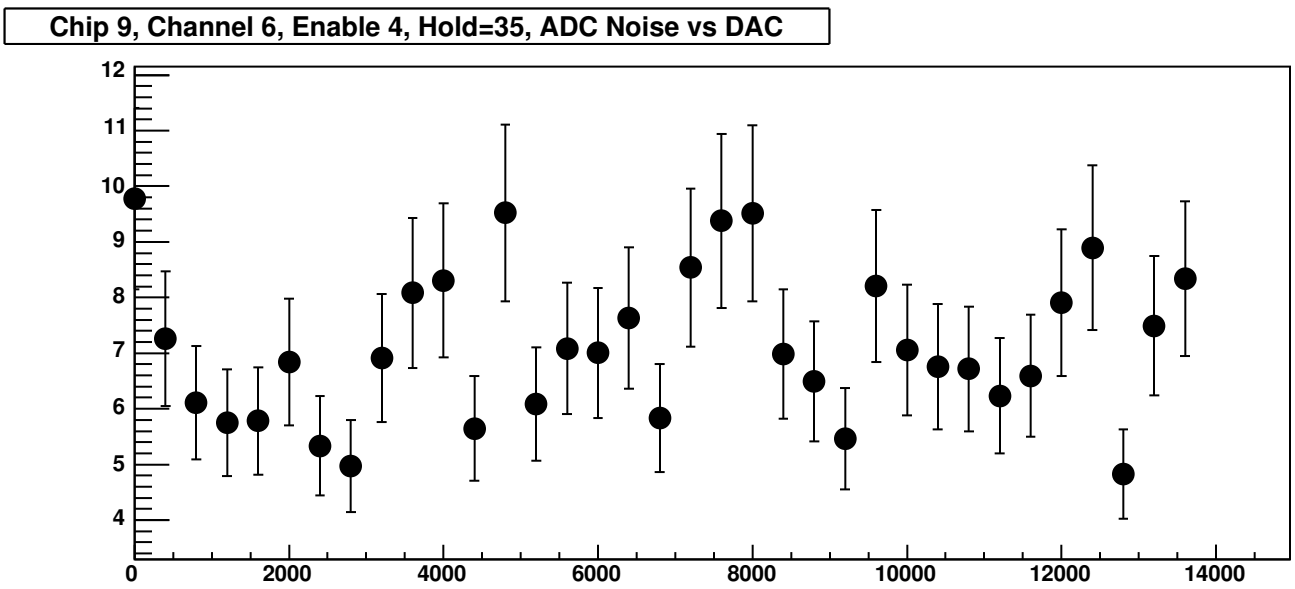

Chip 9, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

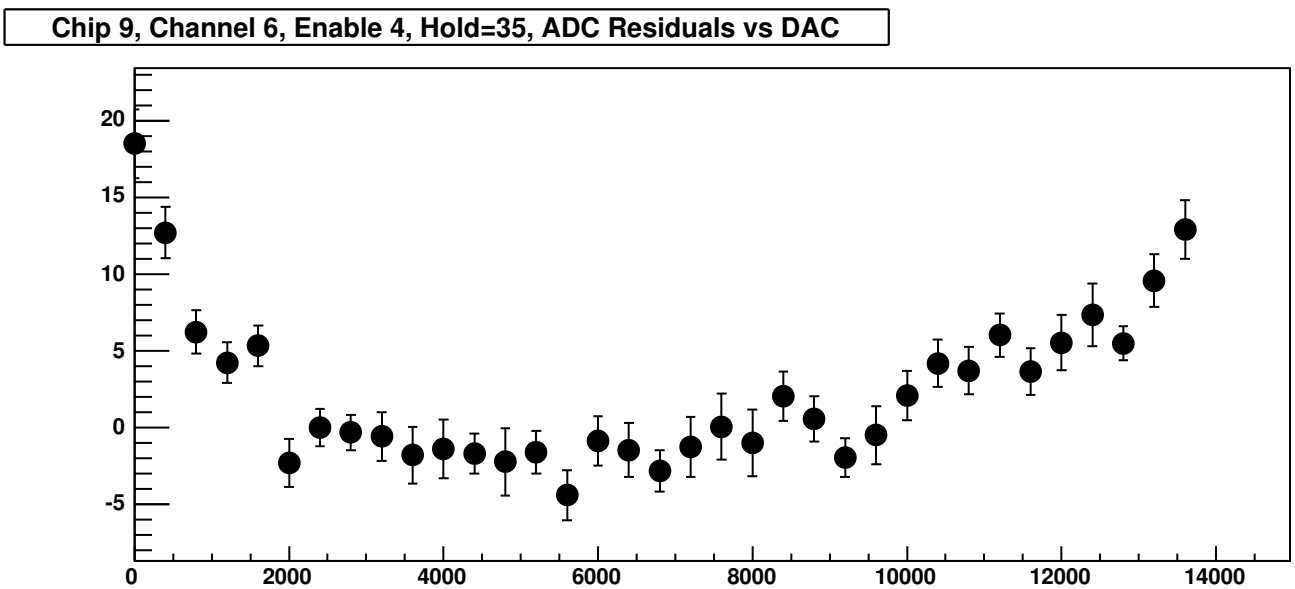

Chip 9, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 54.29 / 23
p0 -559.6 ± 0.7548
p1 0.01798 ± 0.0001193
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

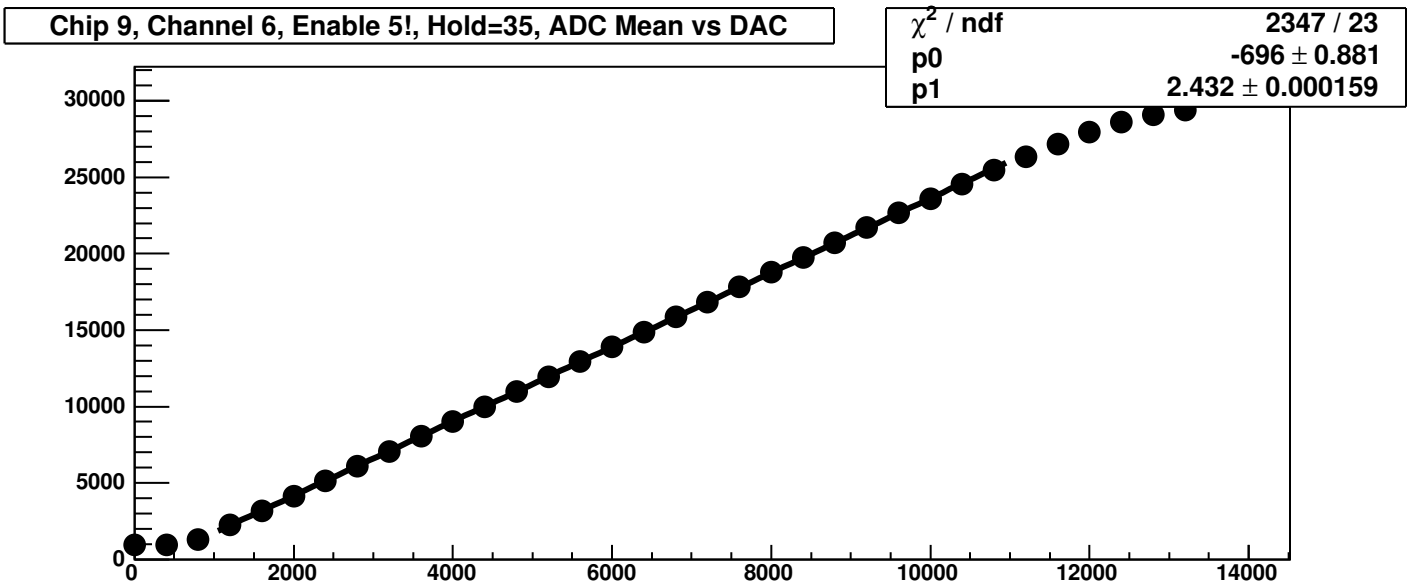

Chip 9, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

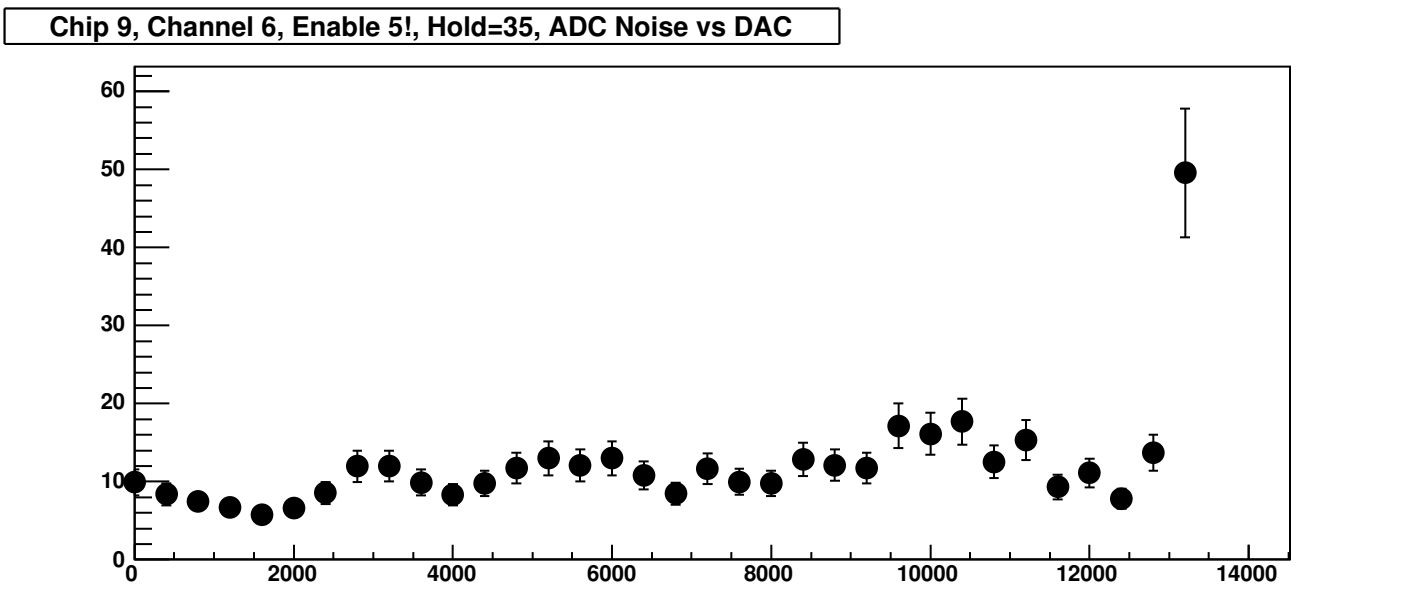

Chip 9, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

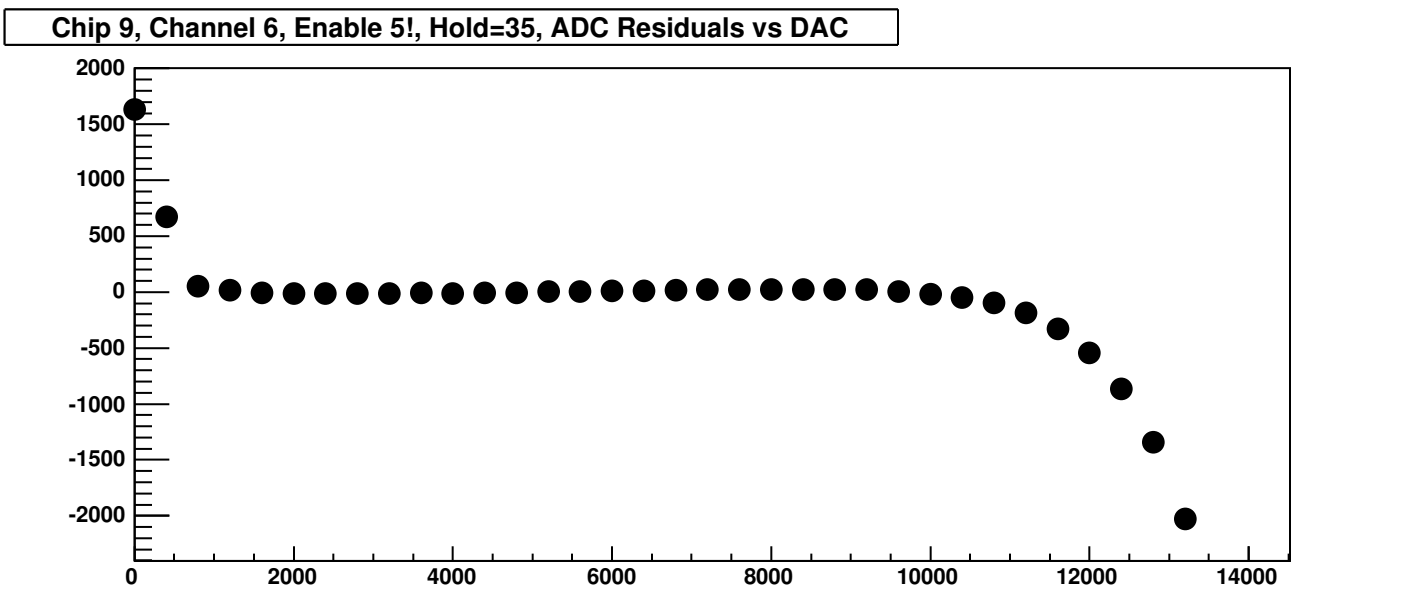

Chip 9, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 66.77 / 23
p0 -559.3 ± 0.654
p1 0.0179 ± 0.000102
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

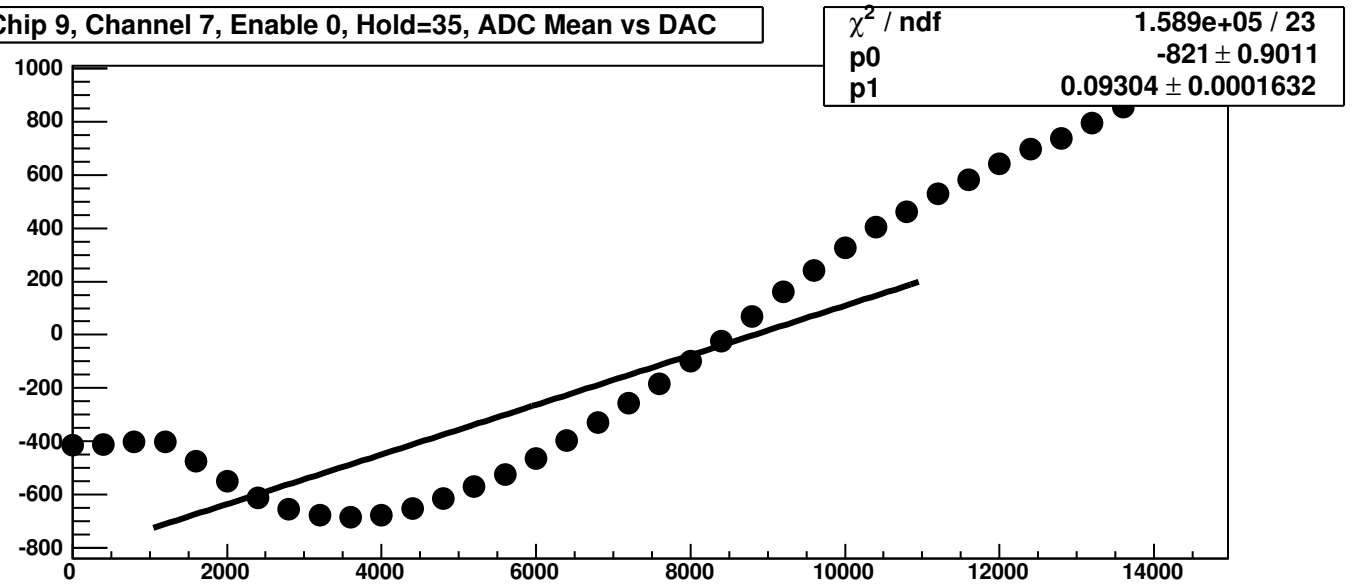

Chip 9, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2347 / 23
p0 -696 ± 0.881
p1 2.432 ± 0.000159
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

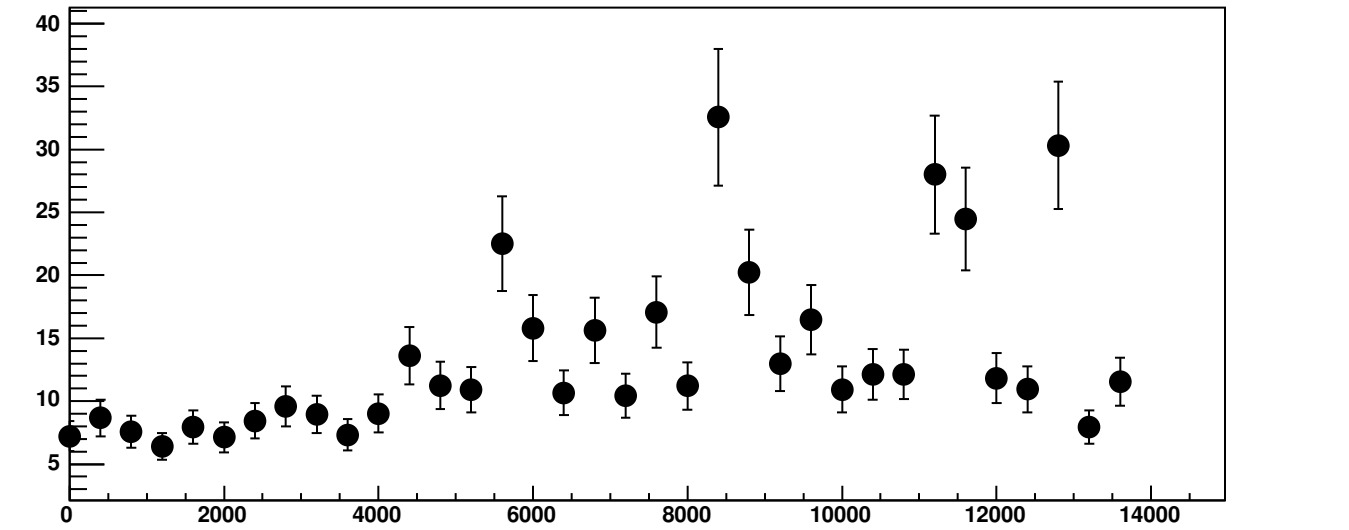

Chip 9, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

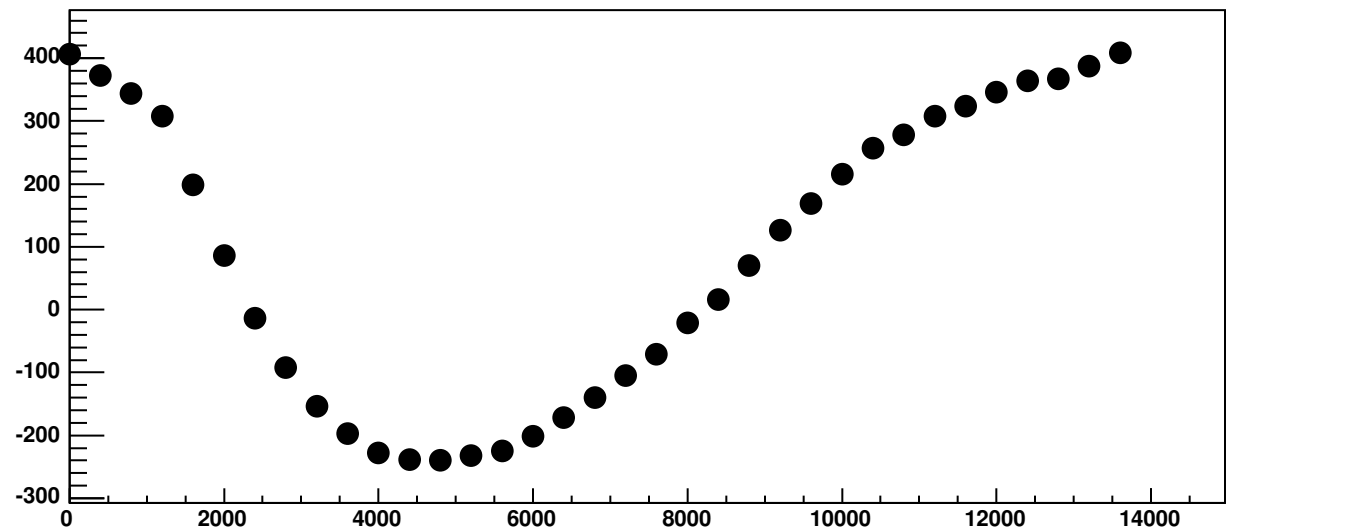
Chip 9, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

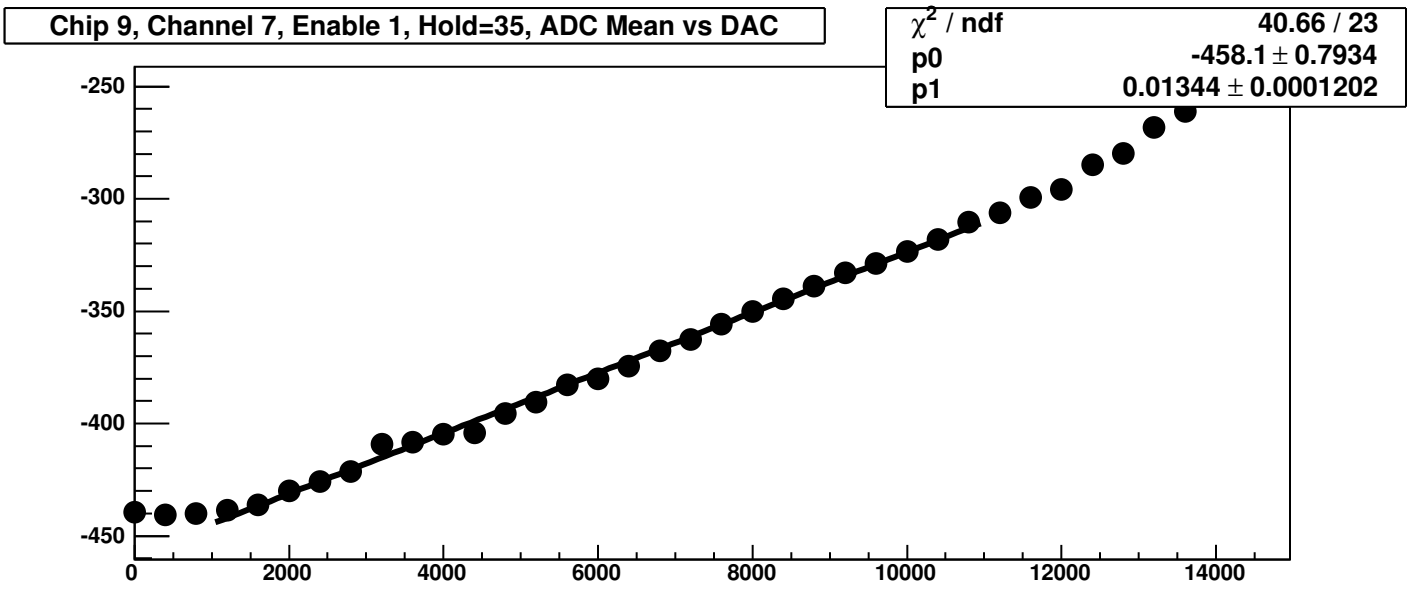


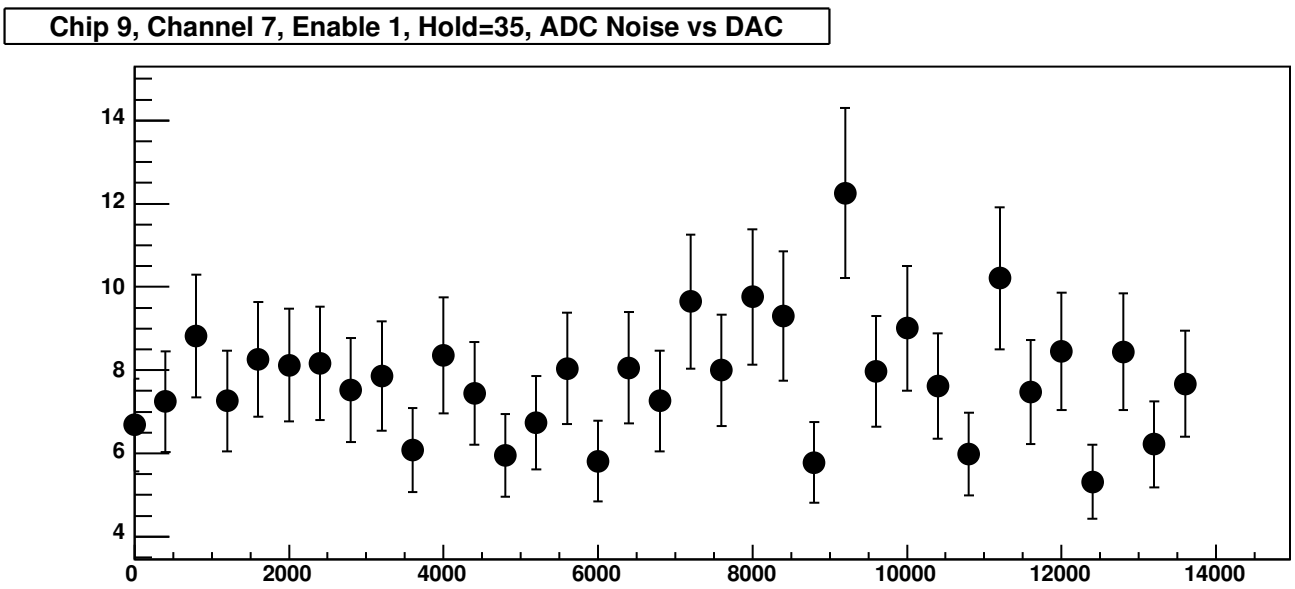
Chip 9, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC

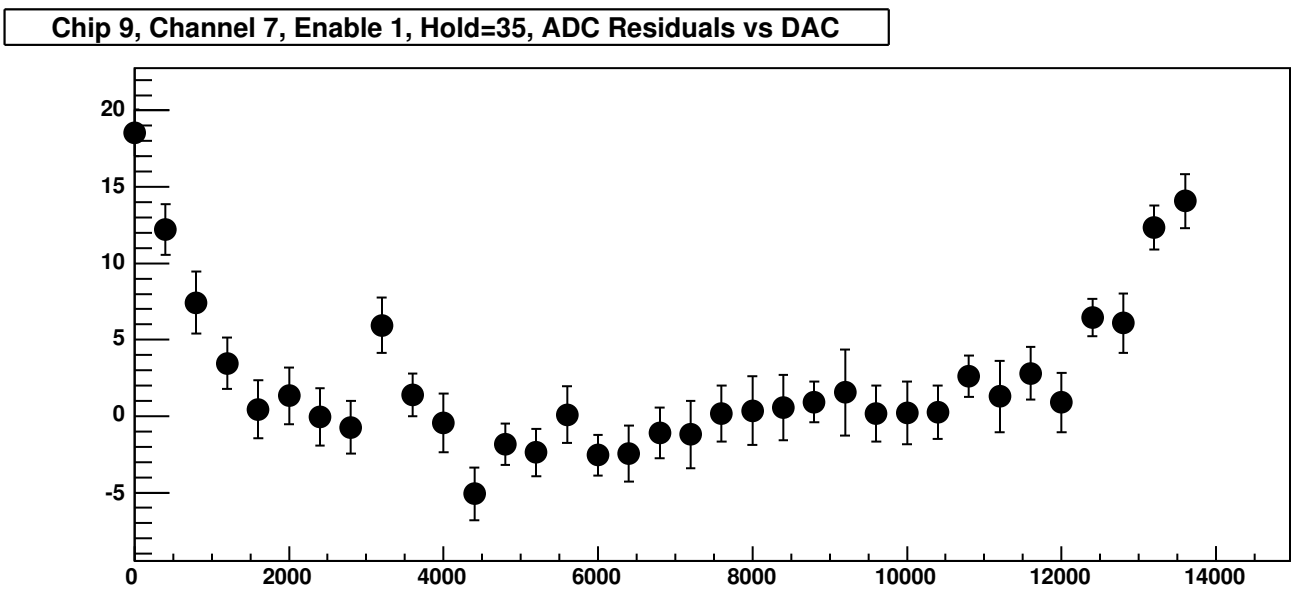


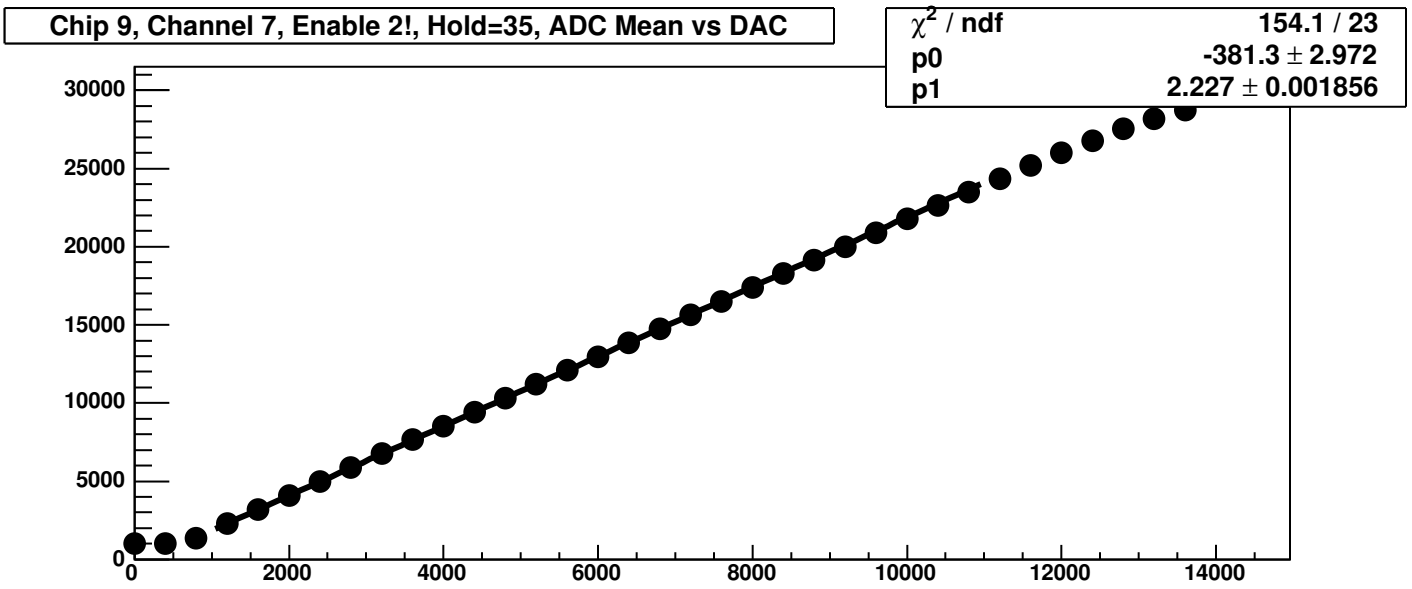
Chip 9, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

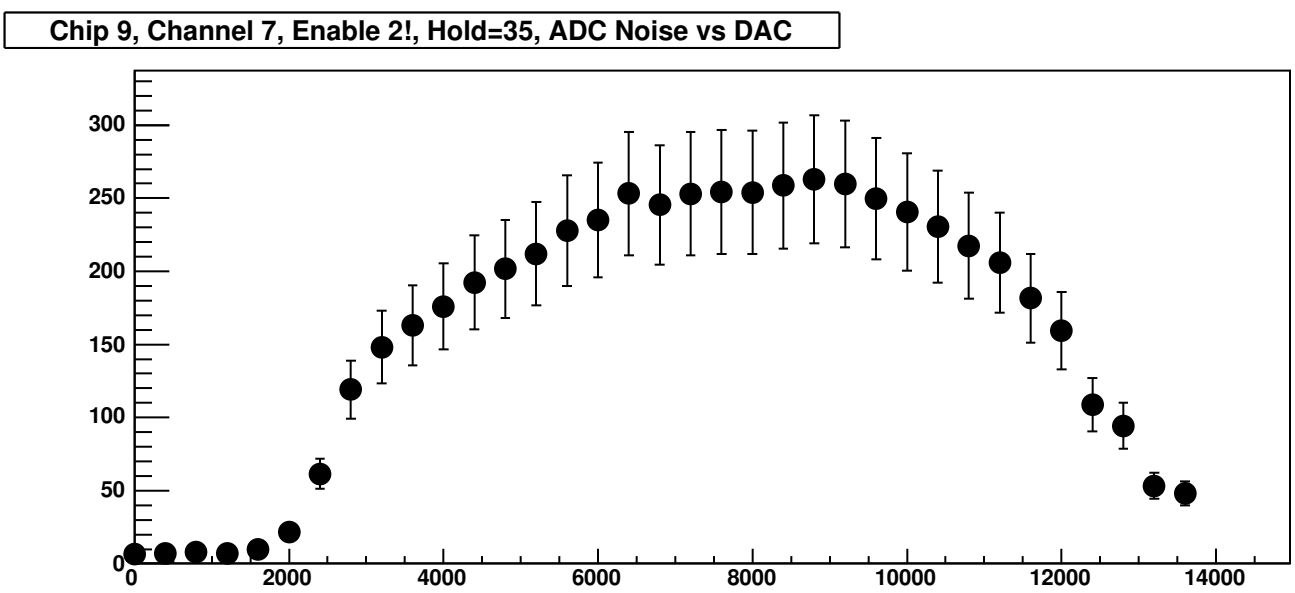


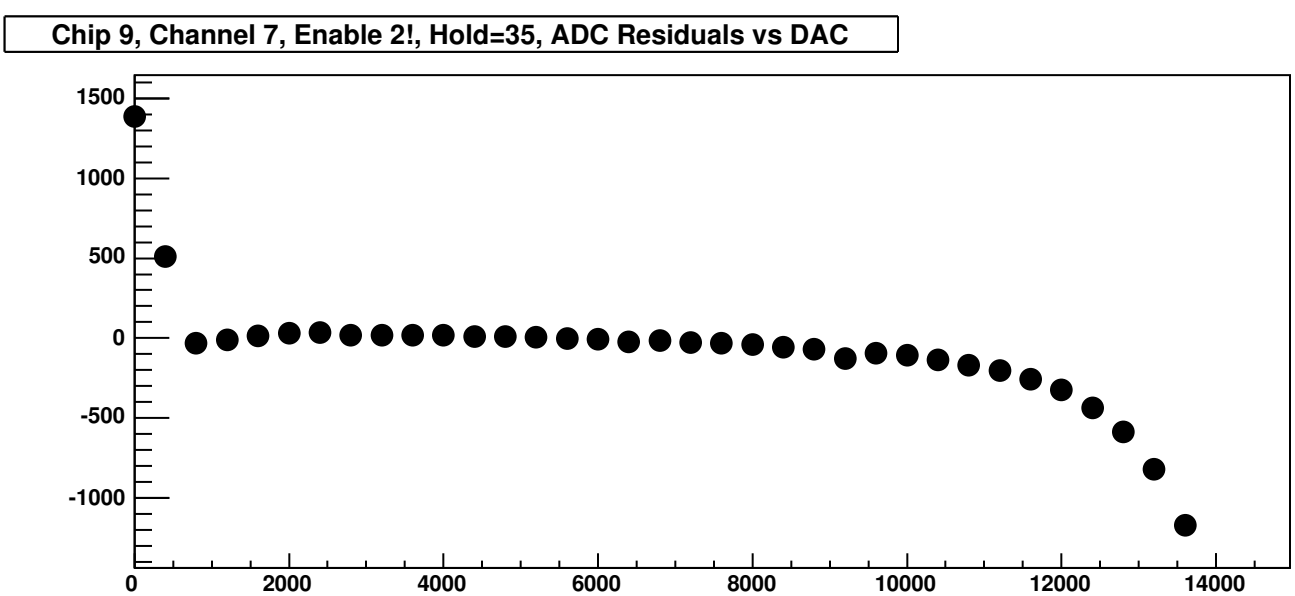

Chip 9, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 40.66 / 23
p0 -458.1 ± 0.7934
p1 0.01344 ± 0.0001202
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

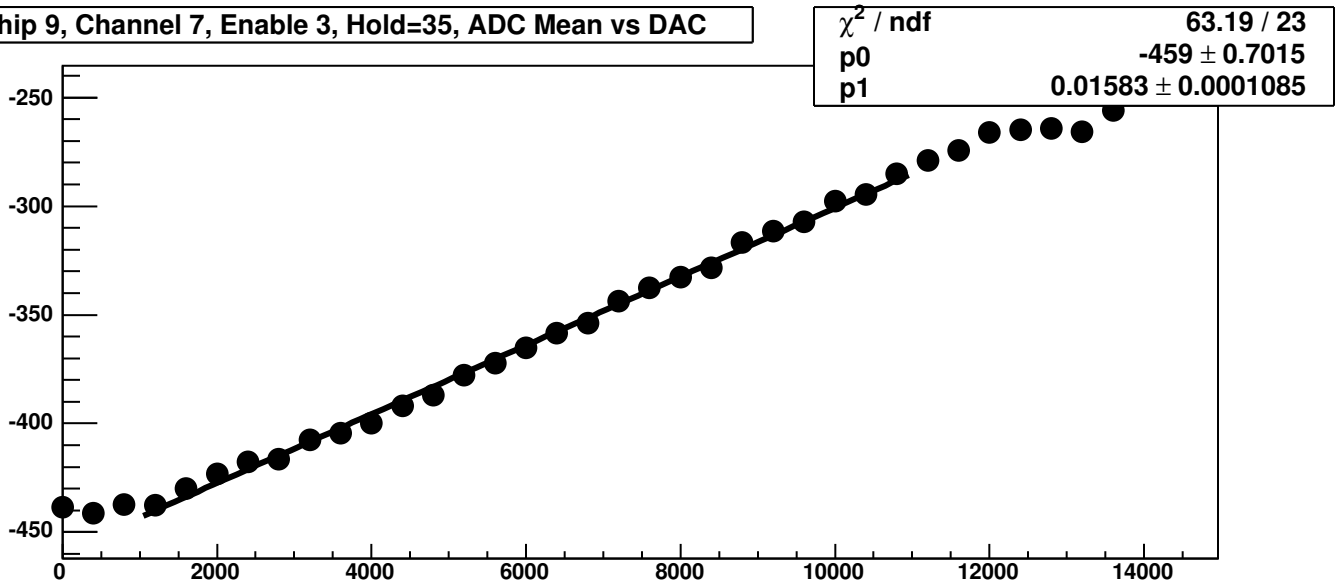

Chip 9, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 154.1 / 23
p0 -381.3 ± 2.972
p1 2.227 ± 0.001856
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

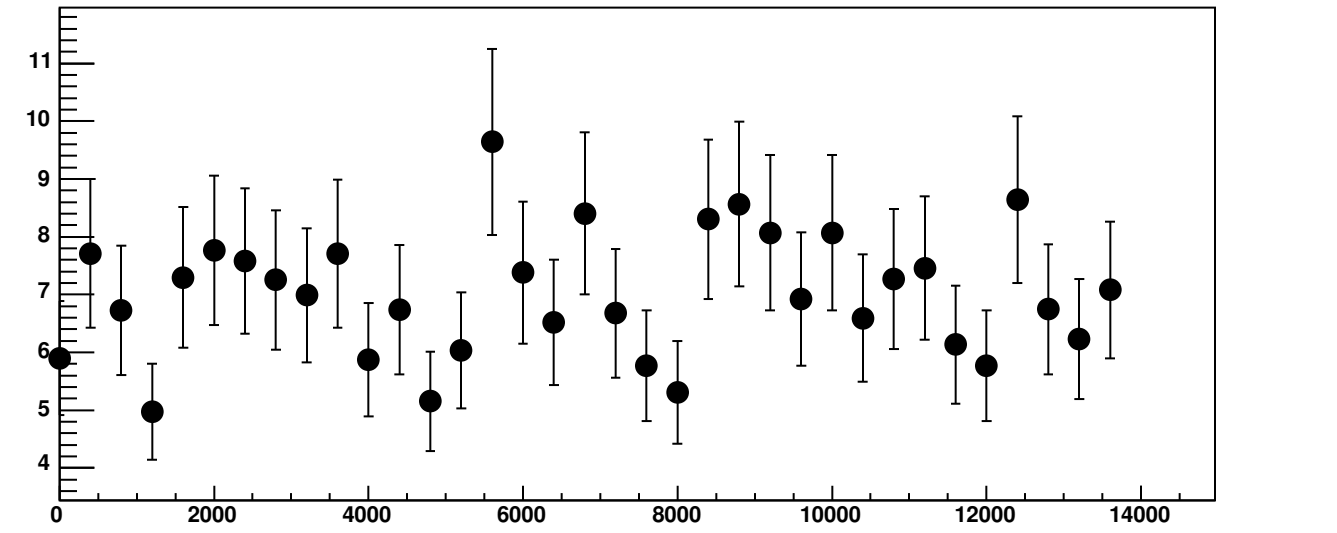

Chip 9, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

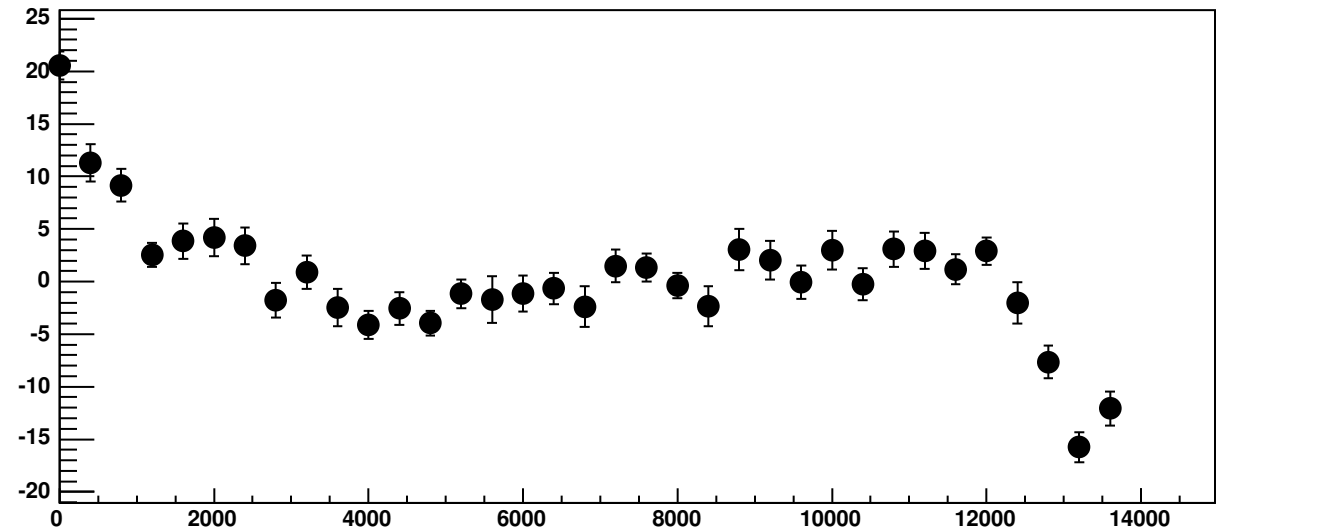
Chip 9, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

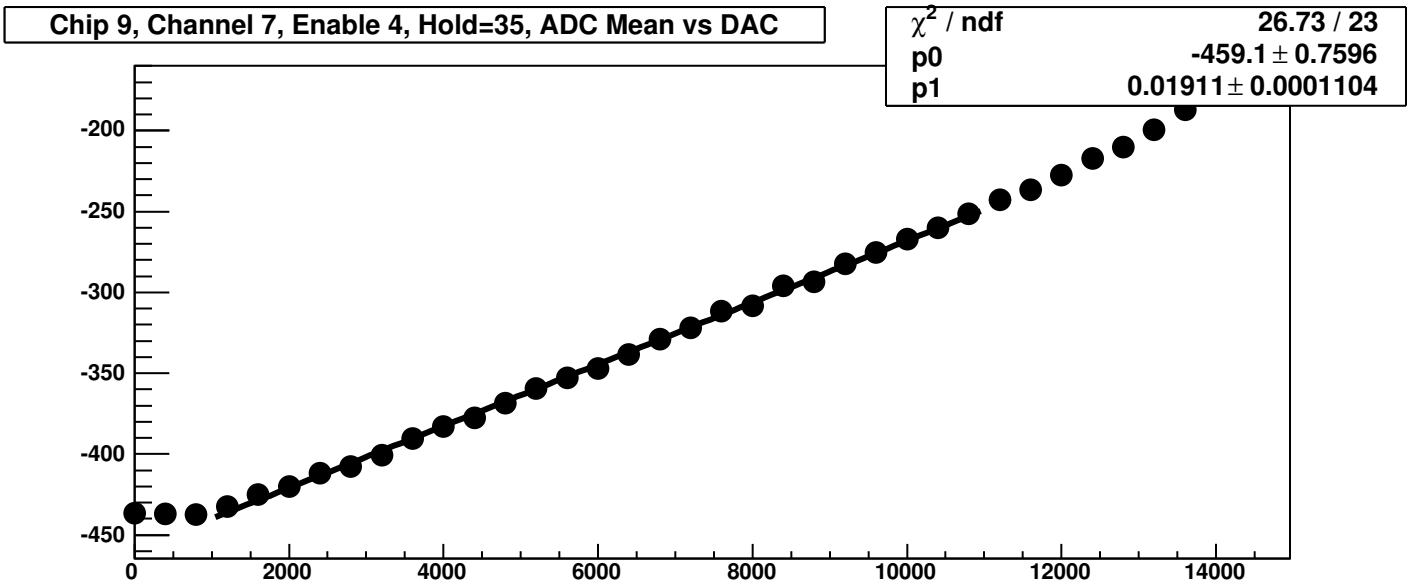


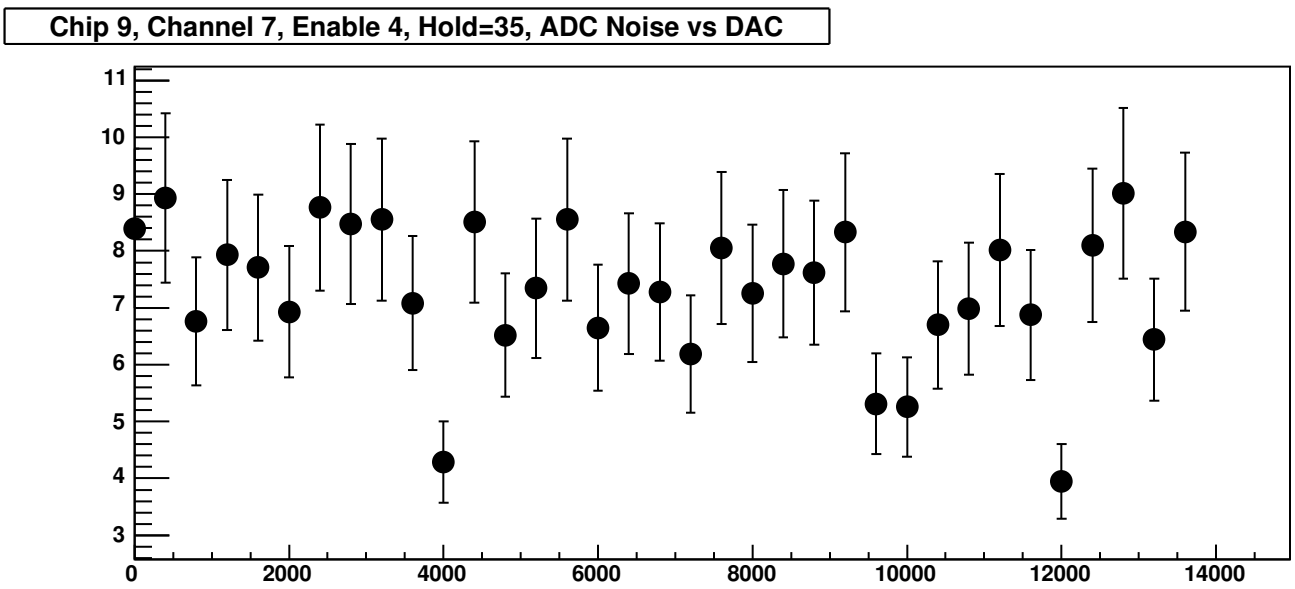
Chip 9, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC

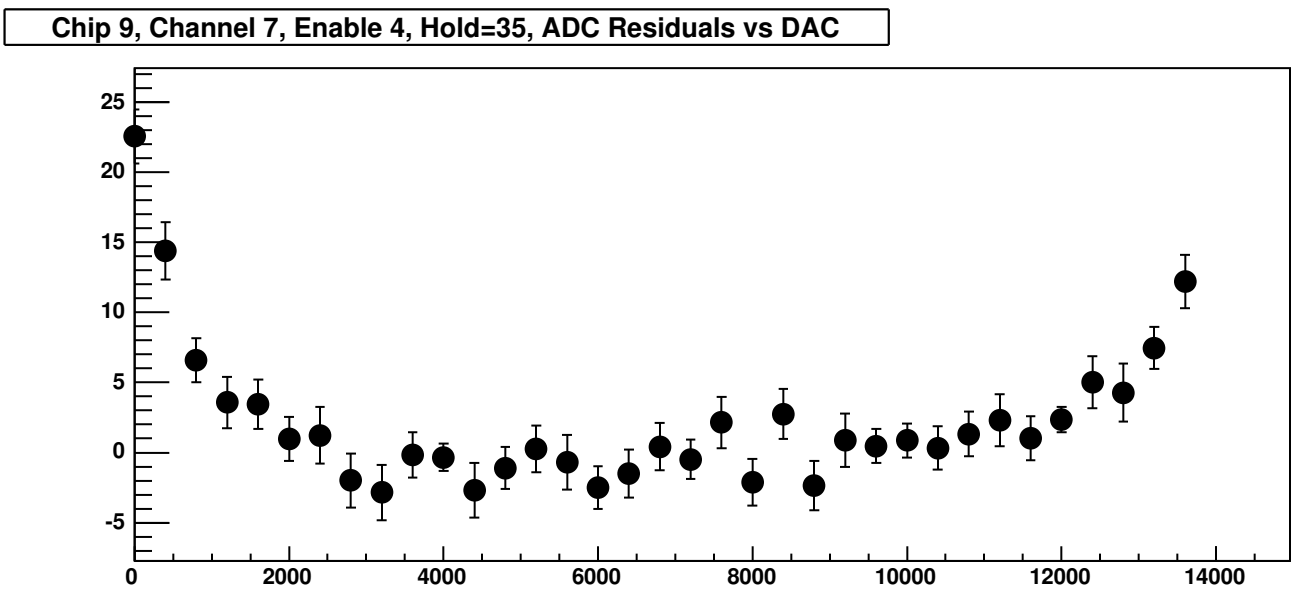


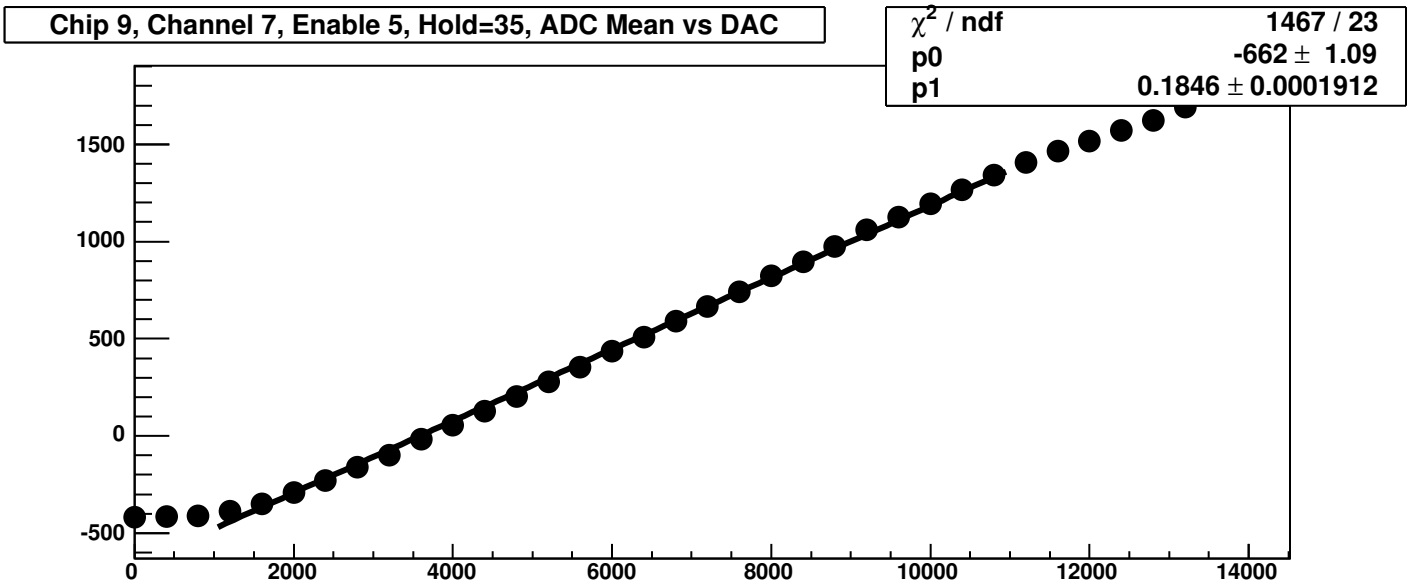
Chip 9, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

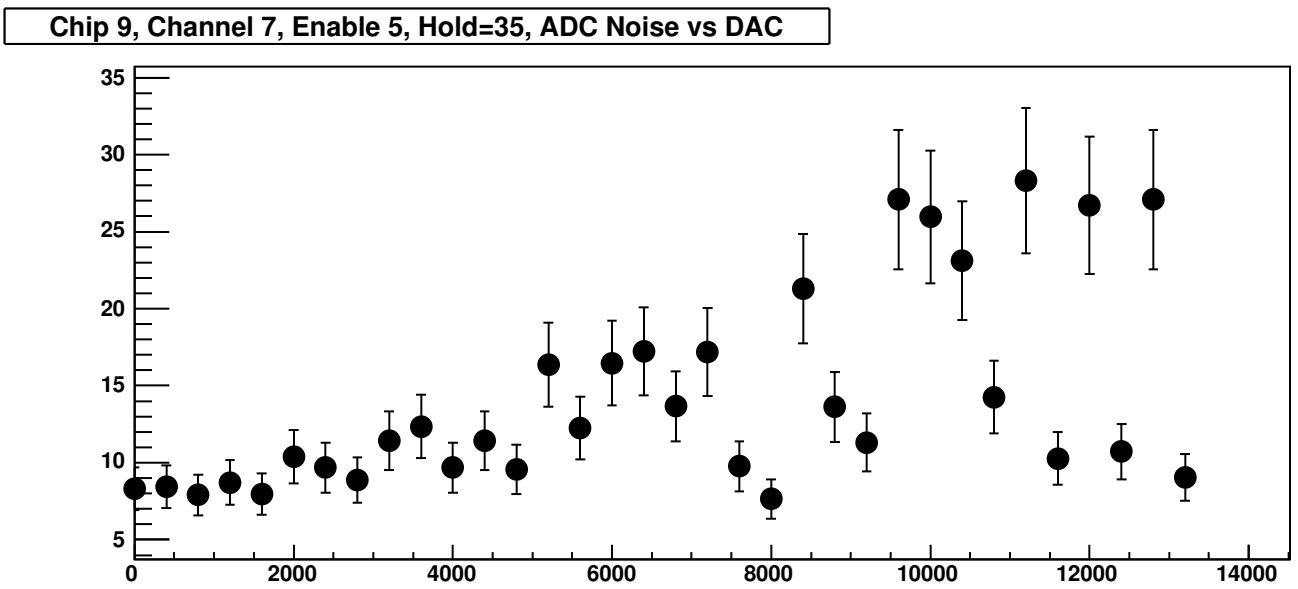


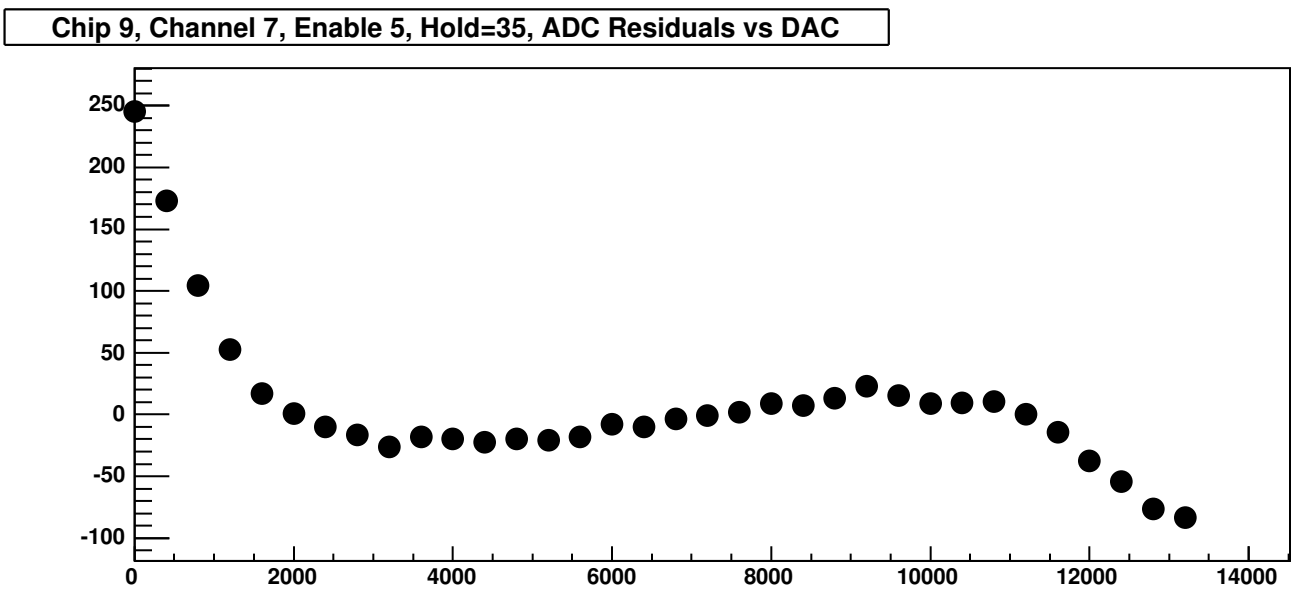

Chip 9, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.73 / 23
p0 -459.1 ± 0.7596
p1 0.01911 ± 0.0001104
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

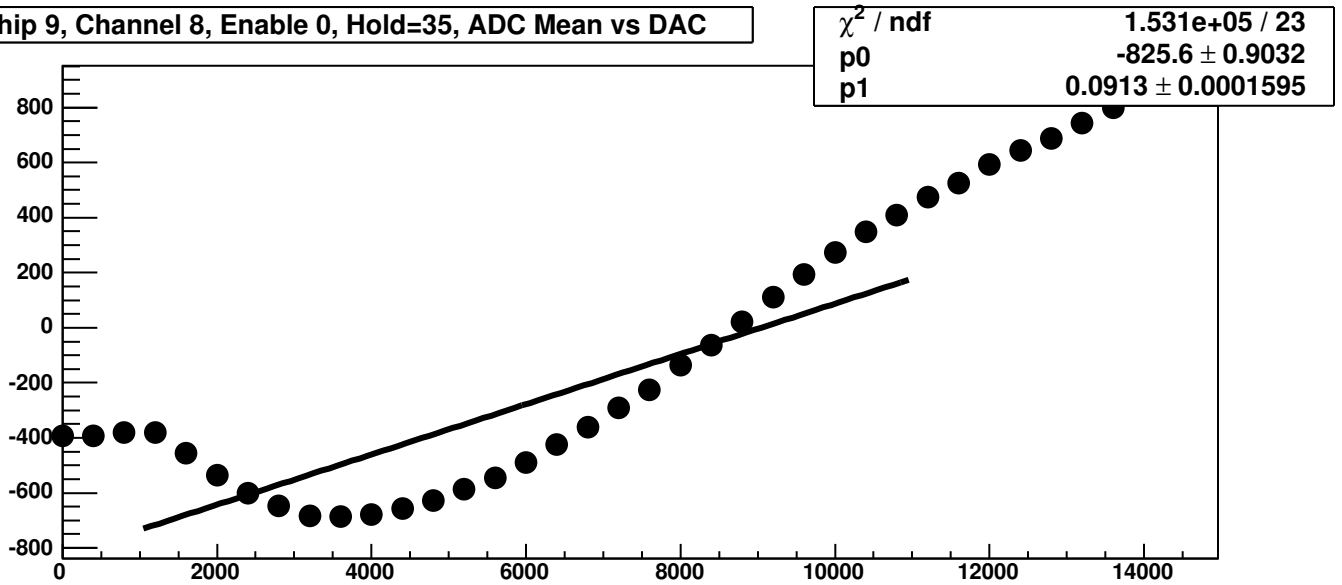

Chip 9, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1467 / 23
p0 -662 ± 1.09
p1 0.1846 ± 0.0001912
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

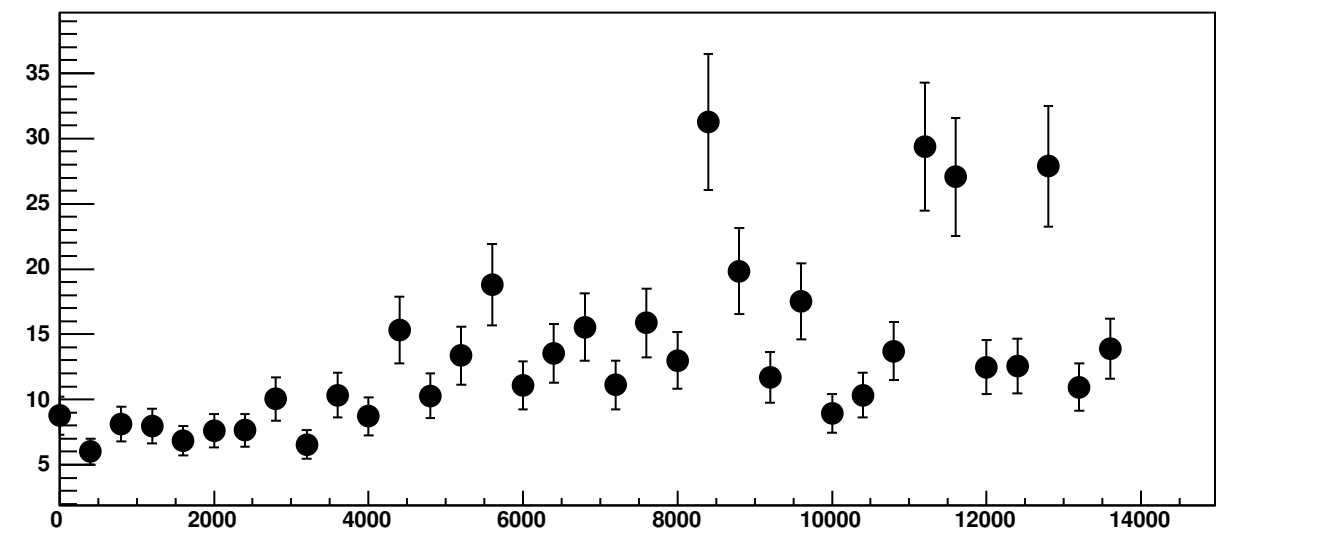

Chip 9, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

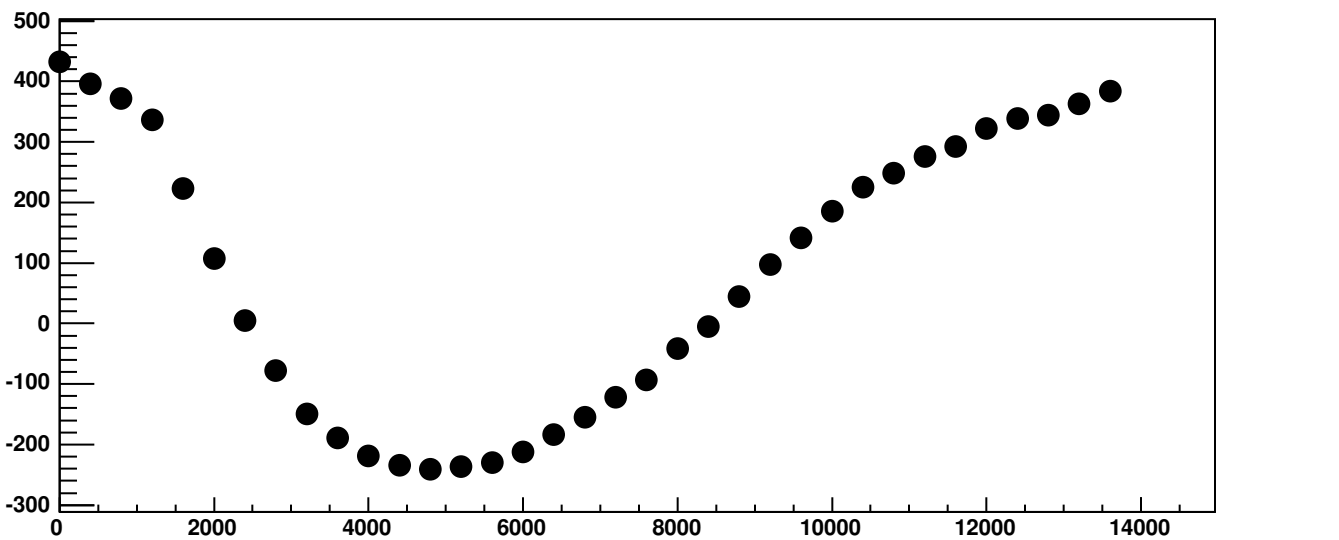
Chip 9, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

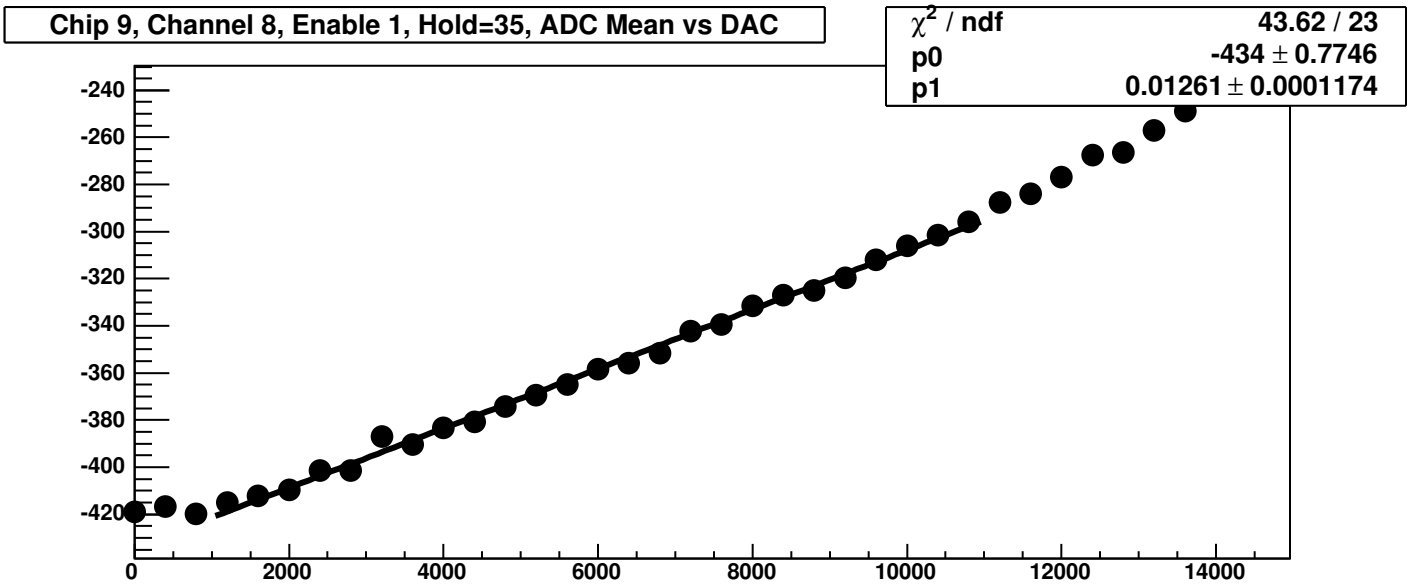


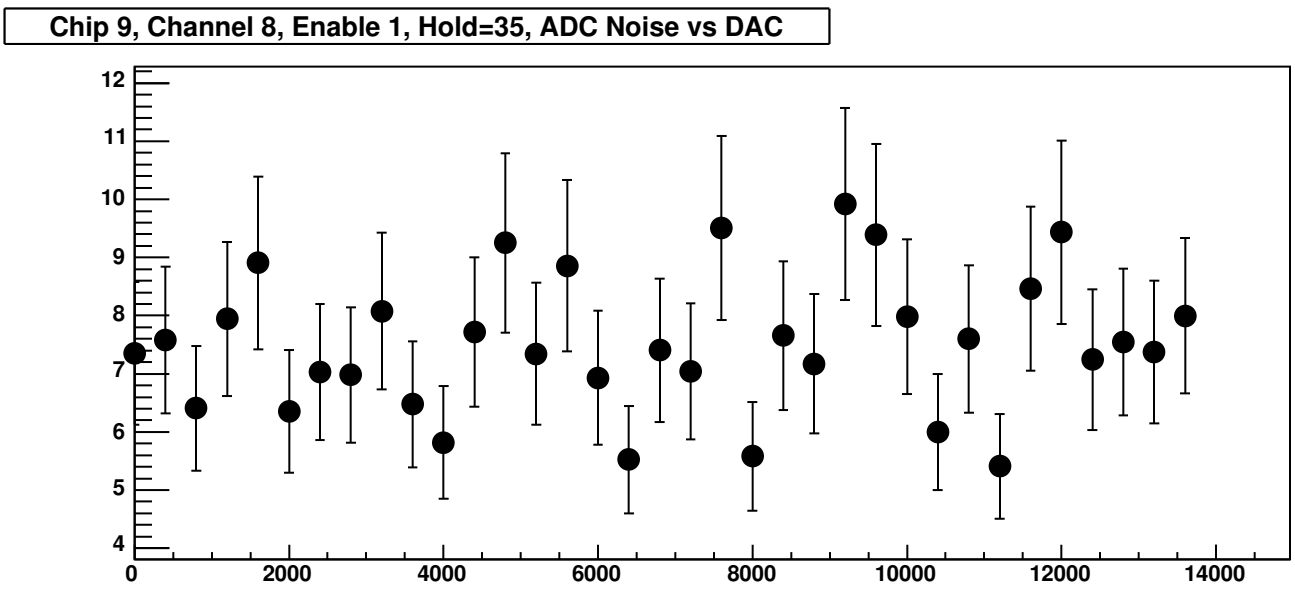
Chip 9, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC

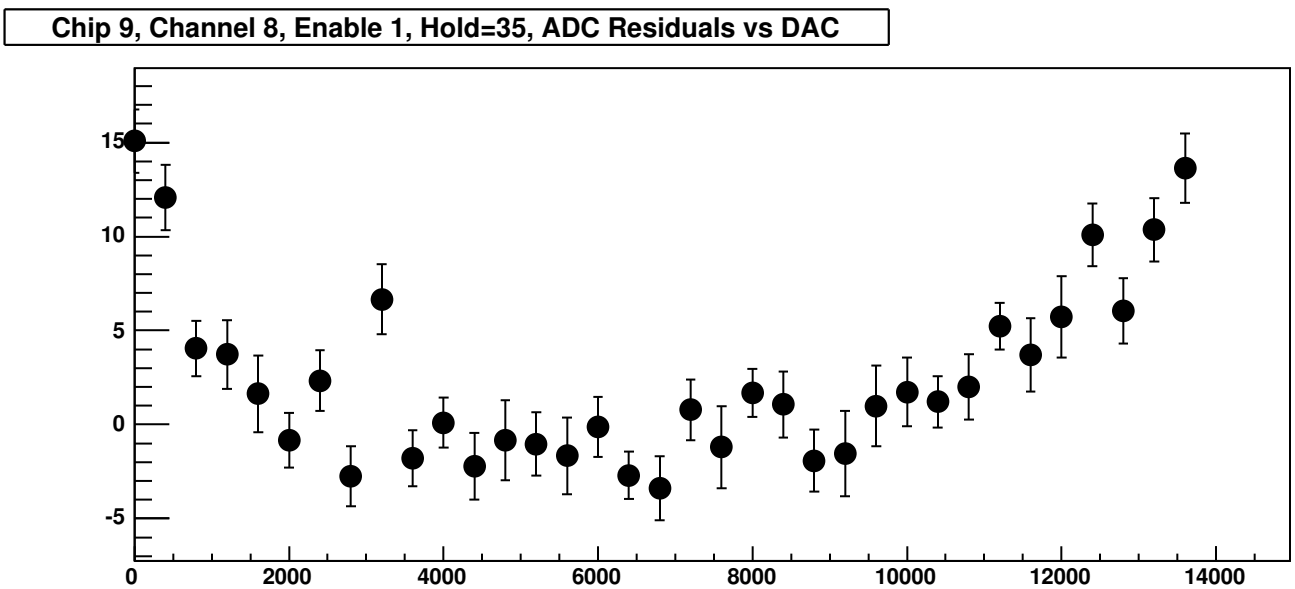


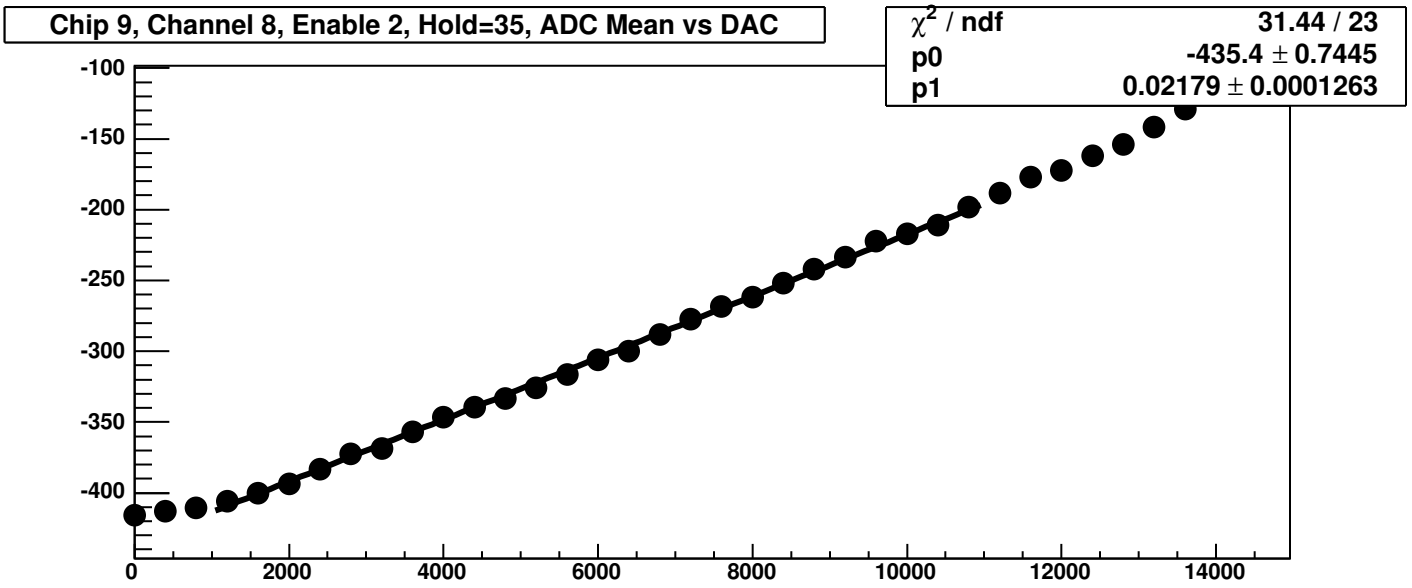
Chip 9, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

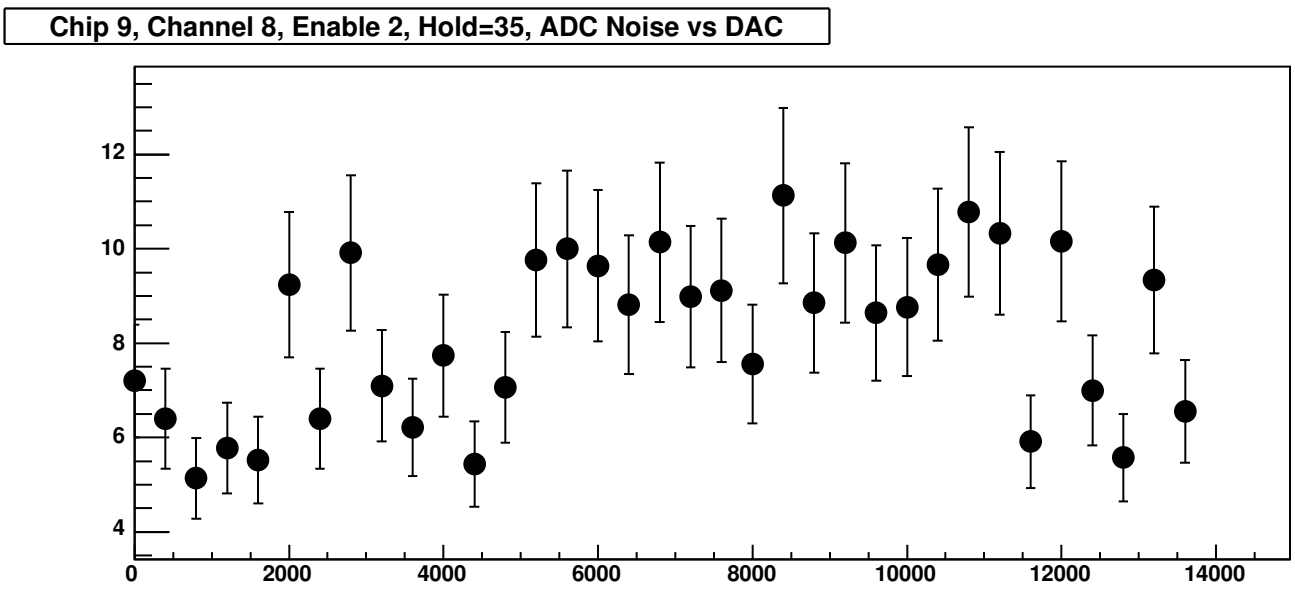


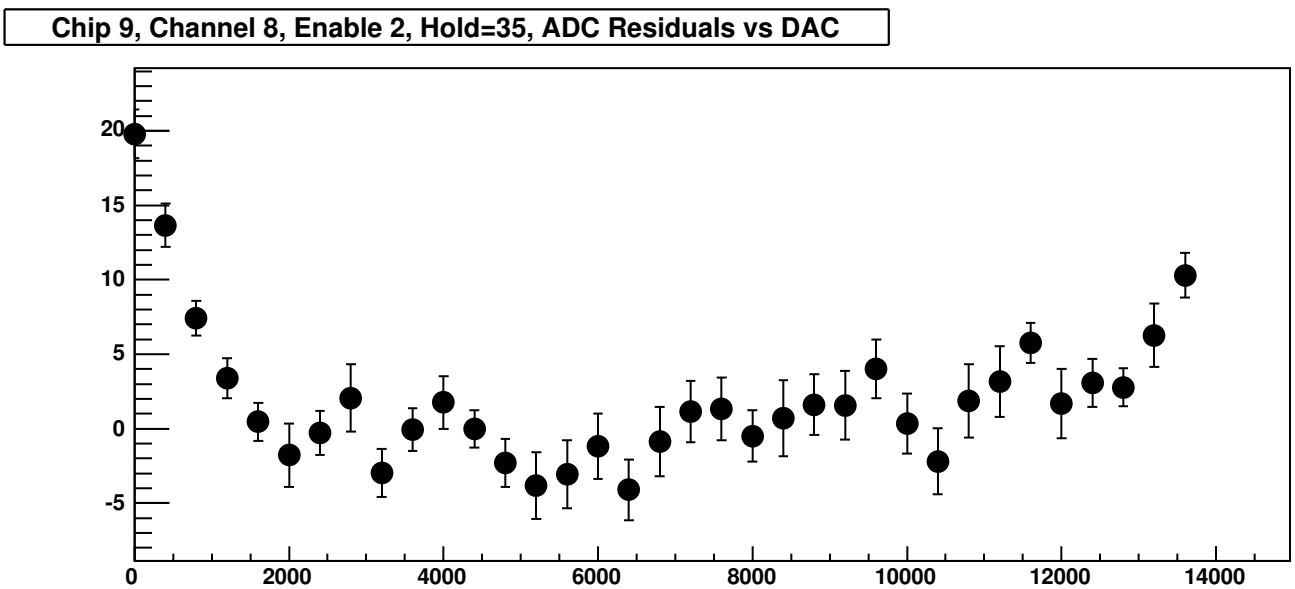

Chip 9, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 43.62 / 23
p0 -434 ± 0.7746
p1 0.01261 ± 0.0001174
-240
-260
-280
-300
-320
-340
-360
-380
-400
-420
0
2000
4000
6000
8000
10000
12000
14000

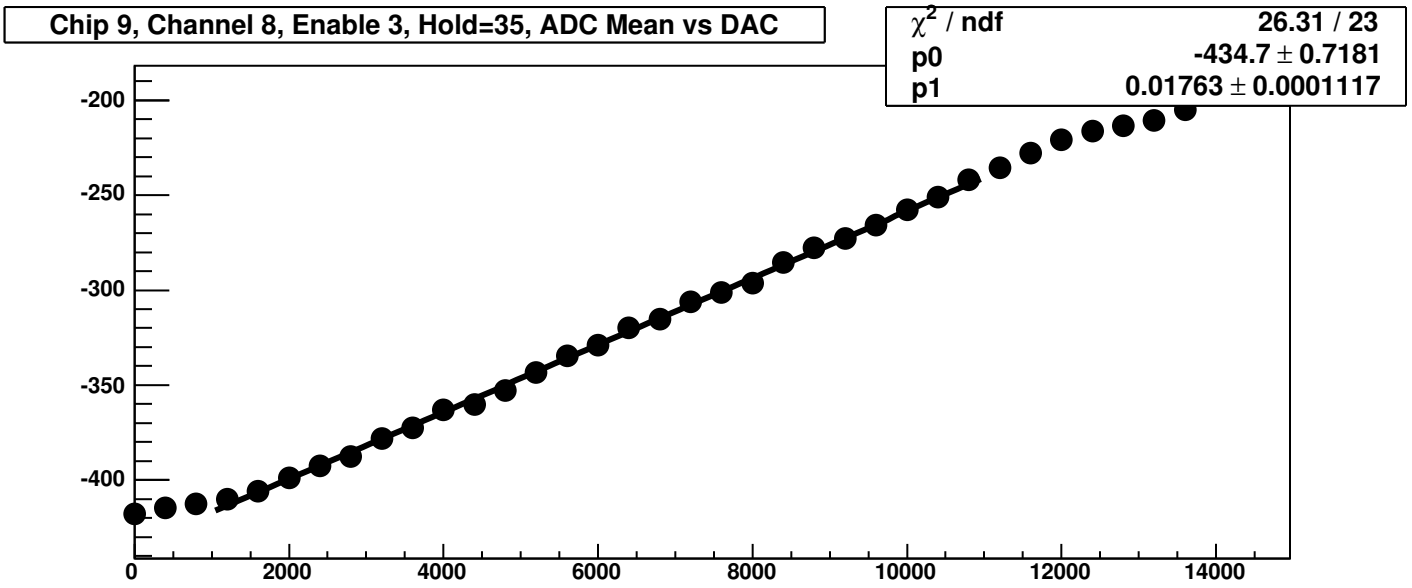

Chip 9, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

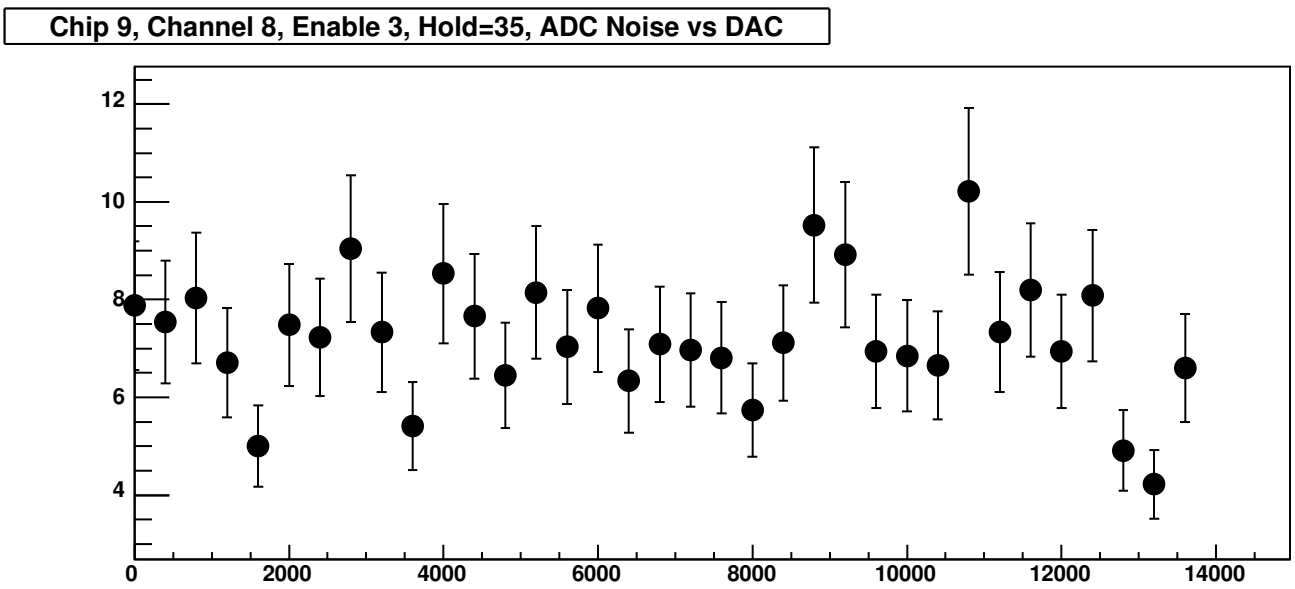

Chip 9, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

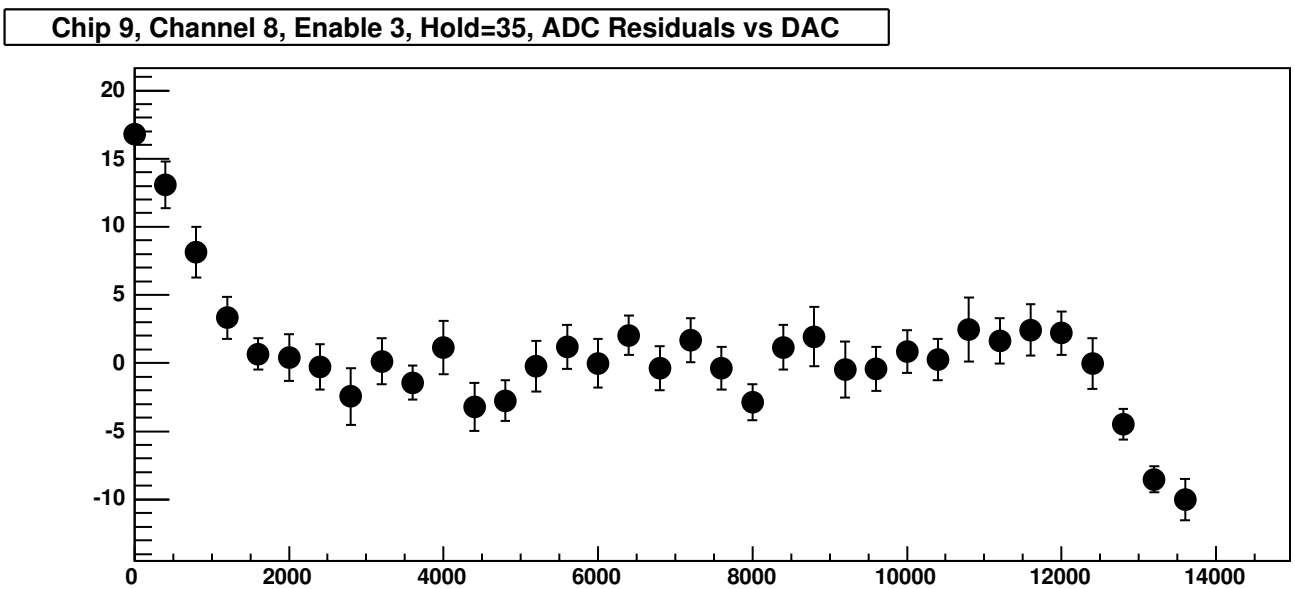

Chip 9, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 31.44 / 23
p0 -435.4 ± 0.7445
p1 0.02179 ± 0.0001263
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

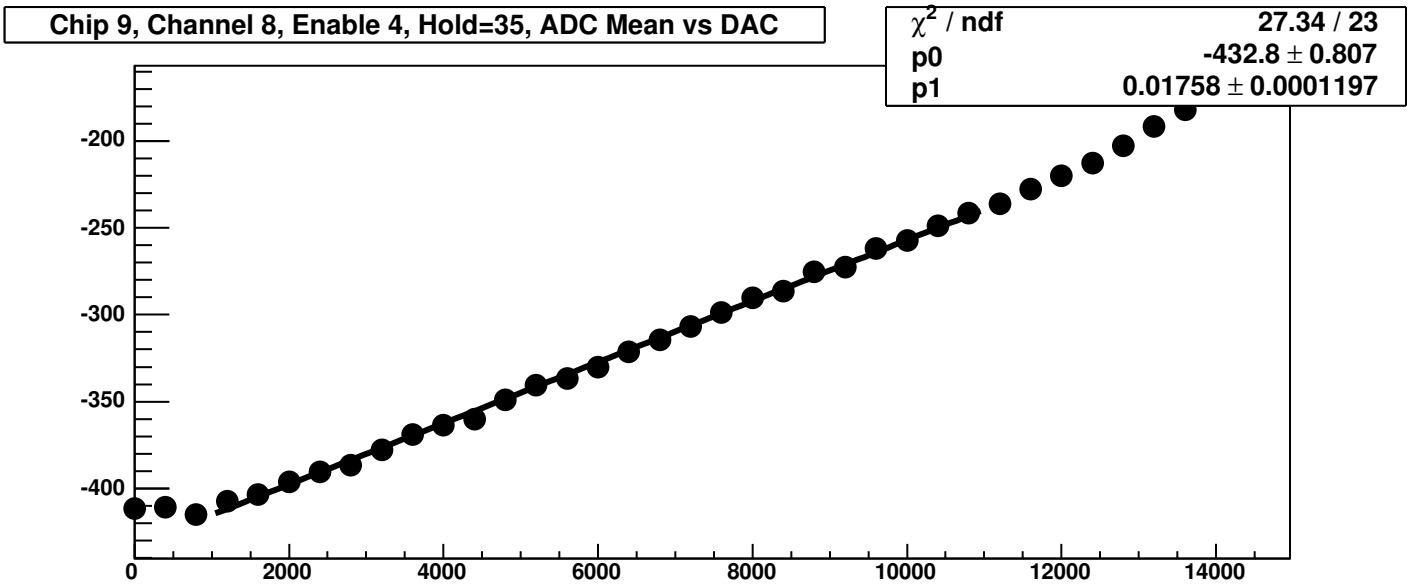

Chip 9, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

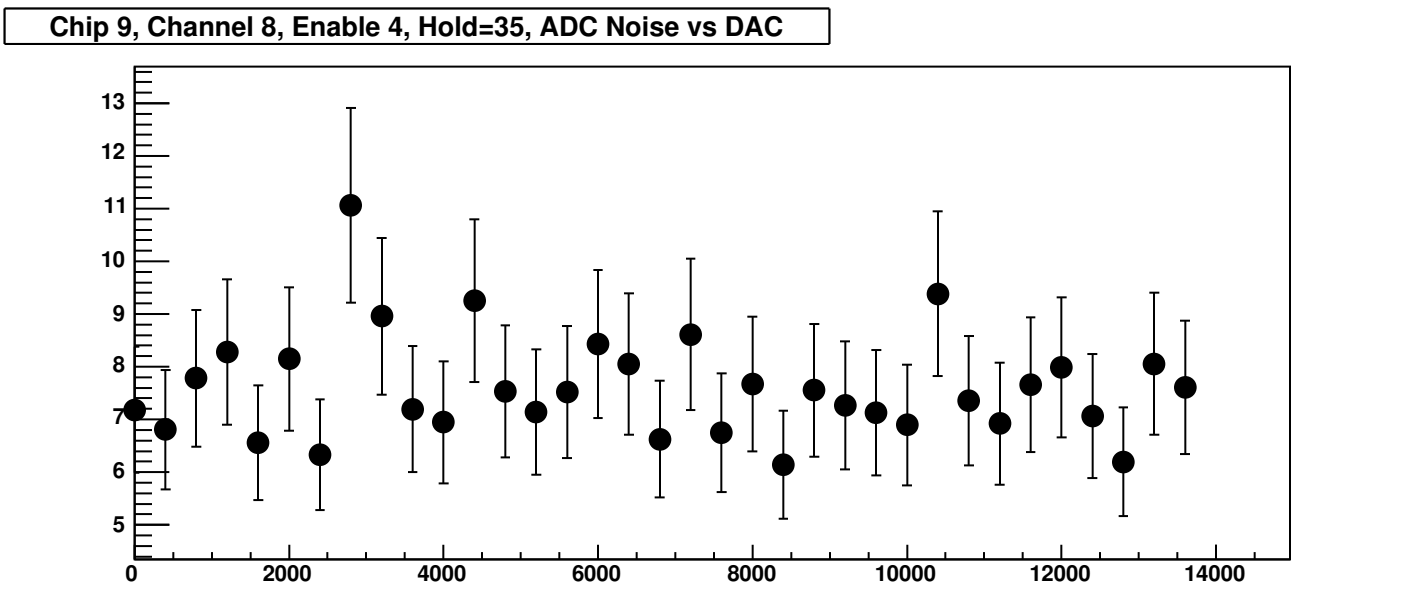

Chip 9, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

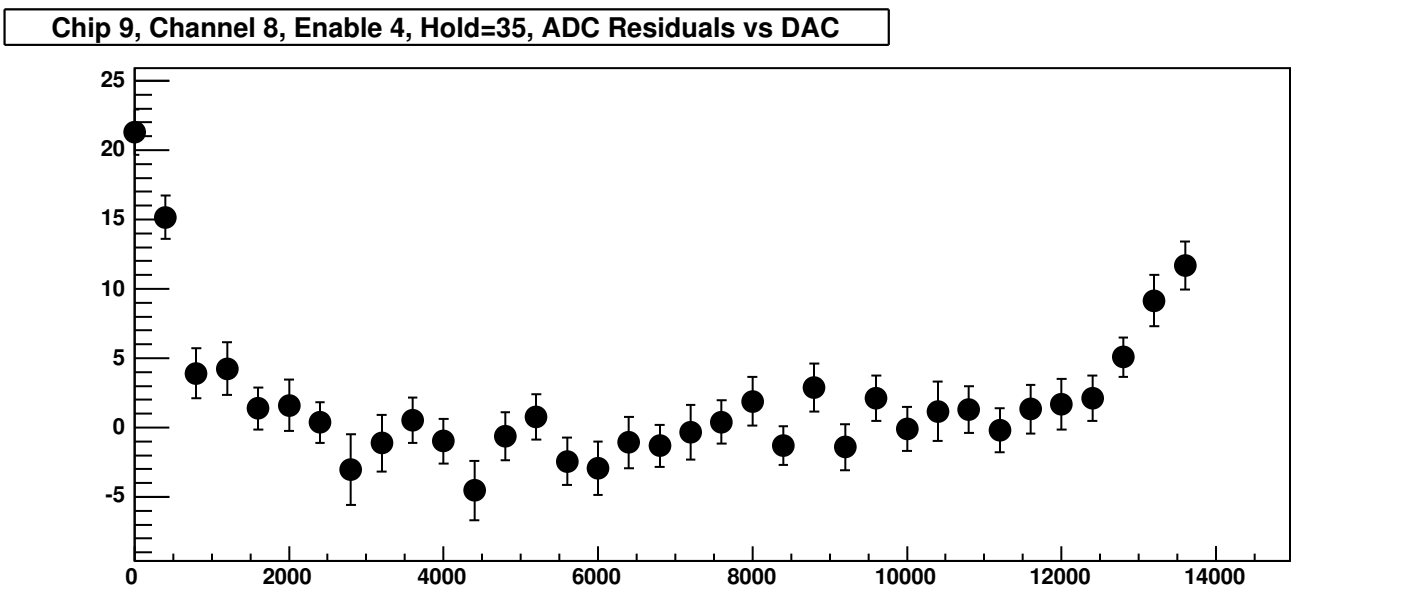

Chip 9, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.31 / 23
p0 -434.7 ± 0.7181
p1 0.01763 ± 0.0001117
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

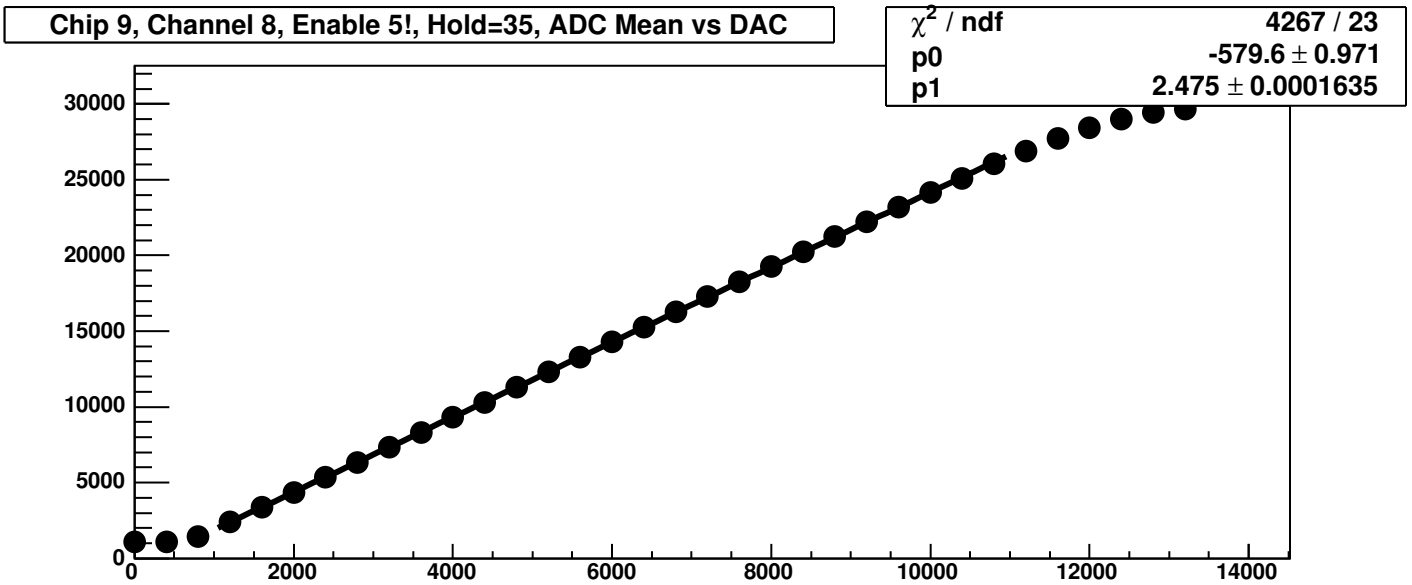

Chip 9, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

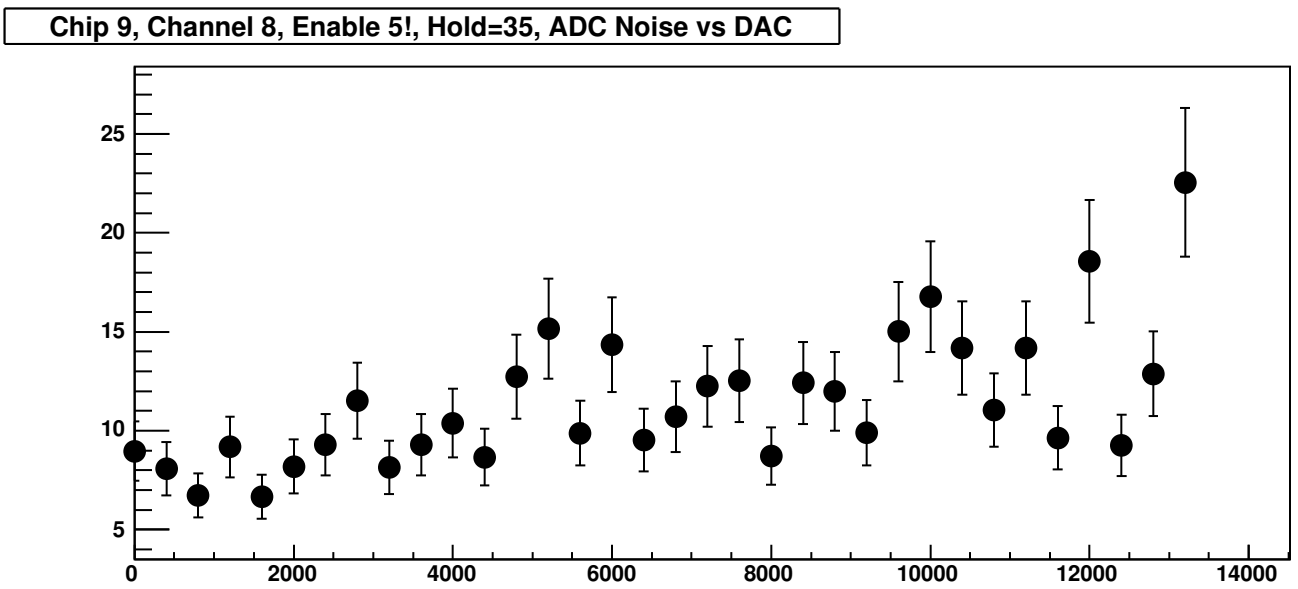

Chip 9, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

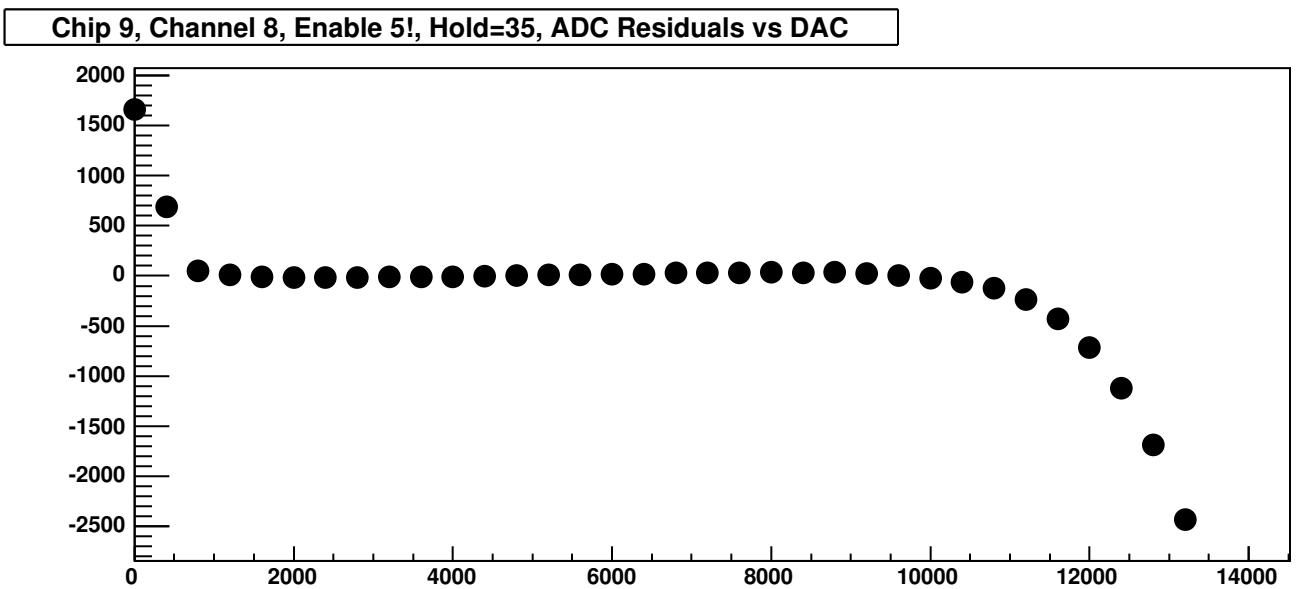

Chip 9, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.34 / 23
p0 -432.8 ± 0.807
p1 0.01758 ± 0.0001197
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

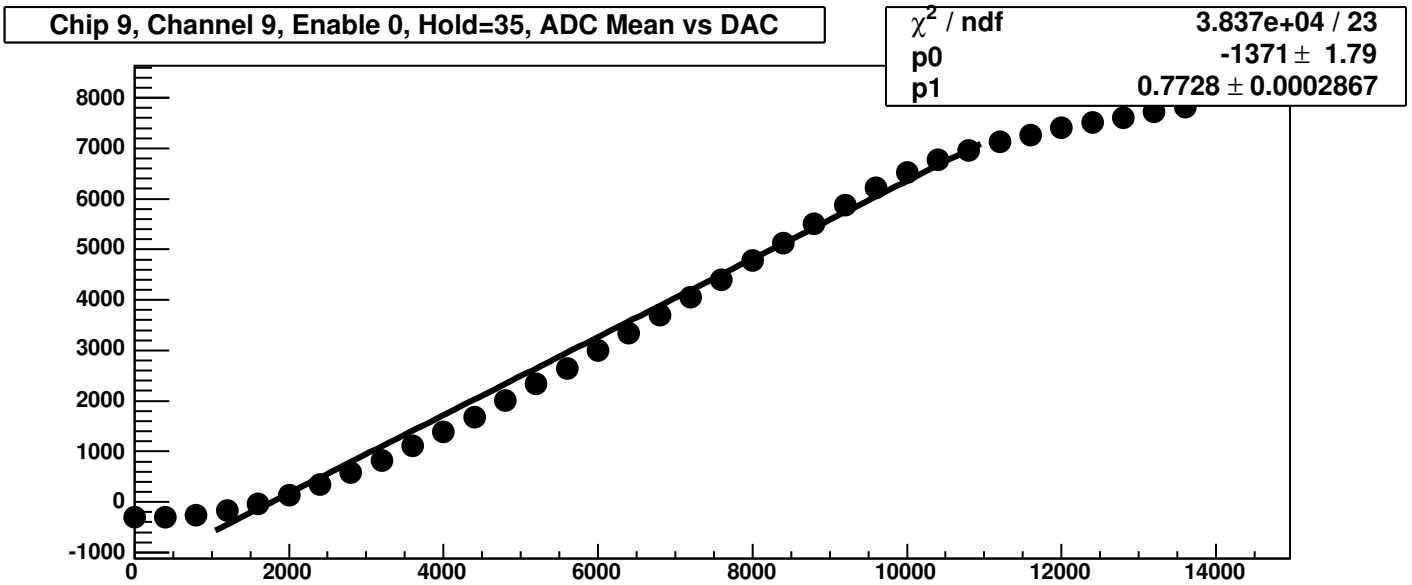

Chip 9, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

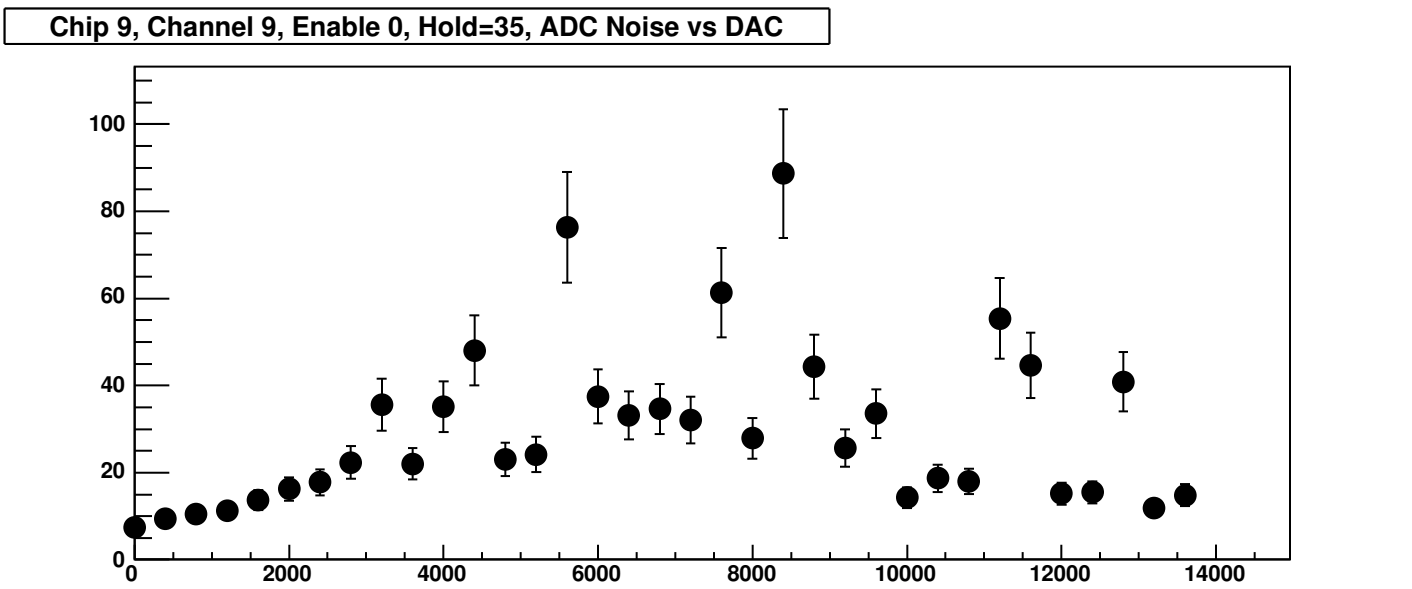

Chip 9, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

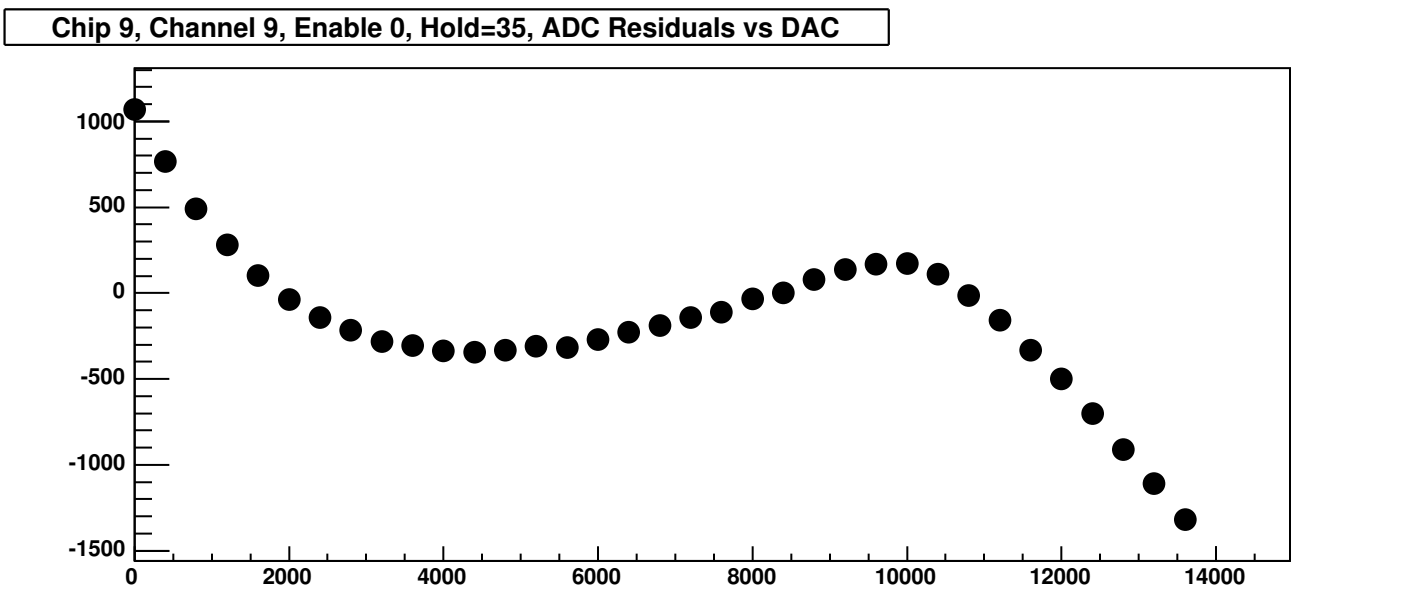

Chip 9, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4267 / 23
p0 -579.6 ± 0.971
p1 2.475 ± 0.0001635
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

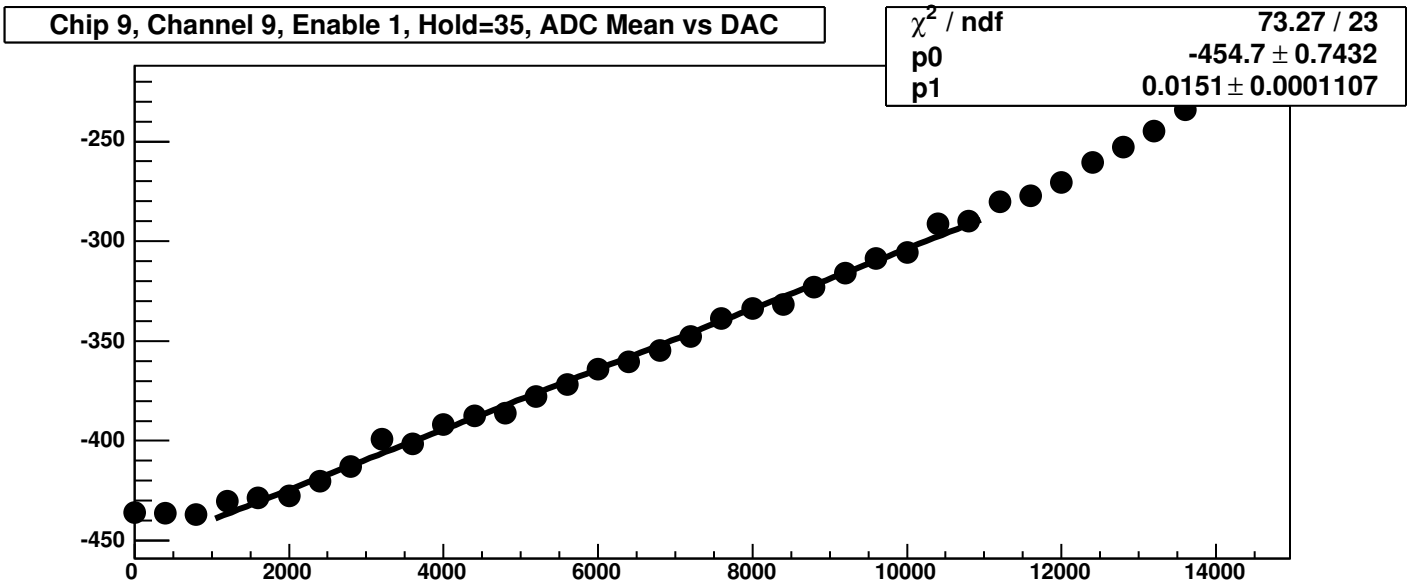

Chip 9, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

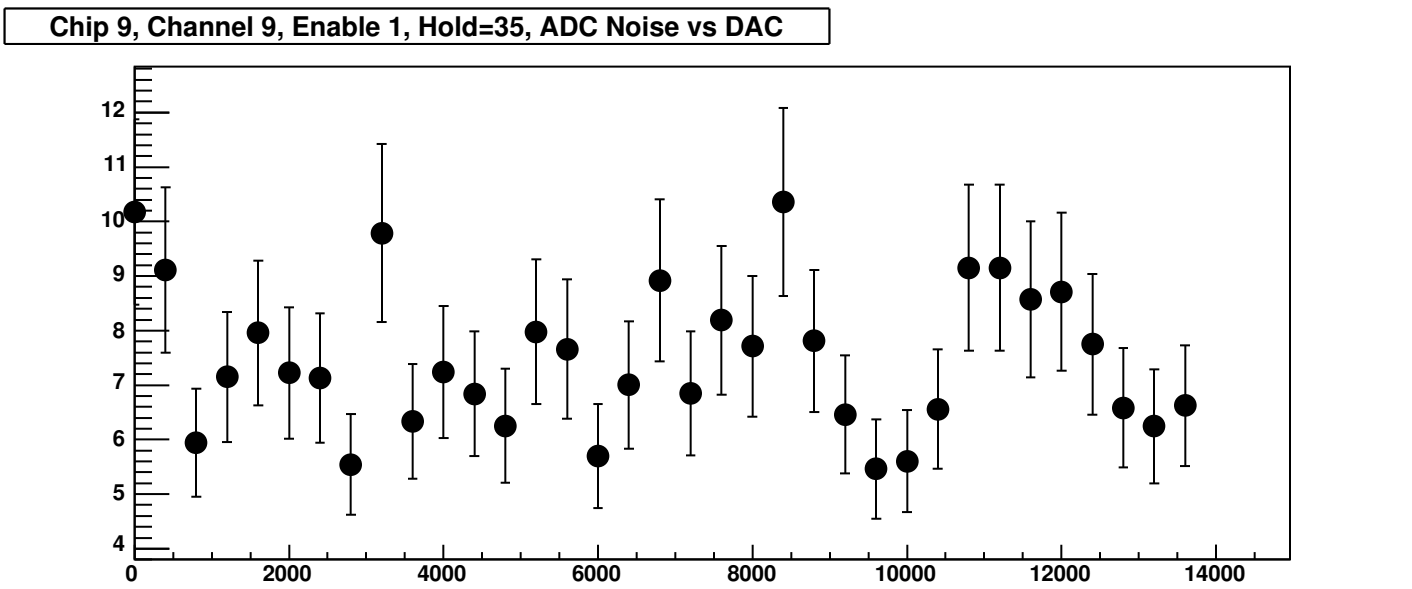

Chip 9, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

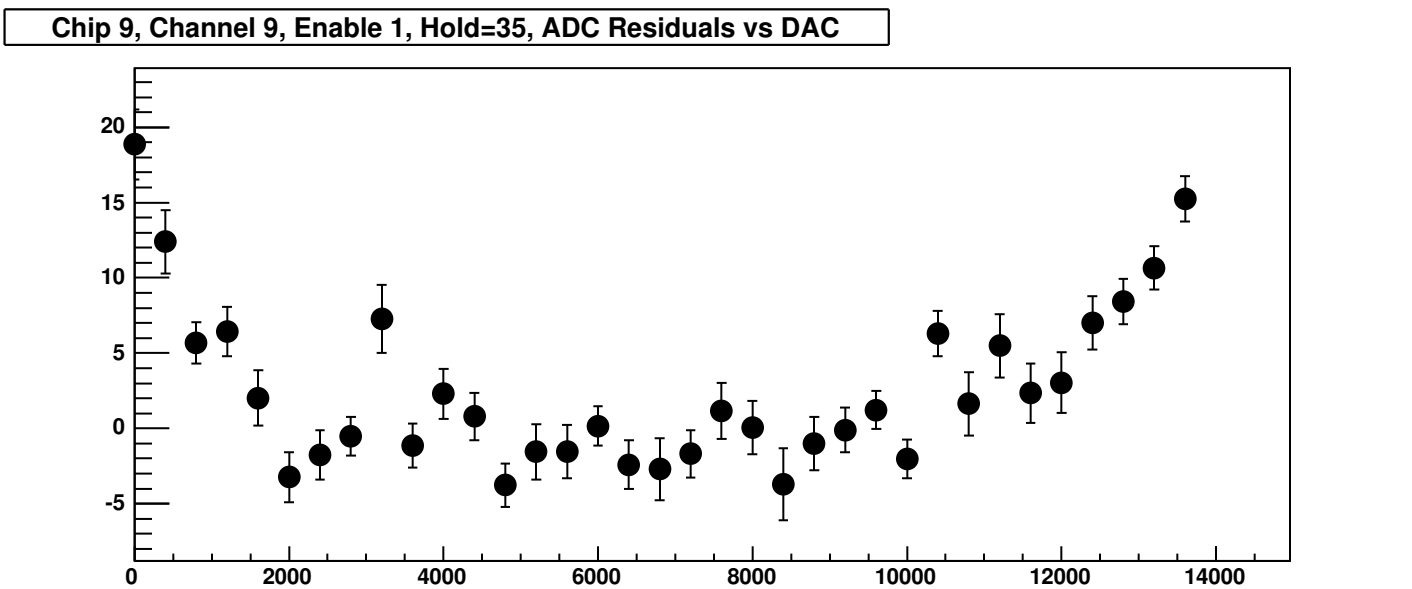

Chip 9, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.837e+04 / 23
p0 -1371 ± 1.79
p1 0.7728 ± 0.0002867
8000
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

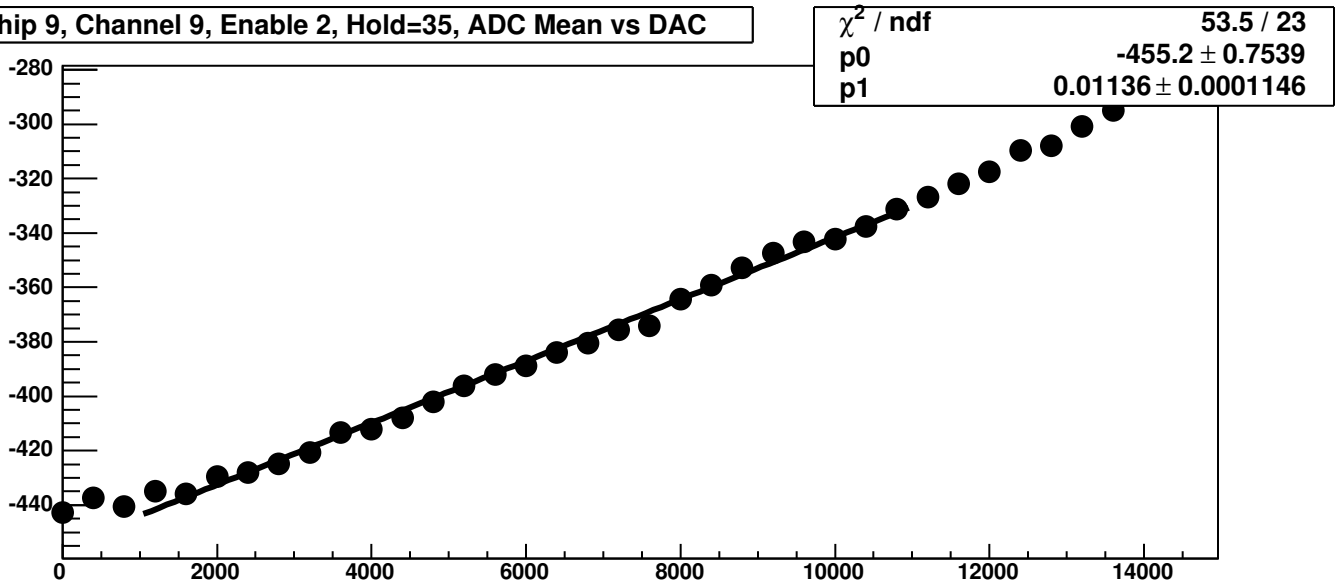

Chip 9, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 73.27 / 23
p0 -454.7 ± 0.7432
p1 0.0151 ± 0.0001107
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

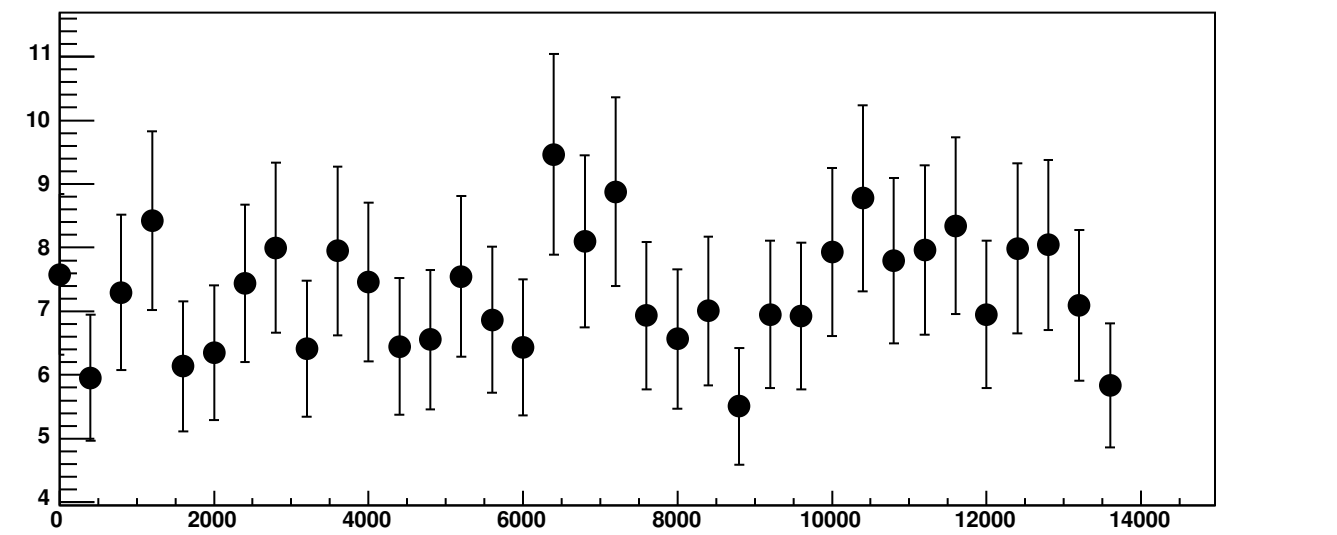

Chip 9, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

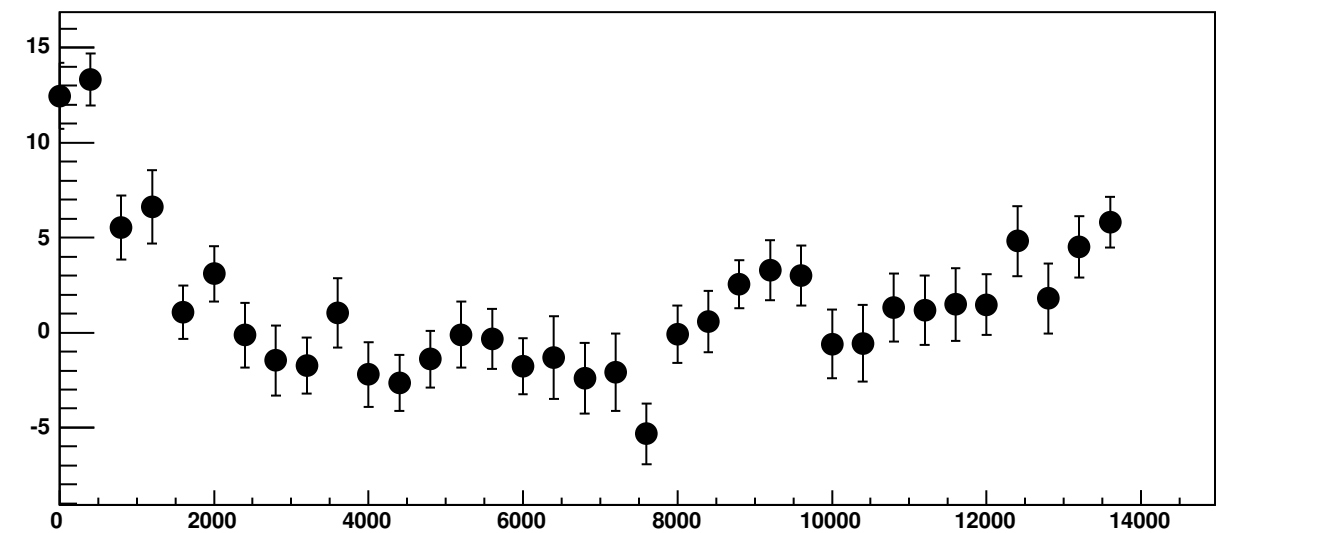
Chip 9, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

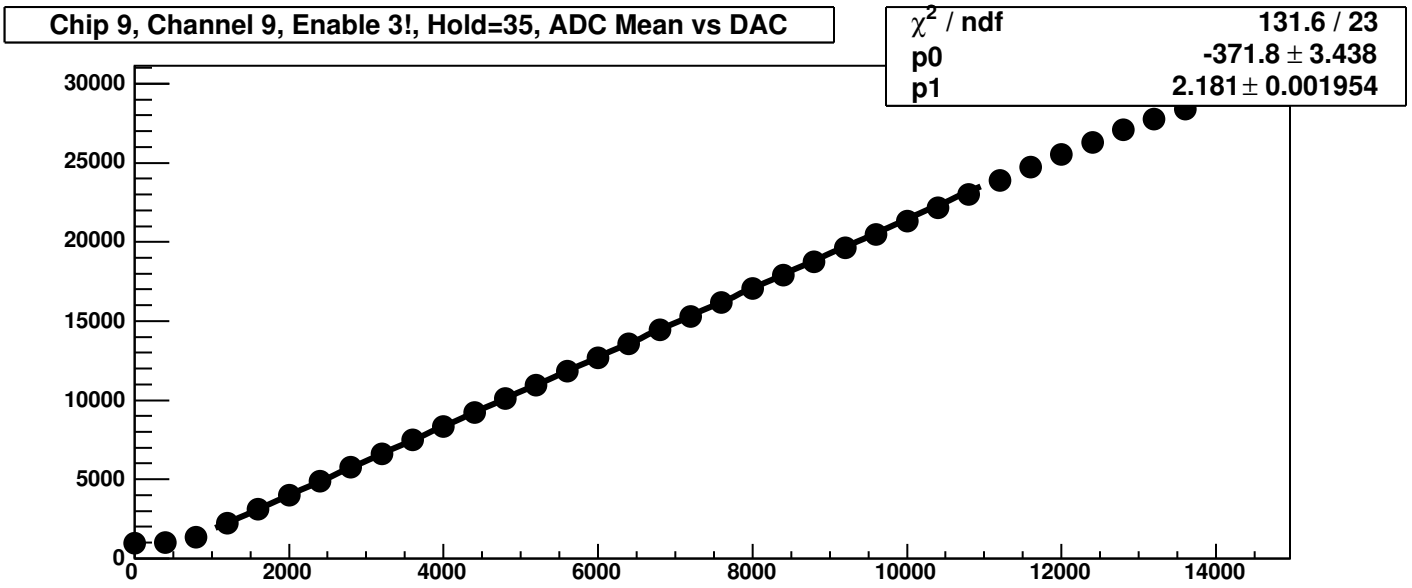


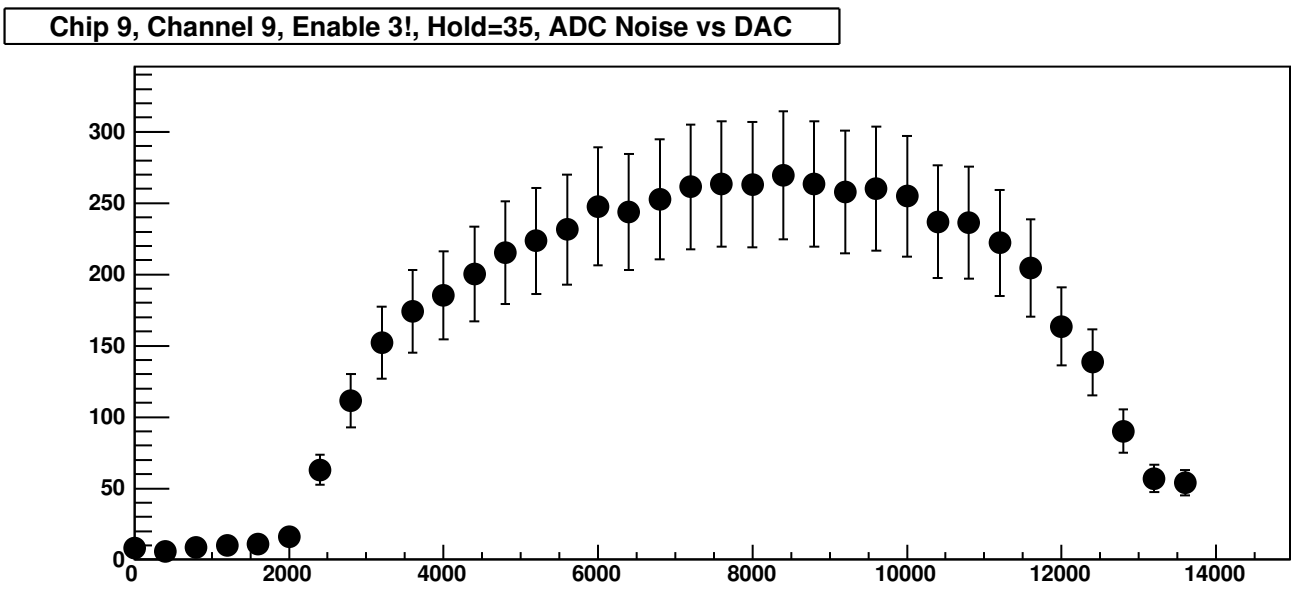
Chip 9, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC

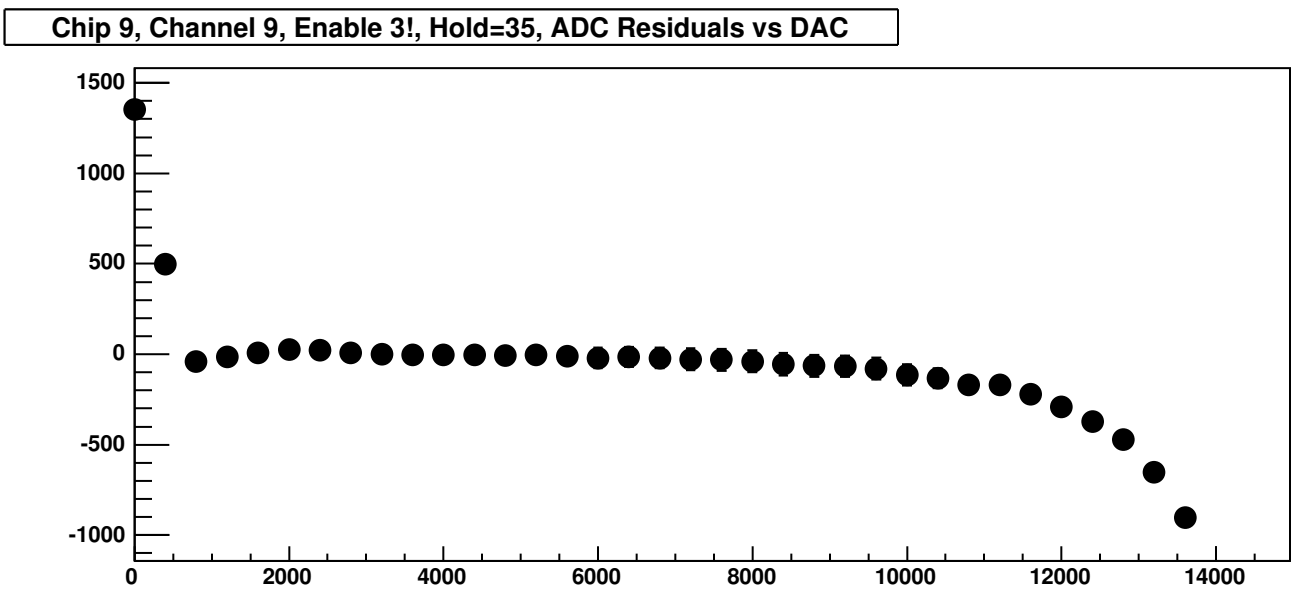


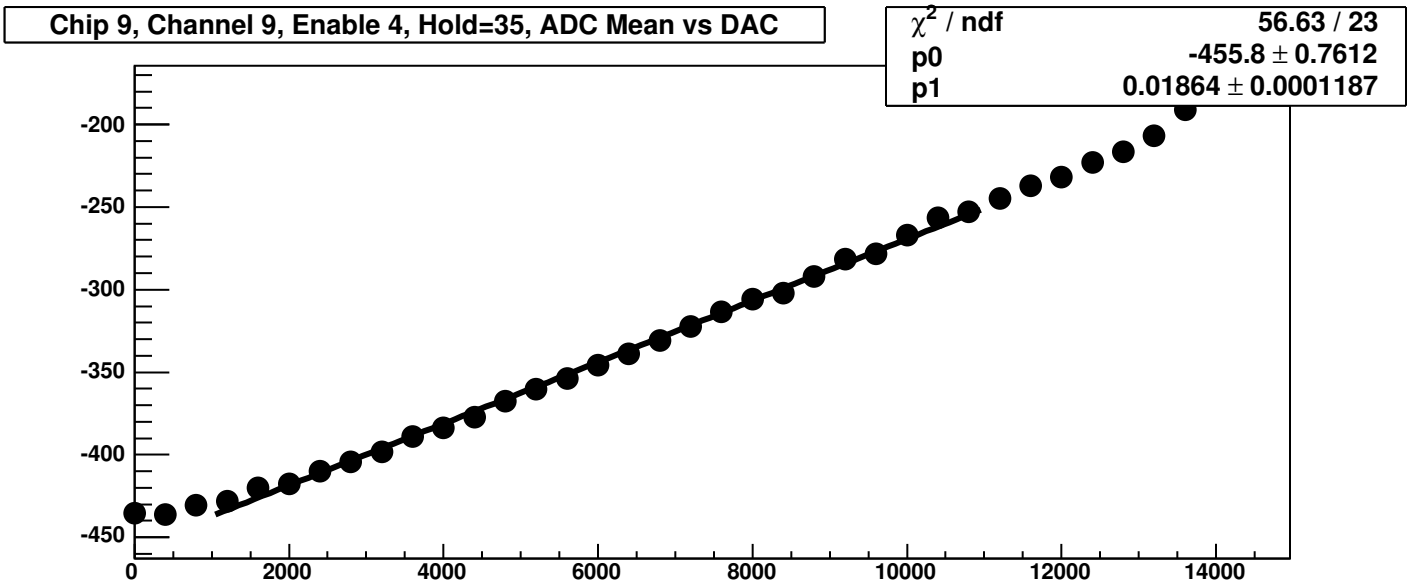
Chip 9, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

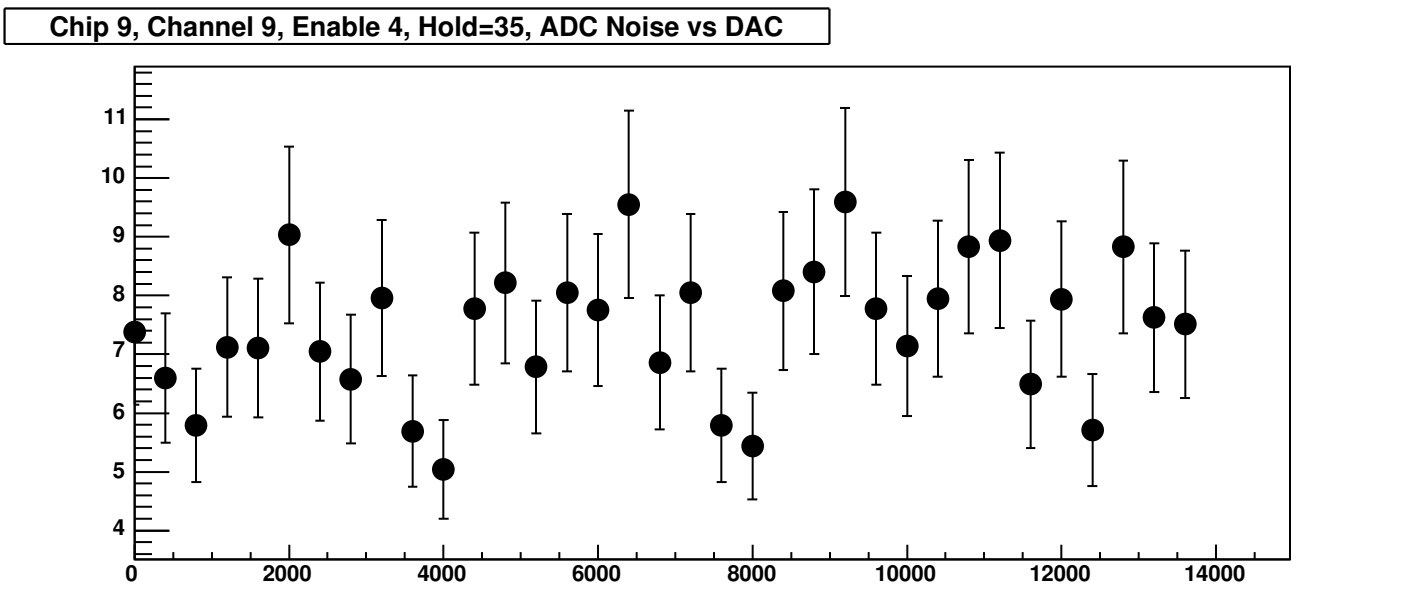


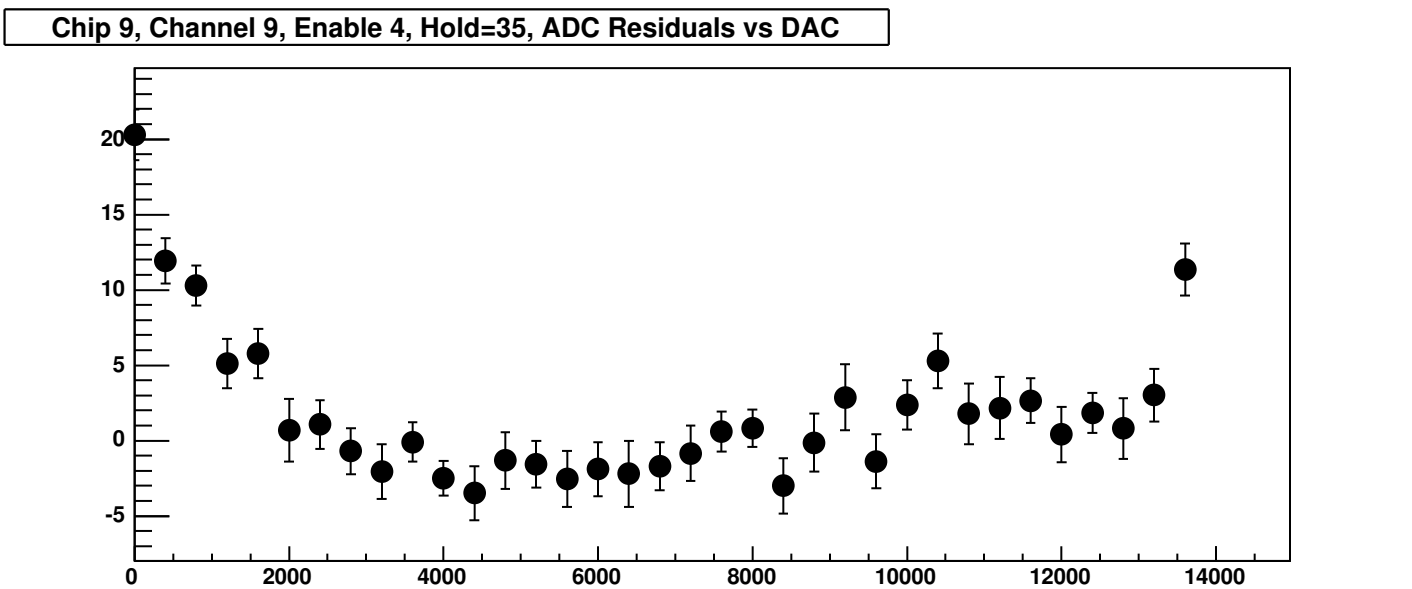

Chip 9, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 131.6 / 23
p0 -371.8 ± 3.438
p1 2.181 ± 0.001954
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

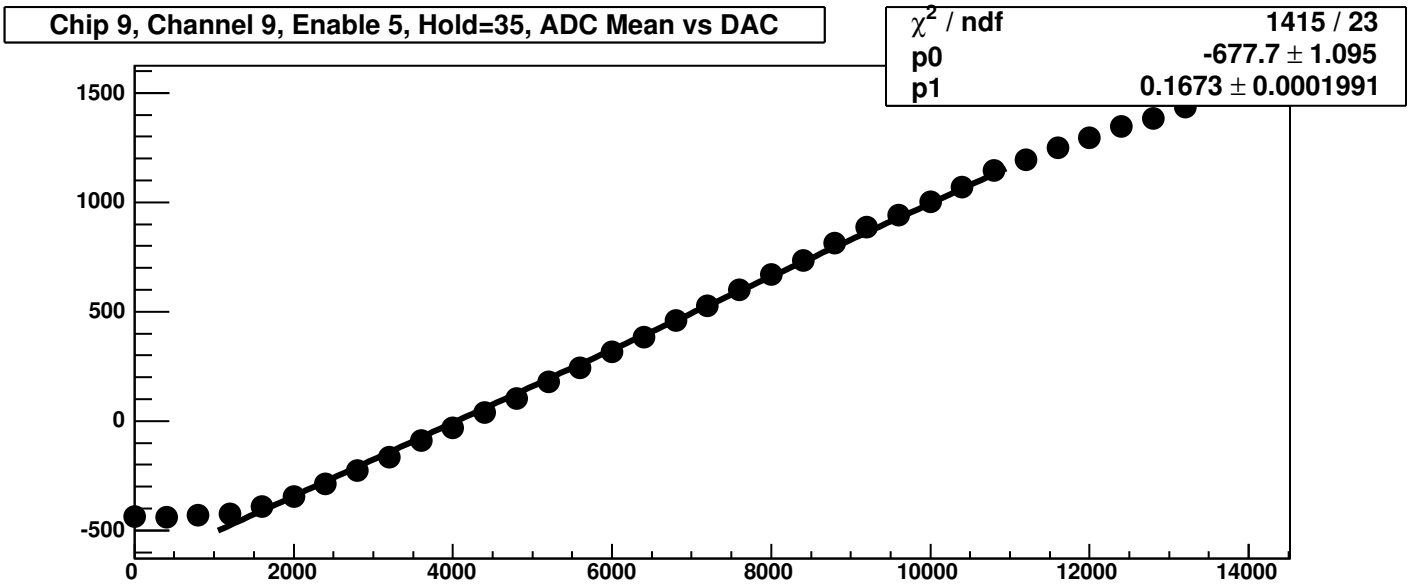

Chip 9, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

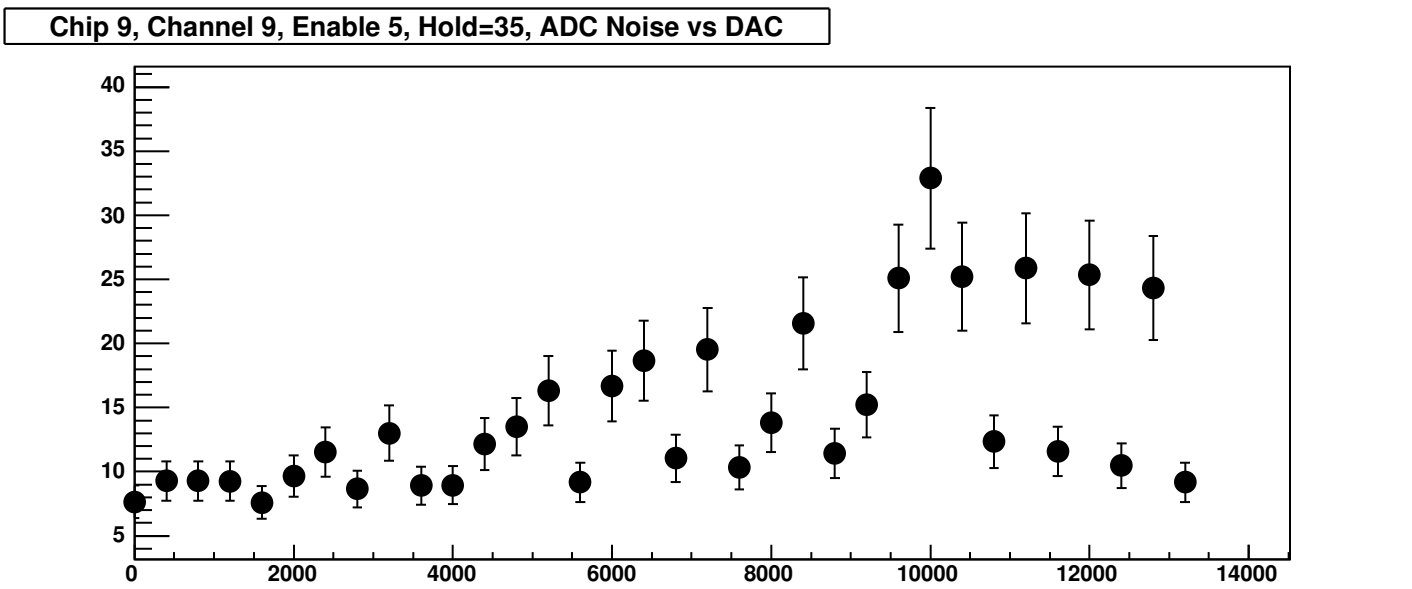

Chip 9, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

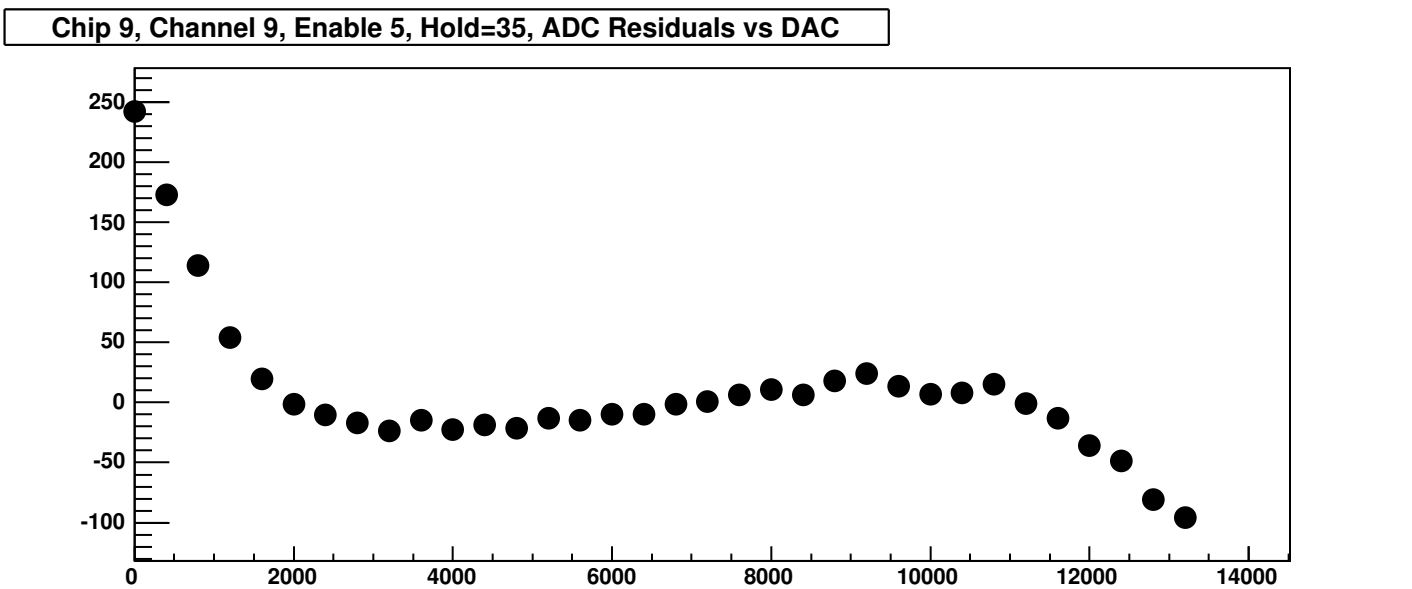

Chip 9, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 56.63 / 23
p0 -455.8 ± 0.7612
p1 0.01864 ± 0.0001187
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

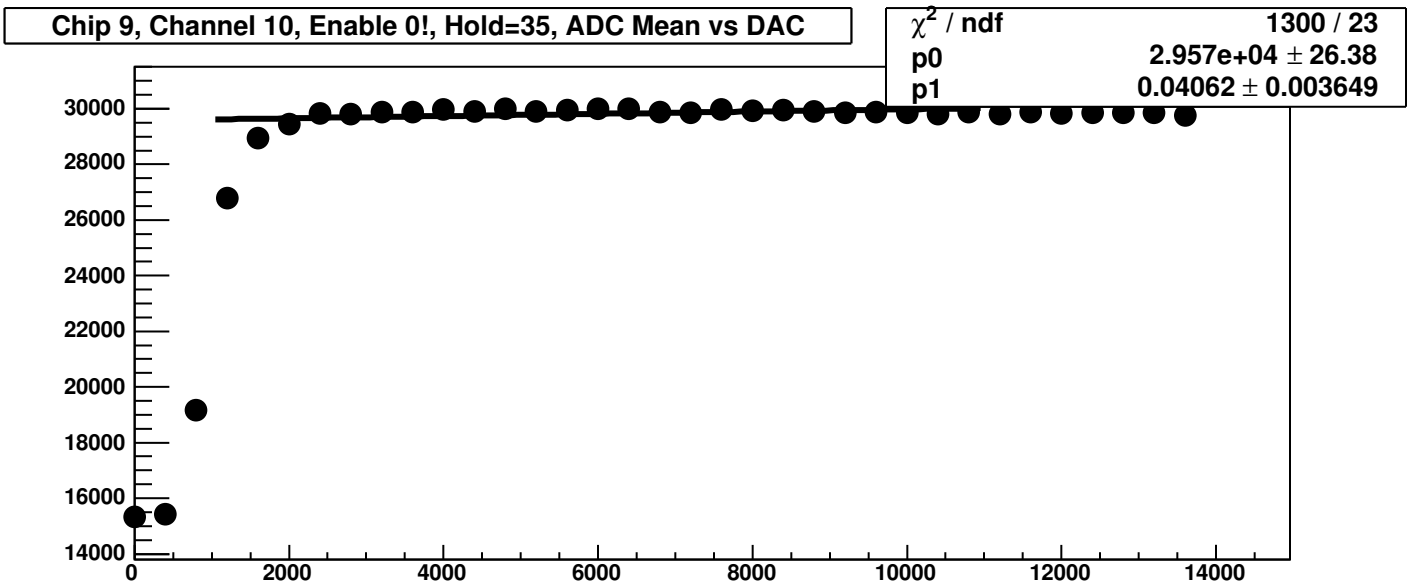

Chip 9, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

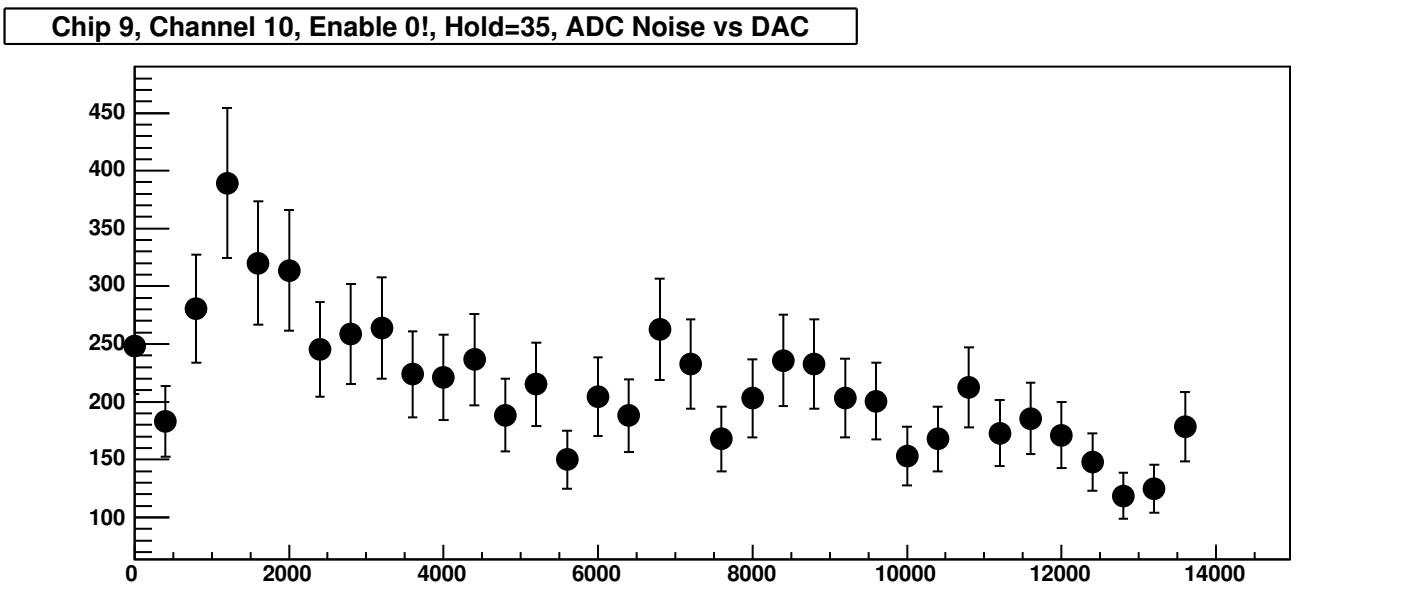

Chip 9, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

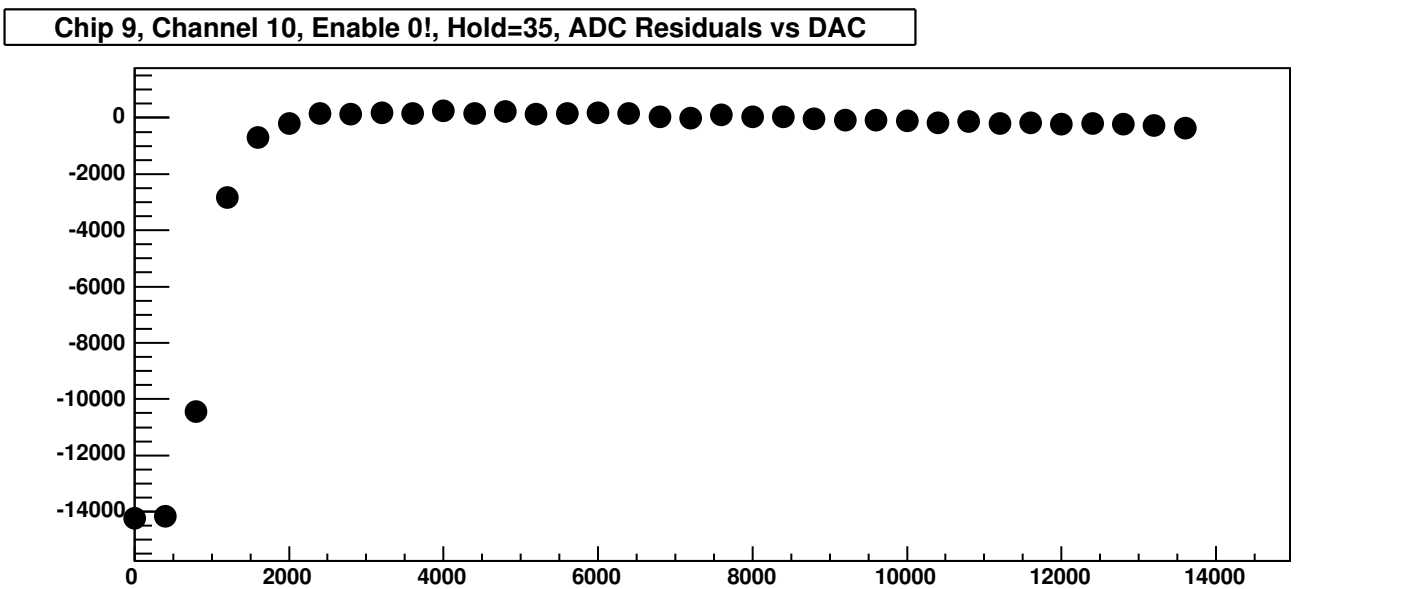

Chip 9, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1415 / 23
p0 -677.7 ± 1.095
p1 0.1673 ± 0.0001991
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

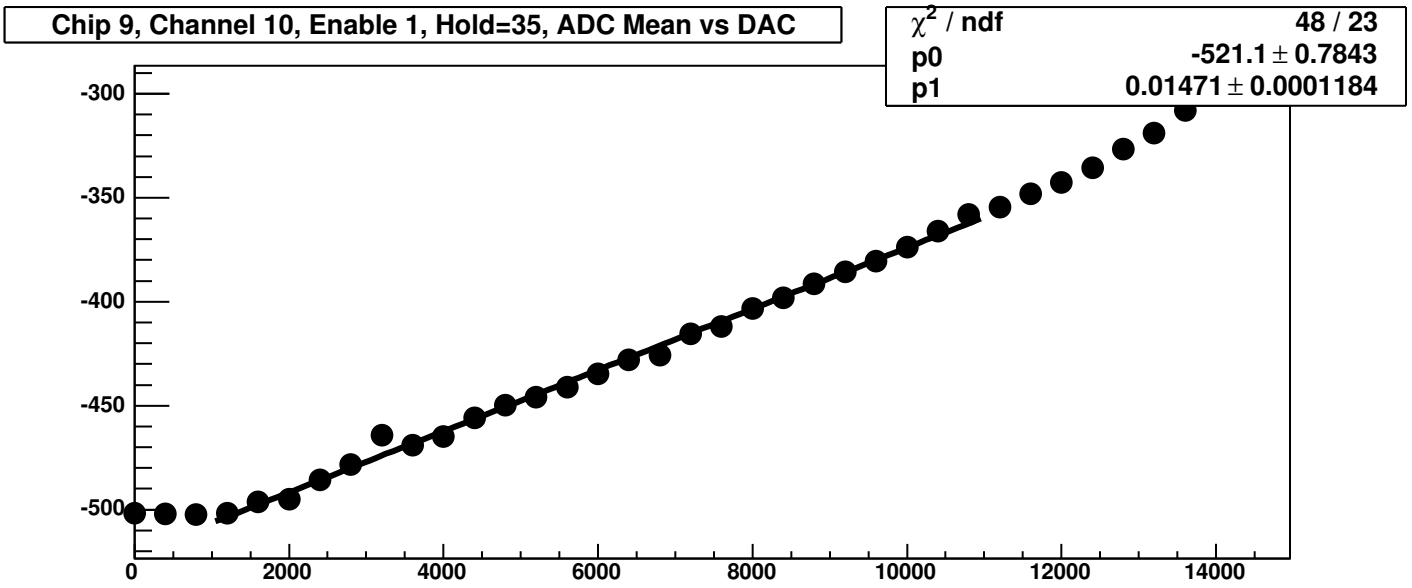

Chip 9, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

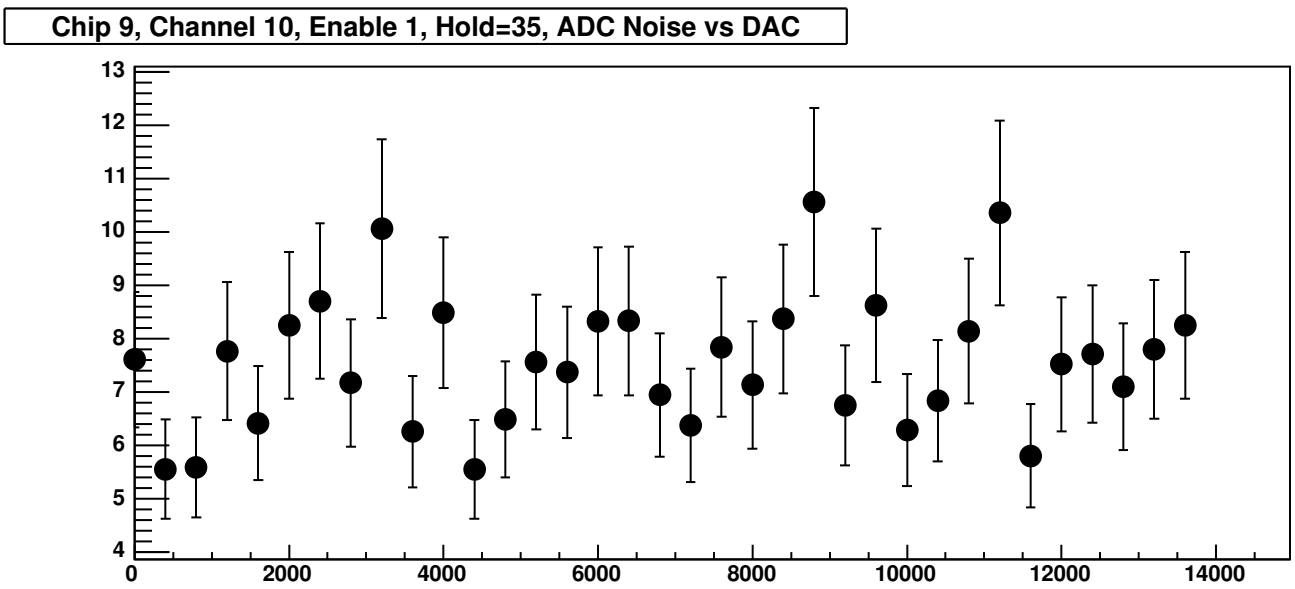

Chip 9, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

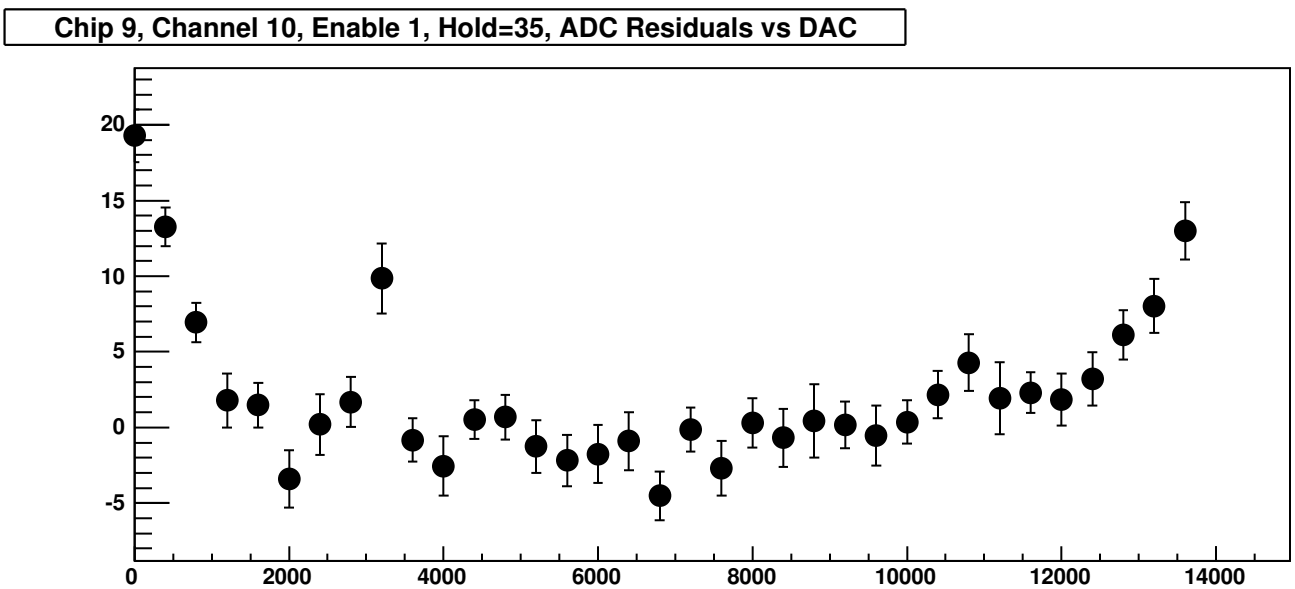

Chip 9, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1300 / 23
p0 2.957e+04 ± 26.38
p1 0.04062 ± 0.003649
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

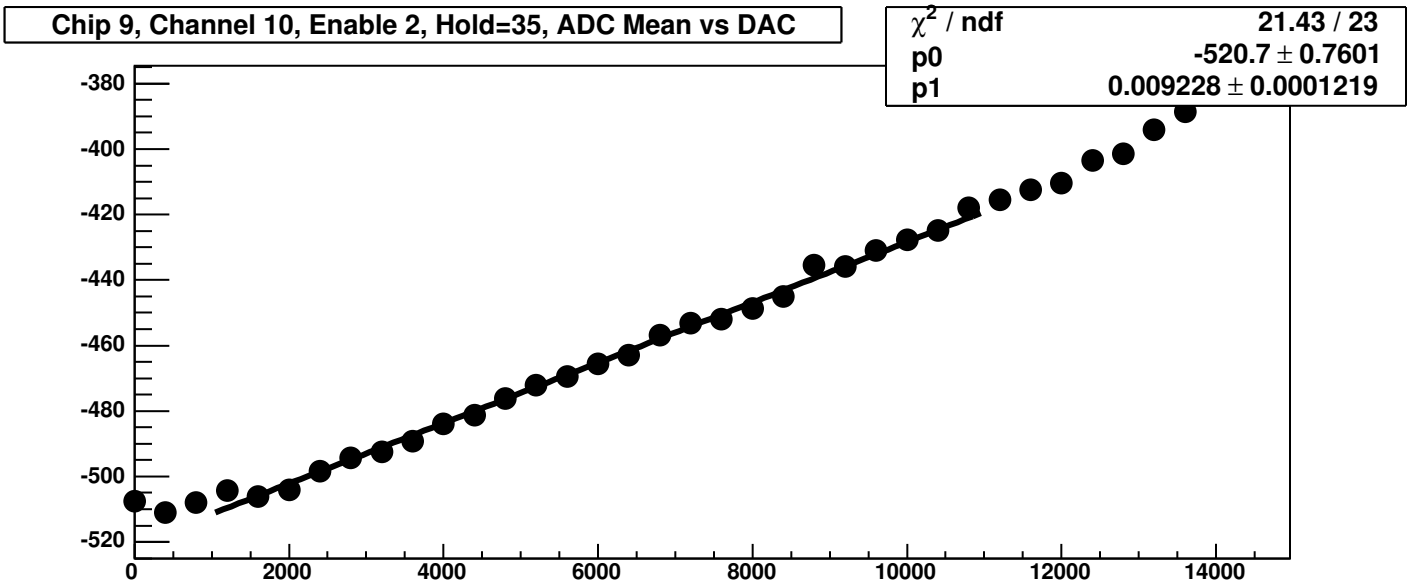

Chip 9, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

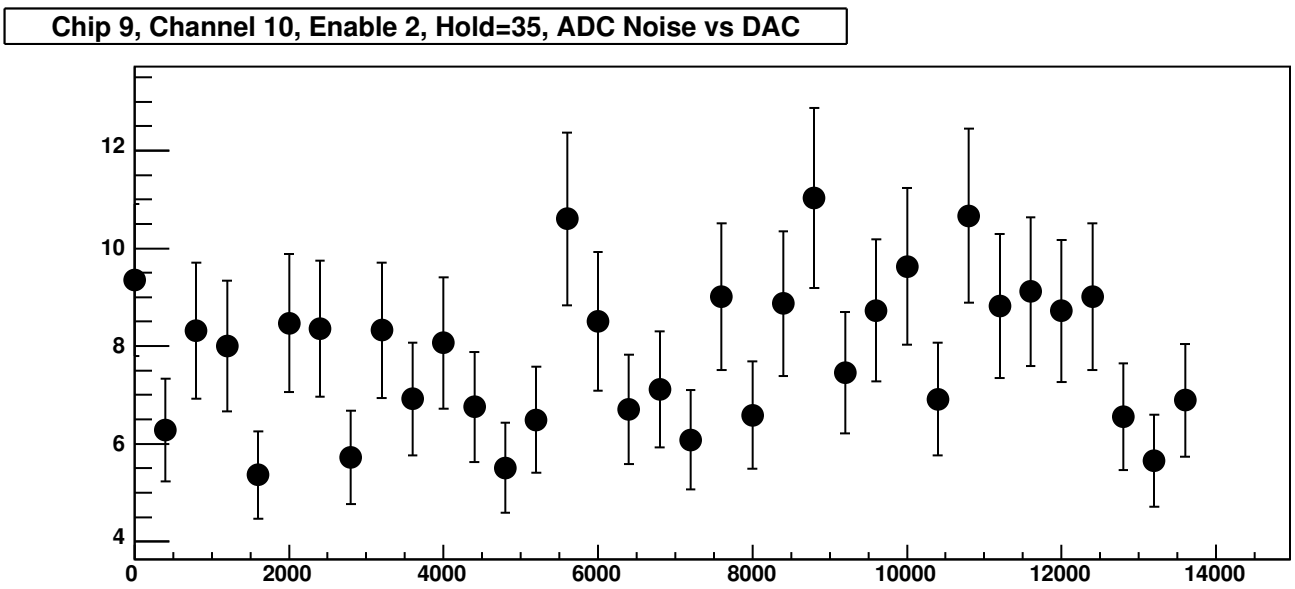

Chip 9, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

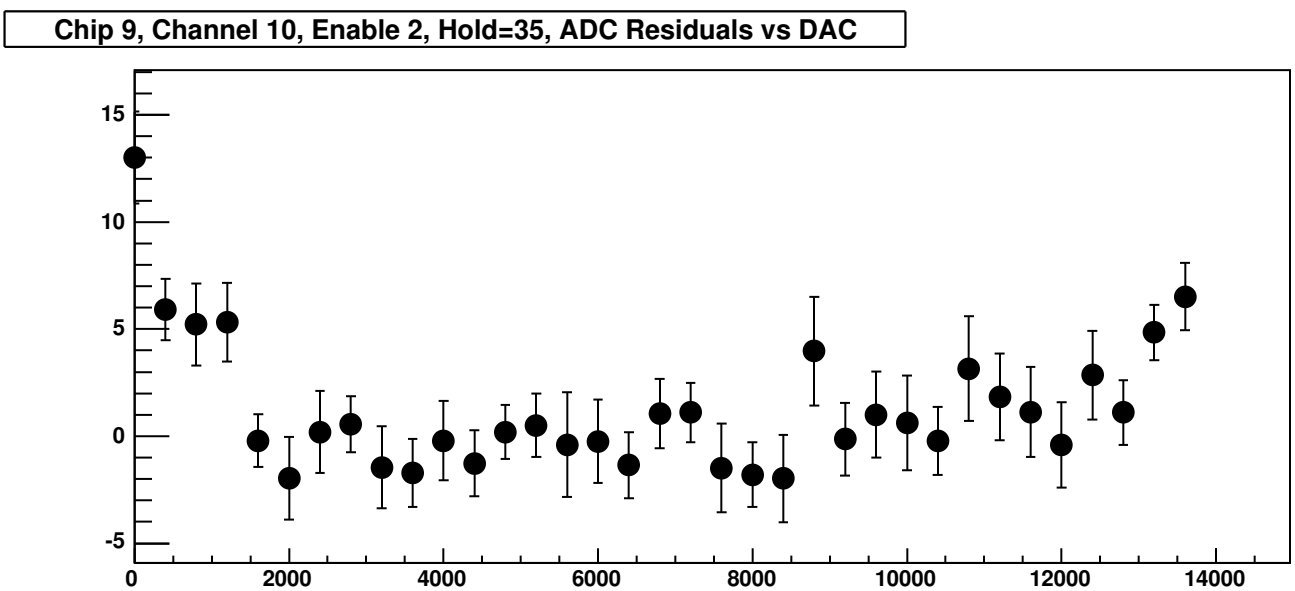

Chip 9, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 48 / 23
p0 -521.1 ± 0.7843
p1 0.01471 ± 0.0001184
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

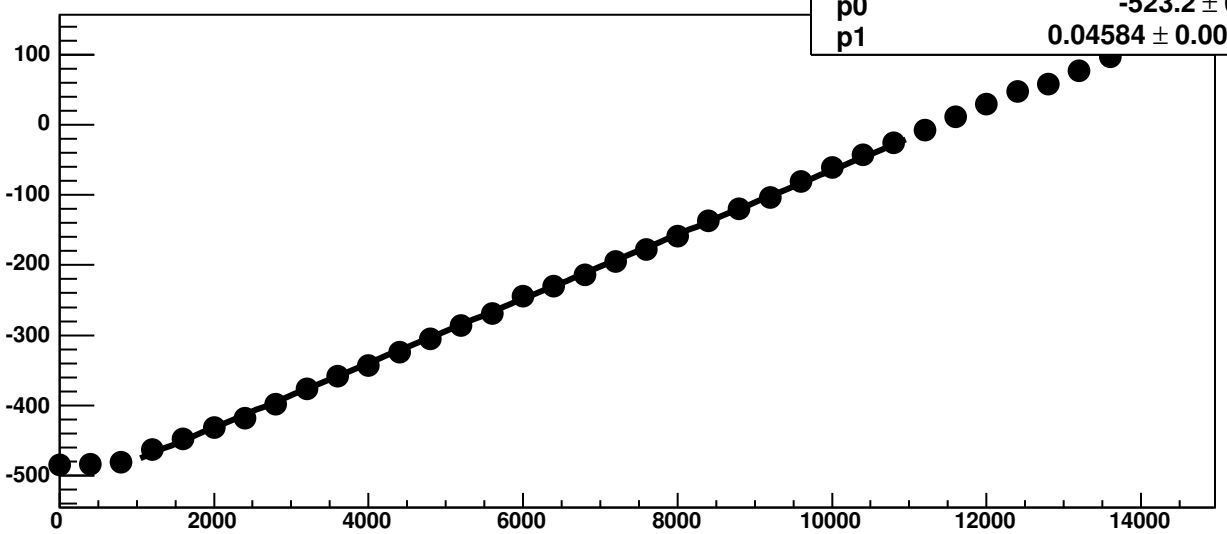

Chip 9, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 21.43 / 23
p0 -520.7 ± 0.7601
p1 0.009228 ± 0.0001219
-380
-400
-420
-440
-460
-480
-500
-520
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

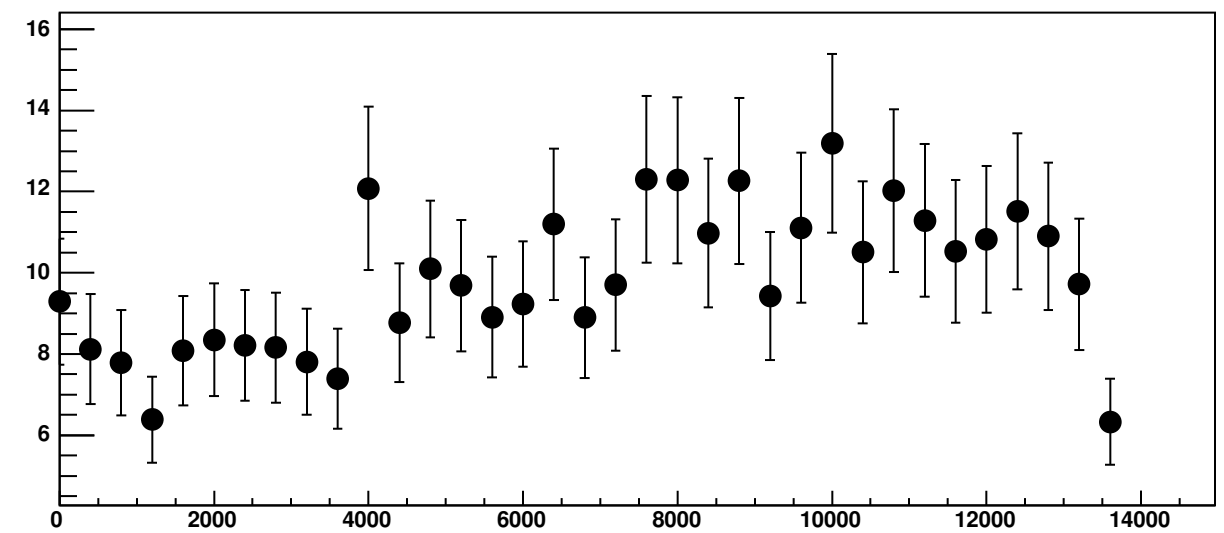

Chip 9, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

Chip 9, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC

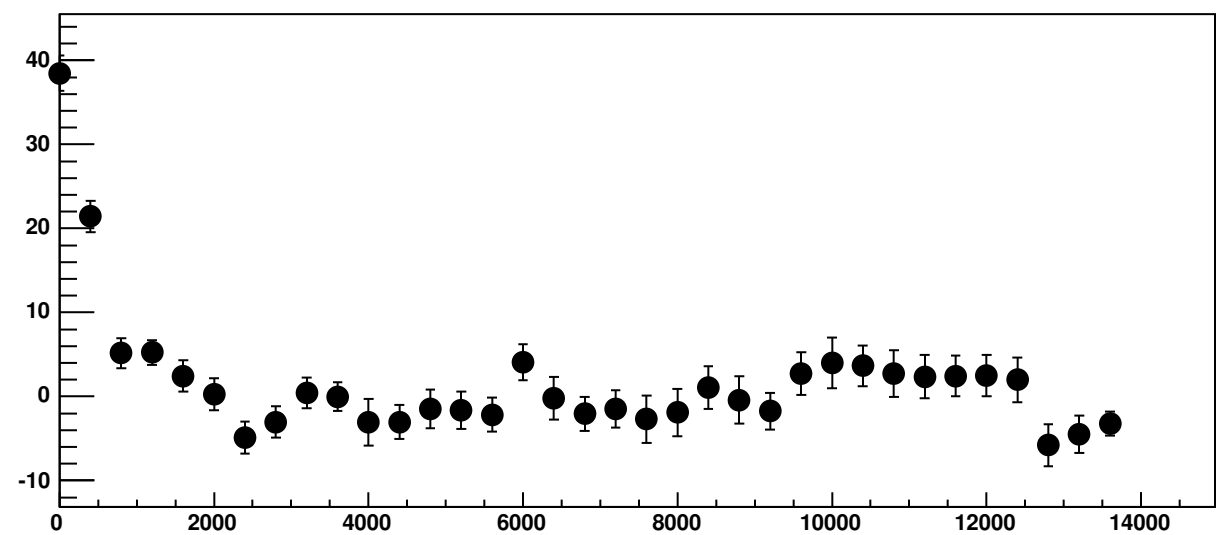
χ^2 / ndf	43.28 / 23
p0	-523.2 ± 0.8801
p1	0.04584 ± 0.0001493

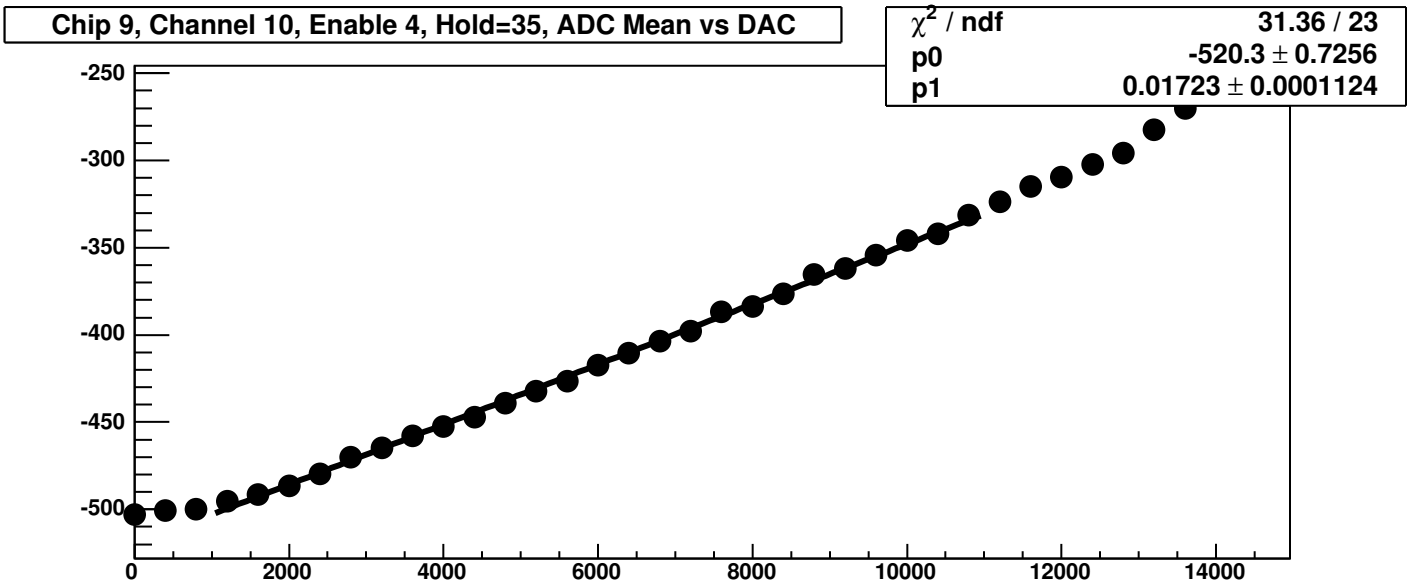


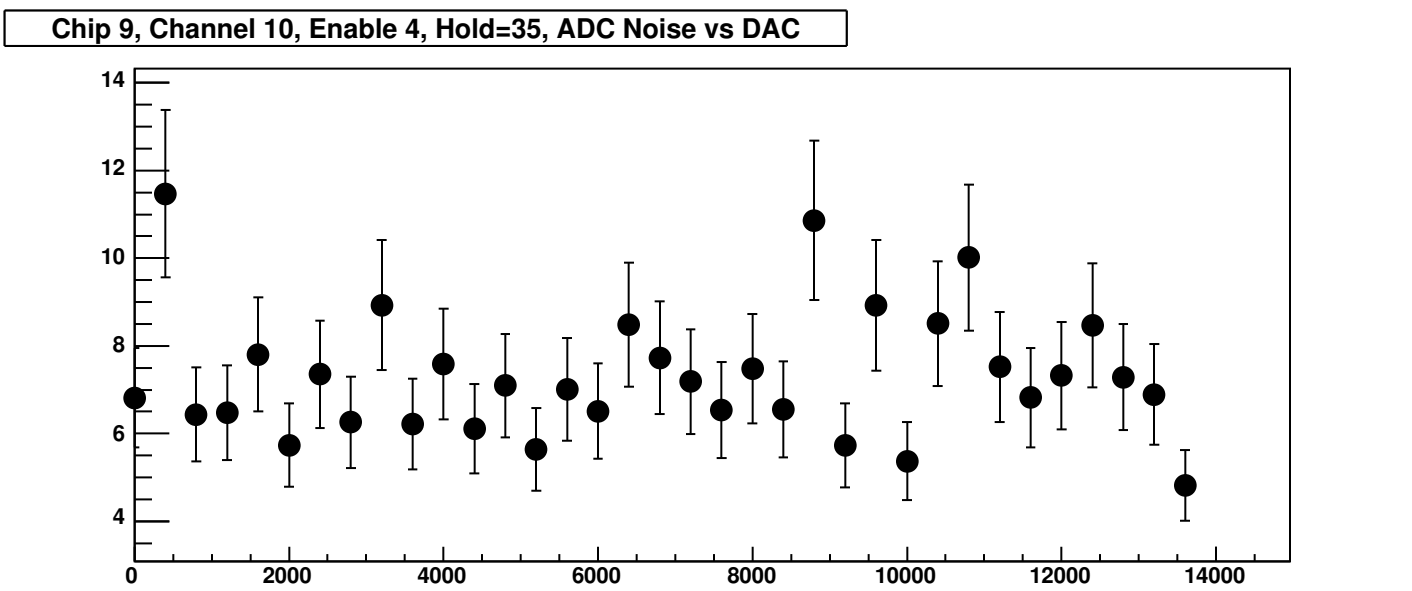
Chip 9, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC

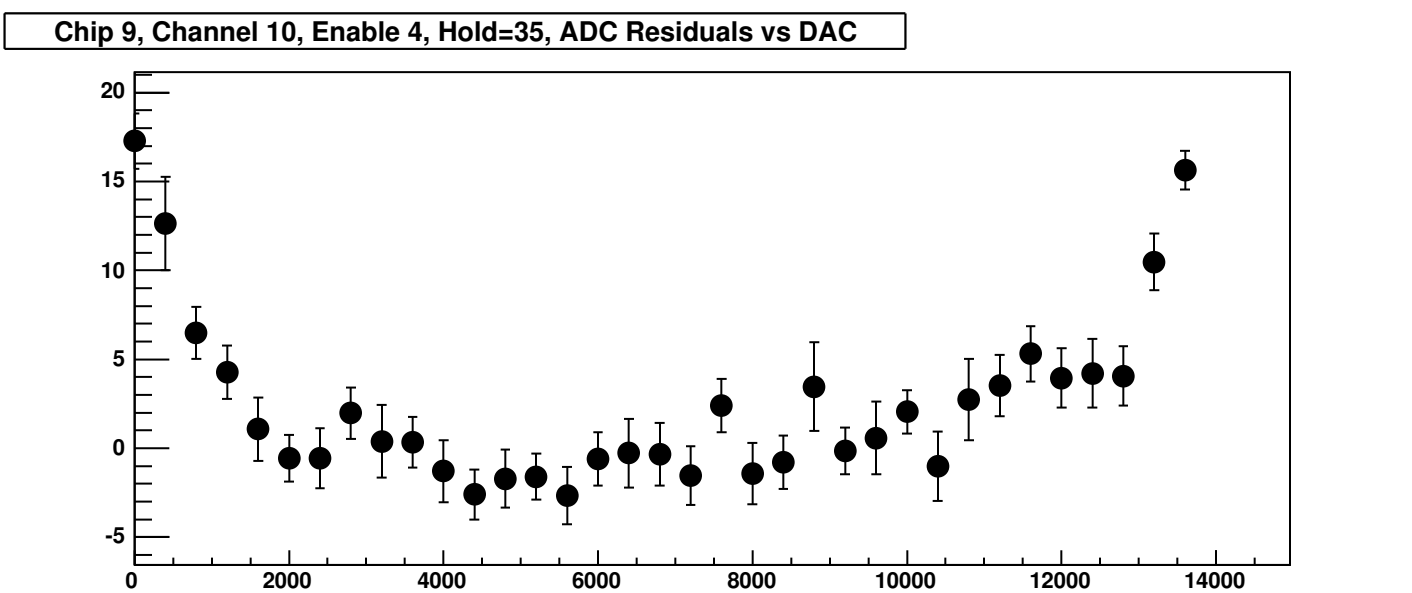


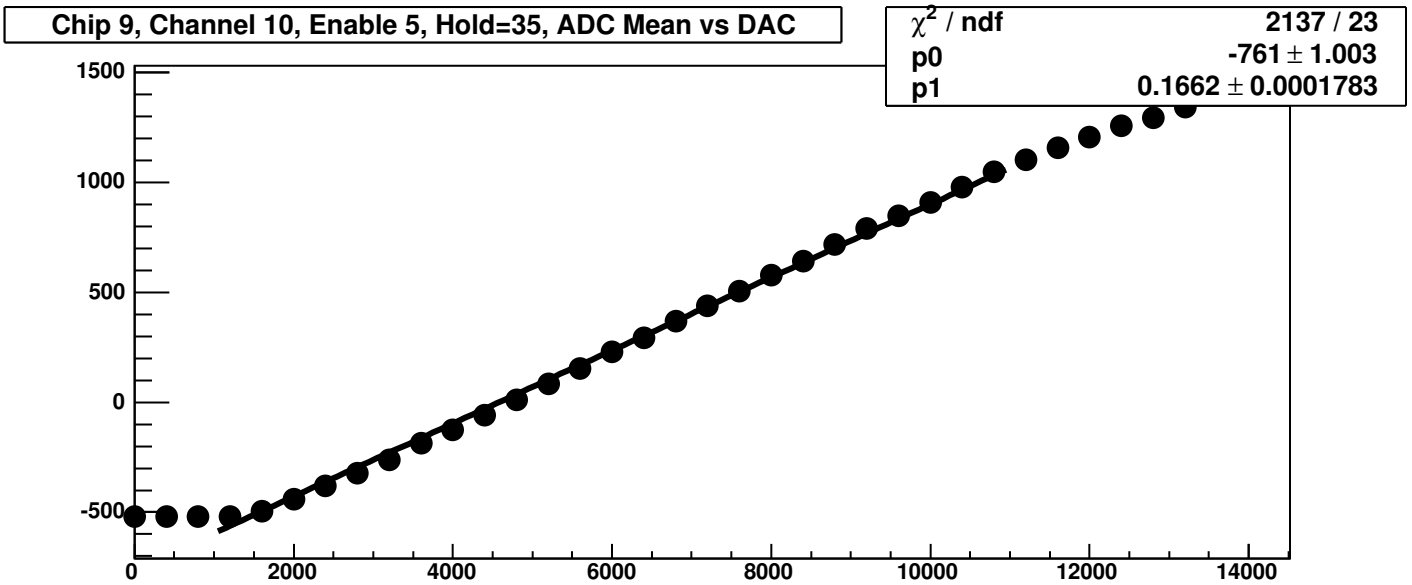
Chip 9, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC

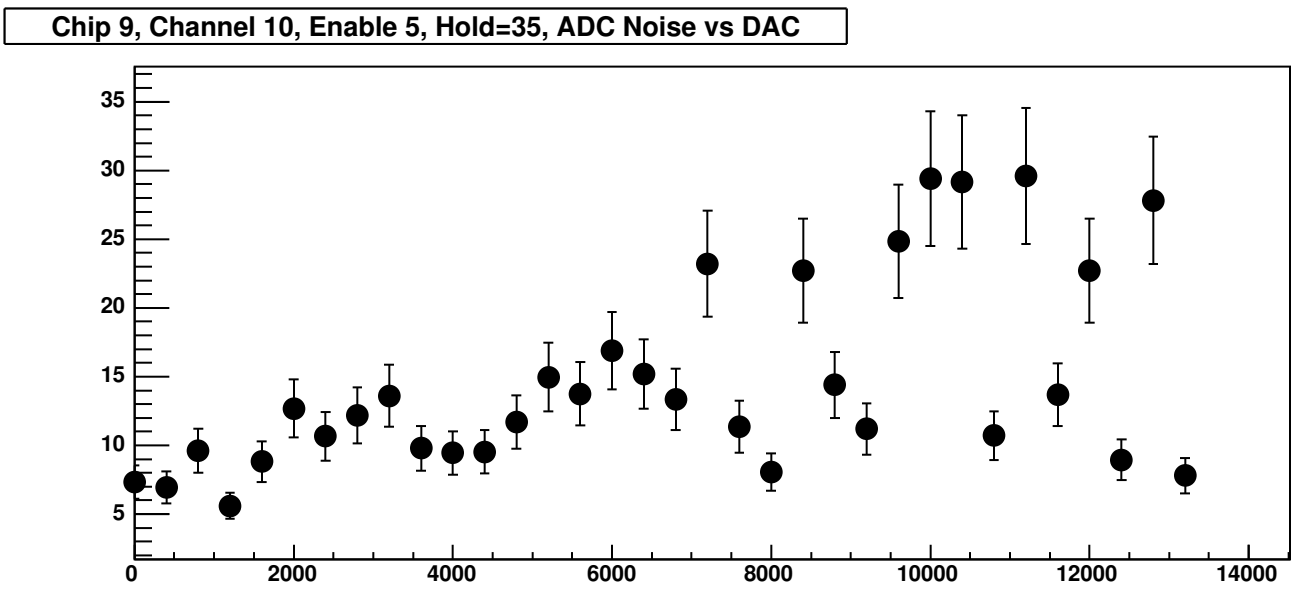


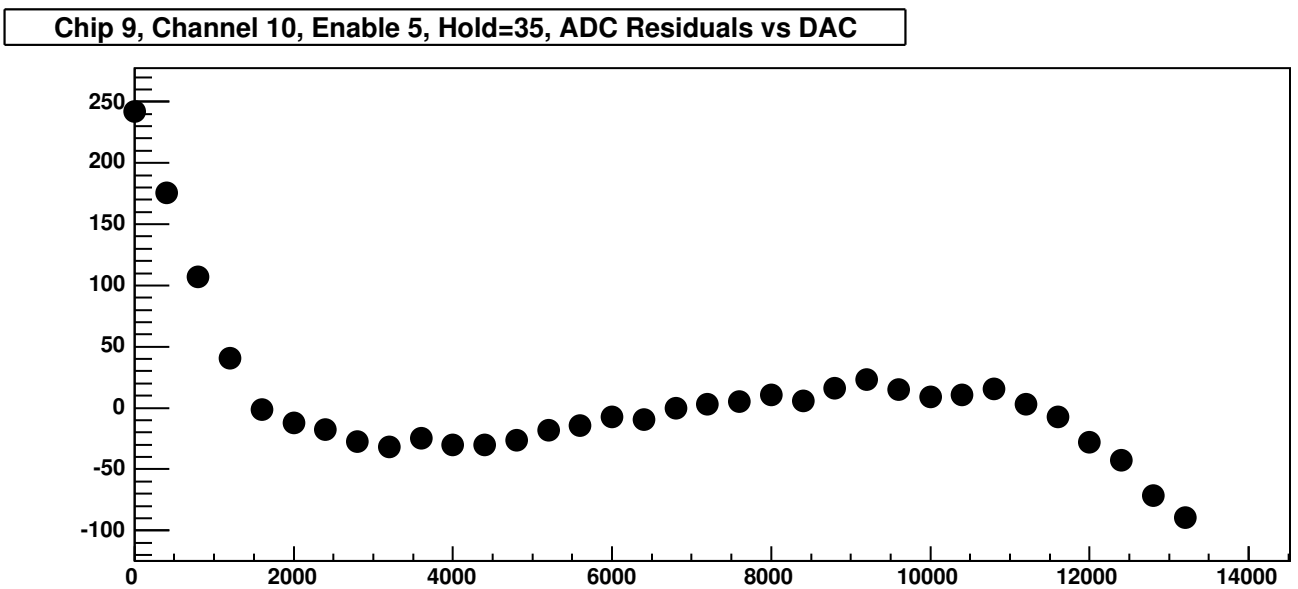

Chip 9, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 31.36 / 23
p0 -520.3 ± 0.7256
p1 0.01723 ± 0.0001124
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

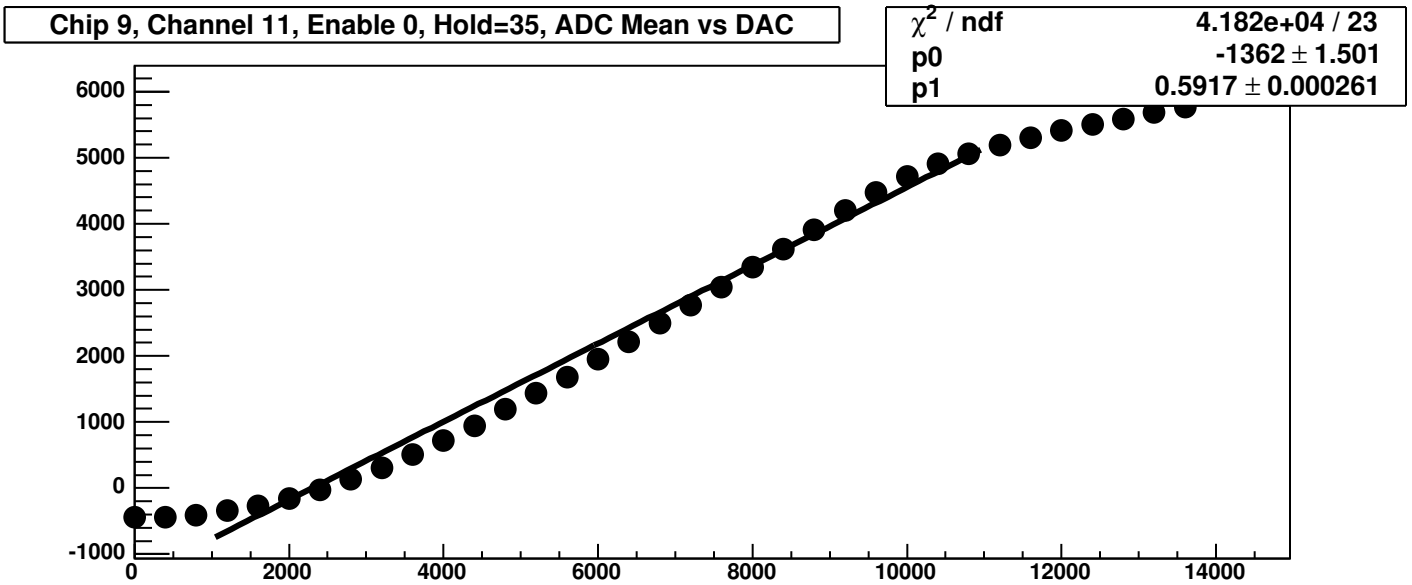

Chip 9, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

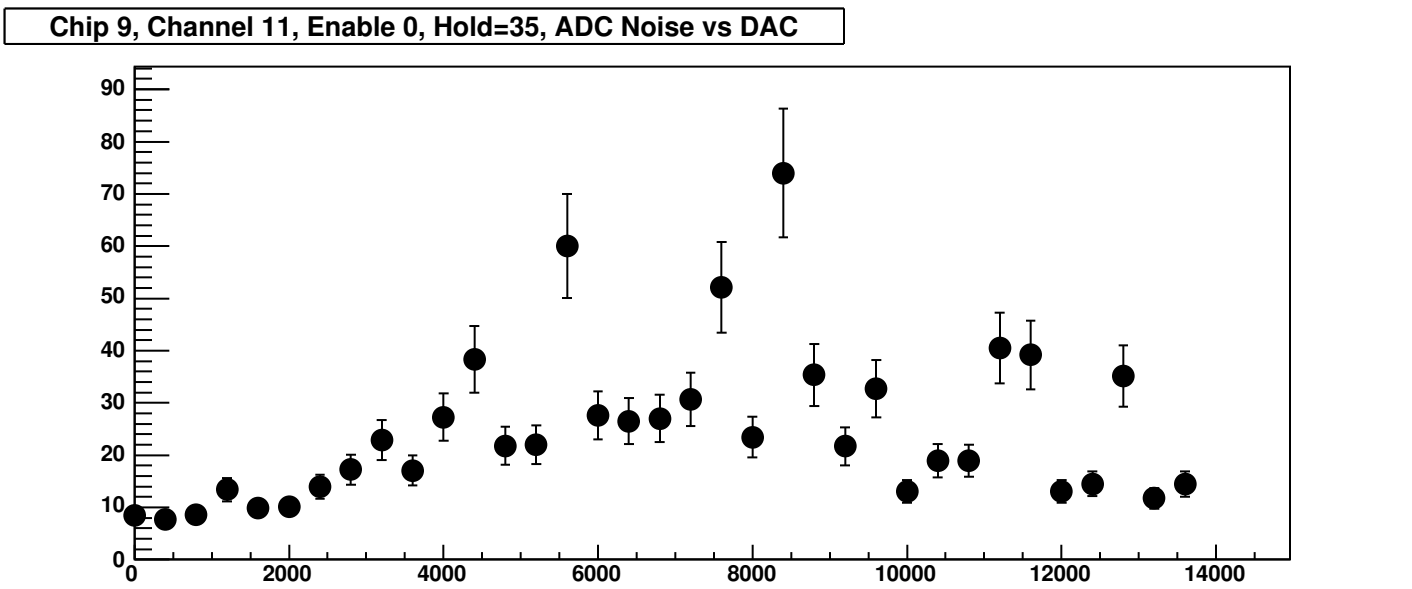

Chip 9, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

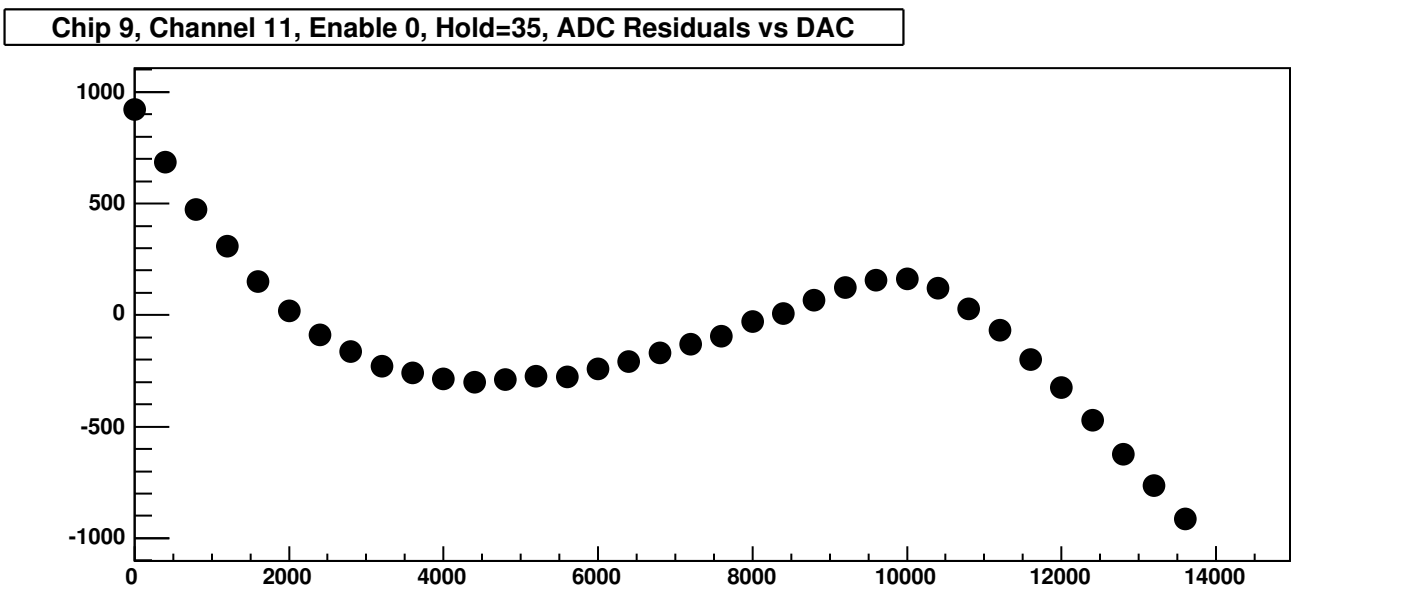

Chip 9, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2137 / 23
p0 -761 ± 1.003
p1 0.1662 ± 0.0001783
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

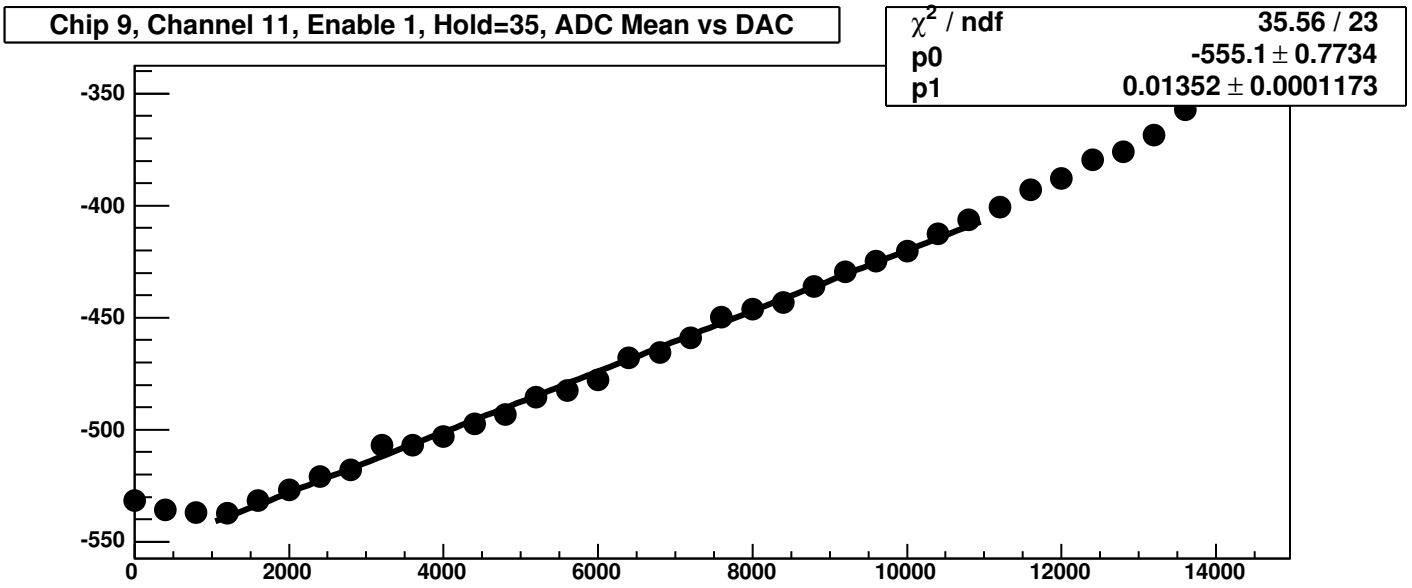

Chip 9, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

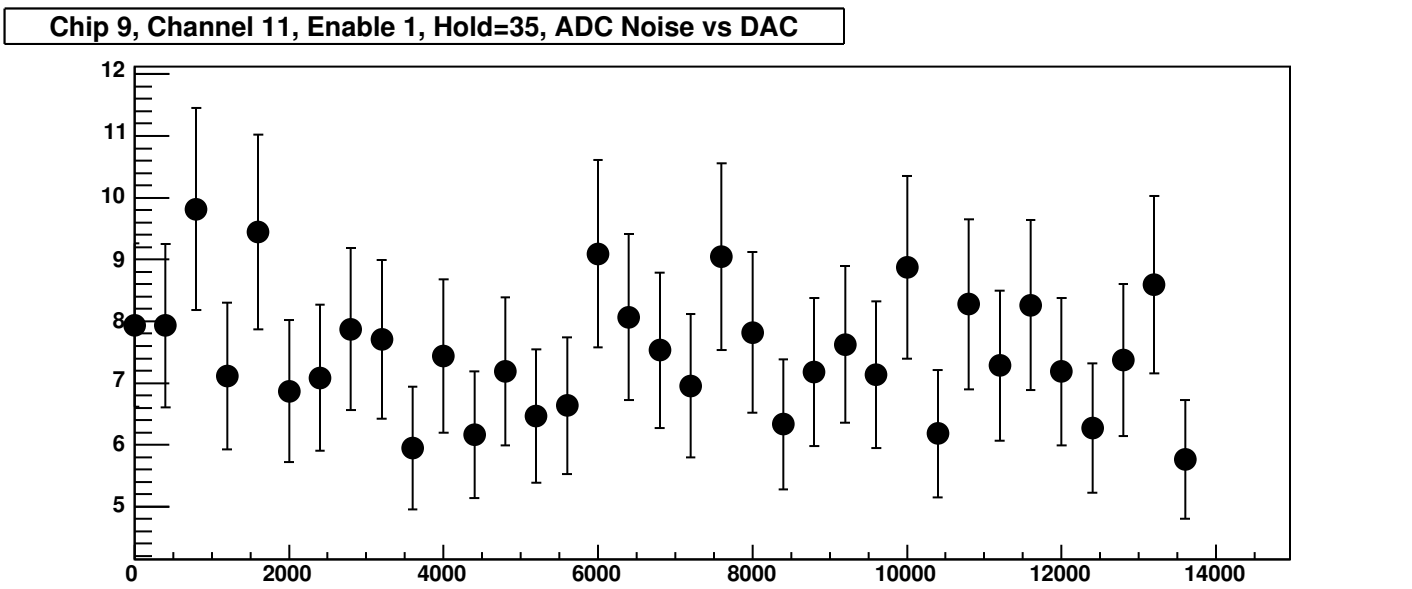

Chip 9, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

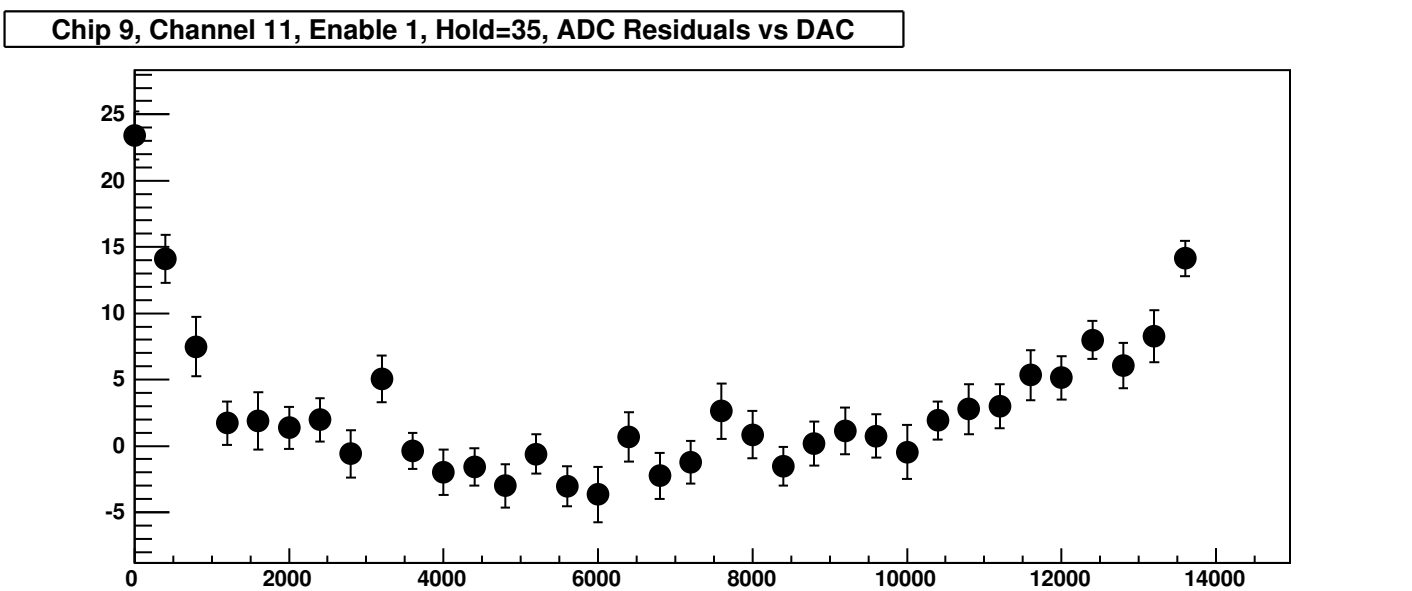

Chip 9, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.182e+04 / 23
p0 -1362 ± 1.501
p1 0.5917 ± 0.000261
6000
5000
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

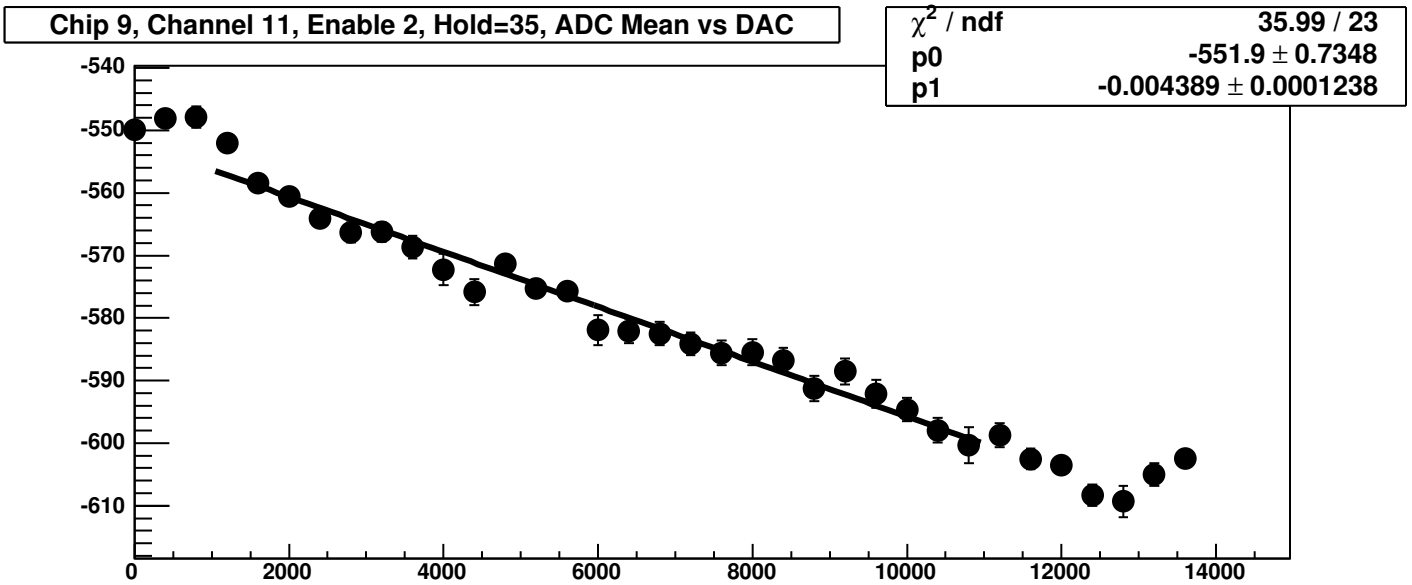

Chip 9, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

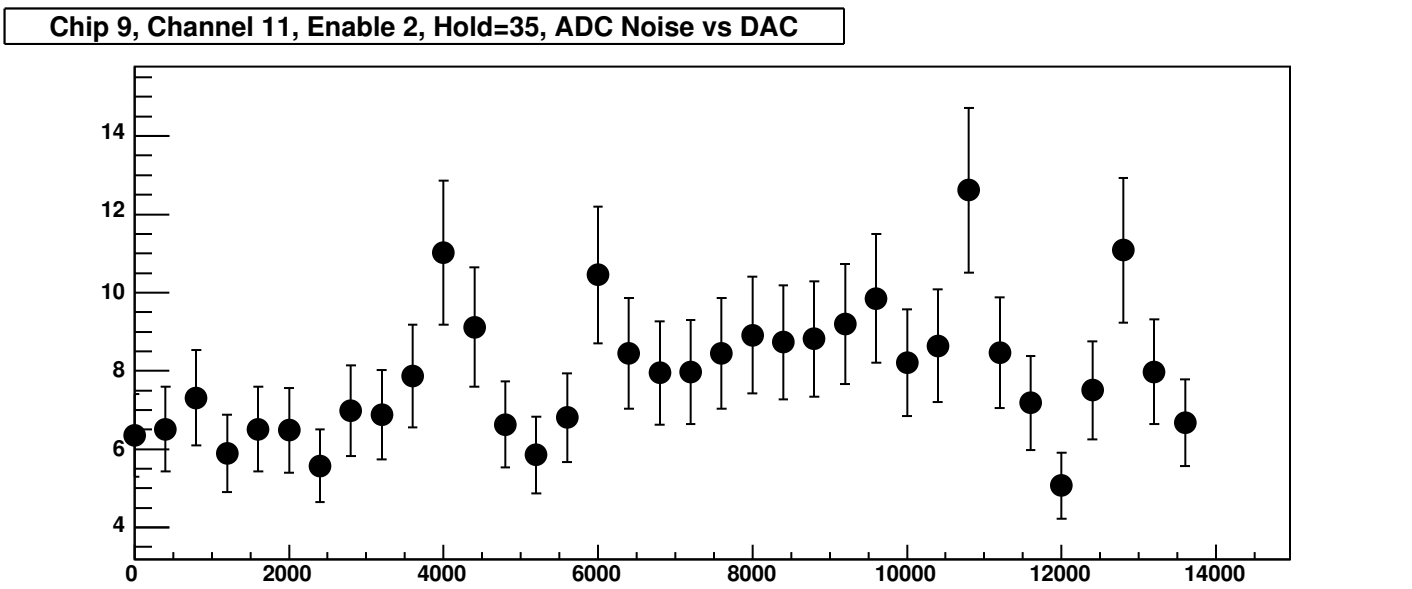

Chip 9, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

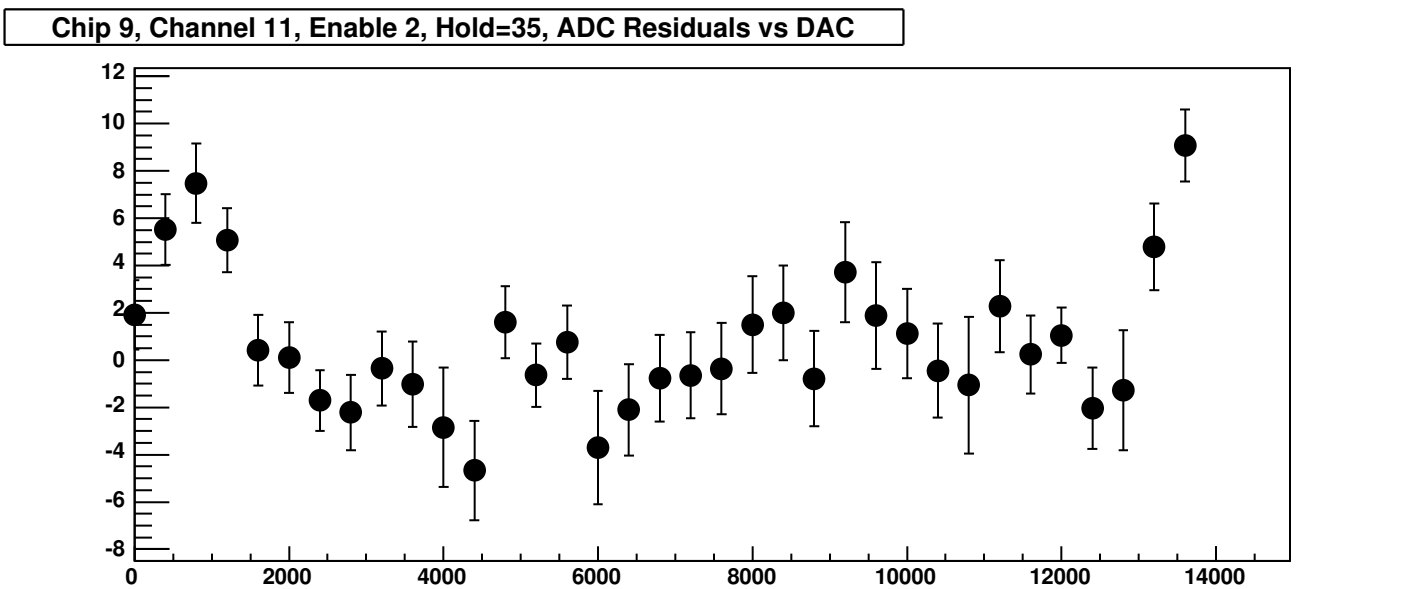

Chip 9, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 35.56 / 23
p0 -555.1 ± 0.7734
p1 0.01352 ± 0.0001173
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

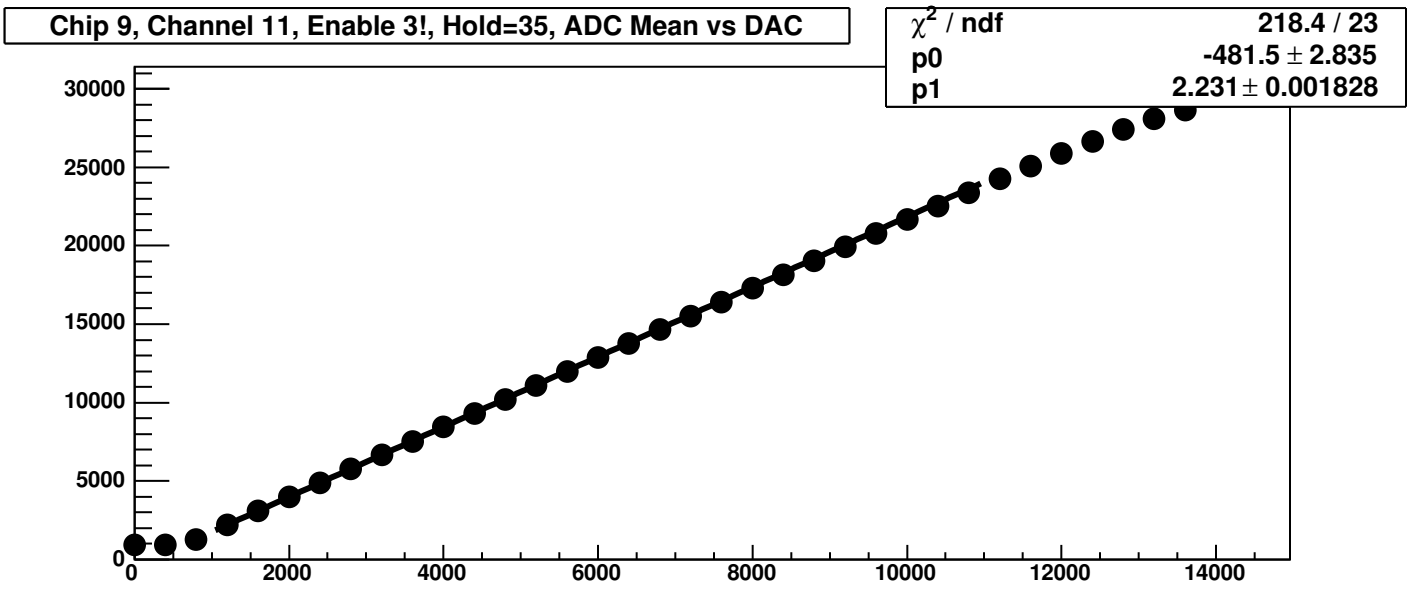

Chip 9, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

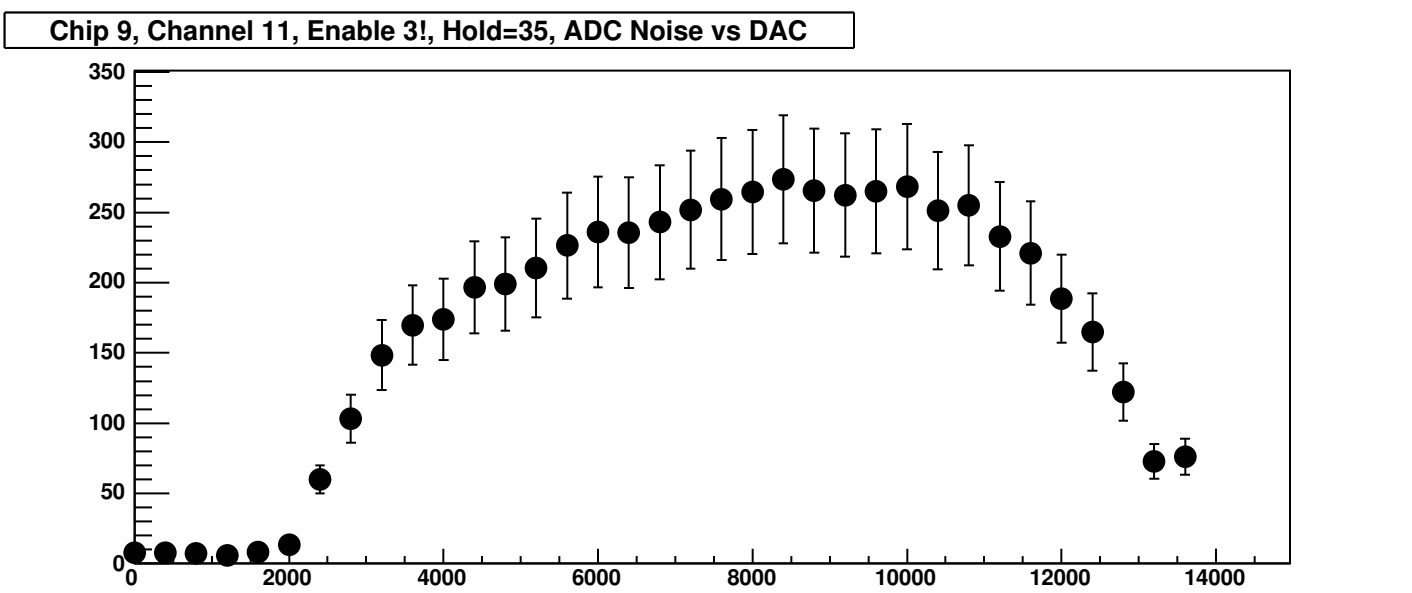

Chip 9, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

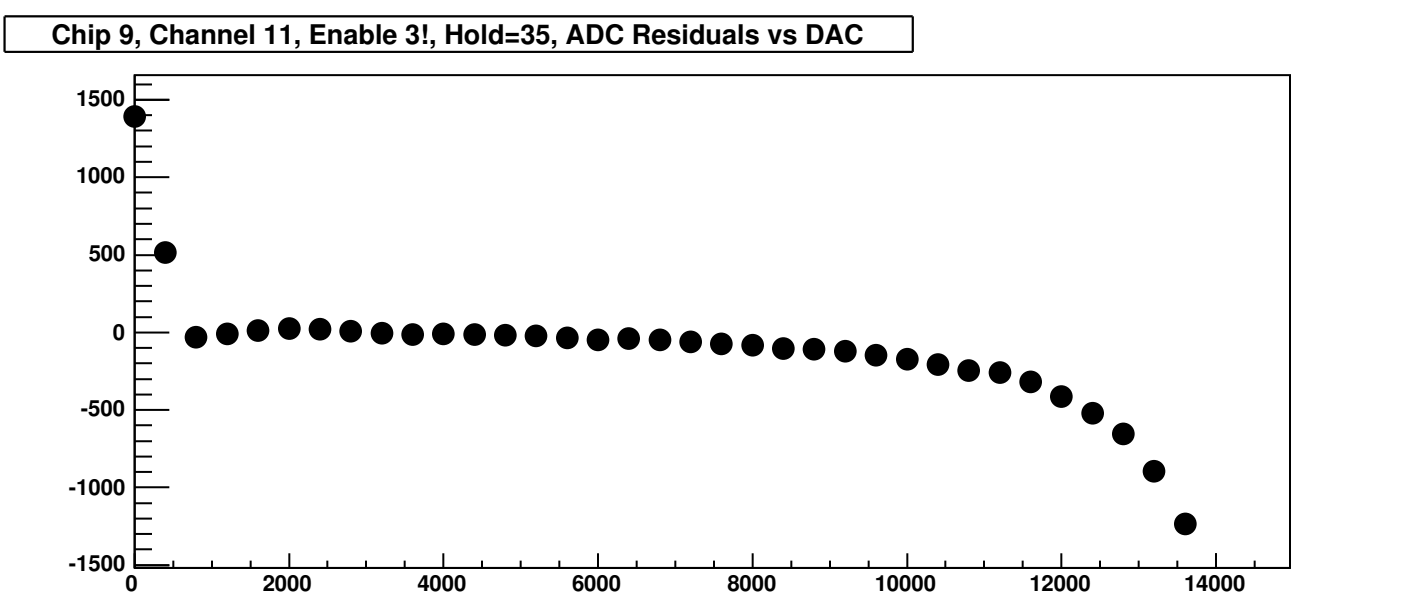

Chip 9, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 35.99 / 23
p0 -551.9 ± 0.7348
p1 -0.004389 ± 0.0001238
-540
-550
-560
-570
-580
-590
-600
-610
0
2000
4000
6000
8000
10000
12000
14000

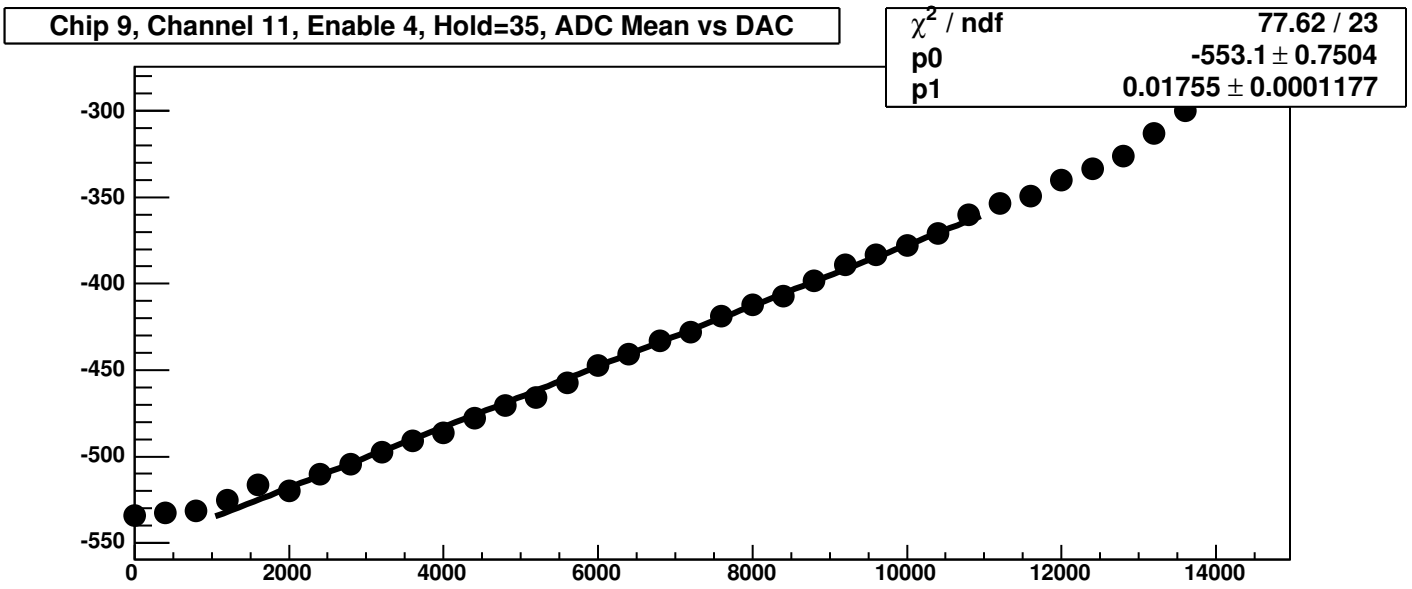

Chip 9, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

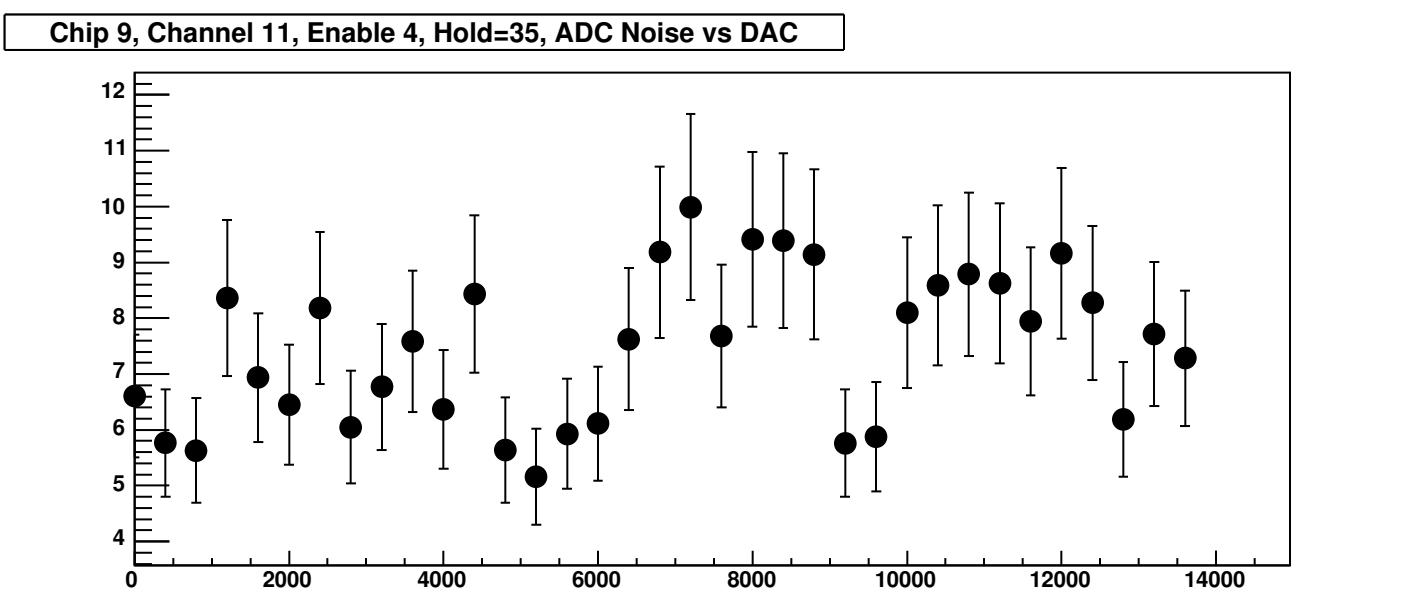

Chip 9, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

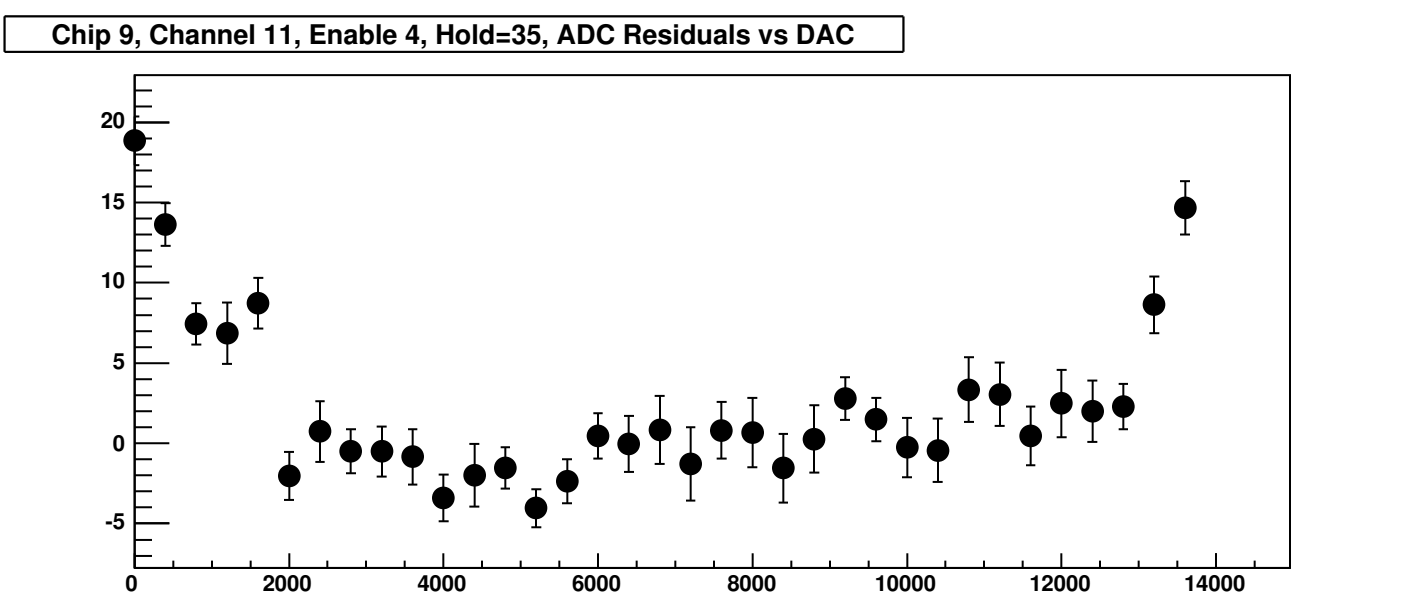

Chip 9, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 218.4 / 23
p0 -481.5 ± 2.835
p1 2.231 ± 0.001828
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

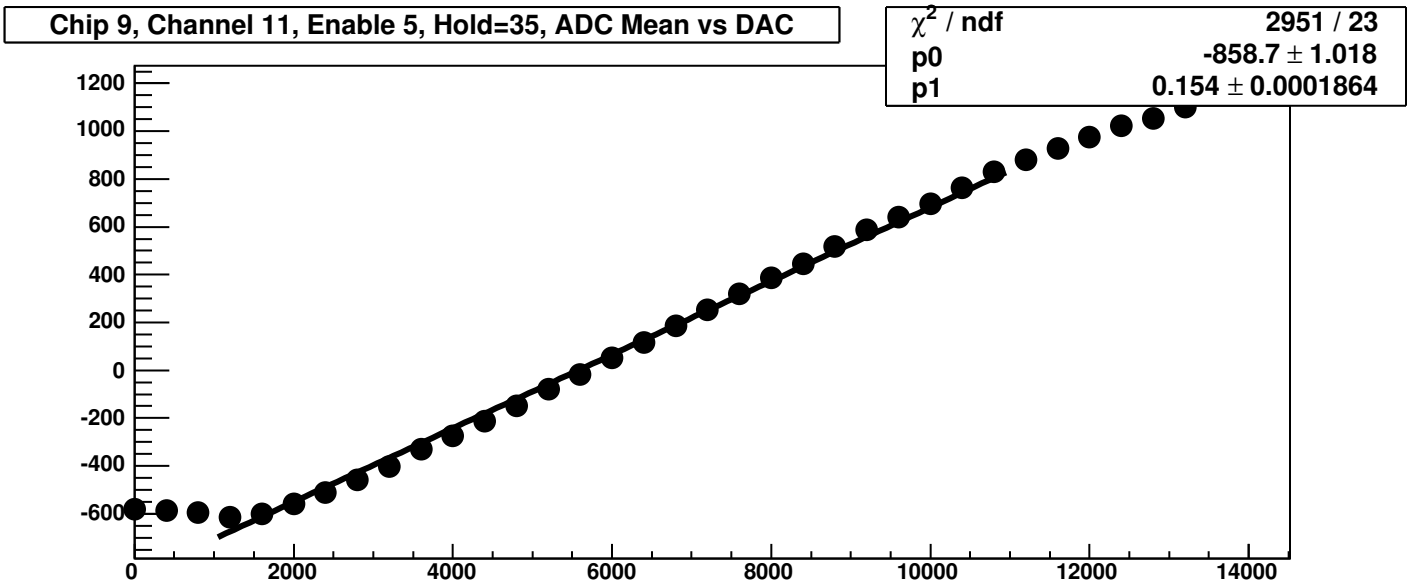

Chip 9, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

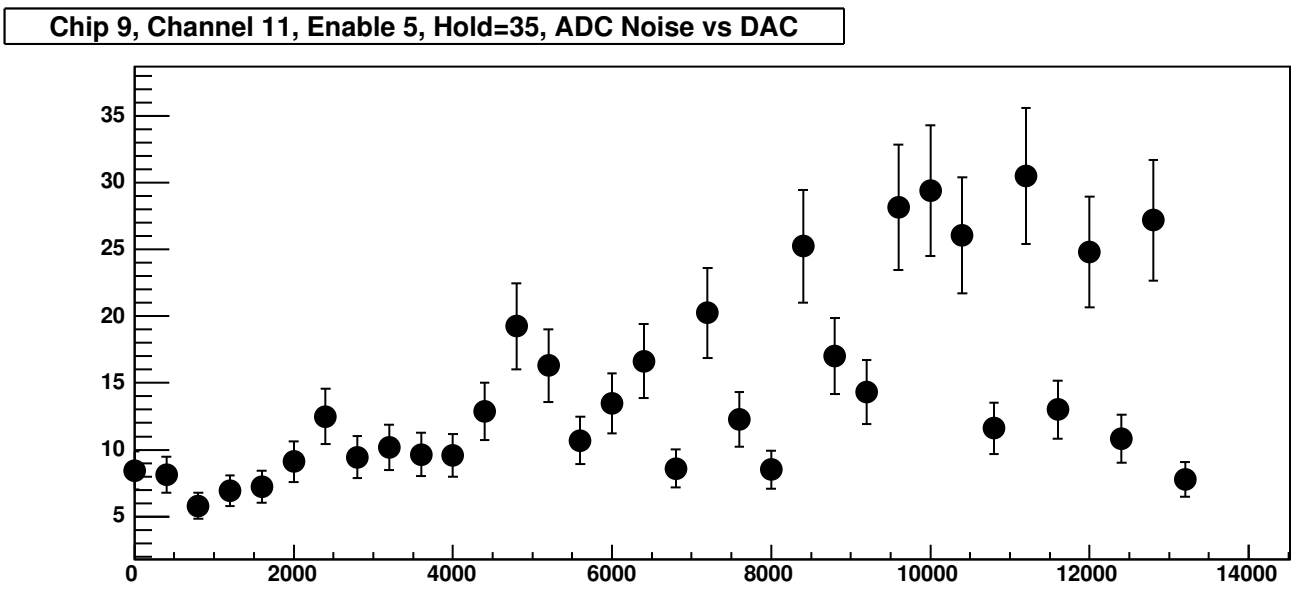

Chip 9, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

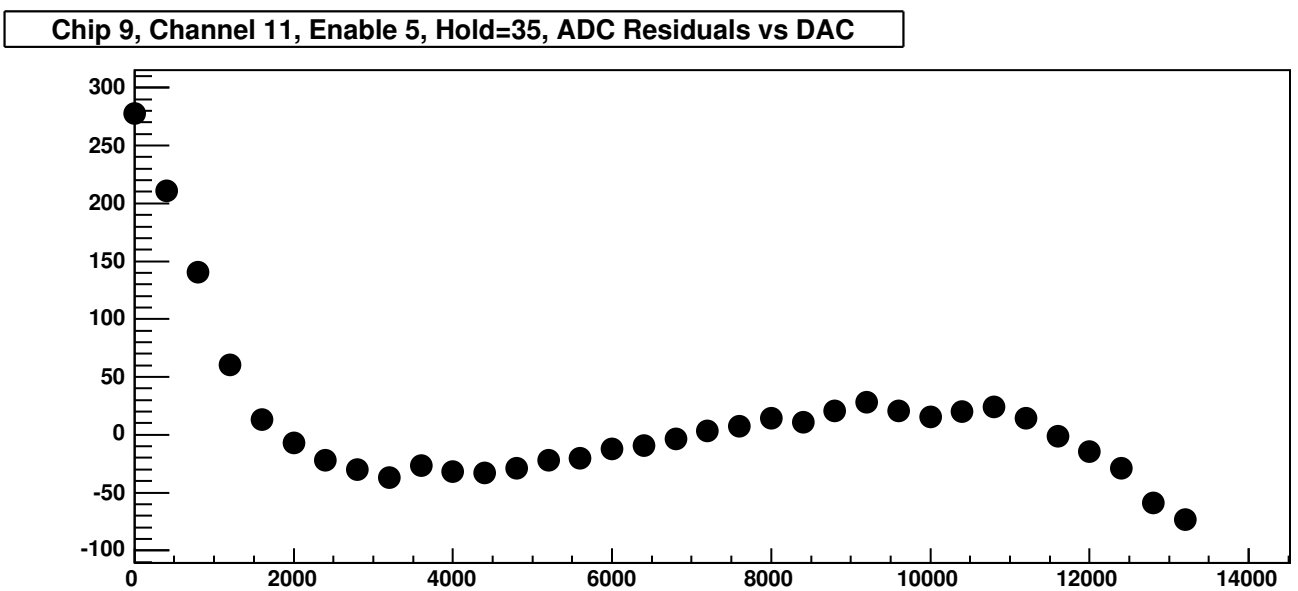

Chip 9, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 77.62 / 23
p0 -553.1 ± 0.7504
p1 0.01755 ± 0.0001177
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

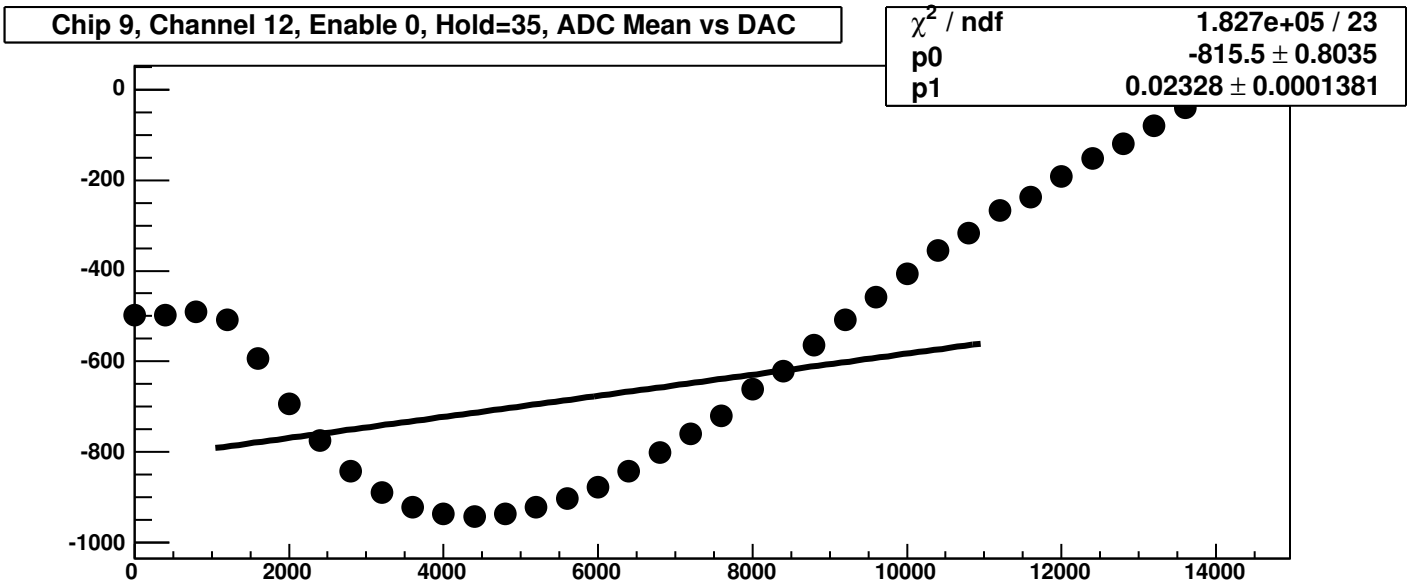

Chip 9, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

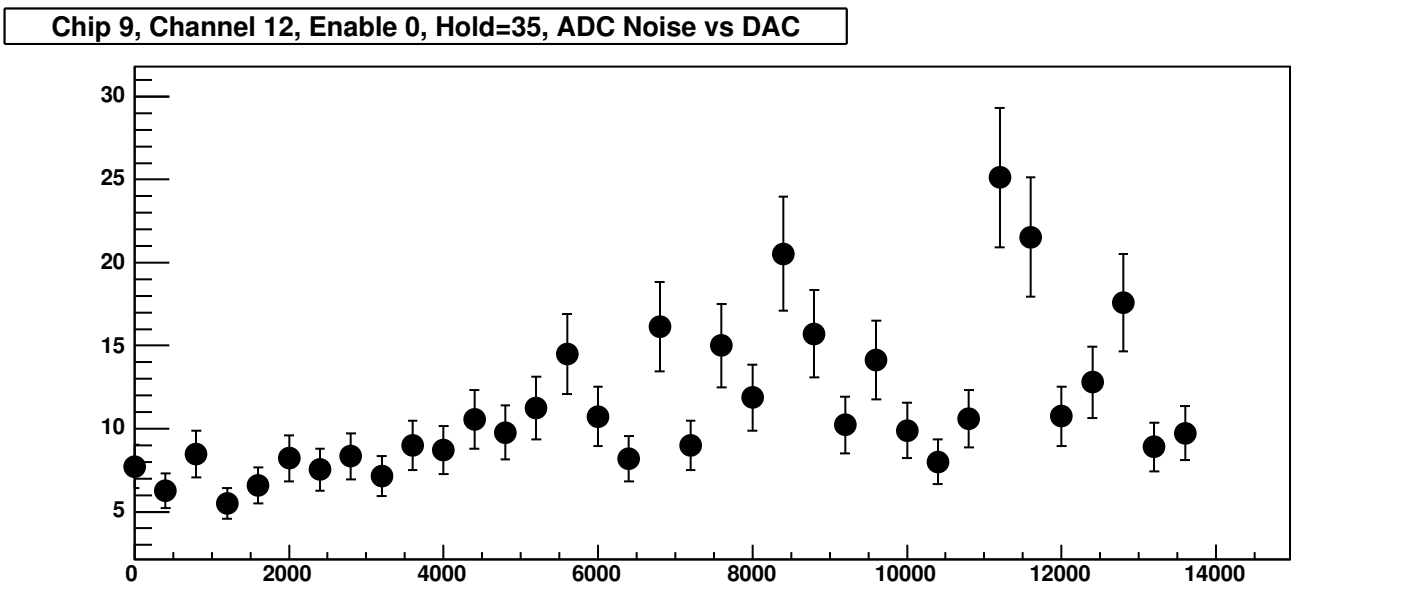

Chip 9, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

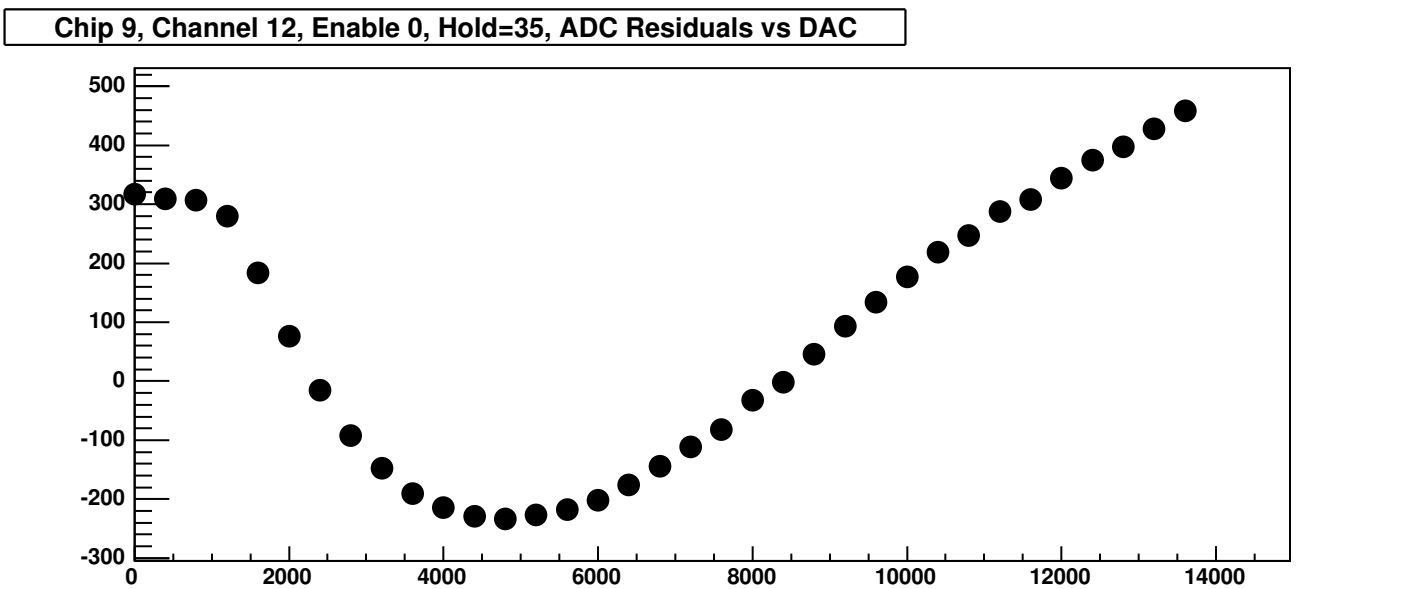

Chip 9, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2951 / 23
p0 -858.7 ± 1.018
p1 0.154 ± 0.0001864
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

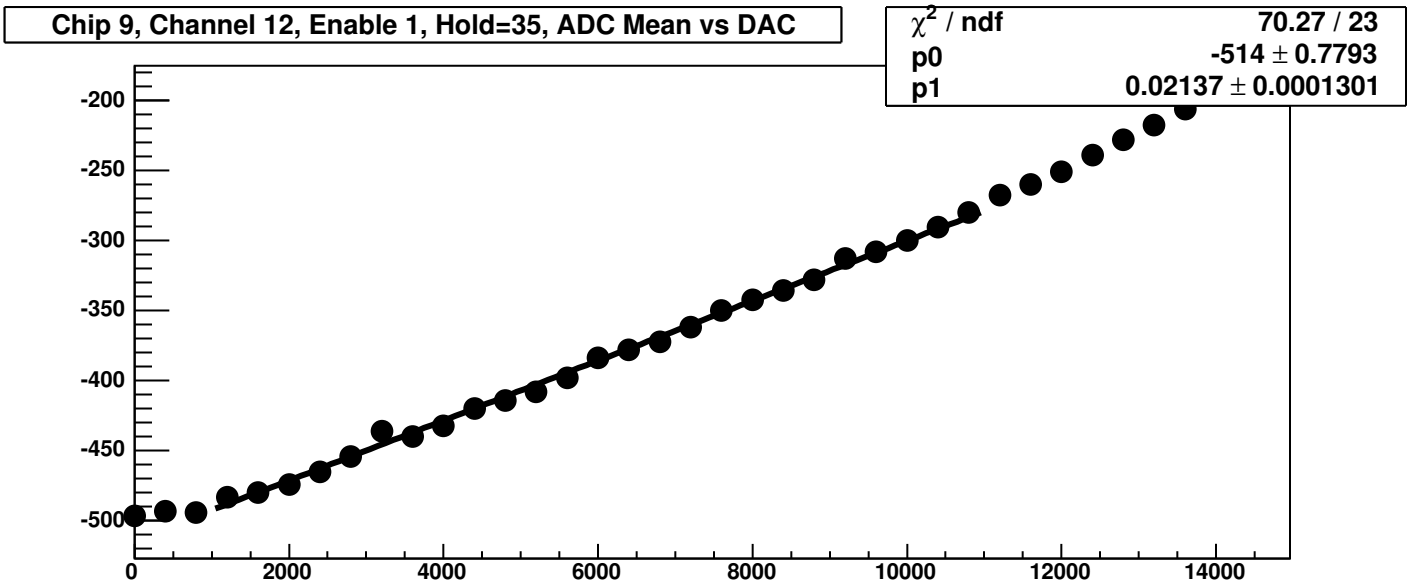

Chip 9, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

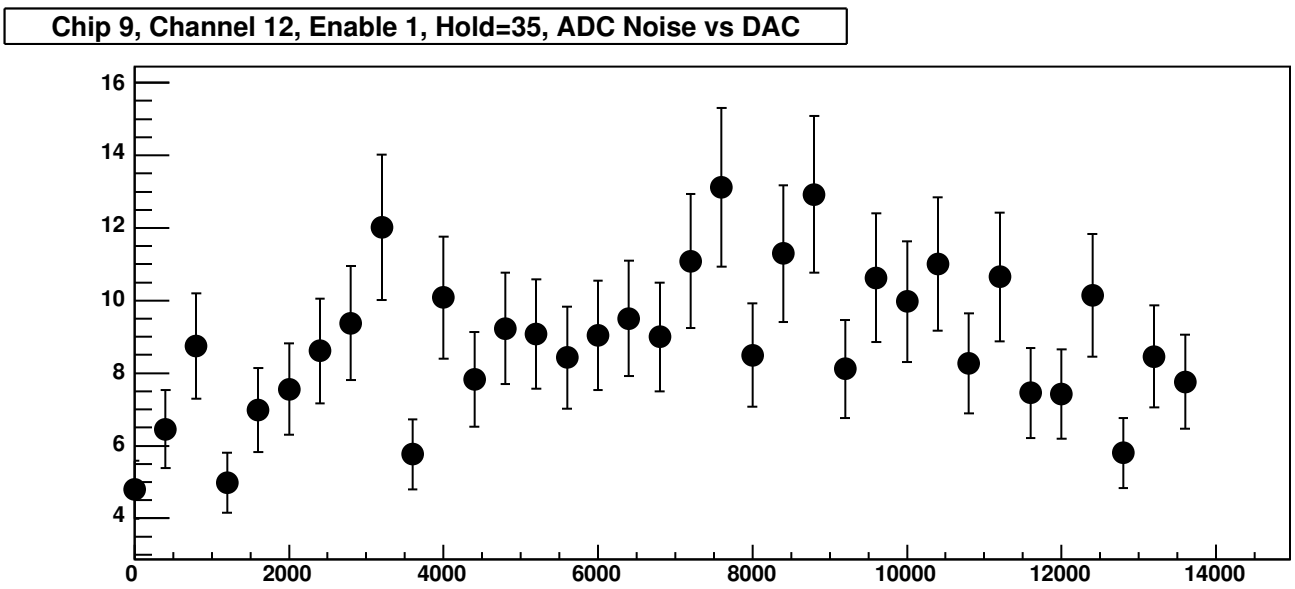

Chip 9, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

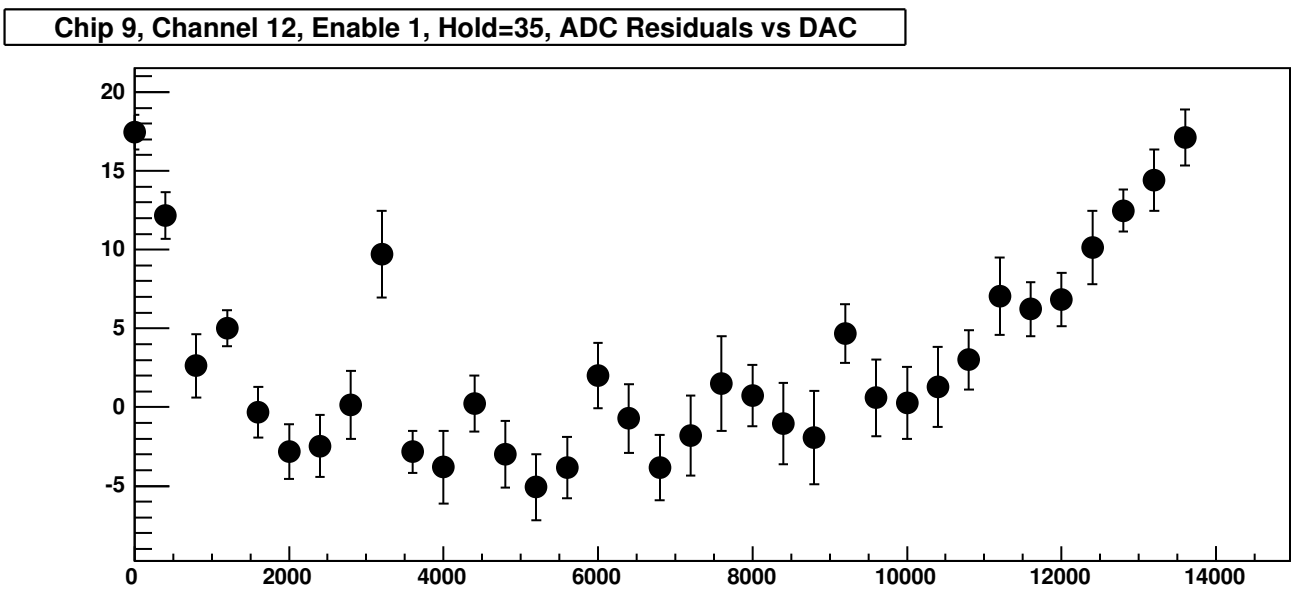

Chip 9, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.827e+05 / 23
p0 -815.5 ± 0.8035
p1 0.02328 ± 0.0001381
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

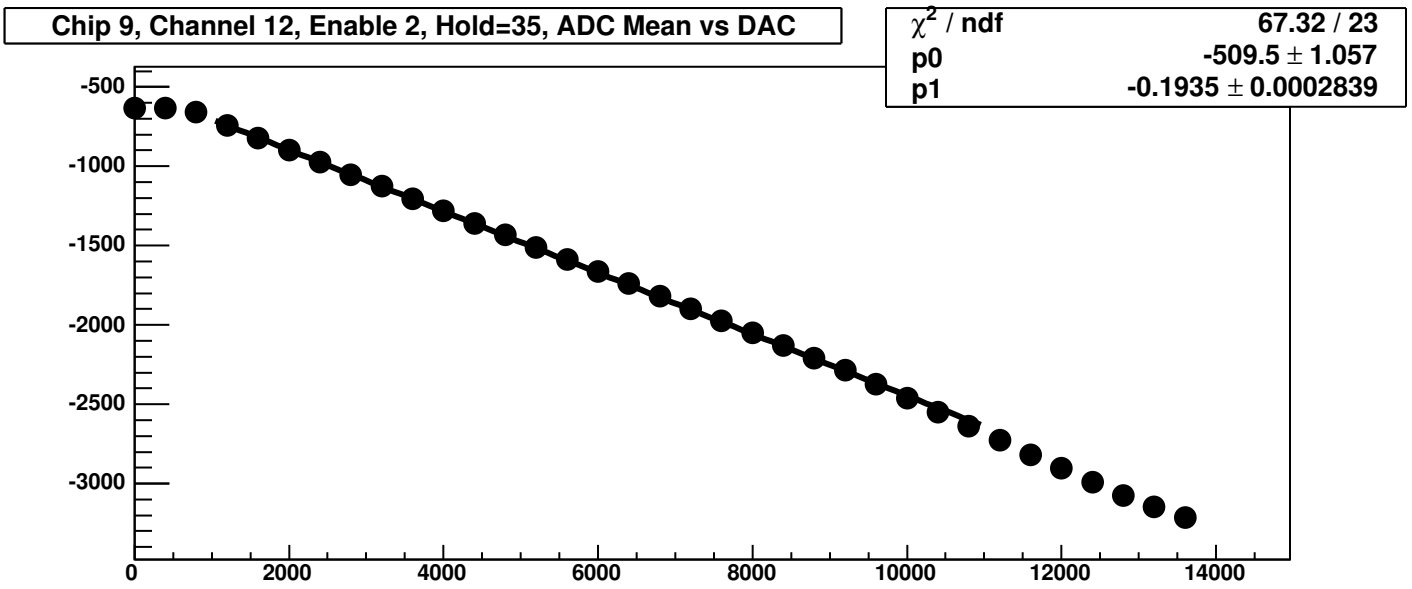

Chip 9, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

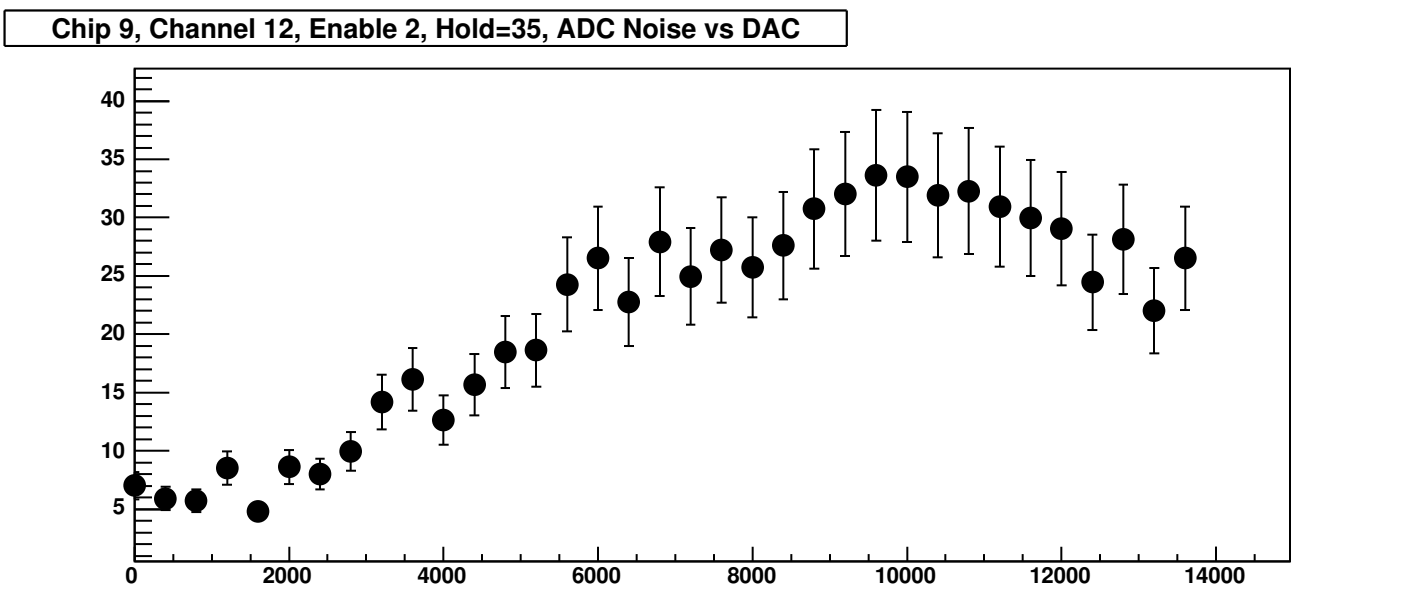

Chip 9, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

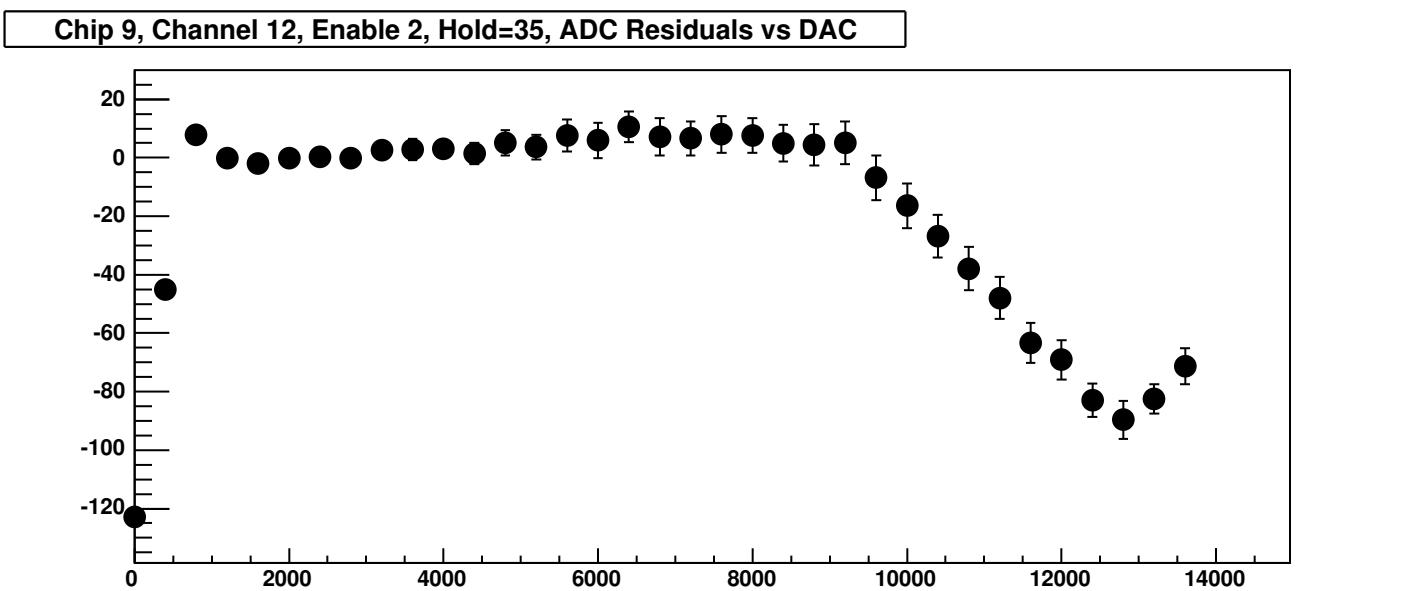

Chip 9, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 70.27 / 23
p0 -514 ± 0.7793
p1 0.02137 ± 0.0001301
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

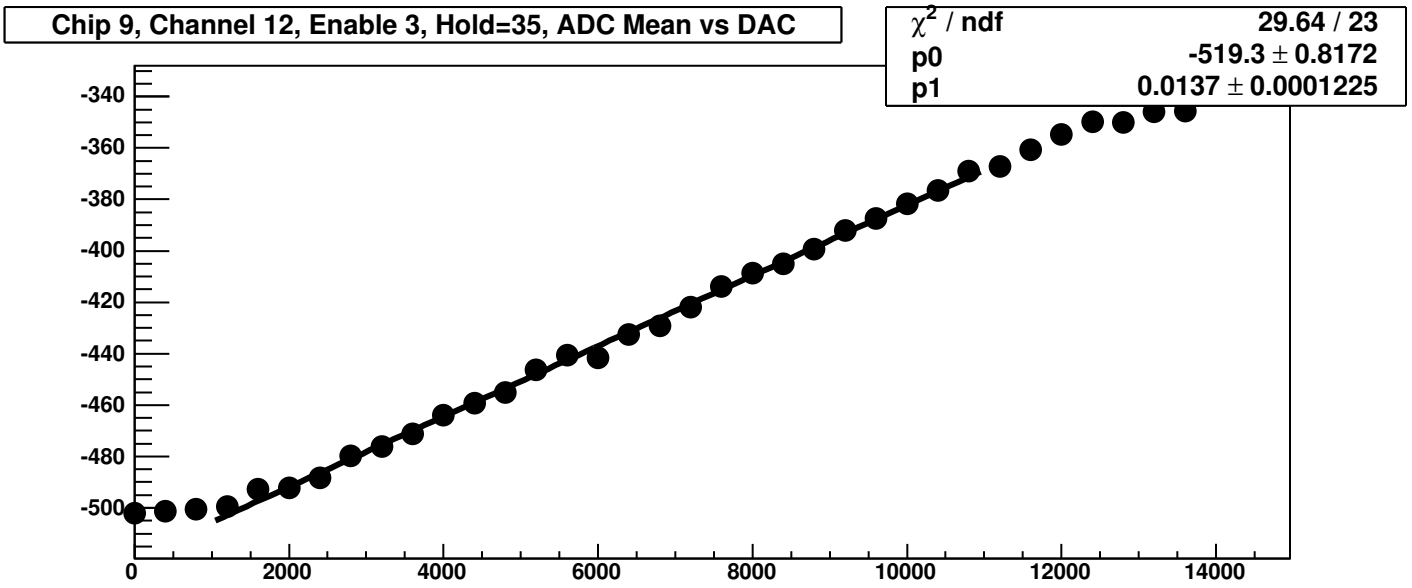

Chip 9, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

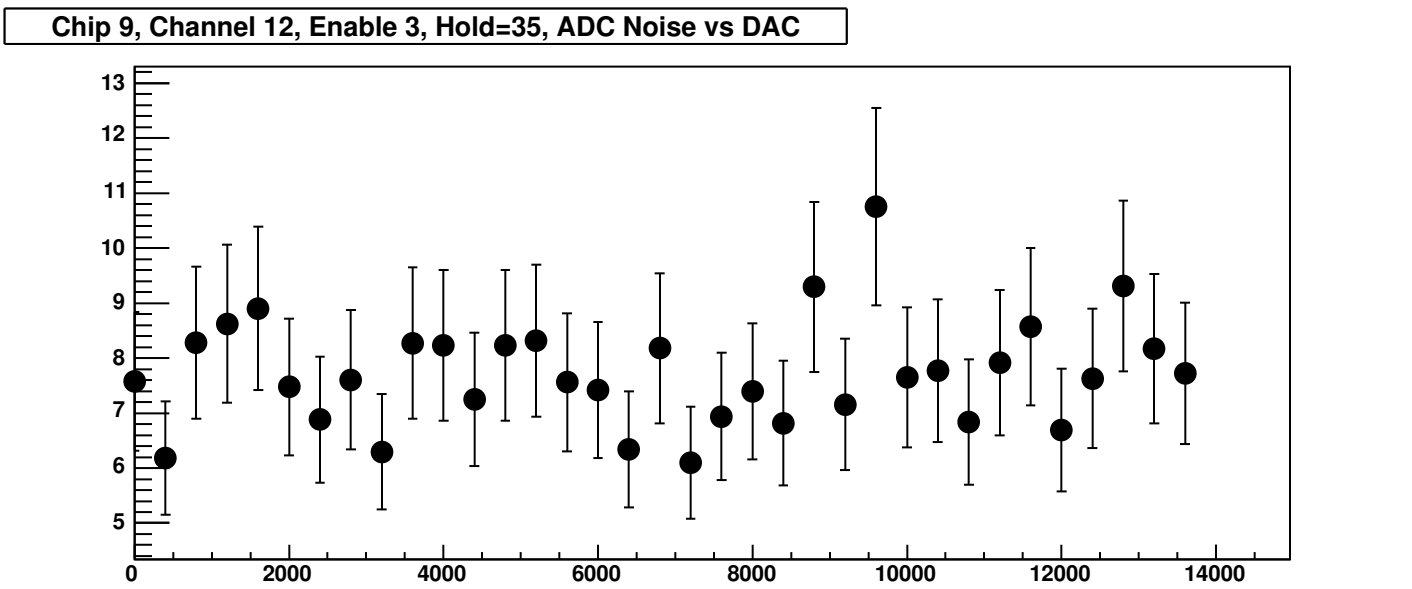

Chip 9, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

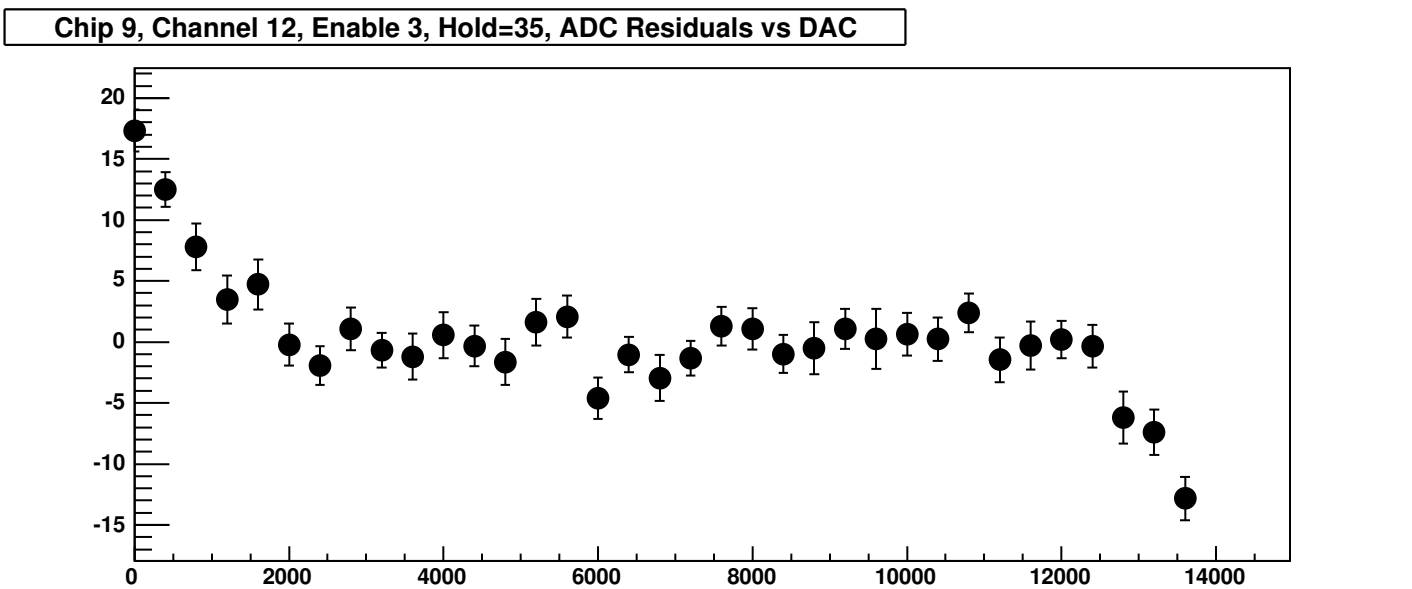

Chip 9, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 67.32 / 23
p0 -509.5 ± 1.057
p1 -0.1935 ± 0.0002839
-500
-1000
-1500
-2000
-2500
-3000
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

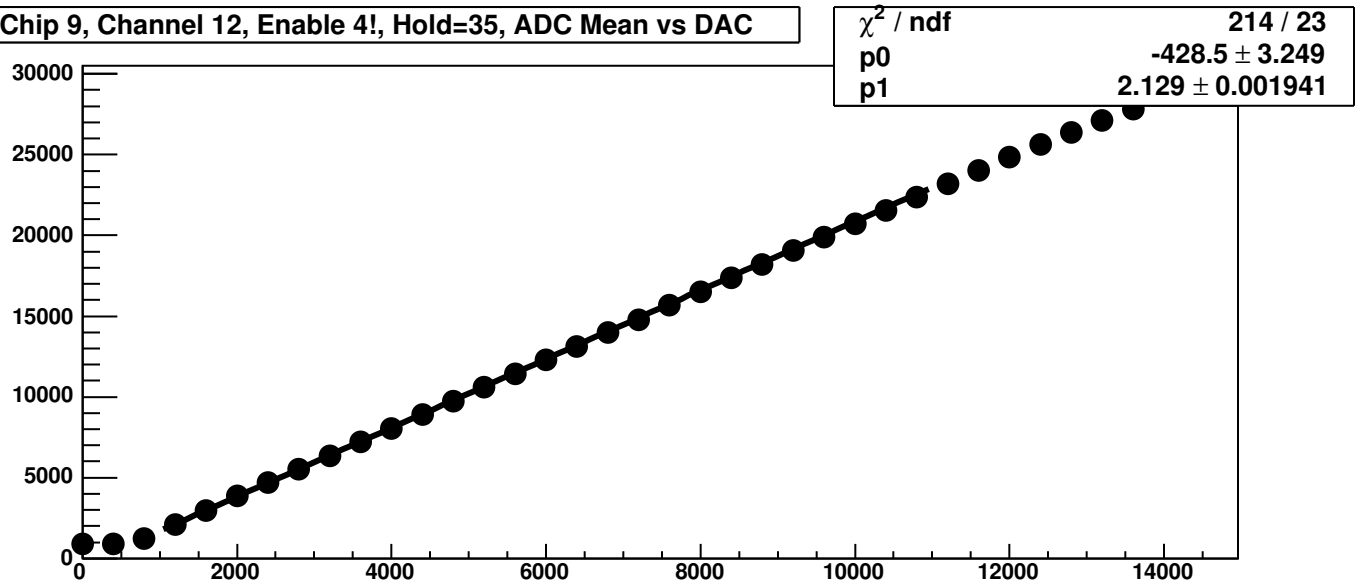

Chip 9, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 29.64 / 23
p0 -519.3 ± 0.8172
p1 0.0137 ± 0.0001225
-340
-360
-380
-400
-420
-440
-460
-480
-500
0
2000
4000
6000
8000
10000
12000
14000

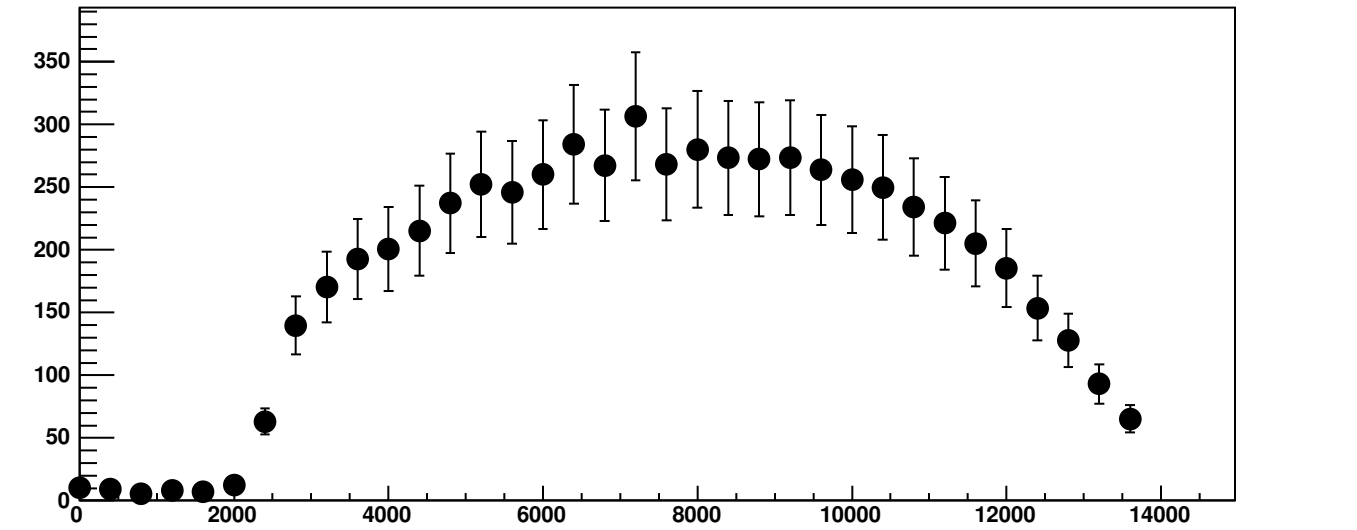

Chip 9, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

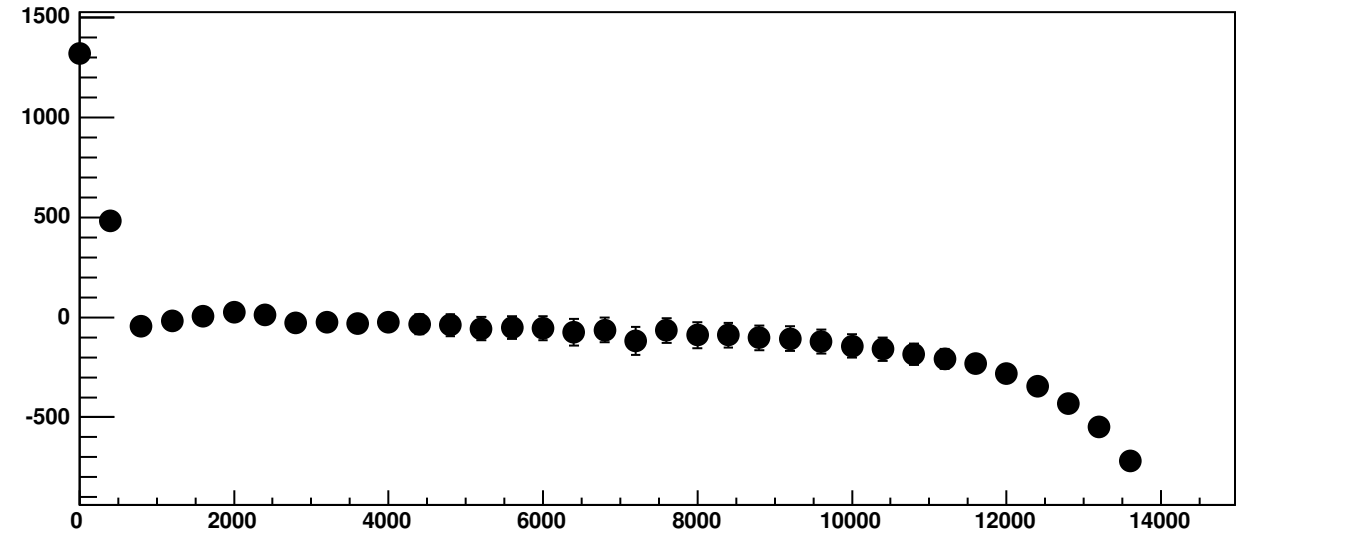
Chip 9, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC

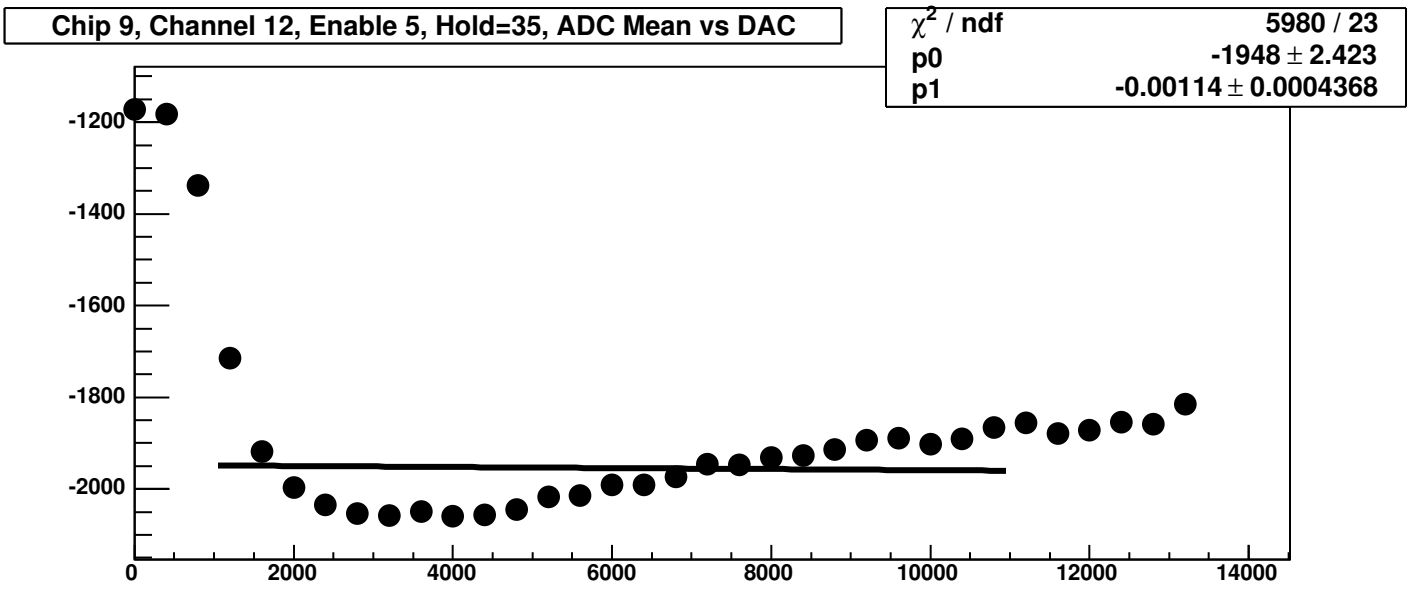


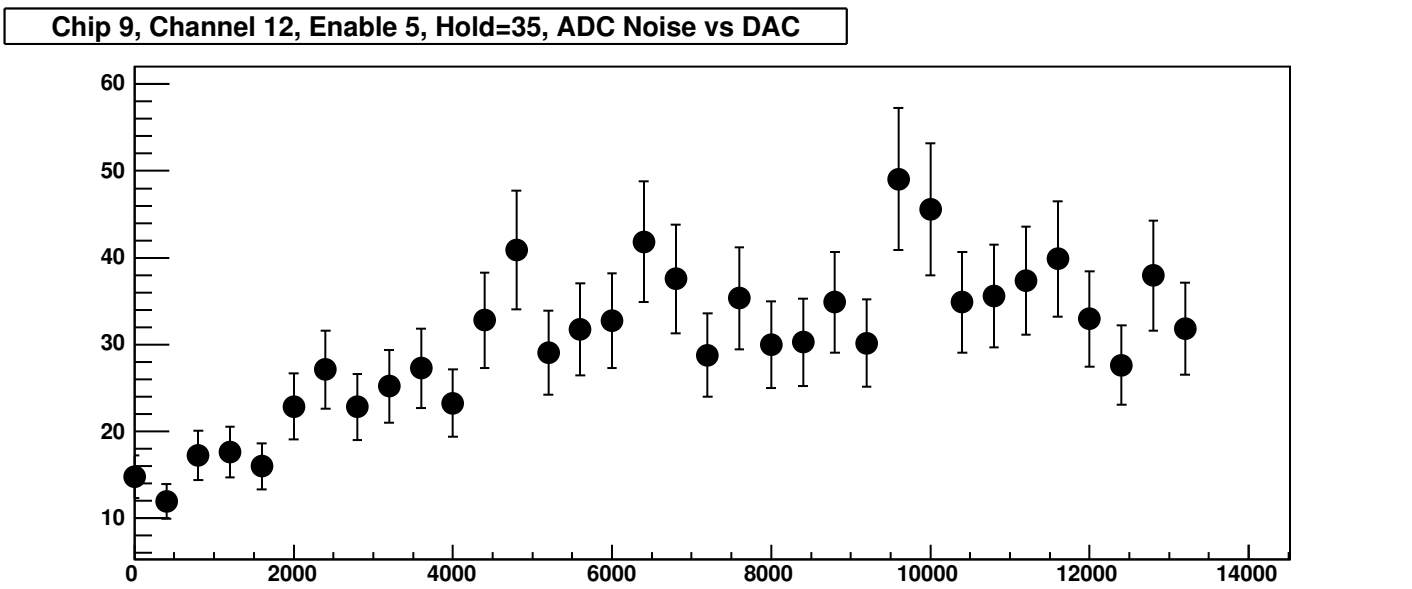
Chip 9, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC

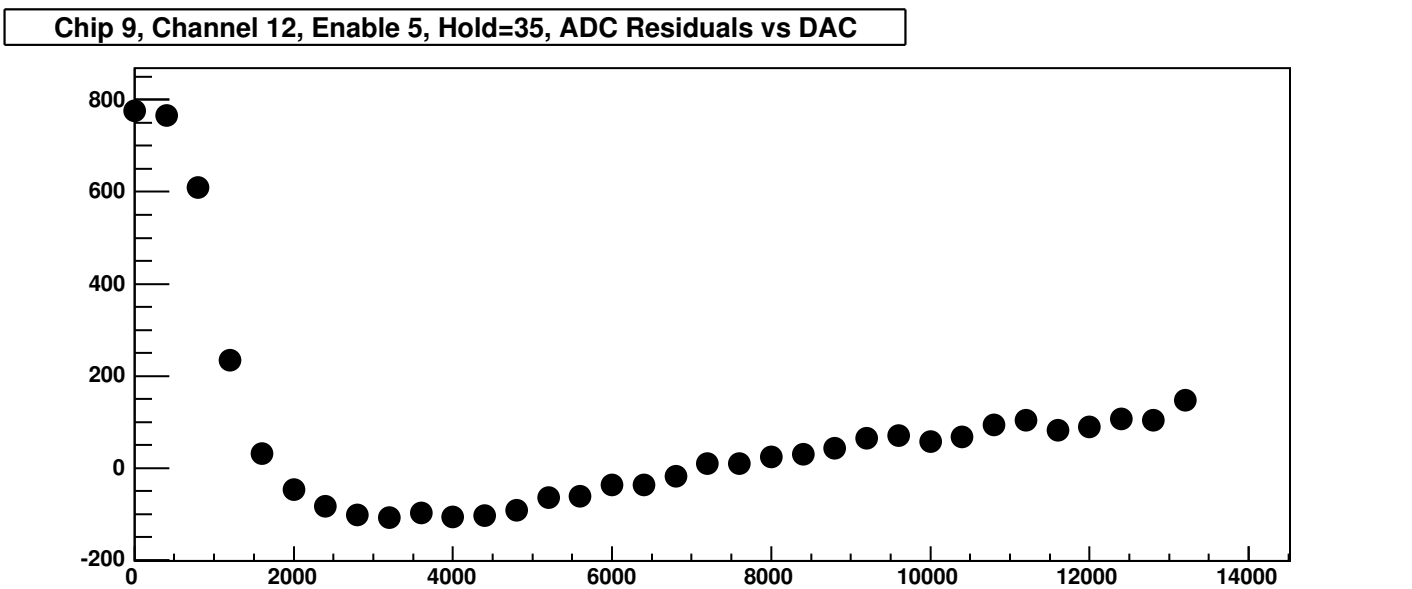


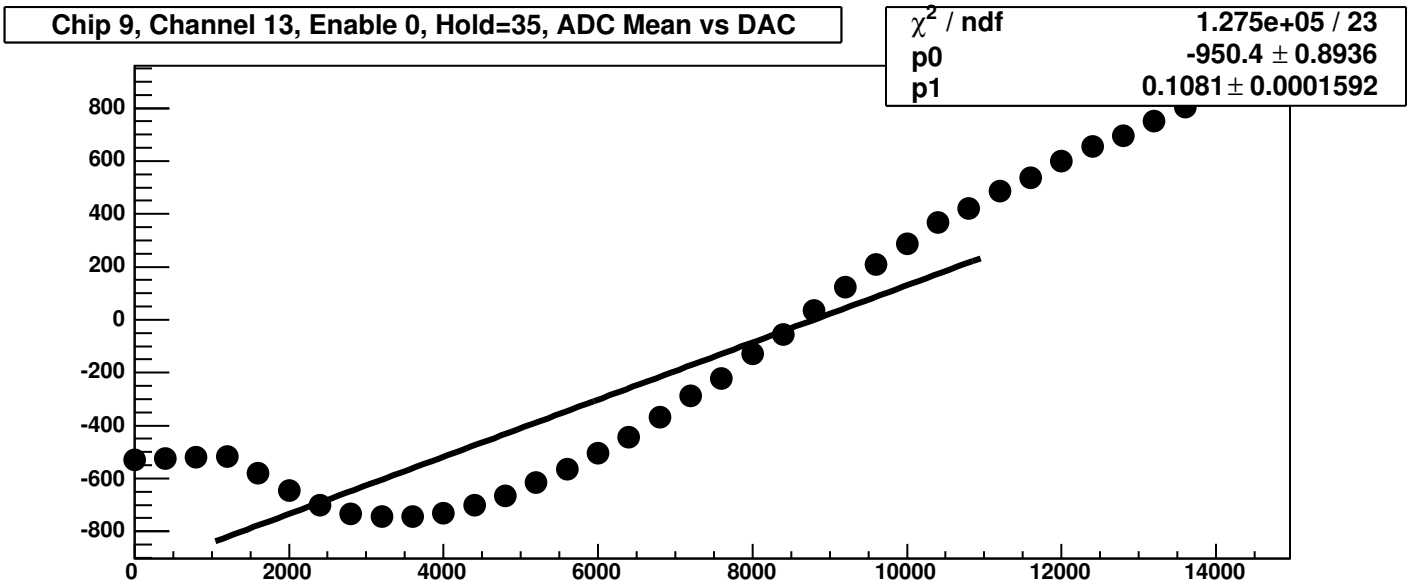
Chip 9, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC

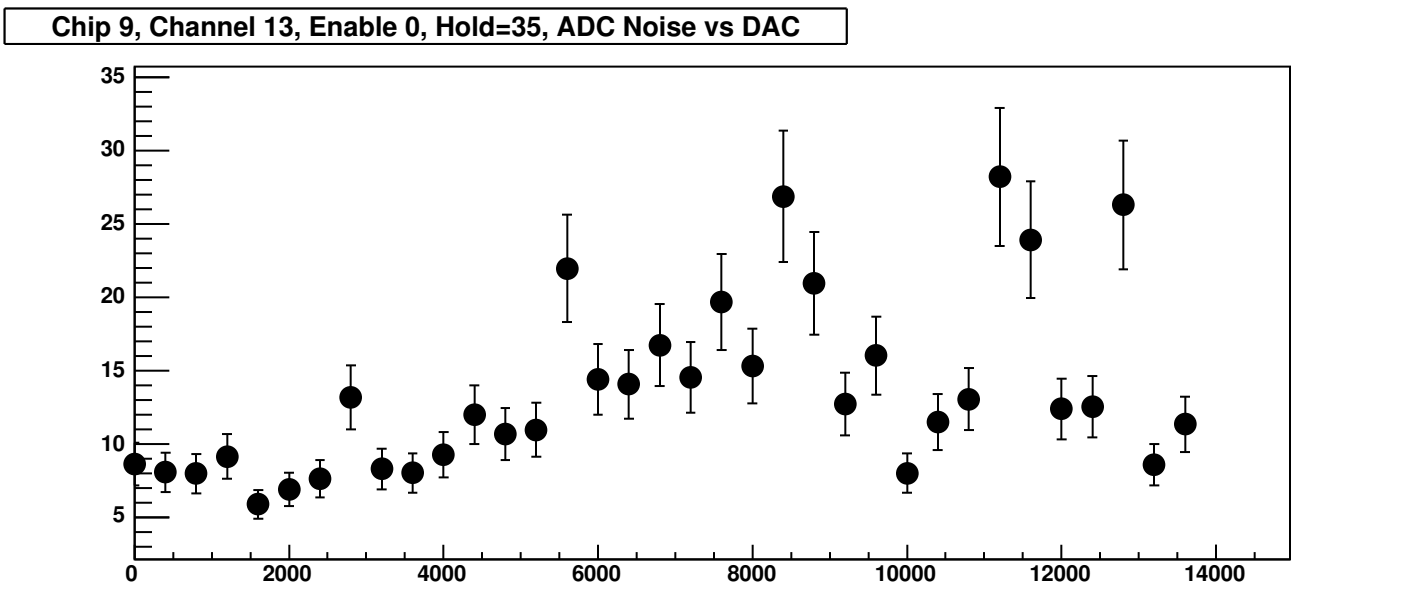


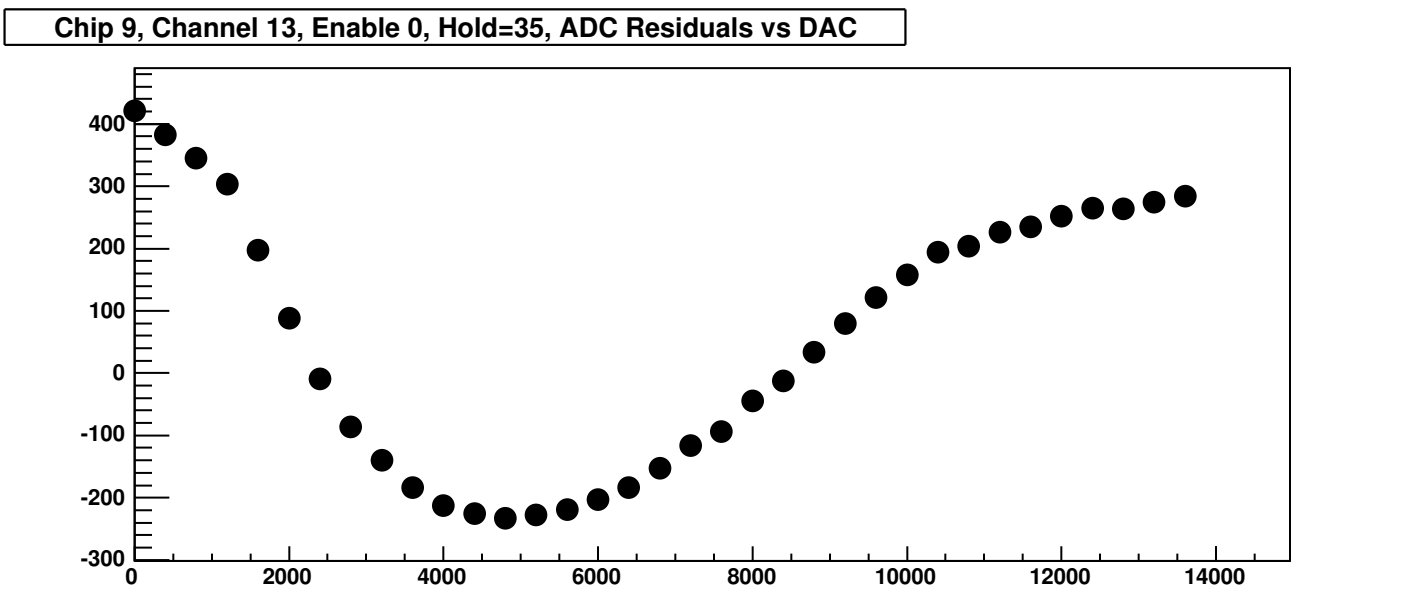

Chip 9, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5980 / 23
p0 -1948 ± 2.423
p1 -0.00114 ± 0.0004368
-1200
-1400
-1600
-1800
-2000
0
2000
4000
6000
8000
10000
12000
14000

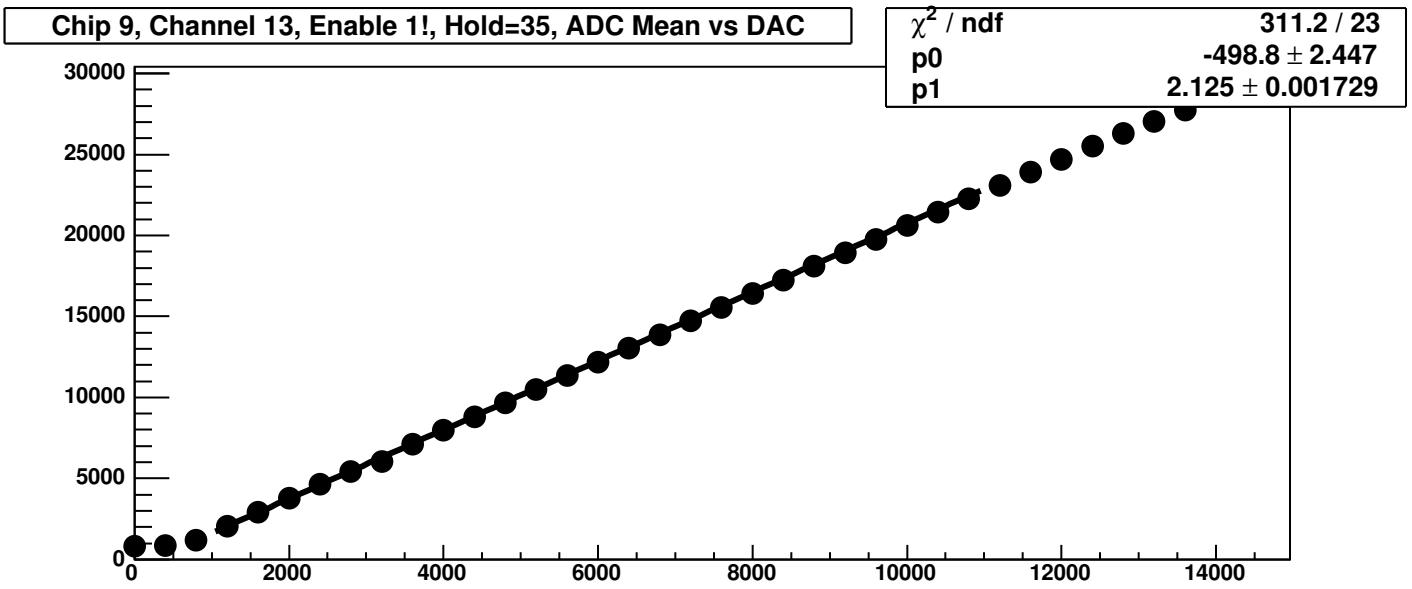

Chip 9, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

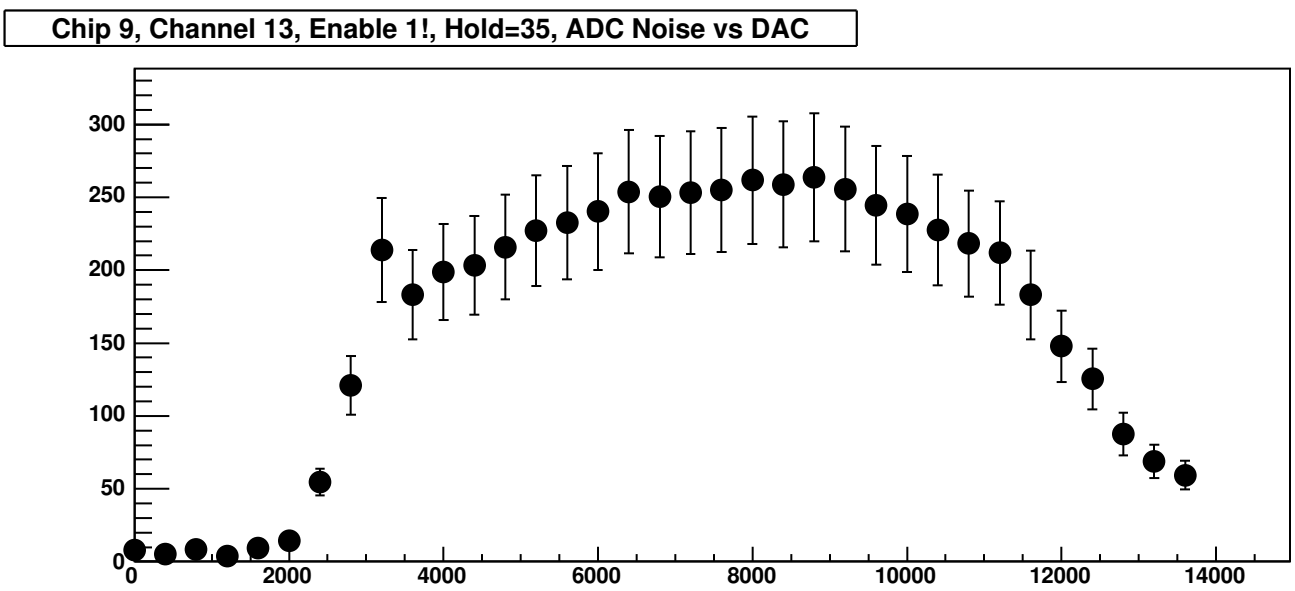

Chip 9, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

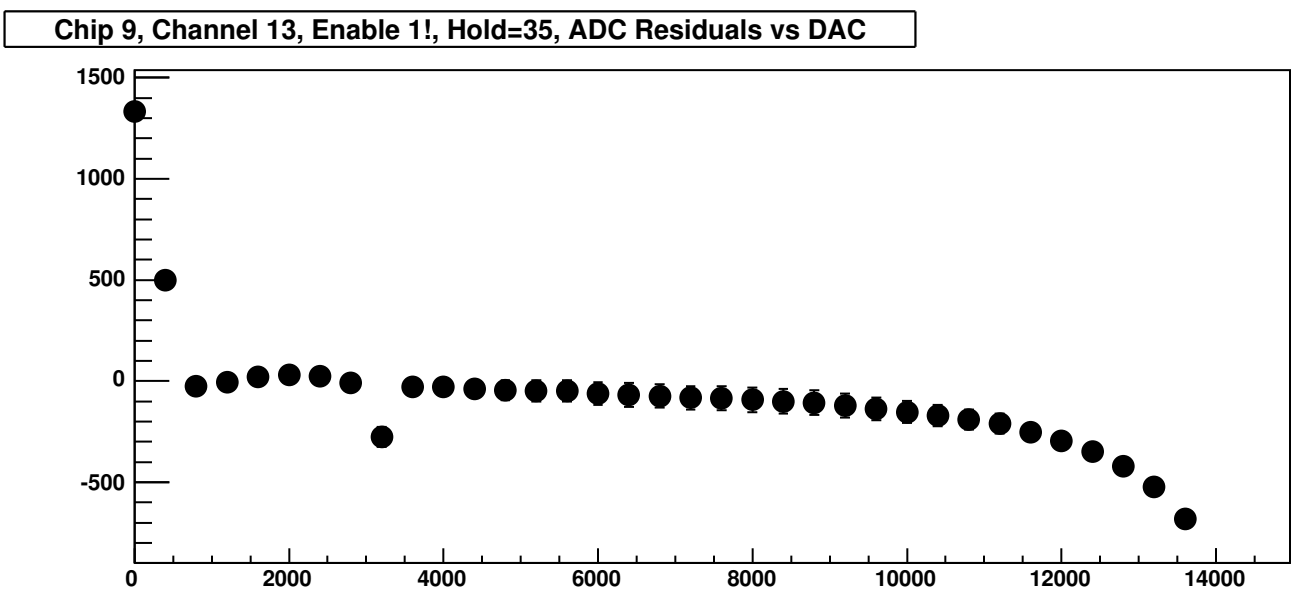

Chip 9, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.275e+05 / 23
p0 -950.4 ± 0.8936
p1 0.1081 ± 0.0001592
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

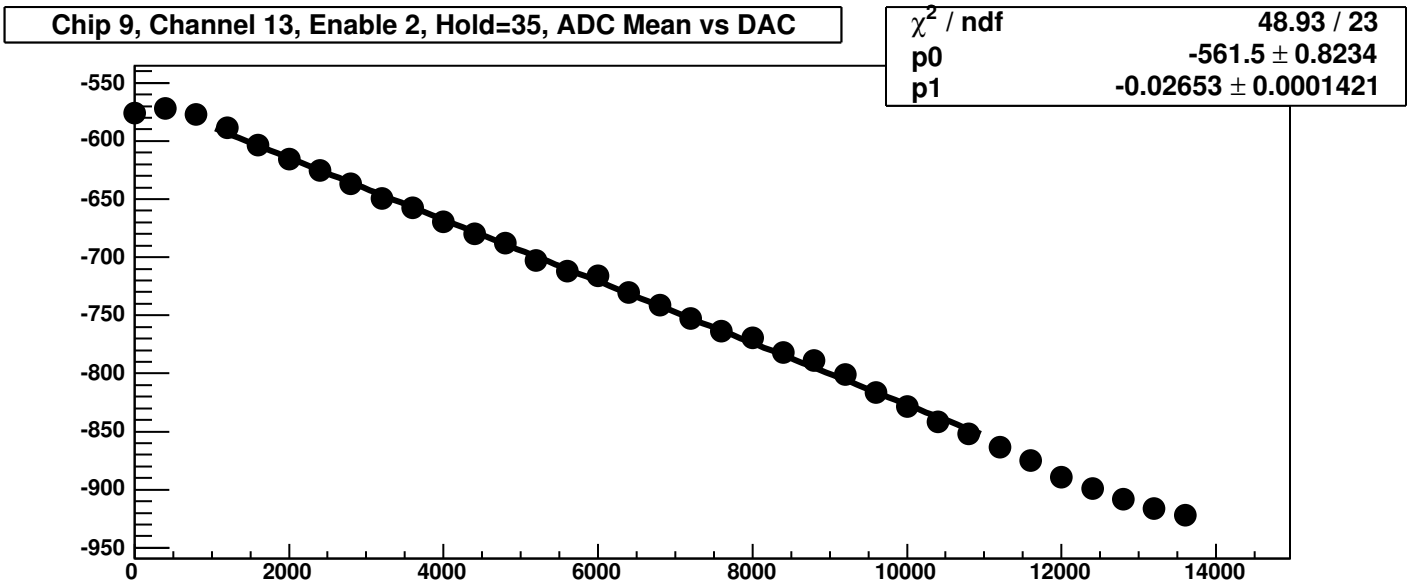

Chip 9, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

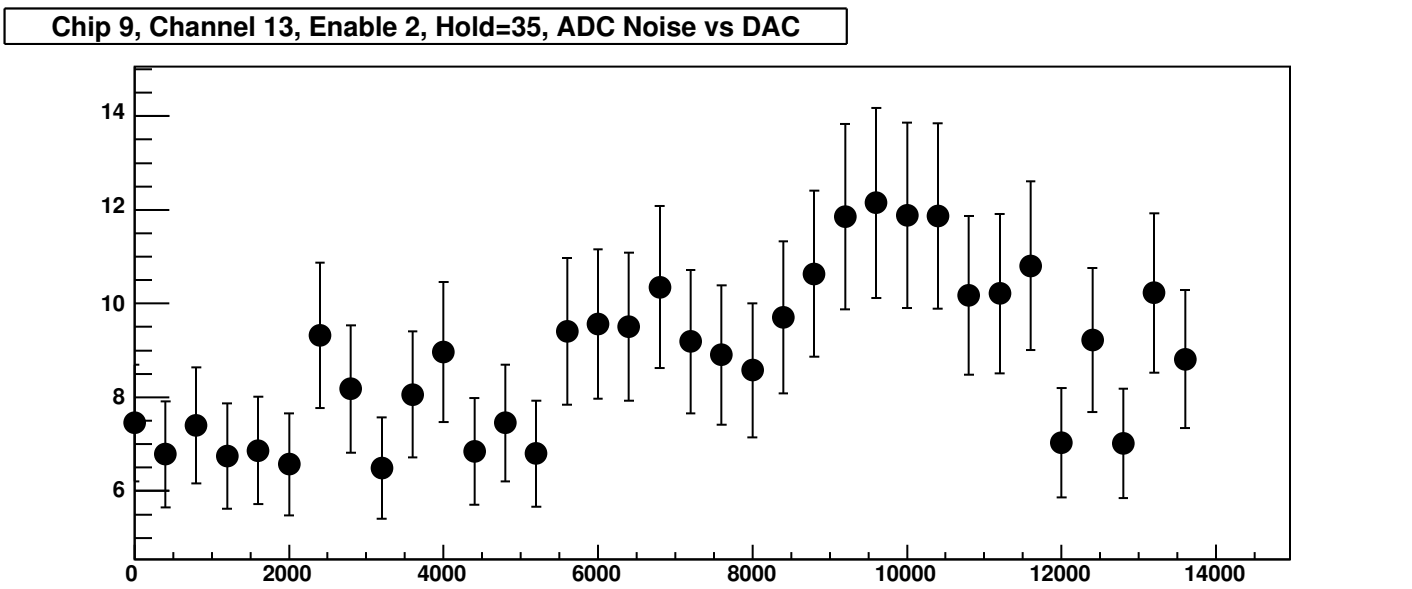

Chip 9, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

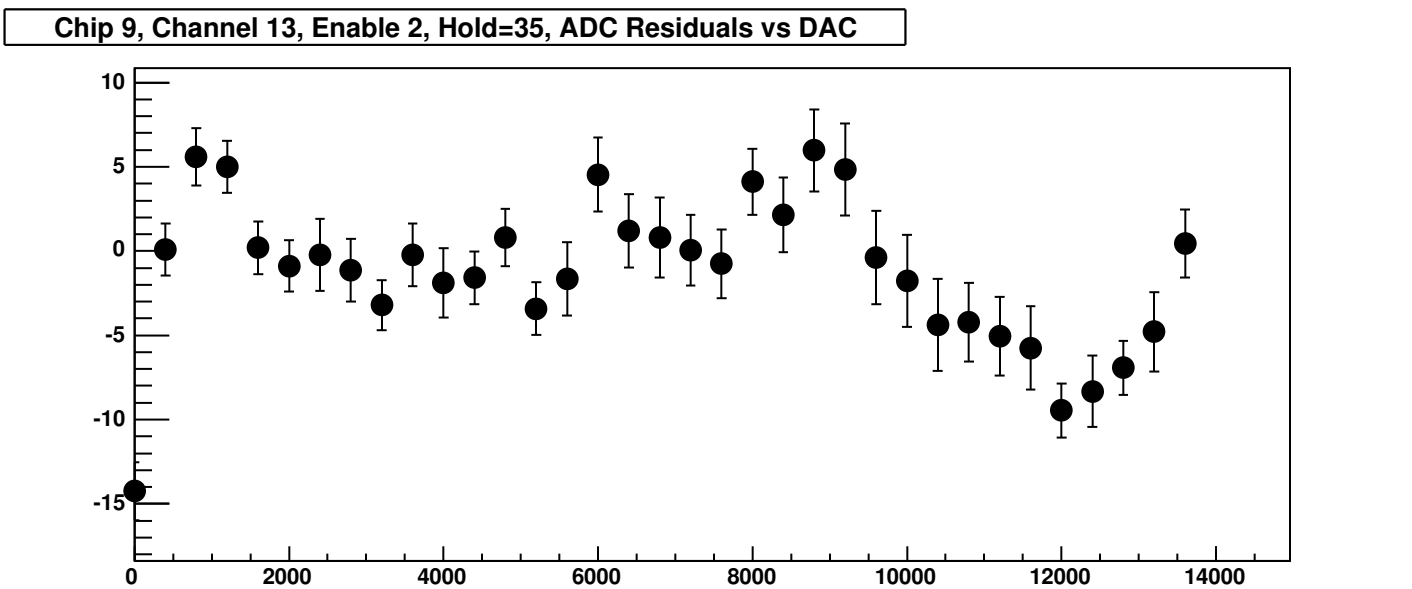

Chip 9, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 311.2 / 23
p0 -498.8 ± 2.447
p1 2.125 ± 0.001729
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

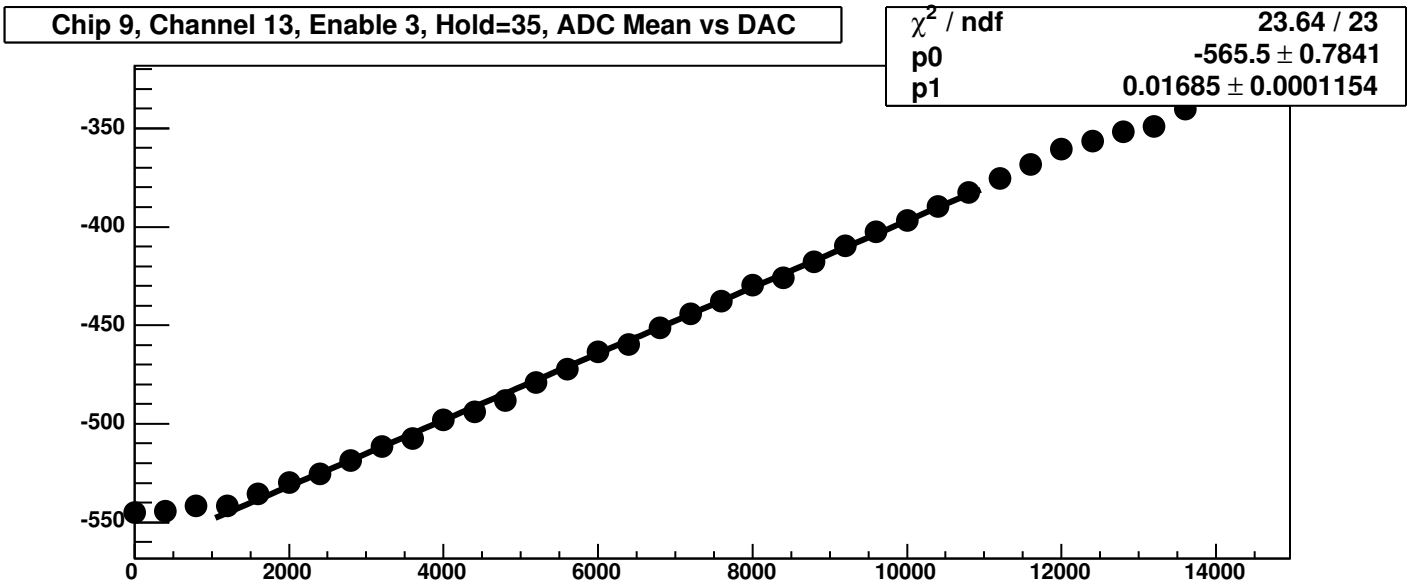

Chip 9, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

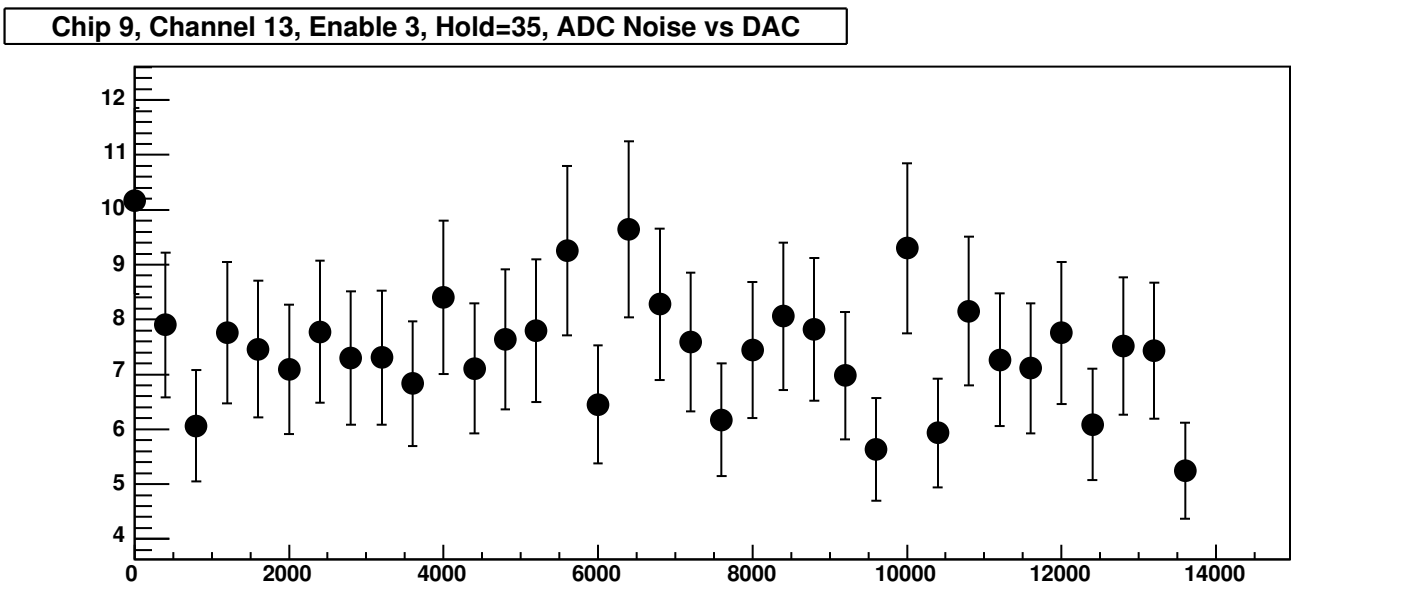

Chip 9, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

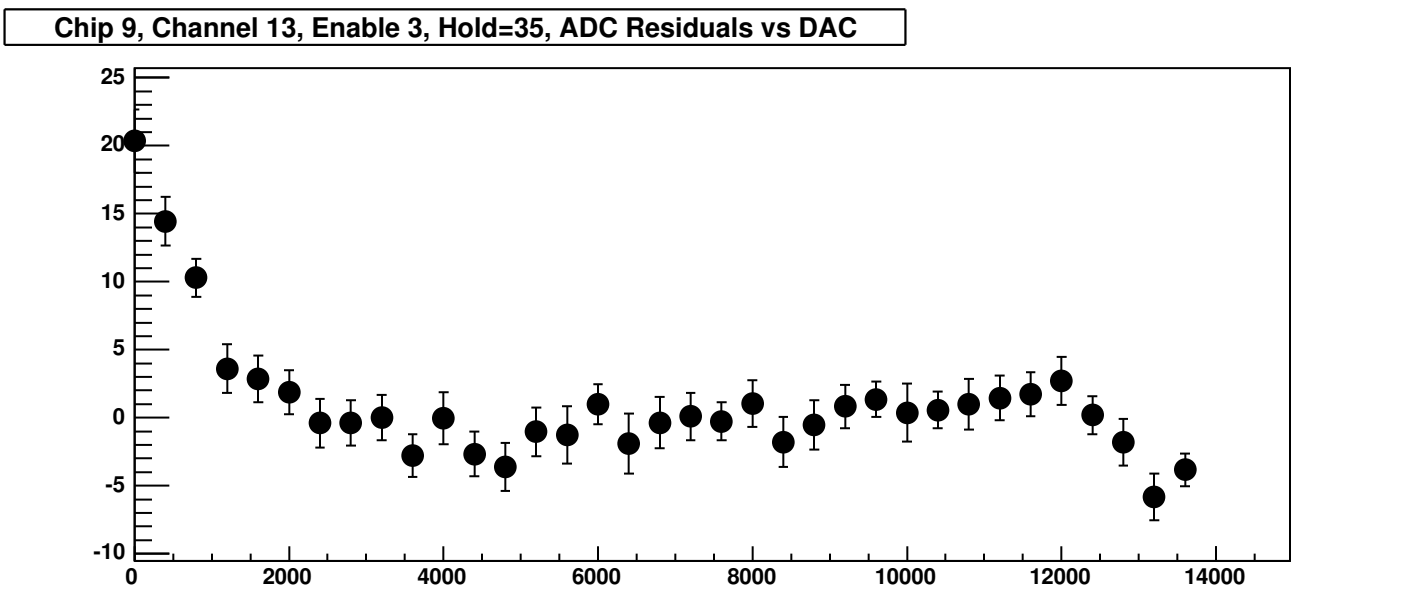

Chip 9, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 48.93 / 23
p0 -561.5 ± 0.8234
p1 -0.02653 ± 0.0001421
-550
-600
-650
-700
-750
-800
-850
-900
-950
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

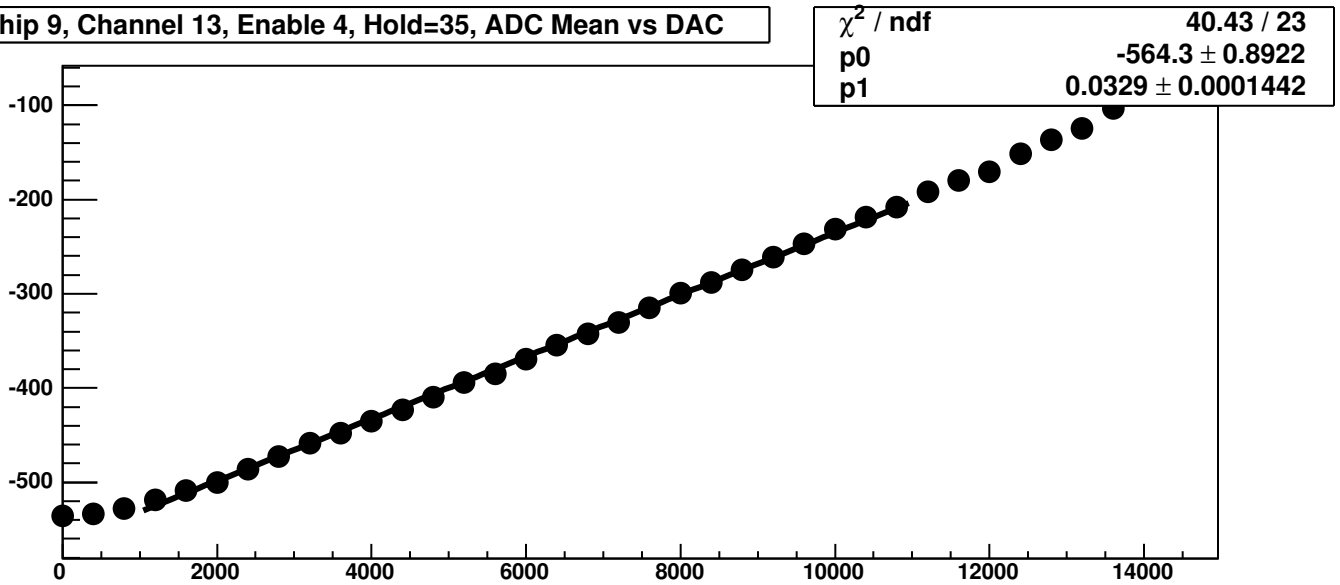

Chip 9, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.64 / 23
p0 -565.5 ± 0.7841
p1 0.01685 ± 0.0001154
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

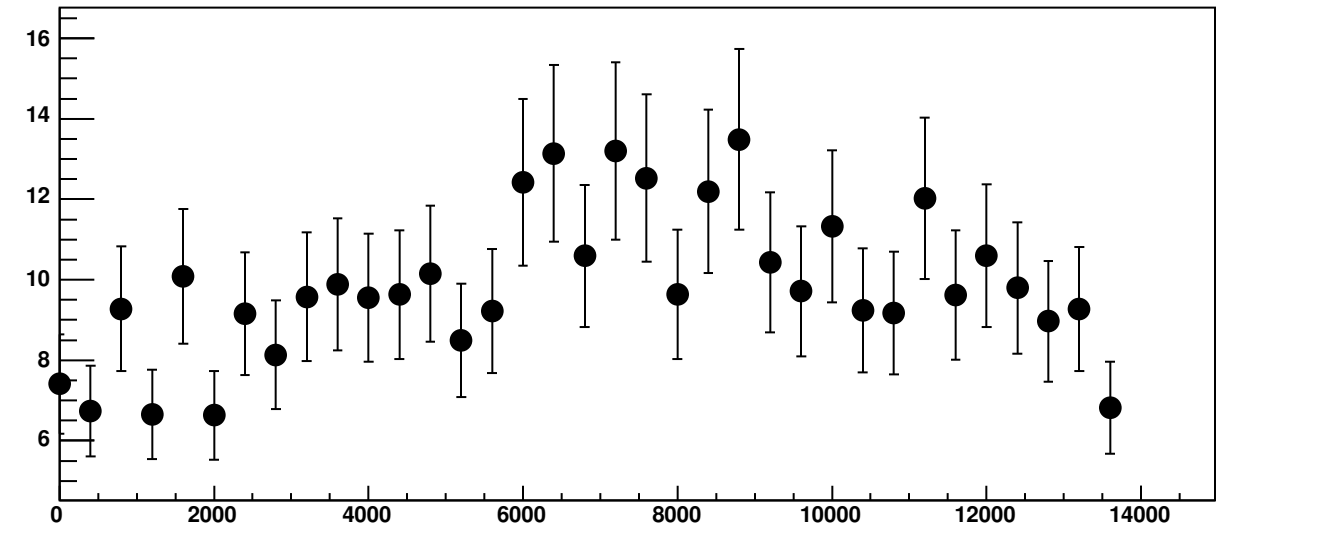

Chip 9, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

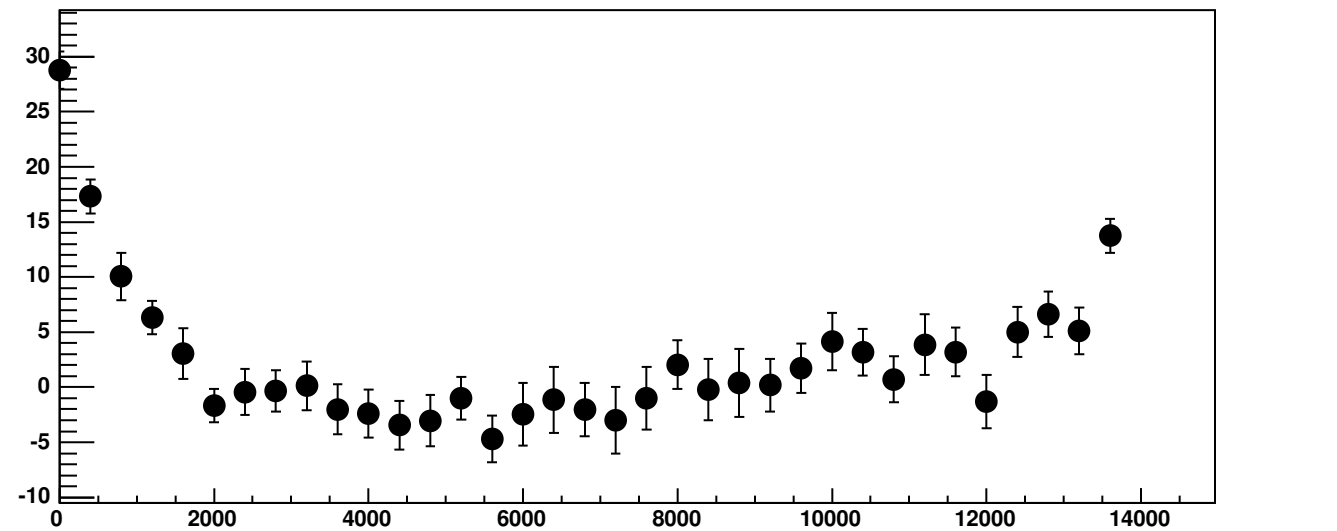
Chip 9, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC



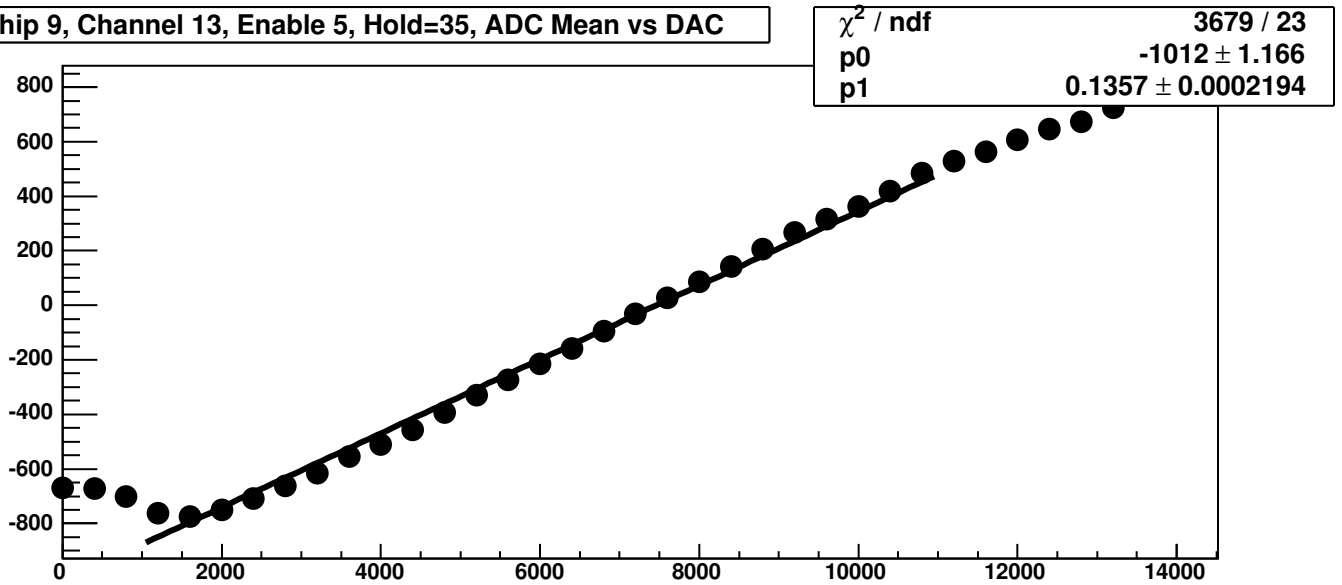
Chip 9, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC



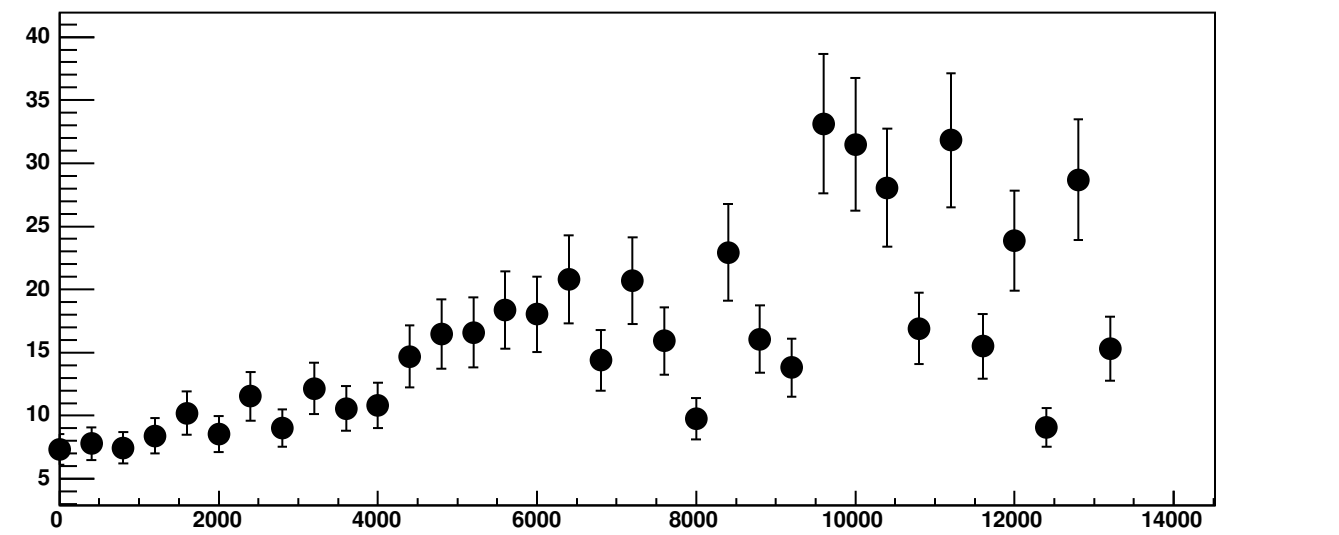
Chip 9, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC



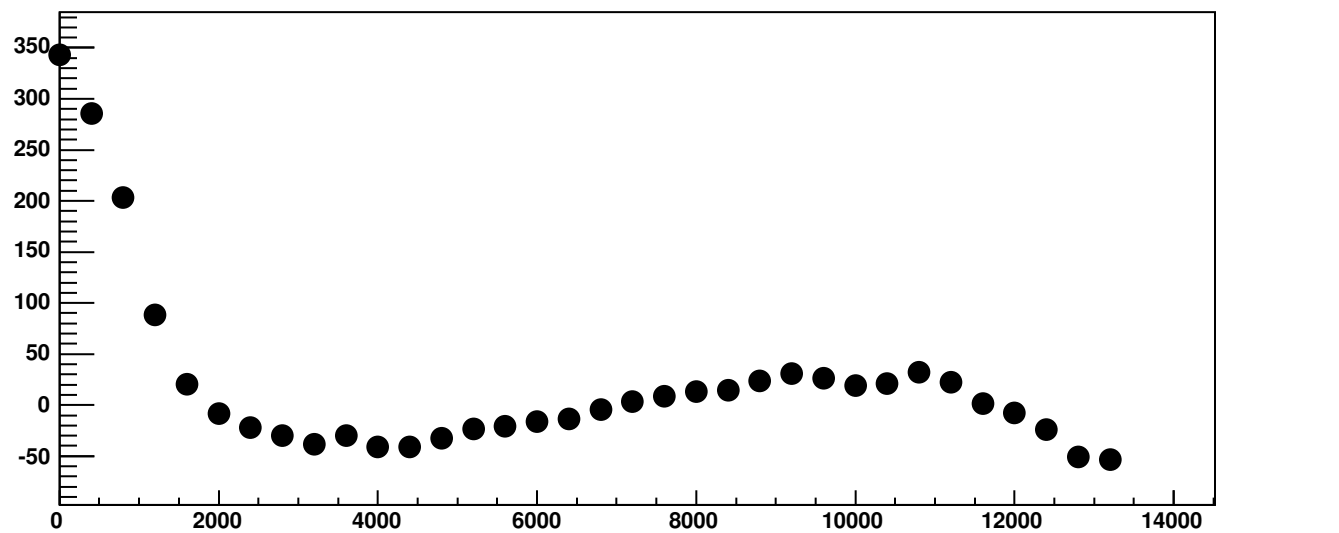
Chip 9, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC



Chip 9, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

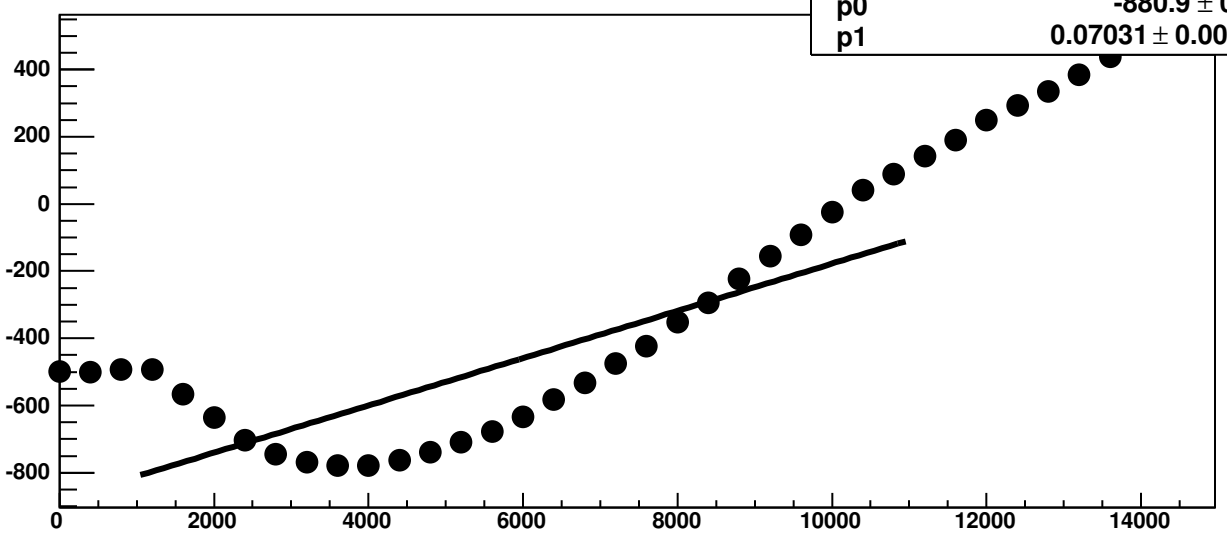


Chip 9, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

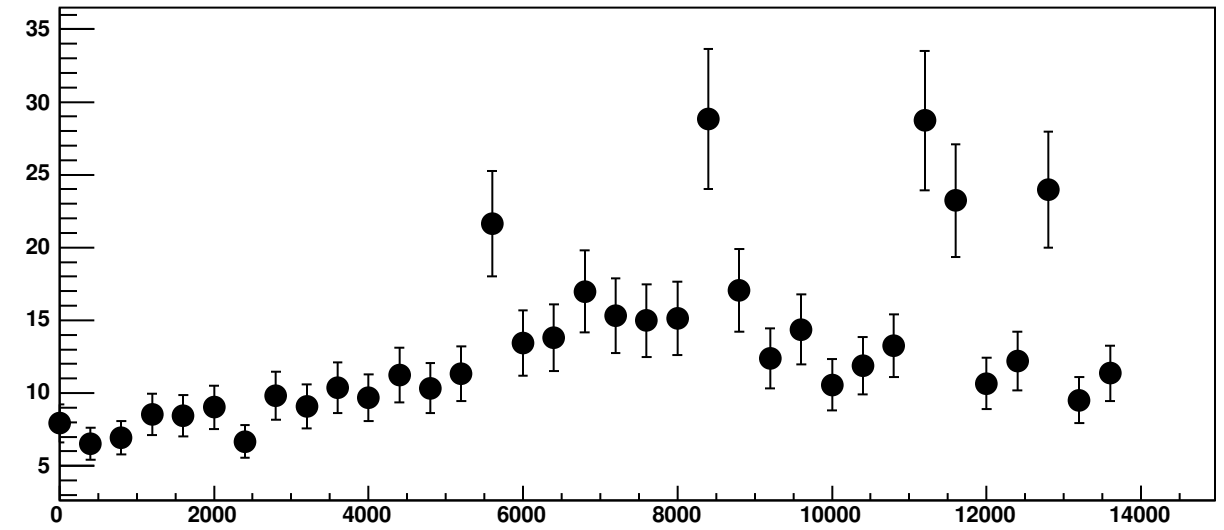


Chip 9, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

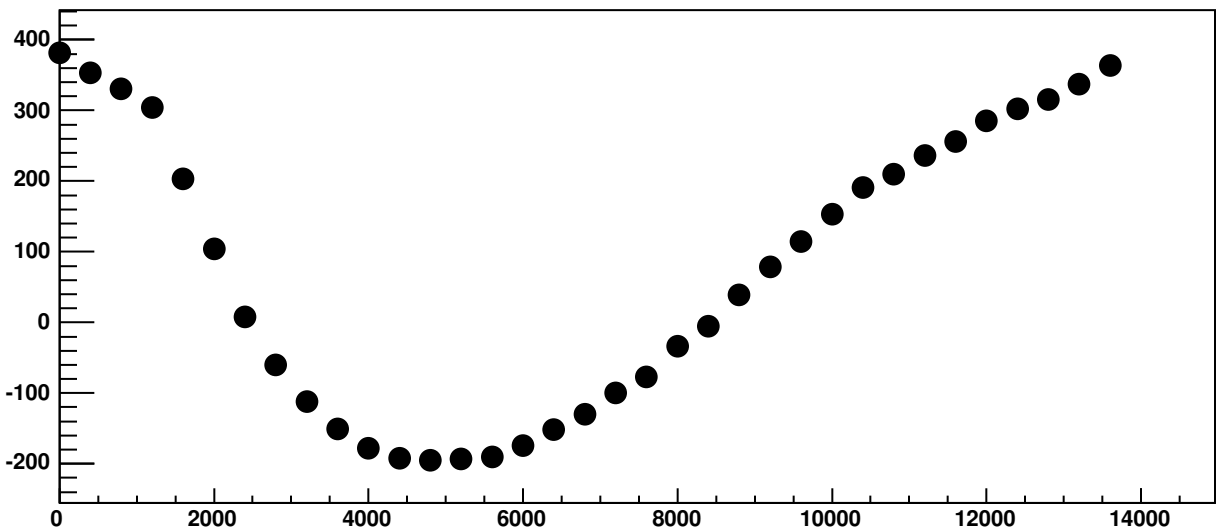
χ^2 / ndf	9.508e+04 / 23
p0	-880.9 ± 0.9828
p1	0.07031 ± 0.0001724

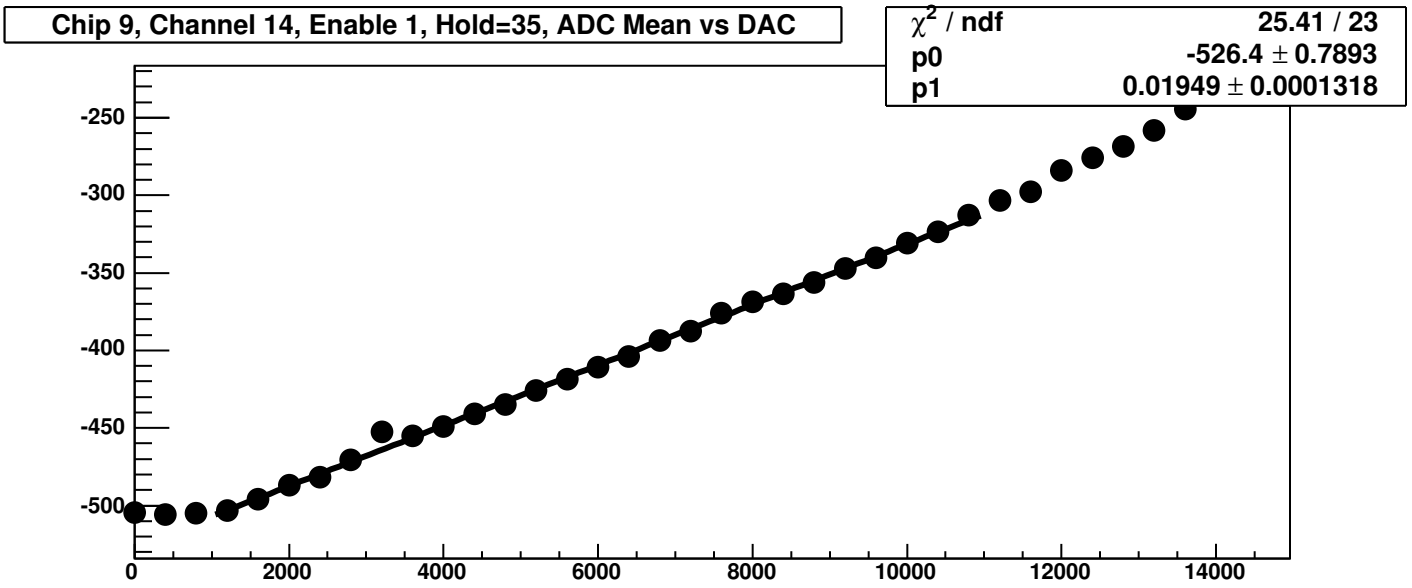


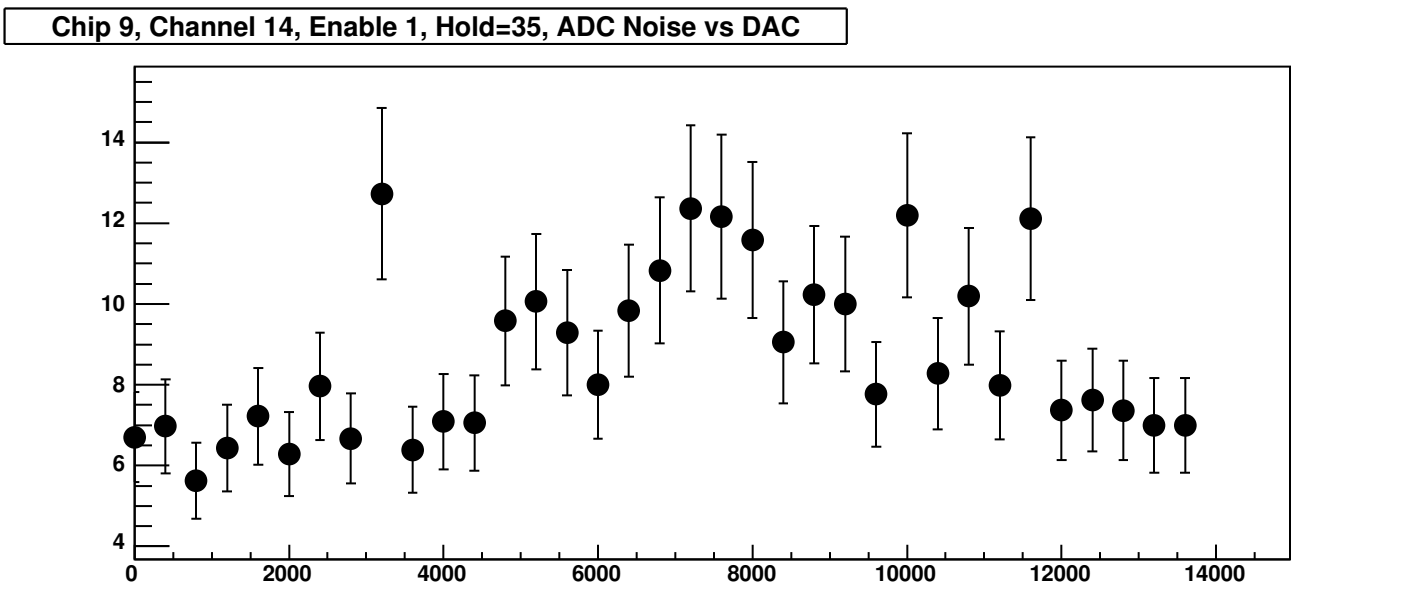
Chip 9, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

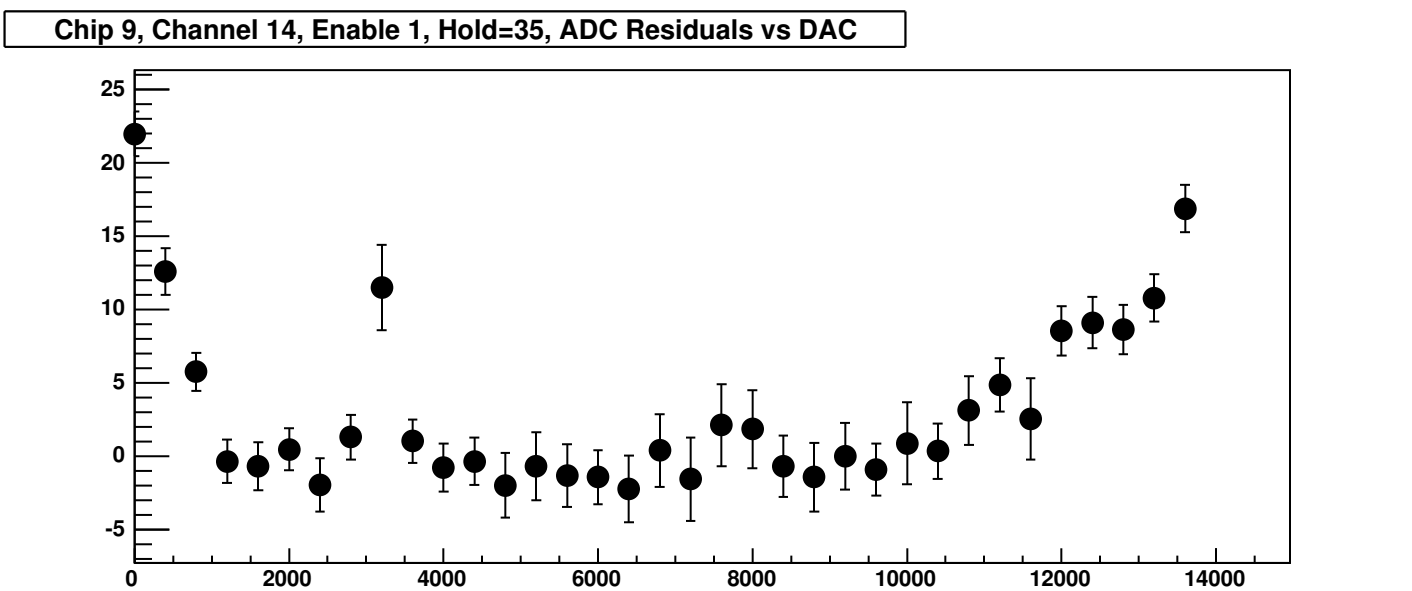


Chip 9, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC



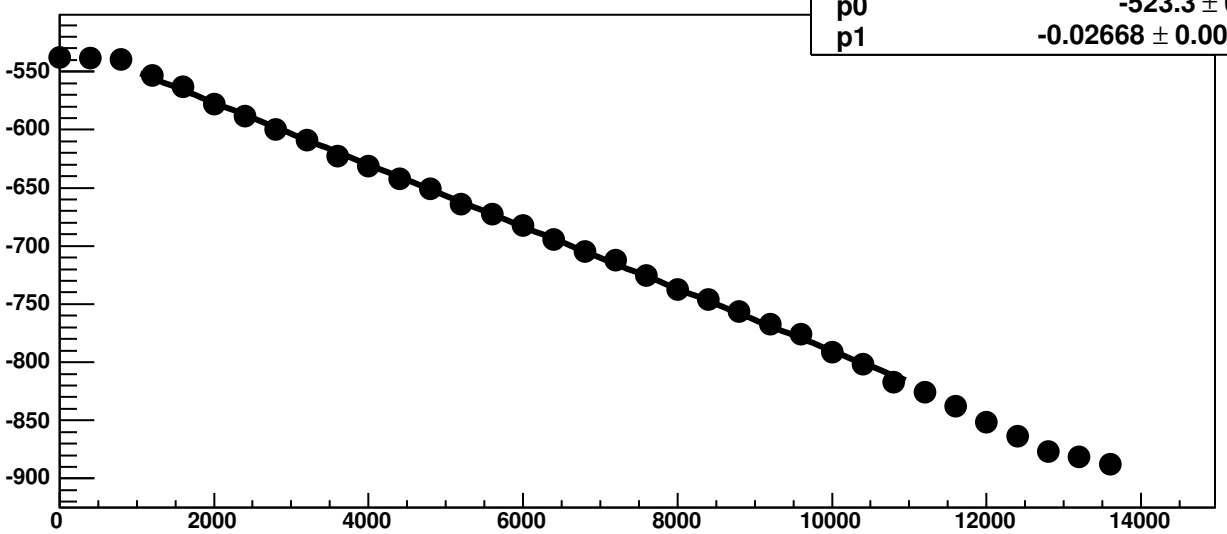

Chip 9, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 25.41 / 23
p0 -526.4 ± 0.7893
p1 0.01949 ± 0.0001318
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

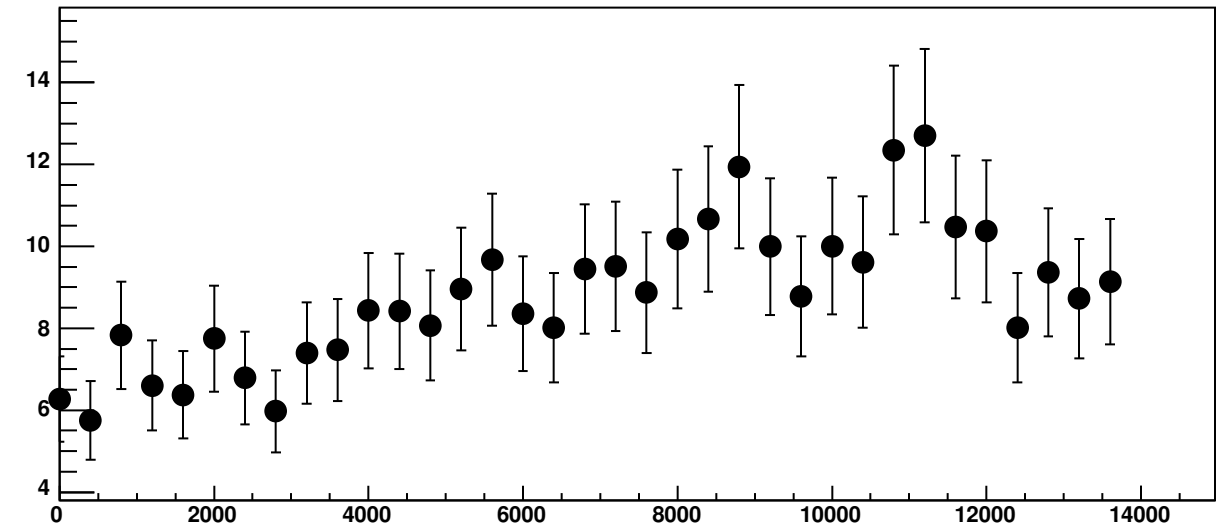

Chip 9, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

Chip 9, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

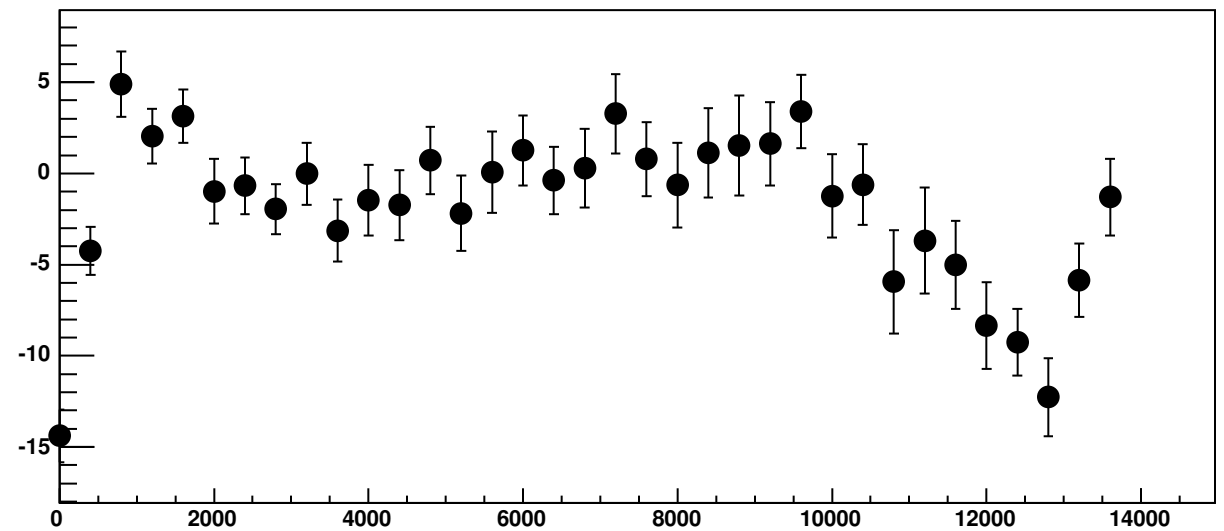
χ^2 / ndf	26.62 / 23
p0	-523.3 ± 0.7851
p1	-0.02668 ± 0.0001345

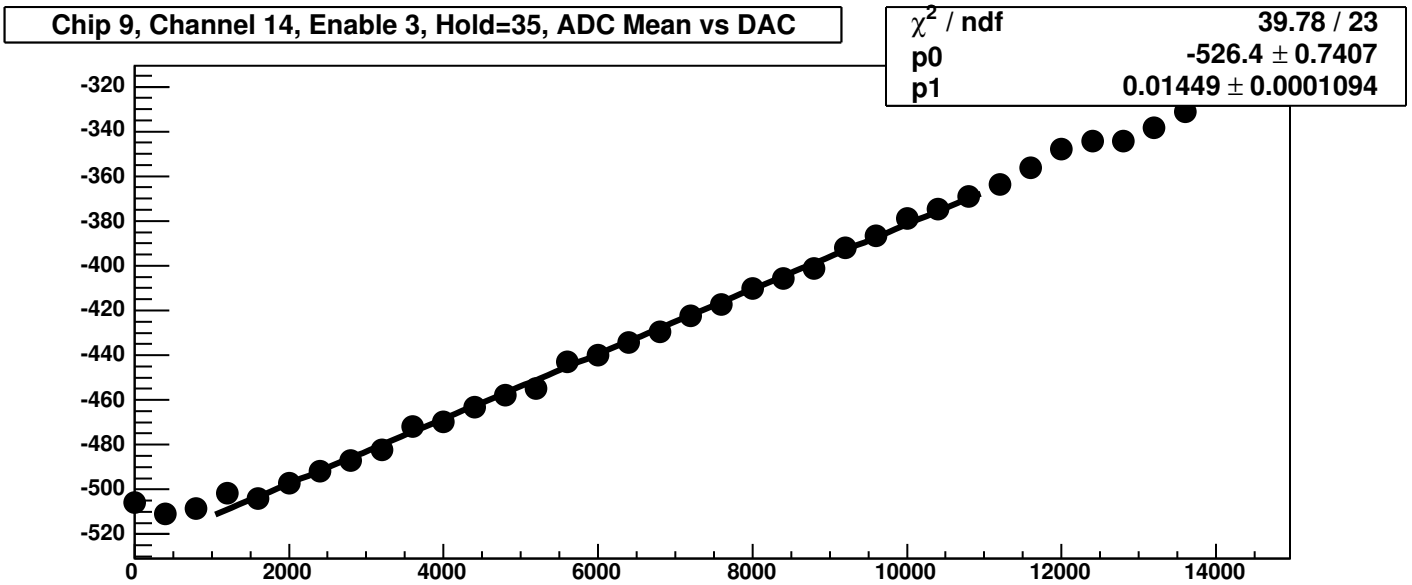


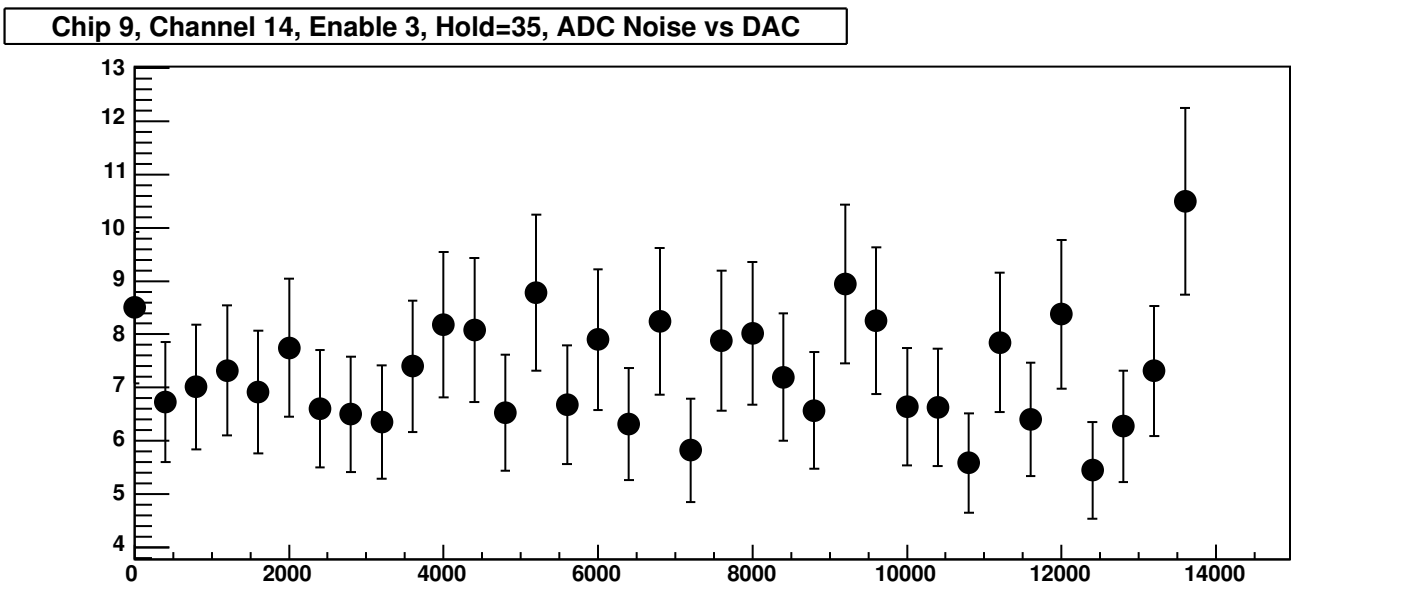
Chip 9, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC

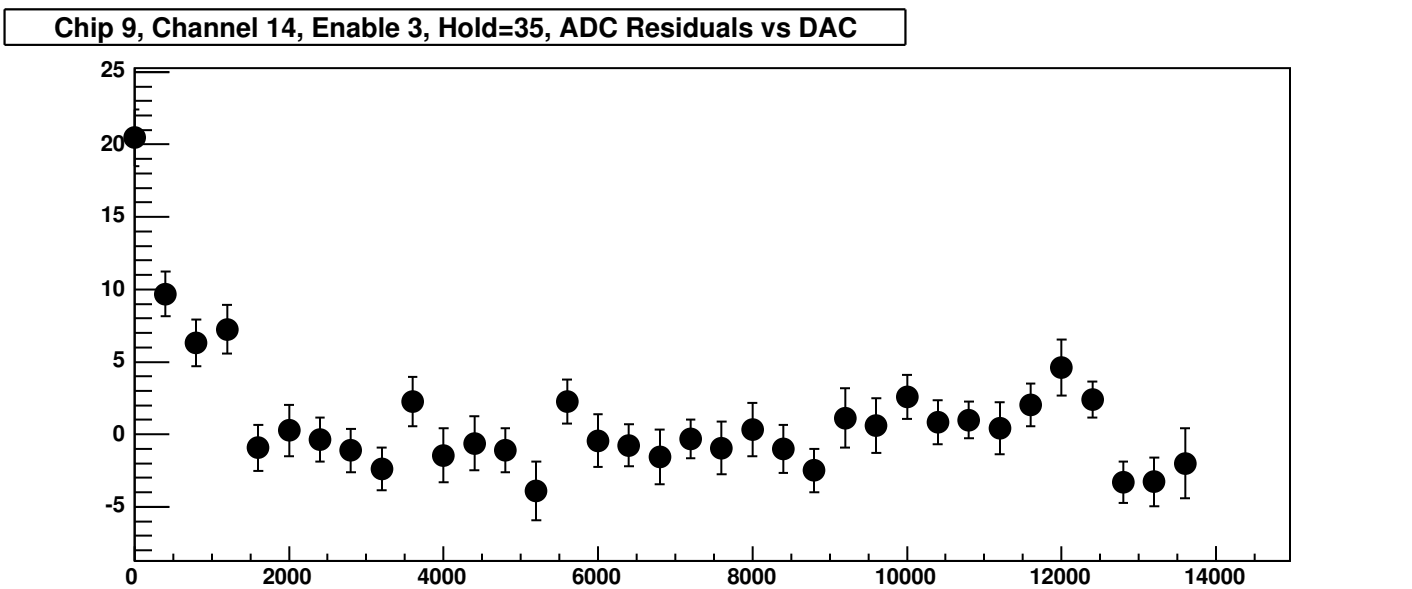


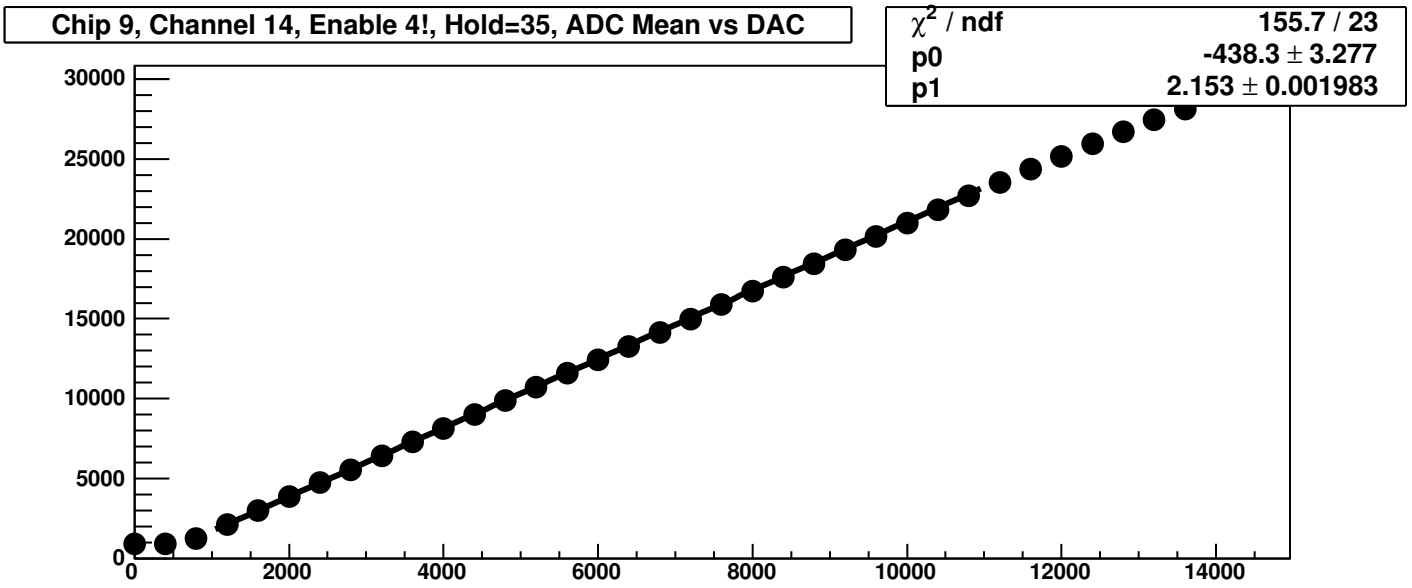
Chip 9, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

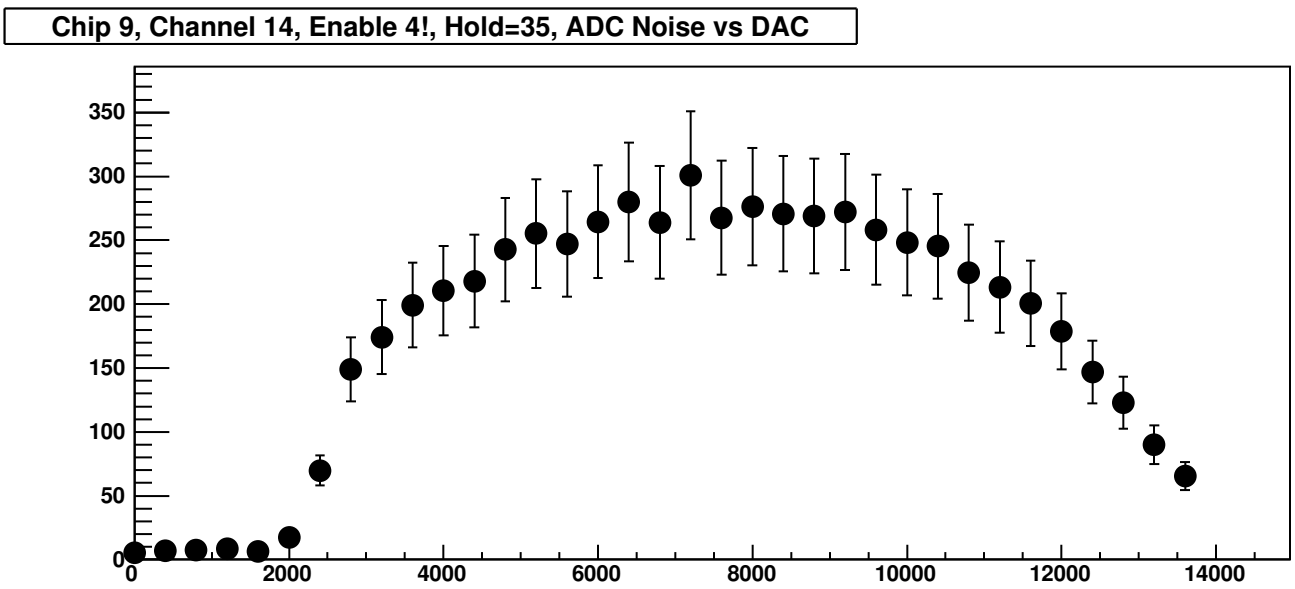


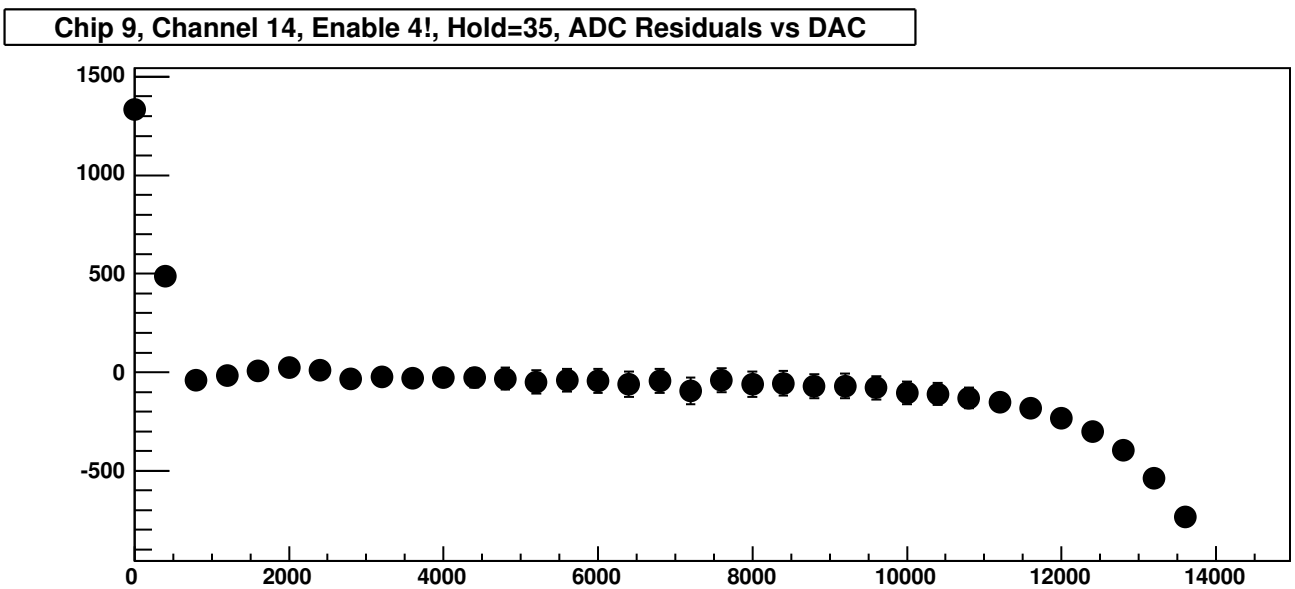

Chip 9, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 39.78 / 23
p0 -526.4 ± 0.7407
p1 0.01449 ± 0.0001094
-320
-340
-360
-380
-400
-420
-440
-460
-480
-500
-520
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

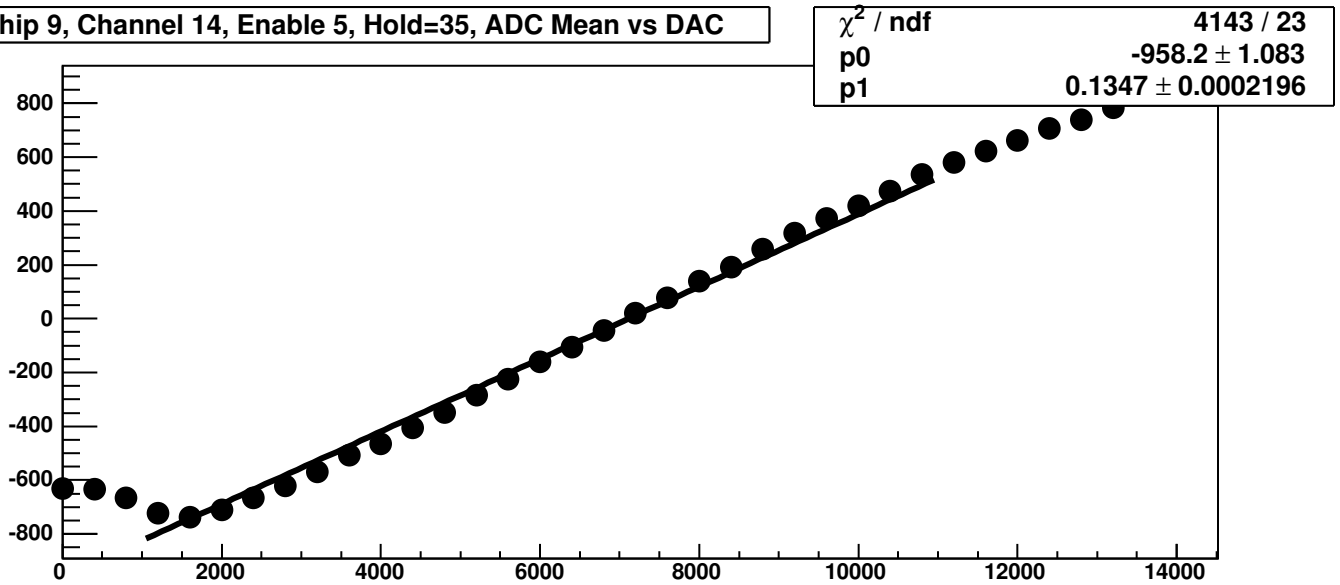

Chip 9, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 155.7 / 23
p0 -438.3 ± 3.277
p1 2.153 ± 0.001983
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

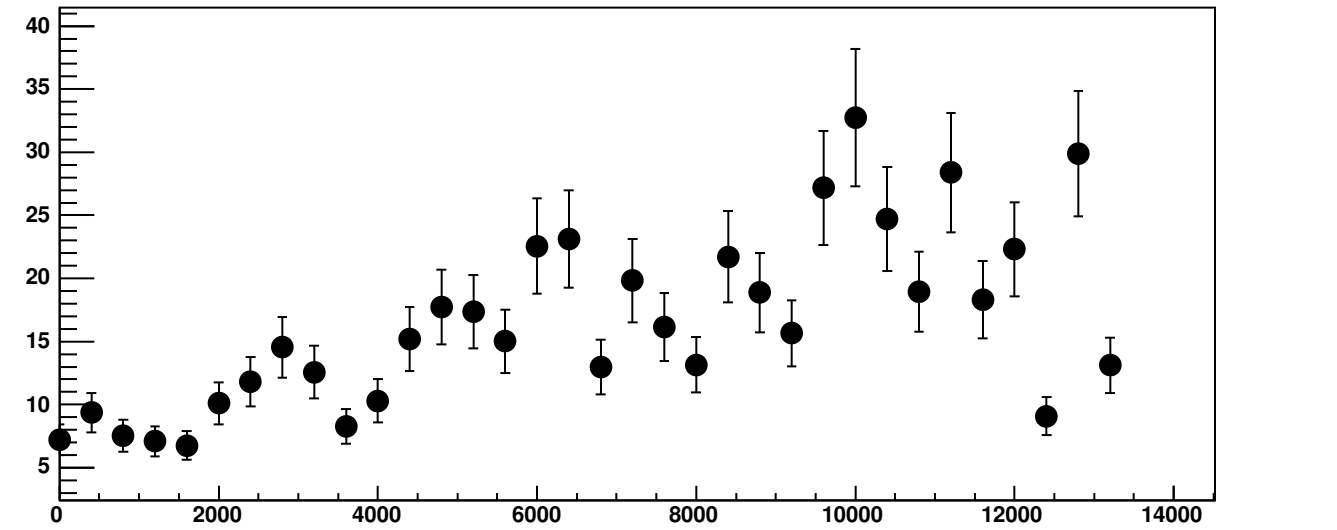

Chip 9, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

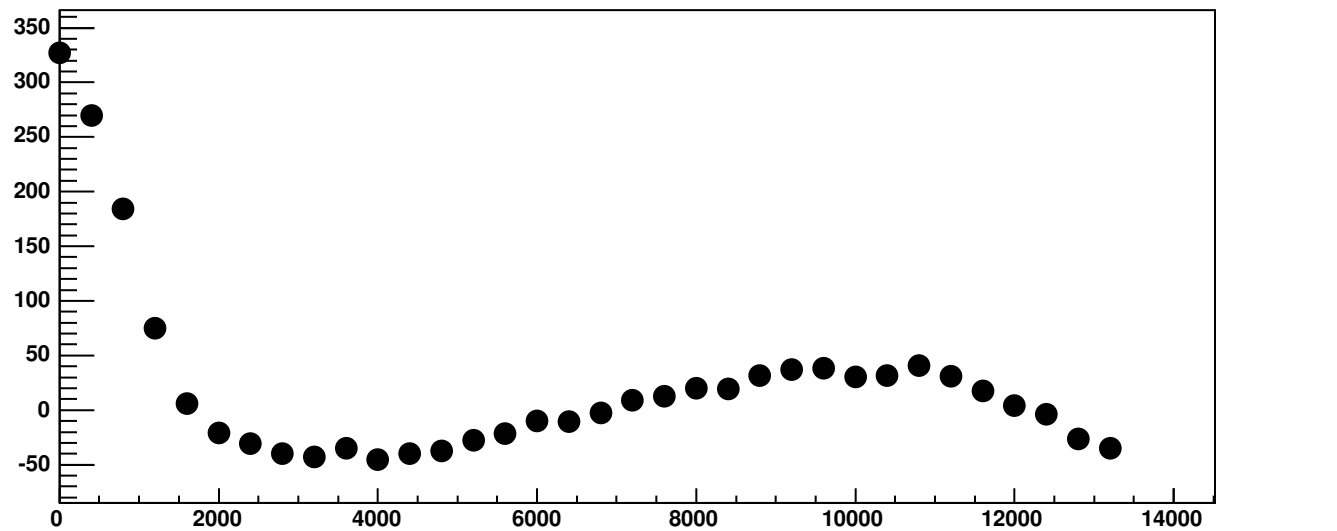
Chip 9, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

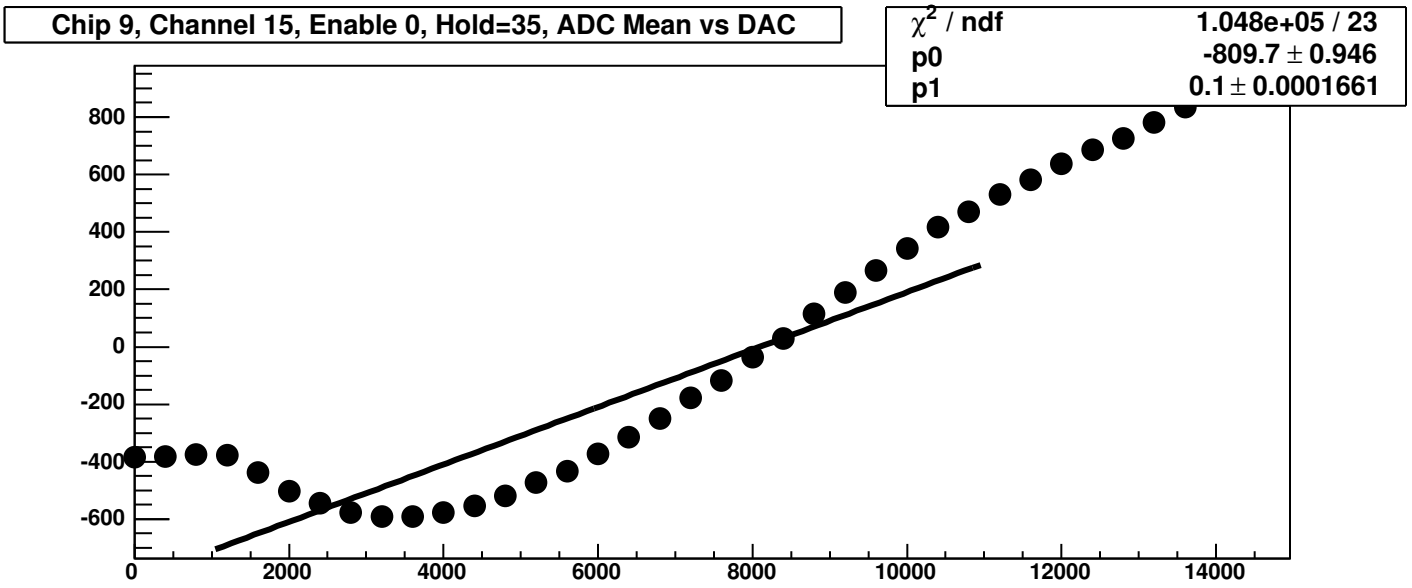


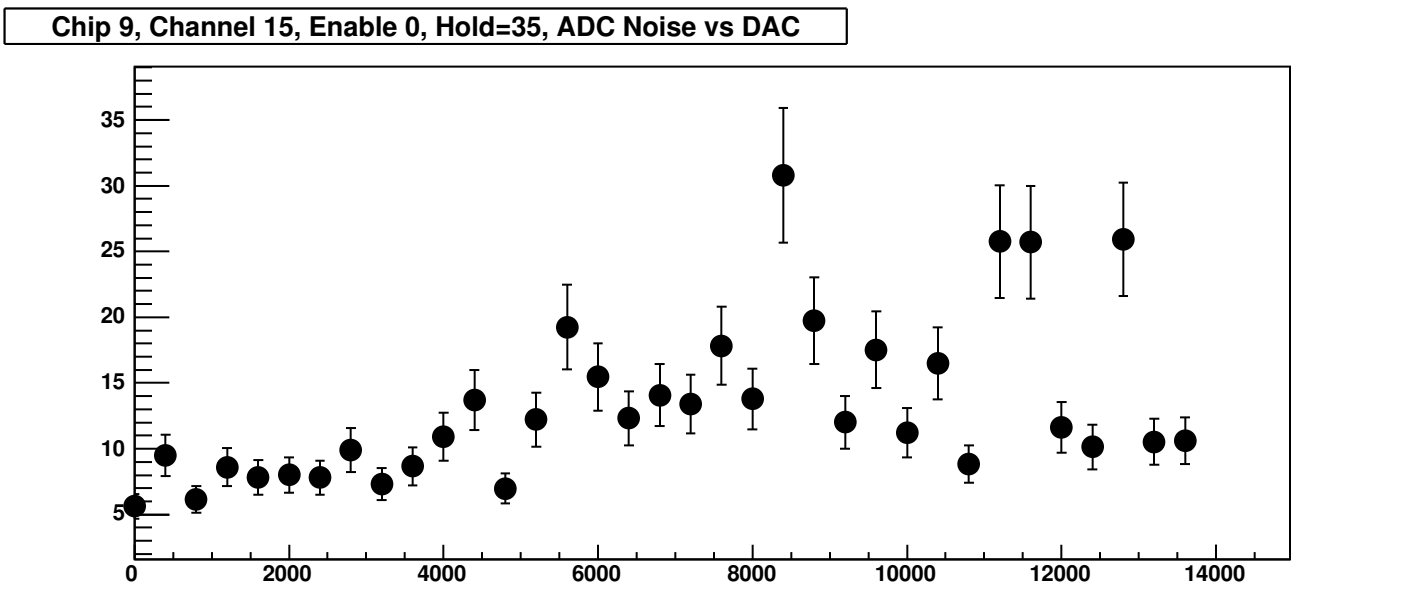
Chip 9, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC

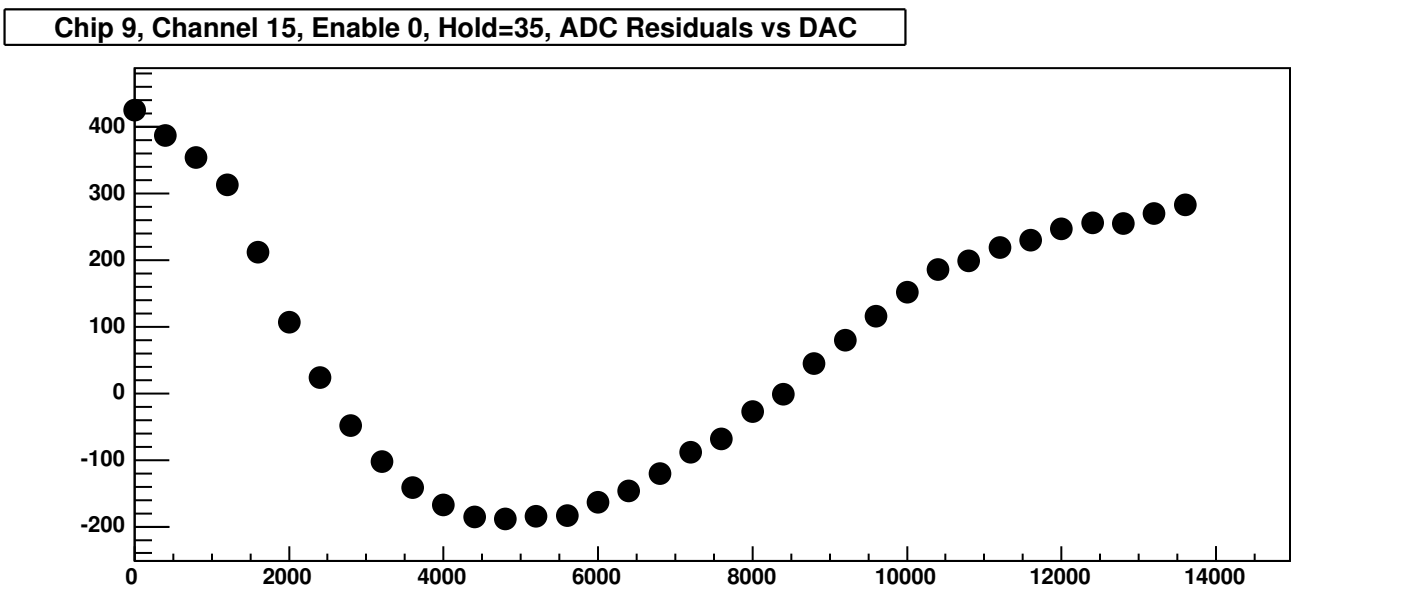


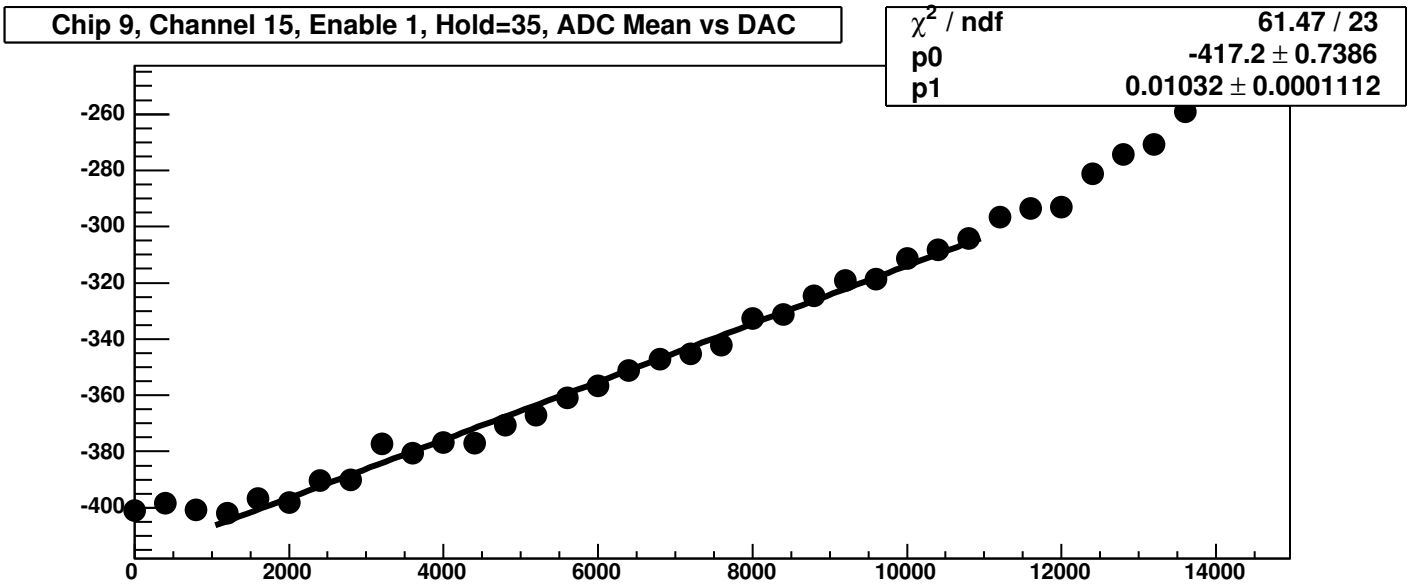
Chip 9, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

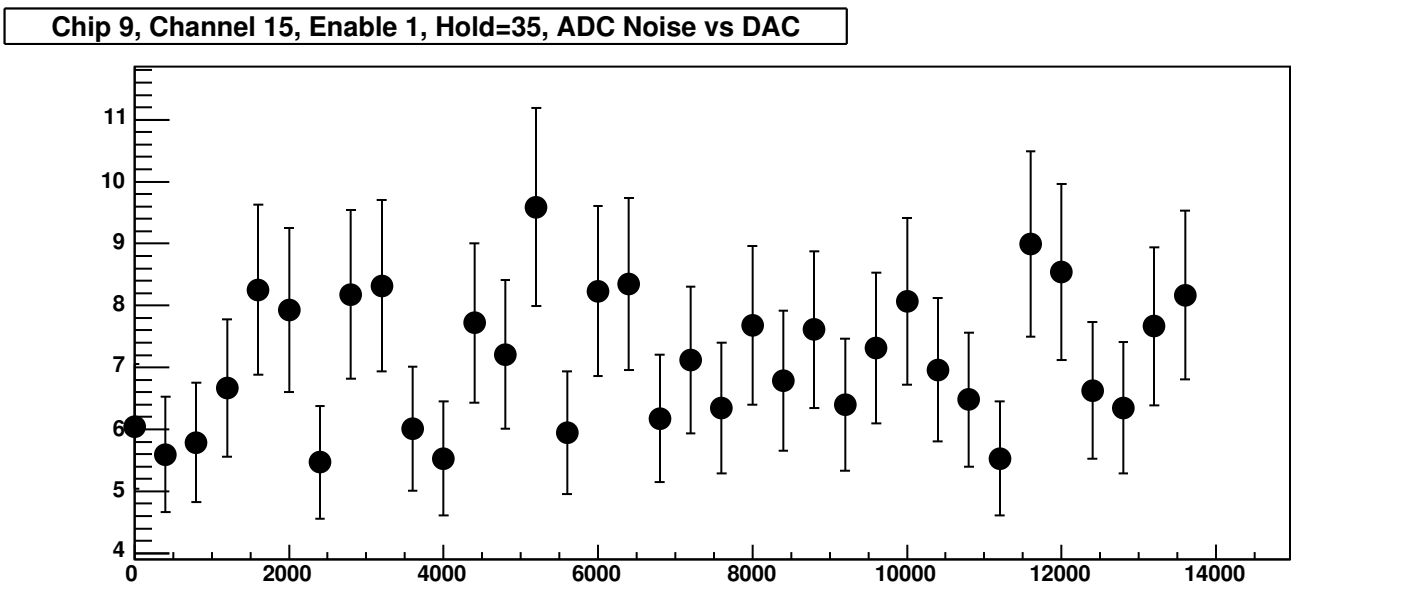


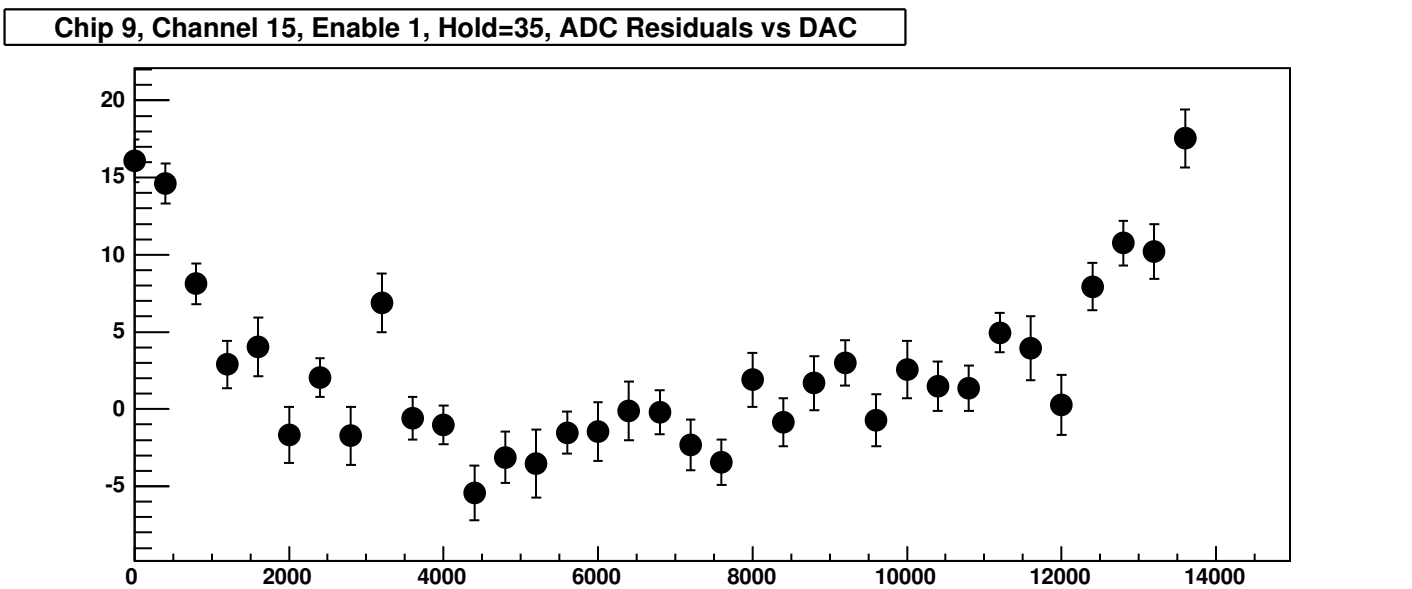

Chip 9, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.048e+05 / 23
p0 -809.7 ± 0.946
p1 0.1 ± 0.0001661
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

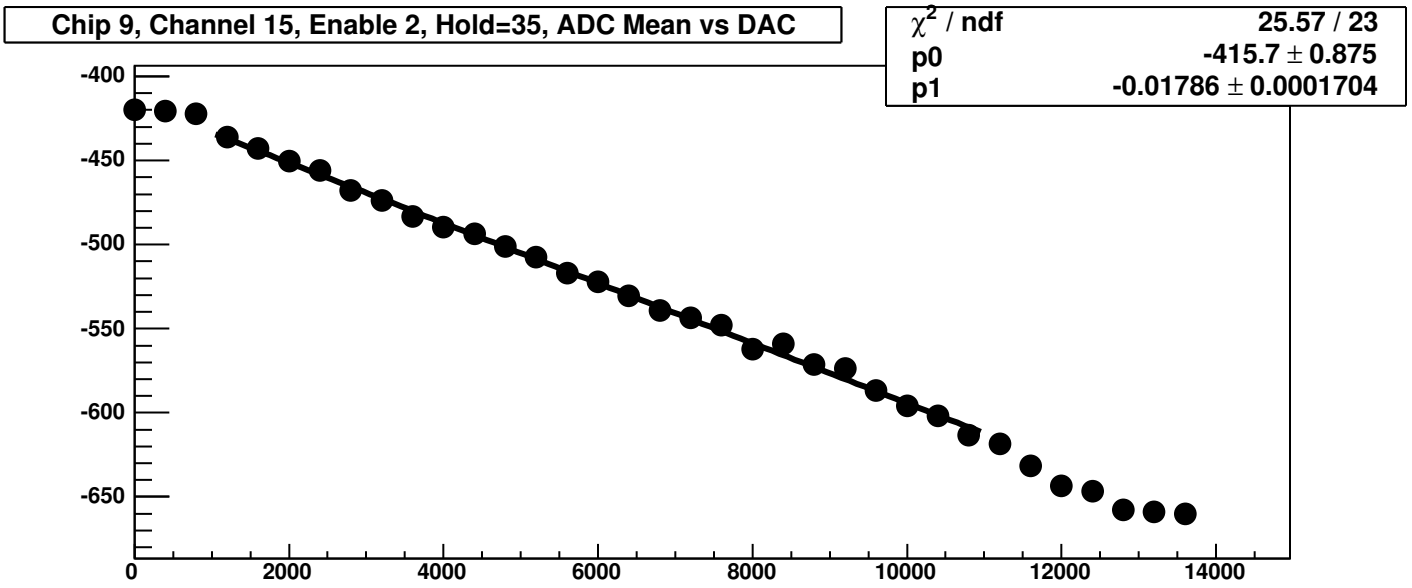

Chip 9, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

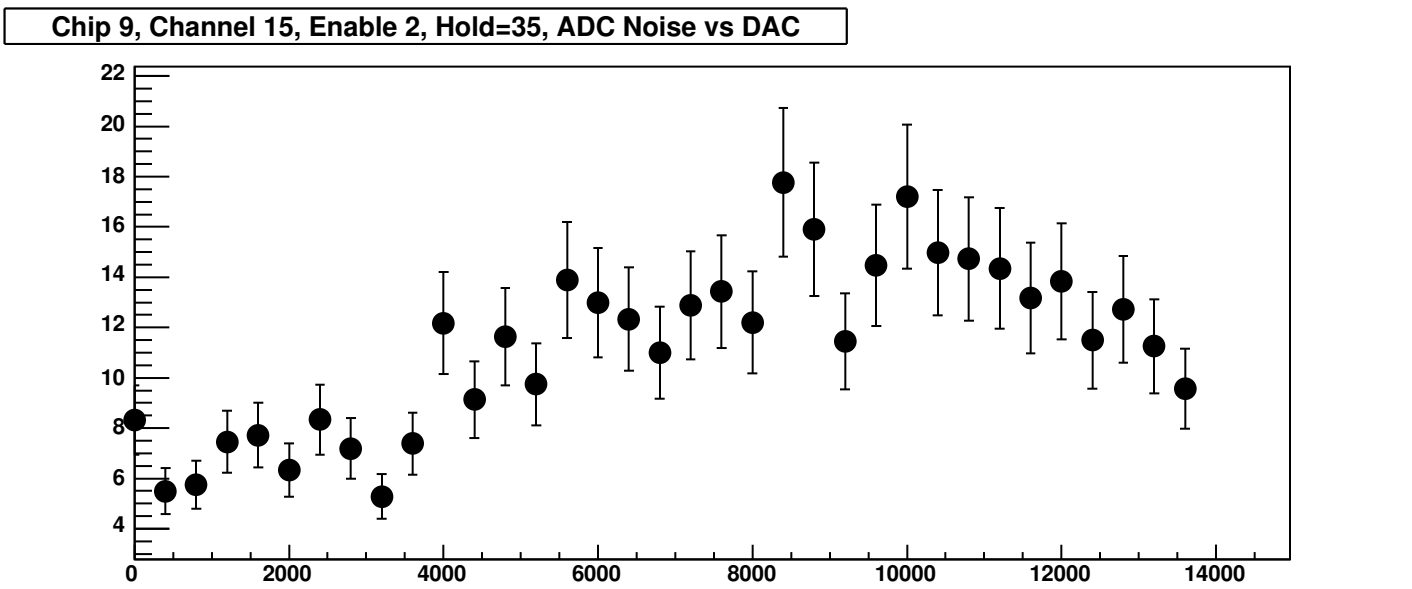

Chip 9, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

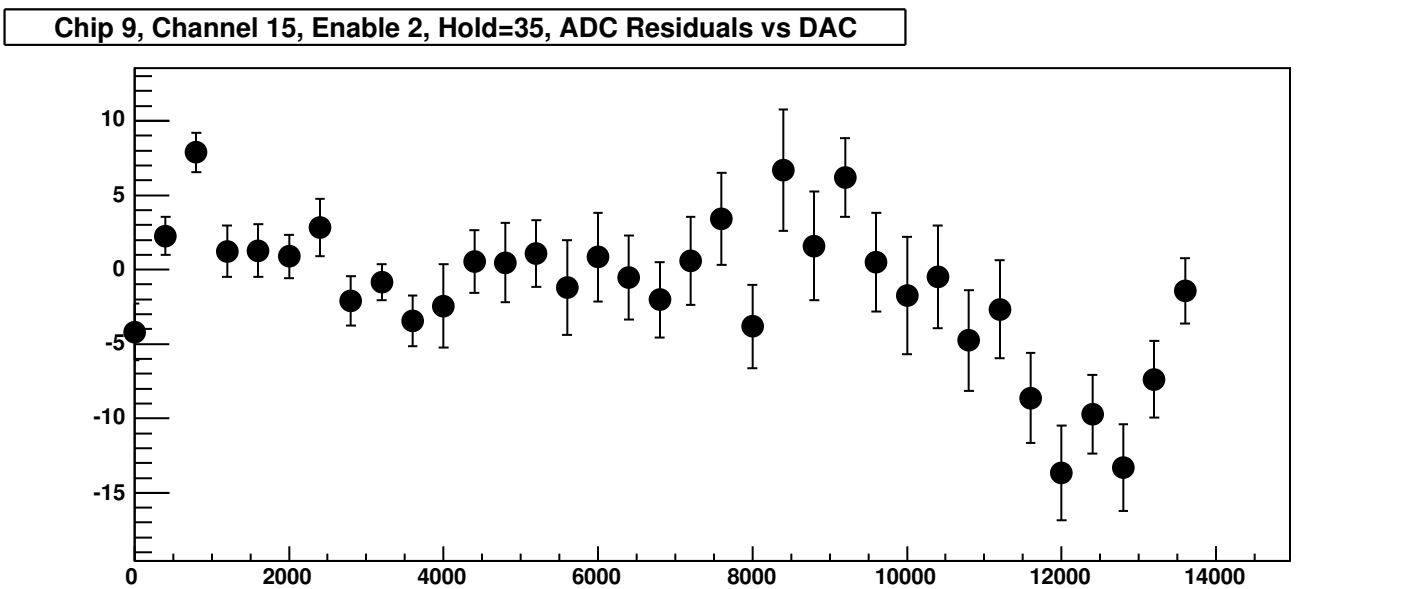

Chip 9, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 61.47 / 23
p0 -417.2 ± 0.7386
p1 0.01032 ± 0.0001112
-260
-280
-300
-320
-340
-360
-380
-400
0
2000
4000
6000
8000
10000
12000
14000

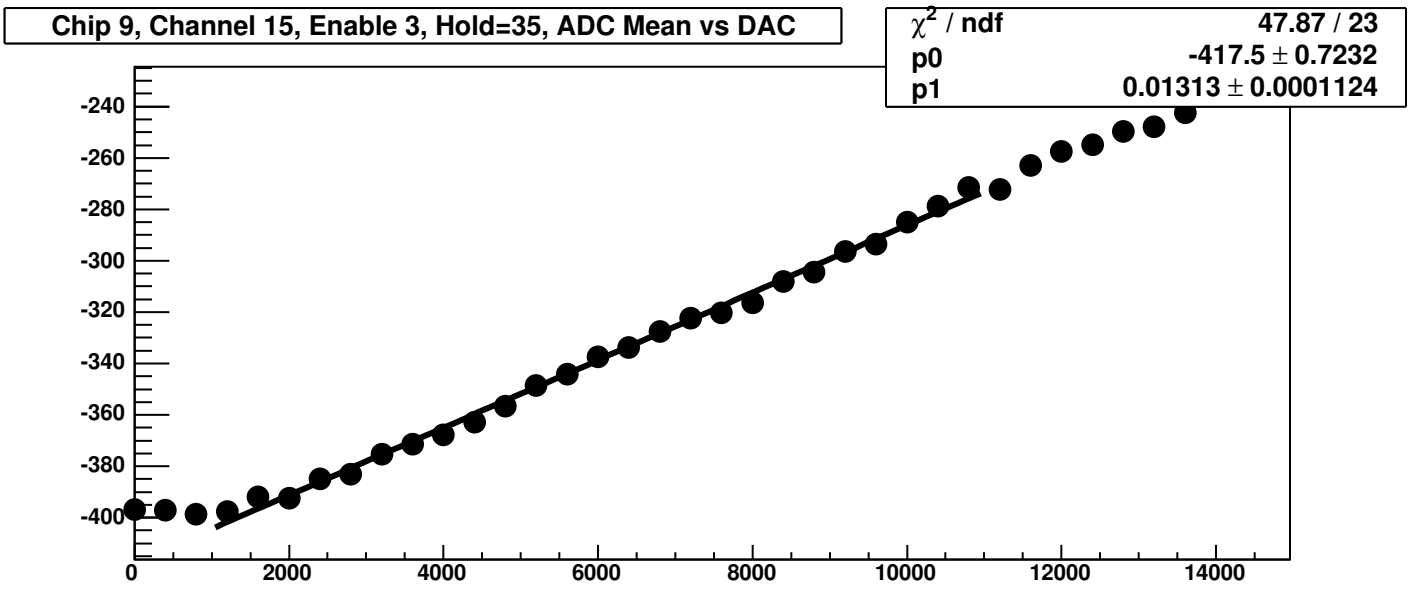

Chip 9, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

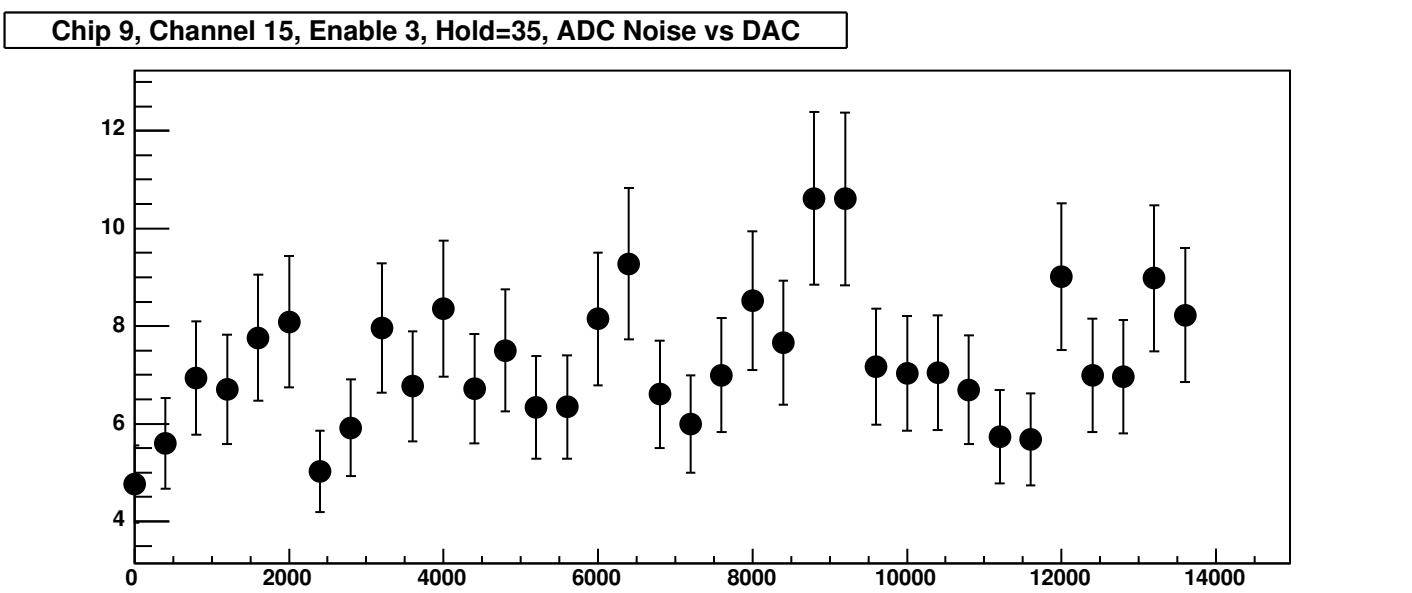

Chip 9, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

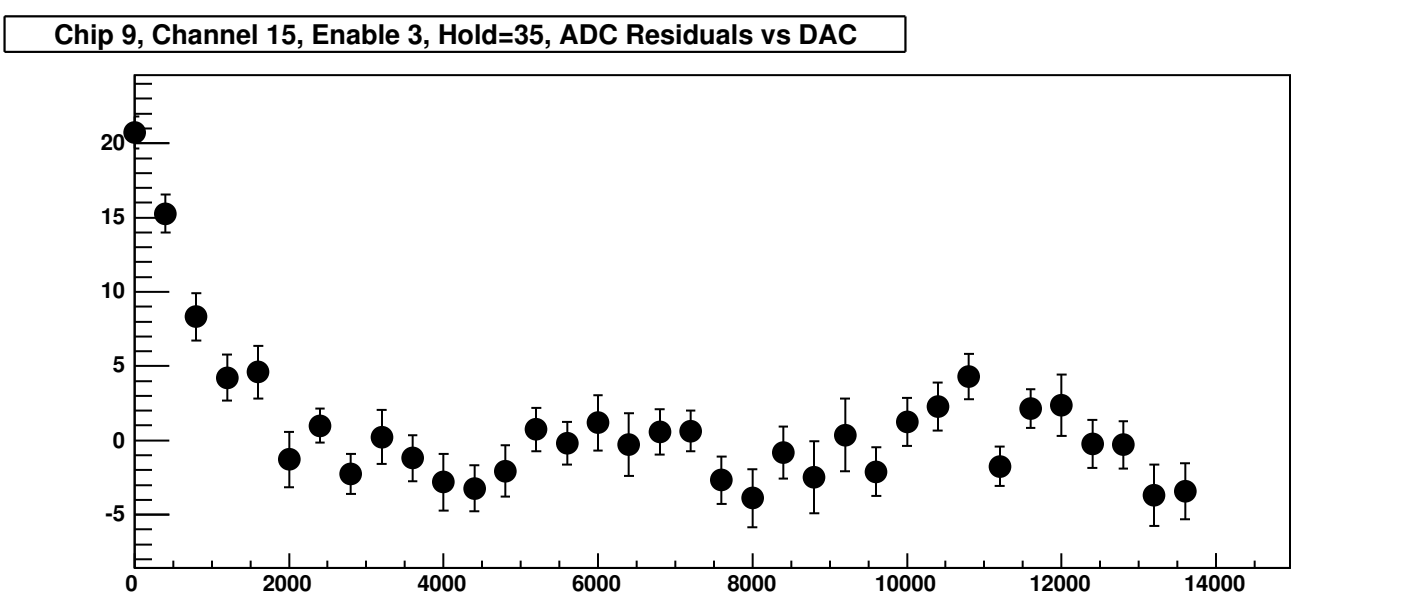

Chip 9, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 25.57 / 23
p0 -415.7 ± 0.875
p1 -0.01786 ± 0.0001704
-400
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

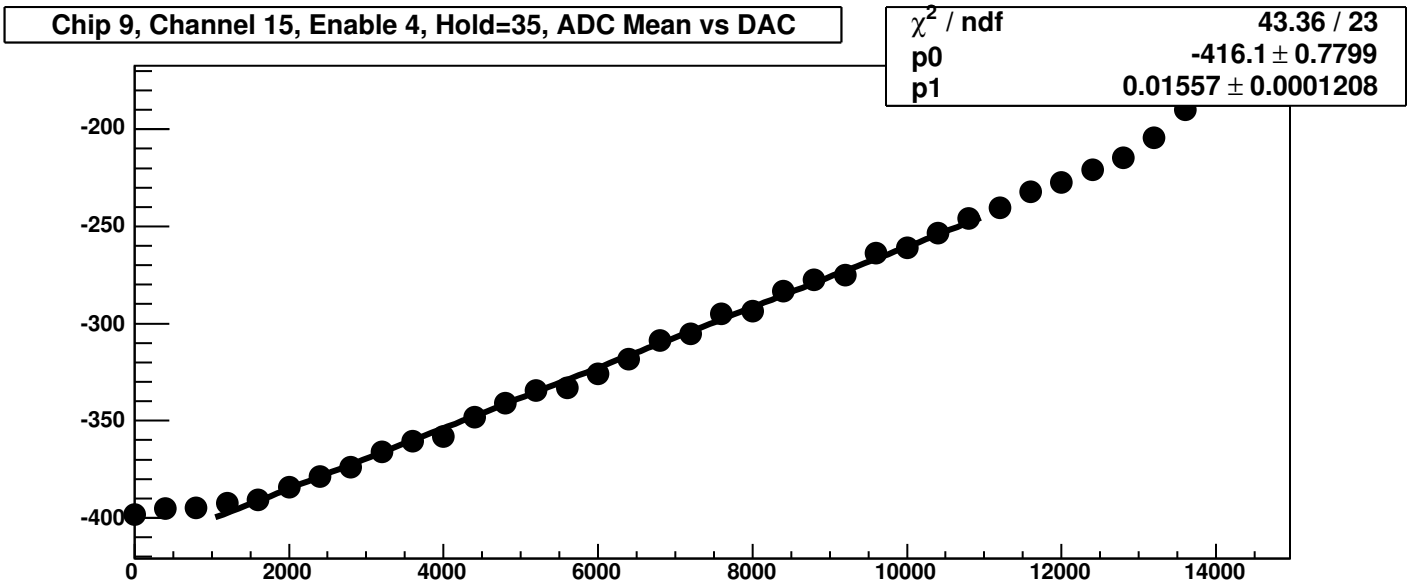

Chip 9, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

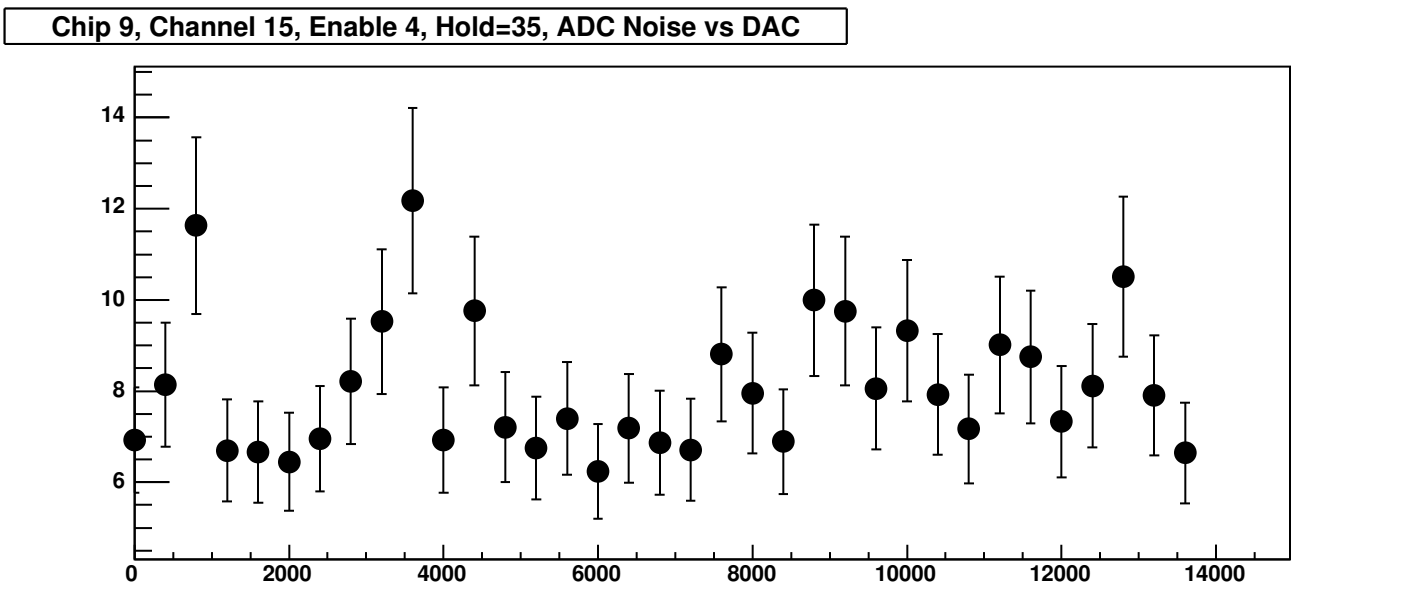

Chip 9, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

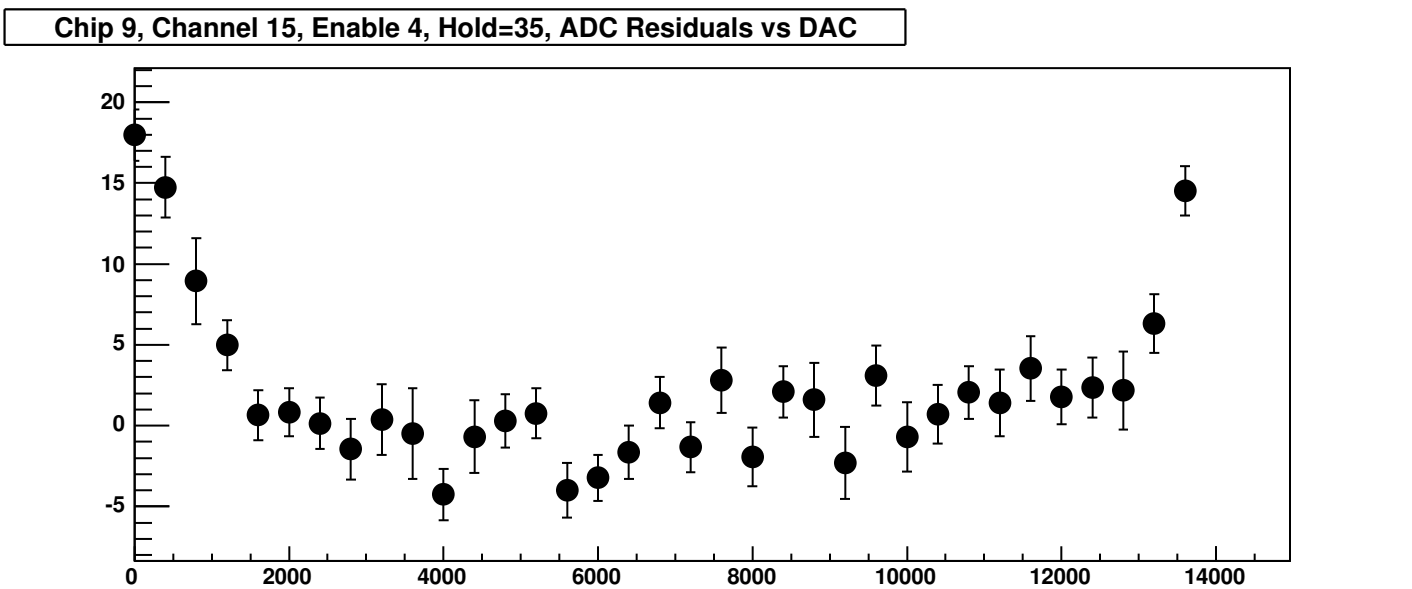

Chip 9, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 47.87 / 23
p0 -417.5 ± 0.7232
p1 0.01313 ± 0.0001124
-240
-260
-280
-300
-320
-340
-360
-380
-400
0
2000
4000
6000
8000
10000
12000
14000

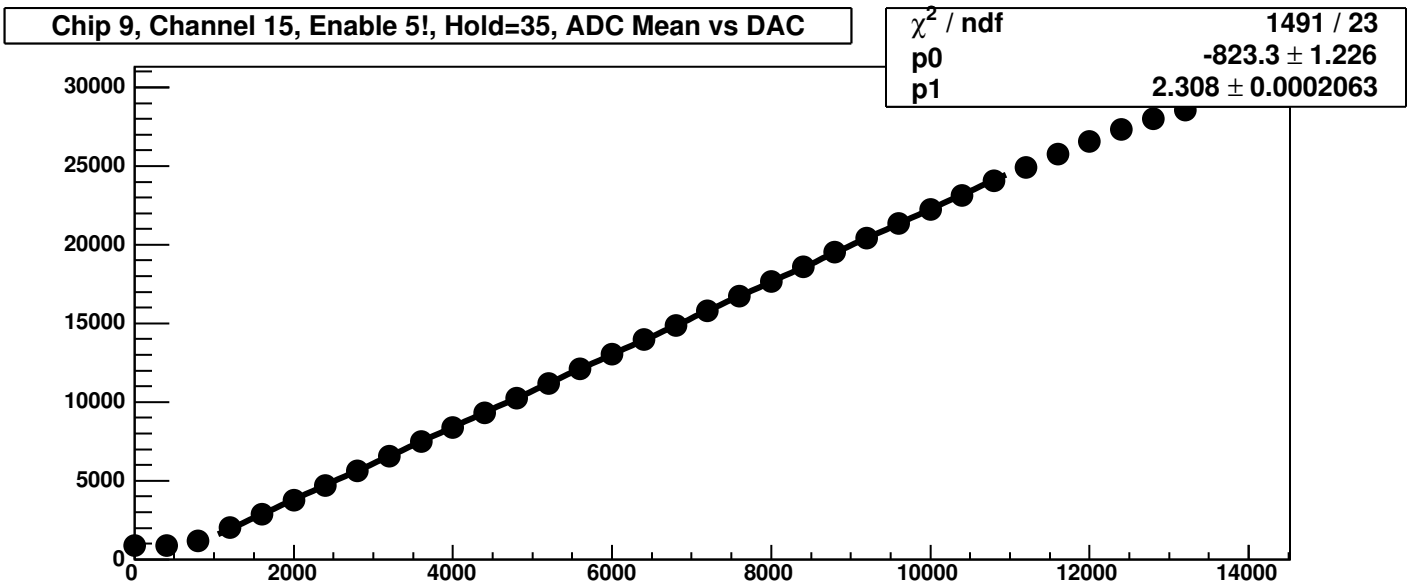

Chip 9, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

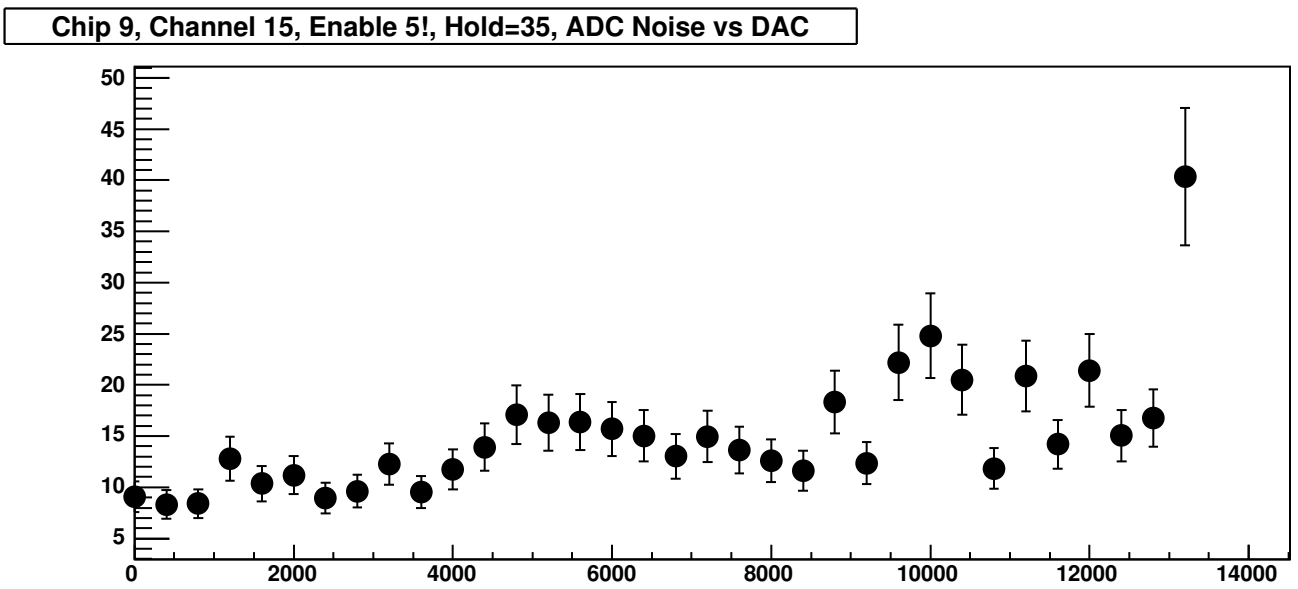

Chip 9, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

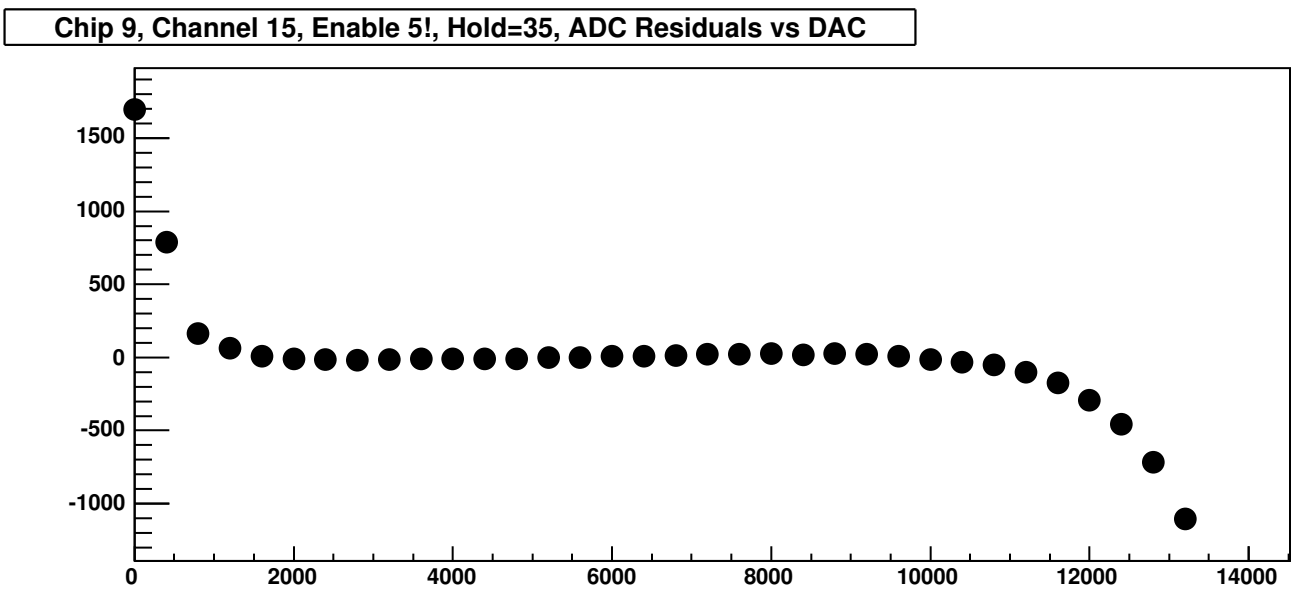

Chip 9, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 43.36 / 23
p0 -416.1 ± 0.7799
p1 0.01557 ± 0.0001208
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

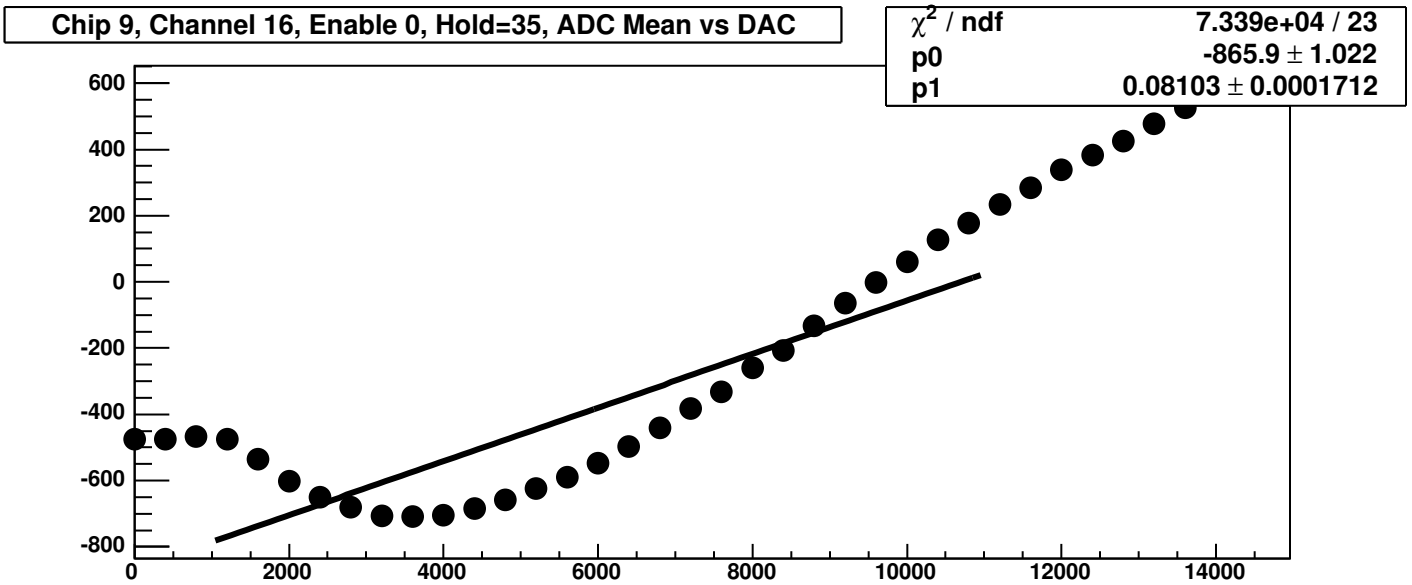

Chip 9, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

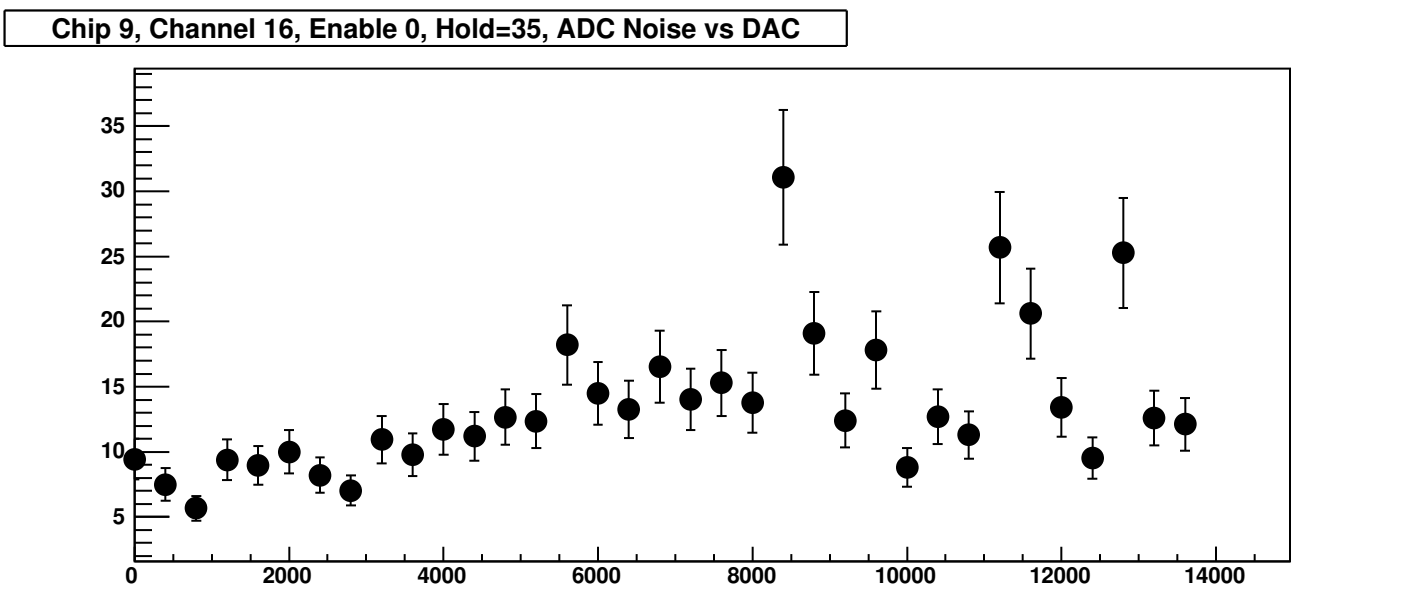

Chip 9, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

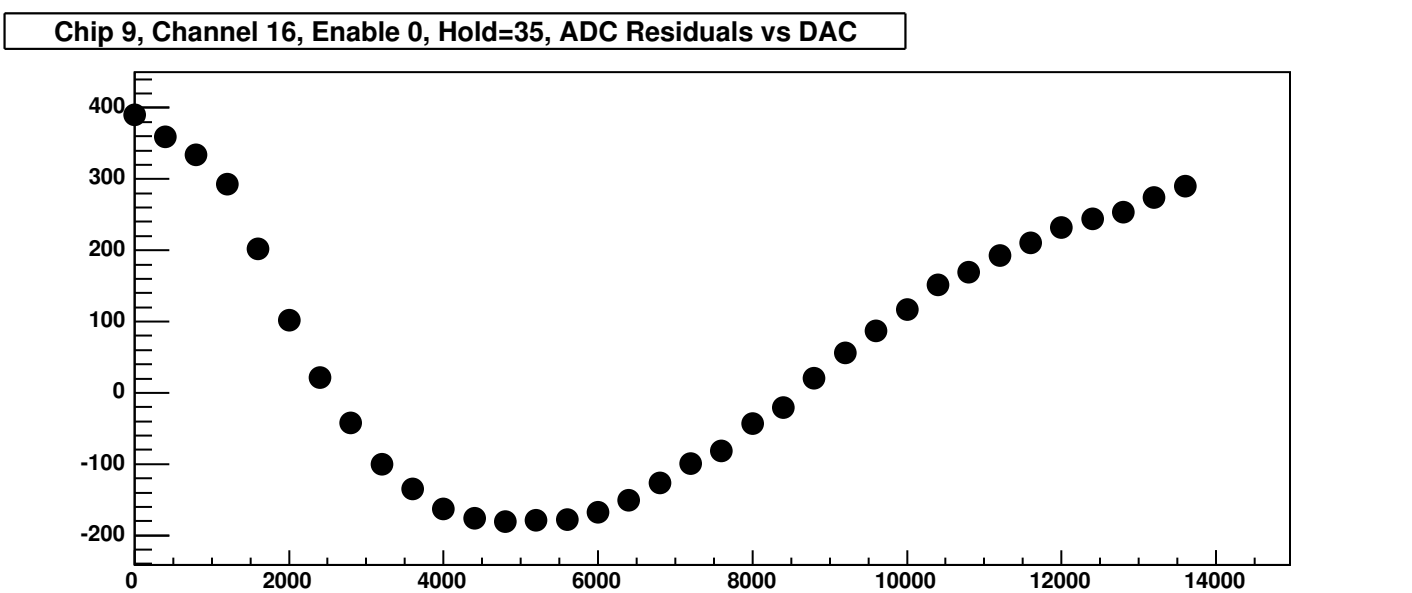

Chip 9, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1491 / 23
p0 -823.3 ± 1.226
p1 2.308 ± 0.0002063
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

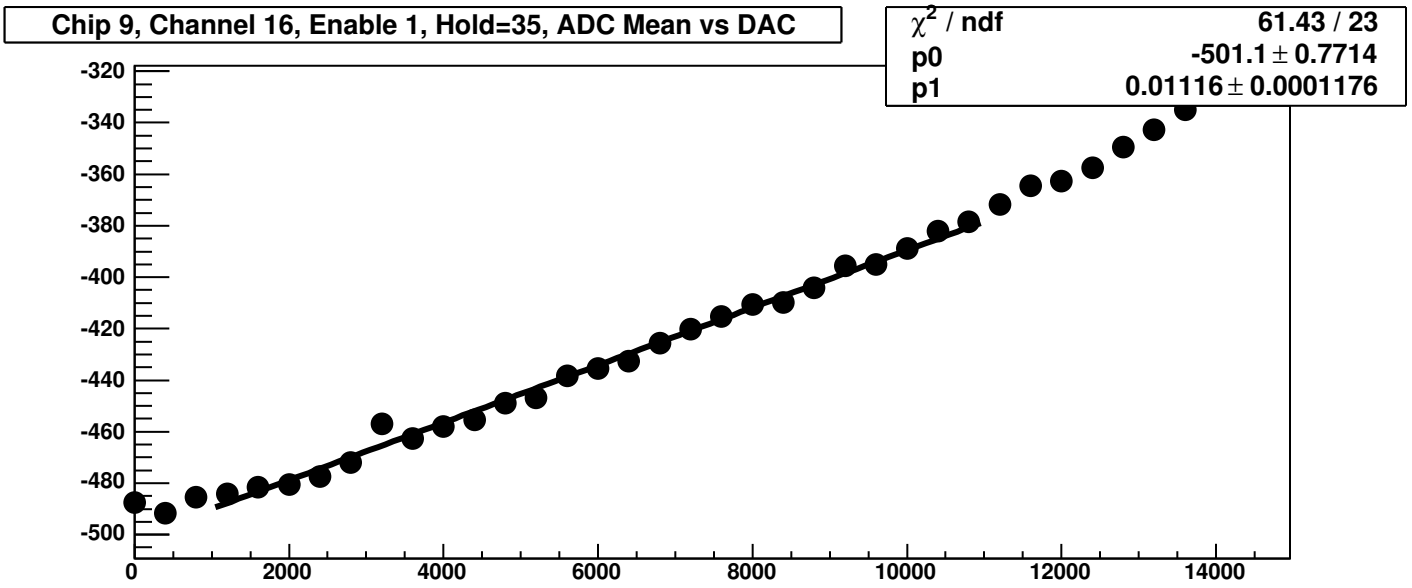

Chip 9, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

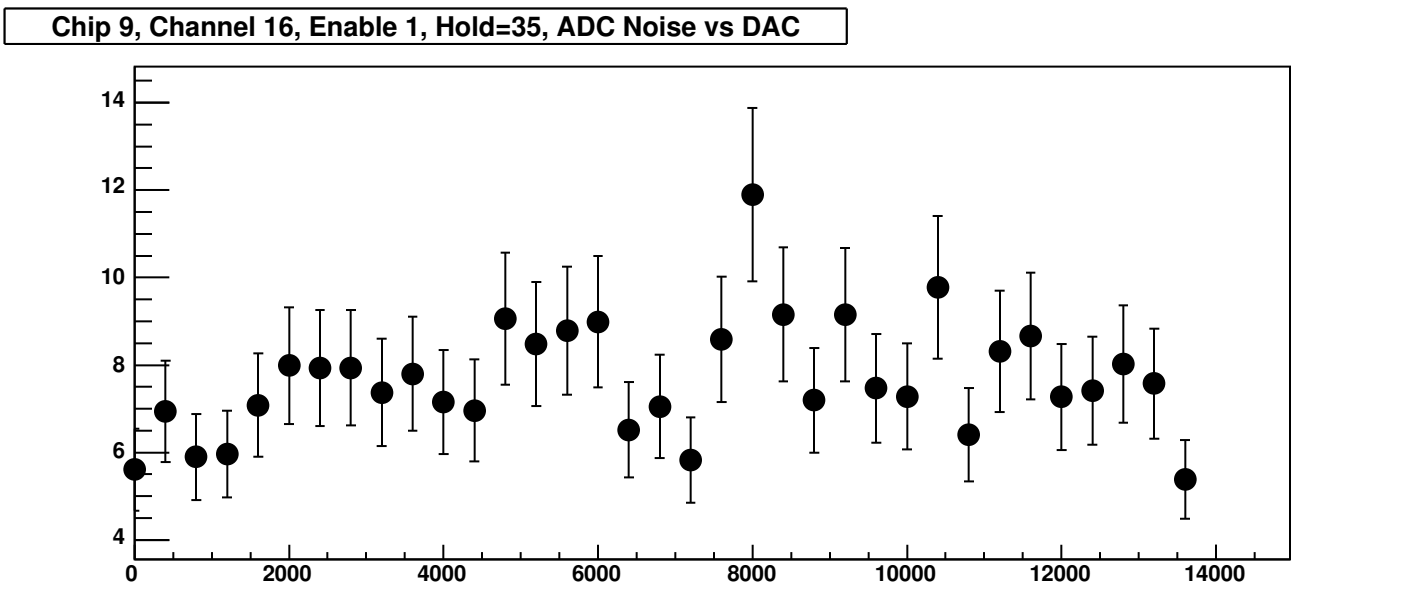

Chip 9, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

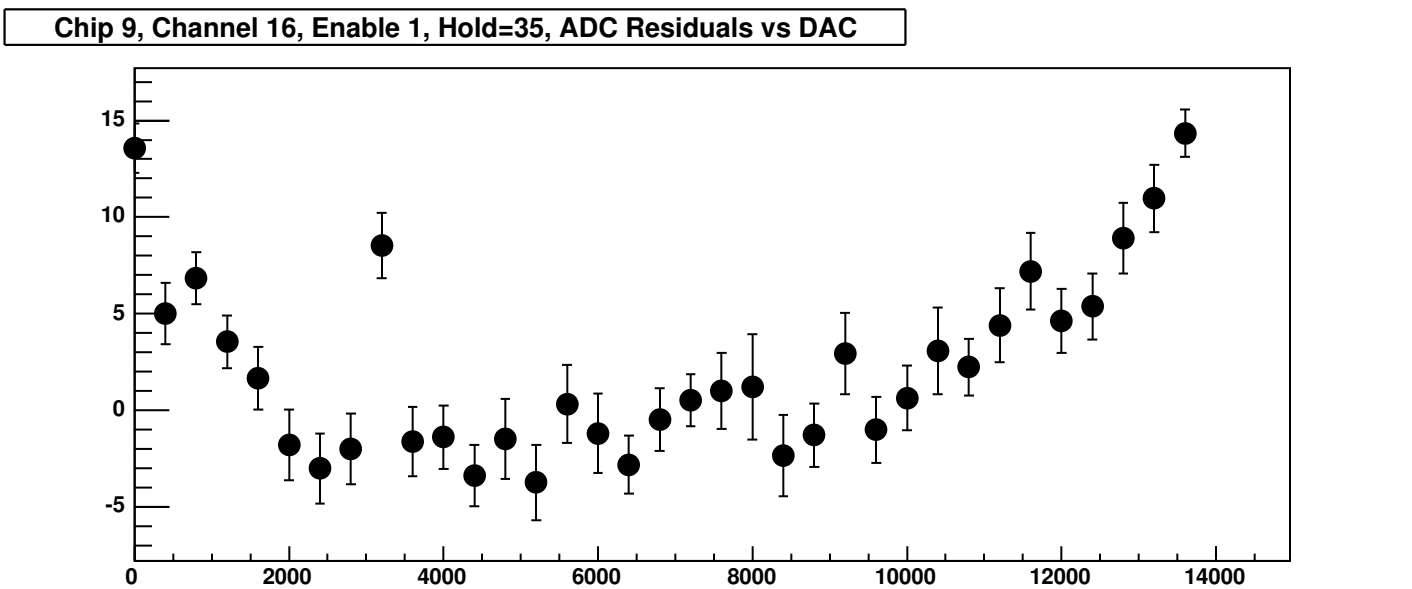

Chip 9, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 7.339e+04 / 23
p0 -865.9 ± 1.022
p1 0.08103 ± 0.0001712
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

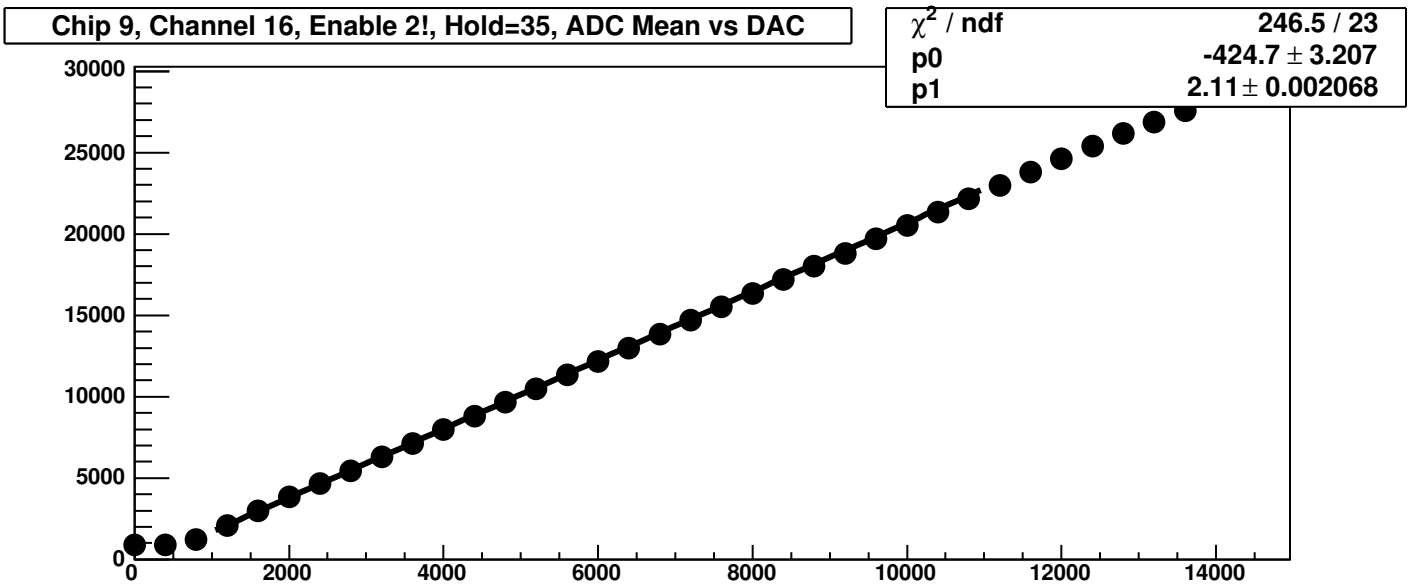

Chip 9, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

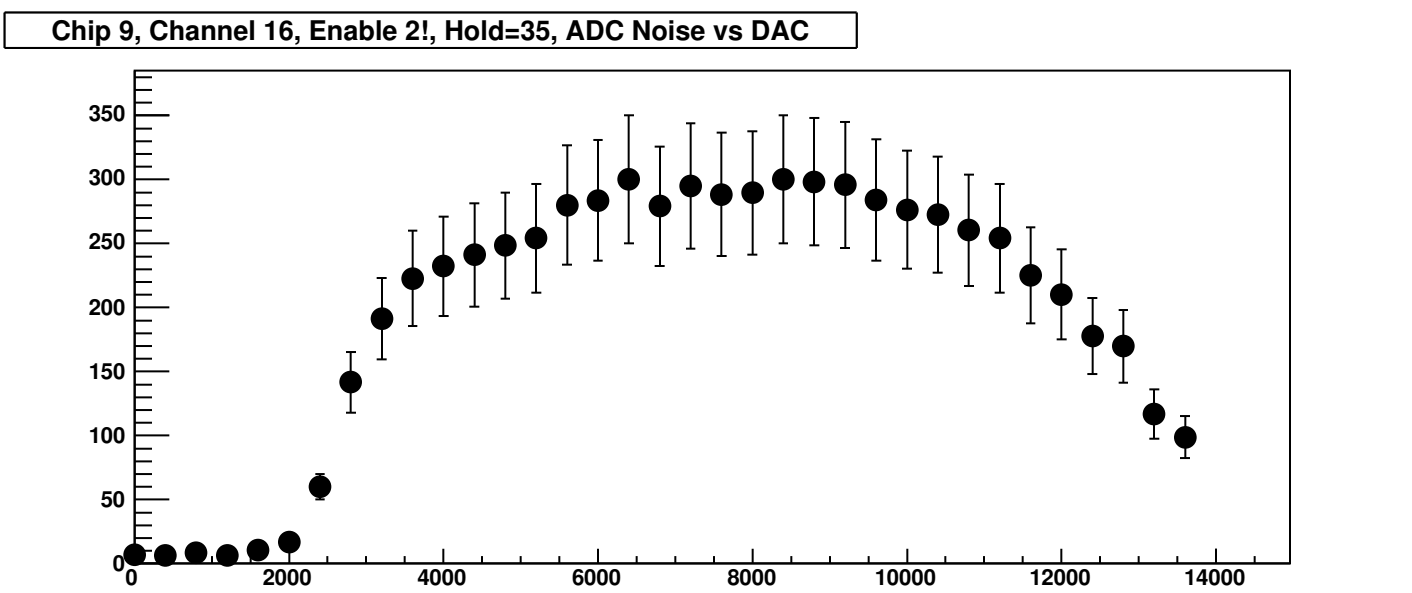

Chip 9, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

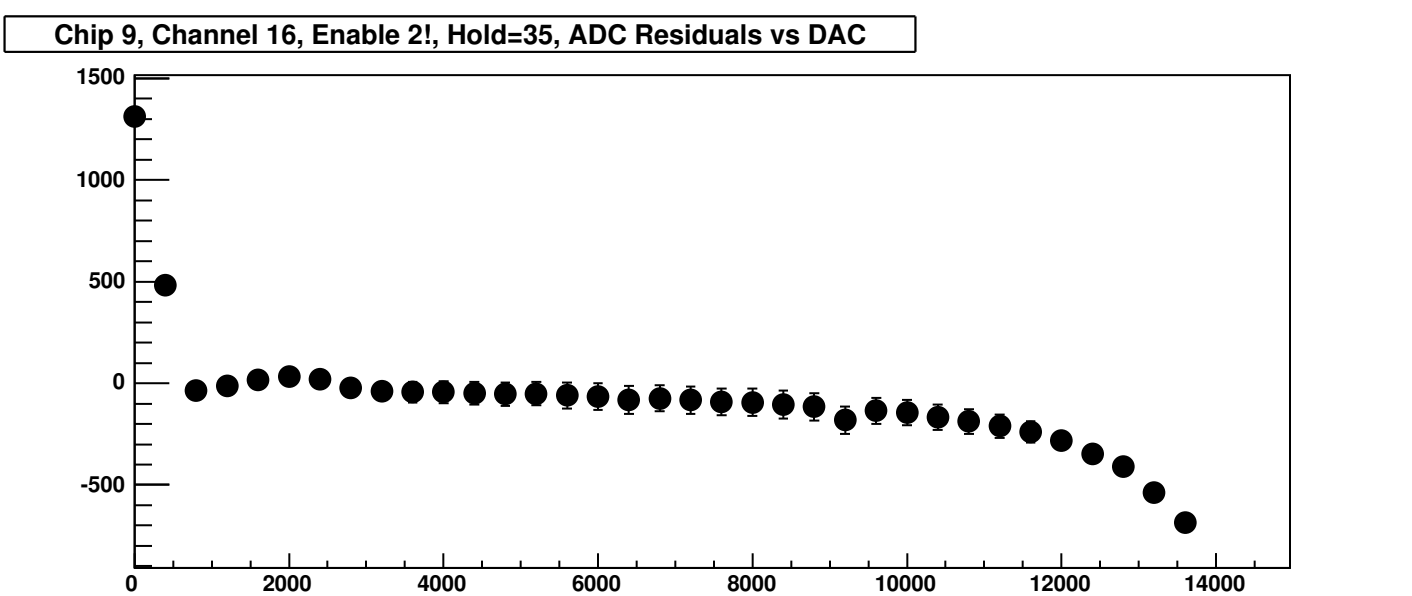

Chip 9, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 61.43 / 23
p0 -501.1 ± 0.7714
p1 0.01116 ± 0.0001176
-320
-340
-360
-380
-400
-420
-440
-460
-480
-500
0
2000
4000
6000
8000
10000
12000
14000

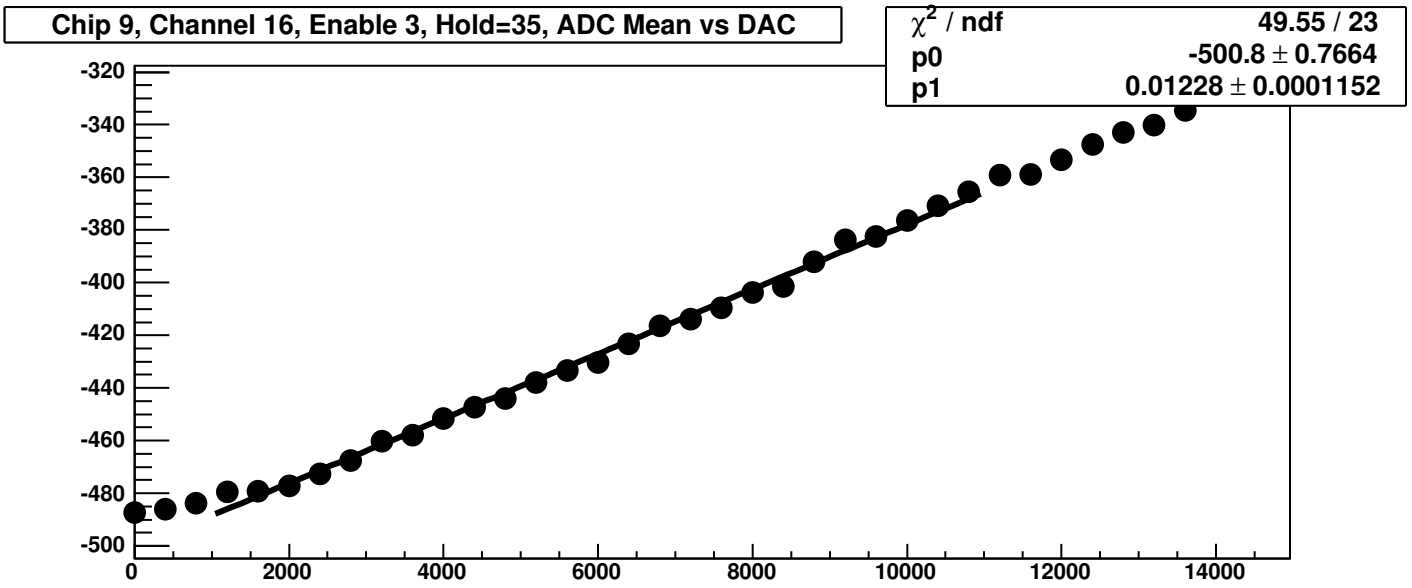

Chip 9, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

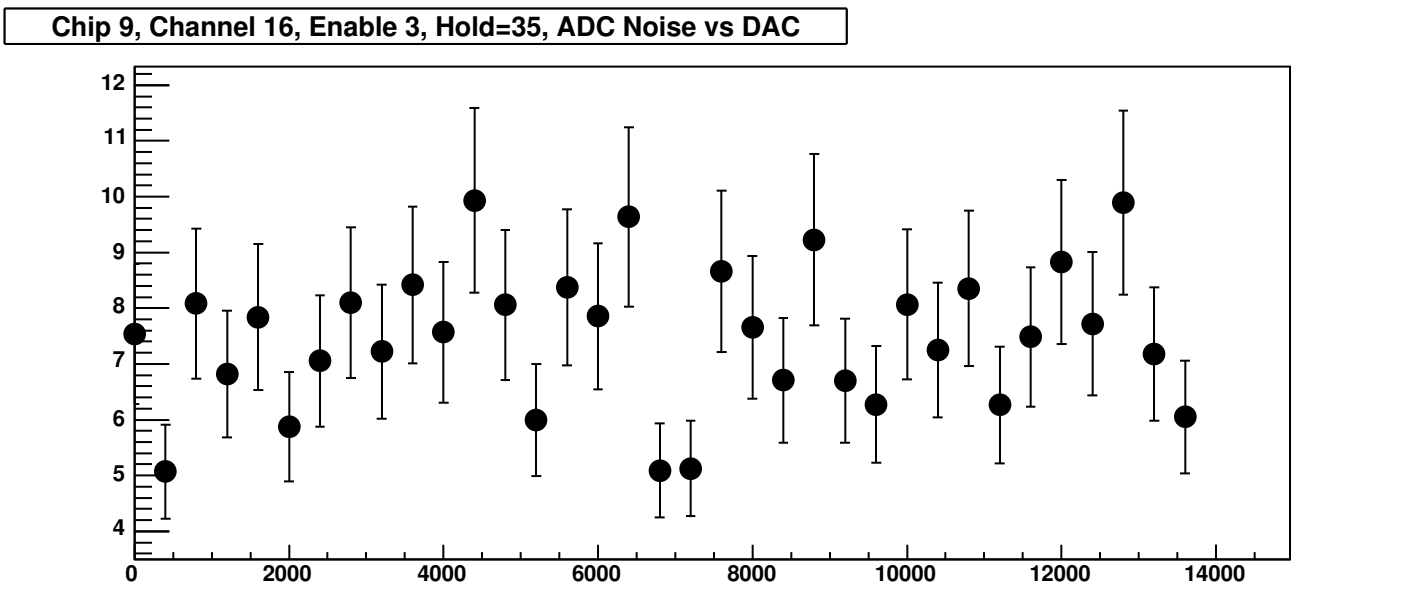

Chip 9, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

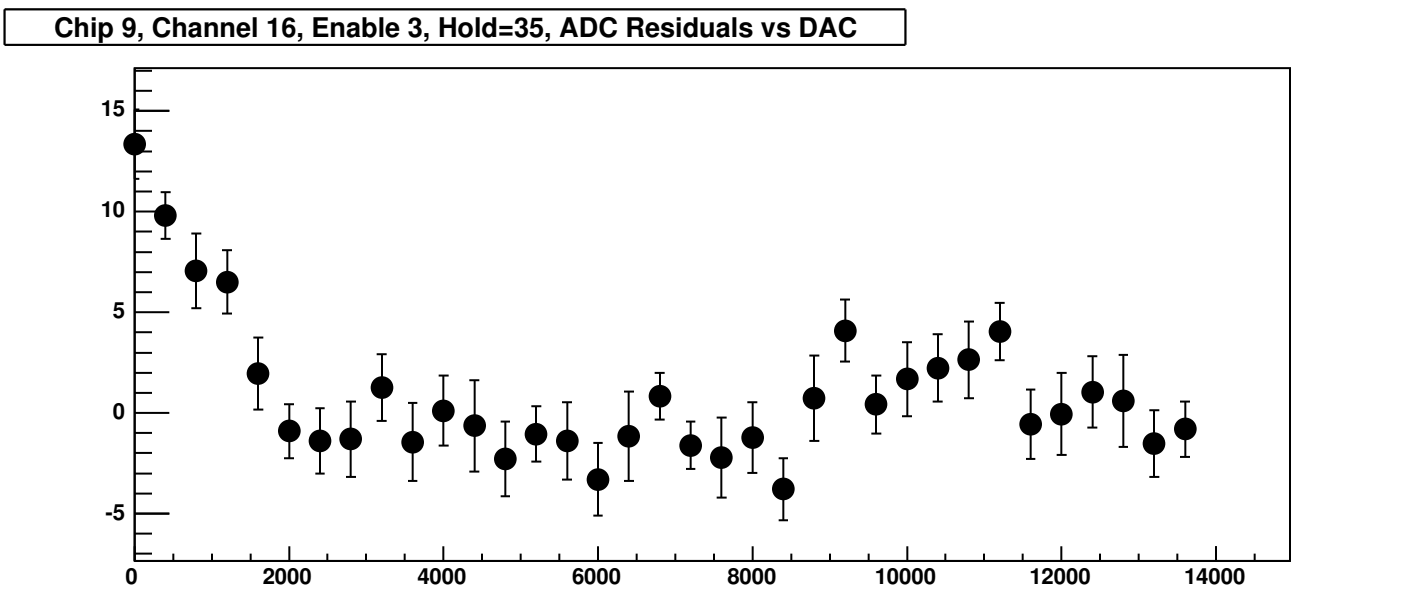

Chip 9, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 246.5 / 23
p0 -424.7 ± 3.207
p1 2.11 ± 0.002068
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

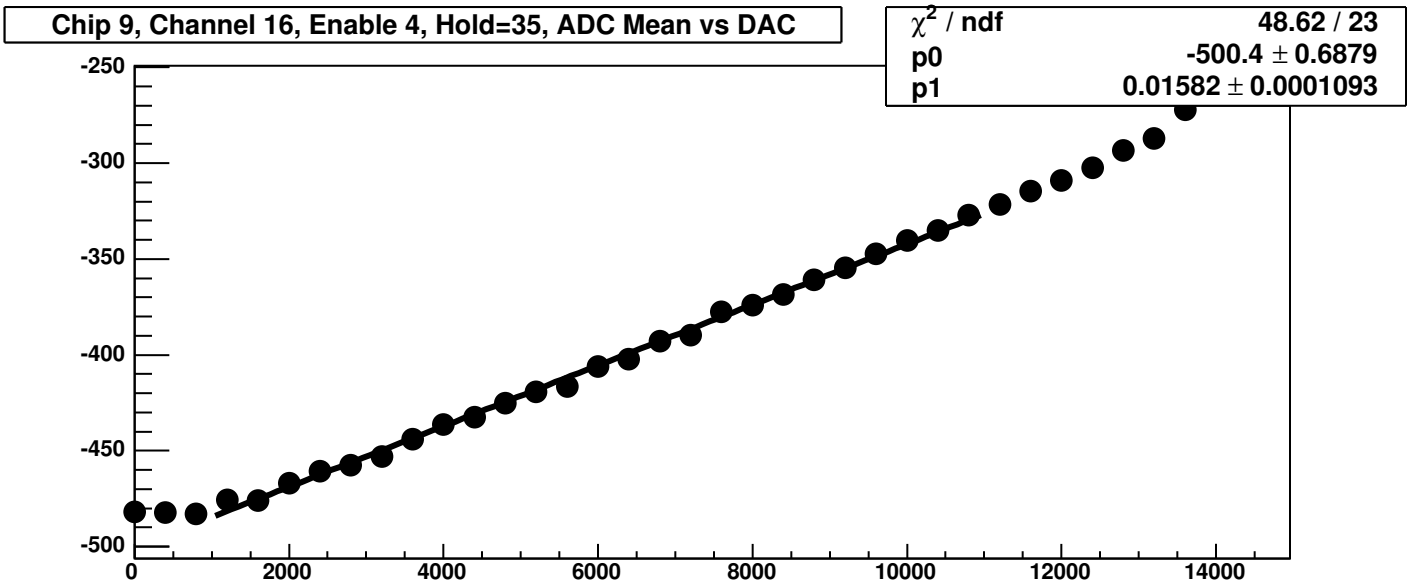

Chip 9, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

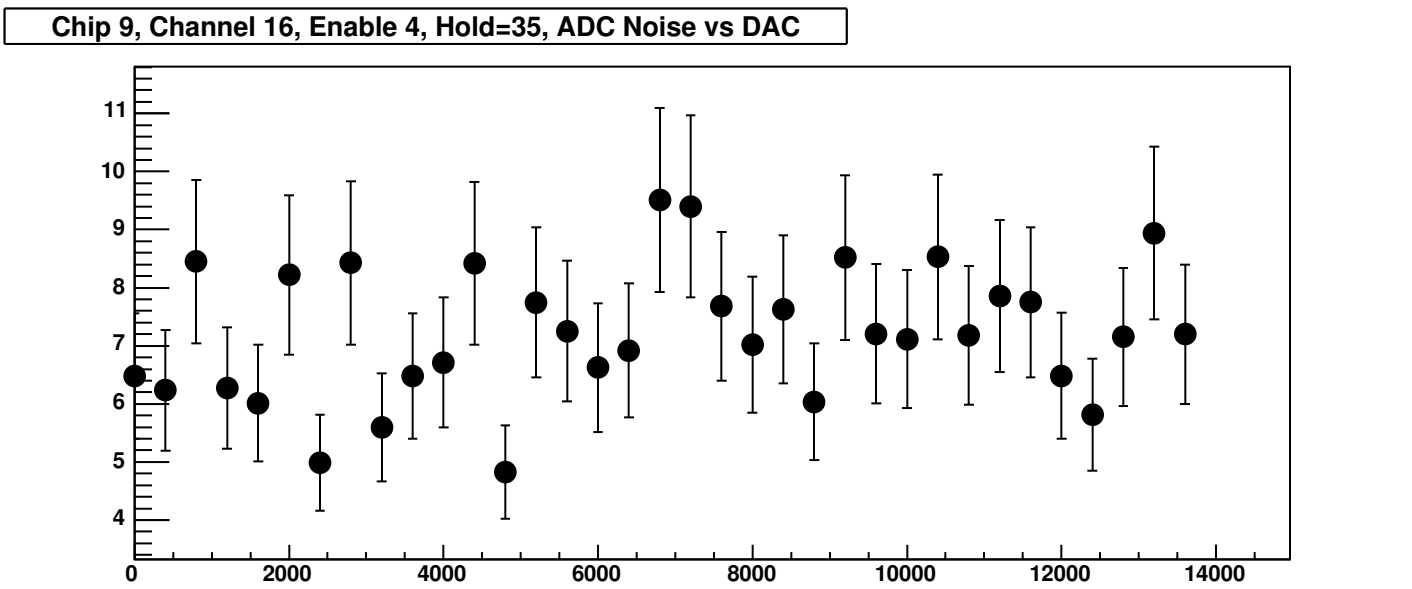

Chip 9, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

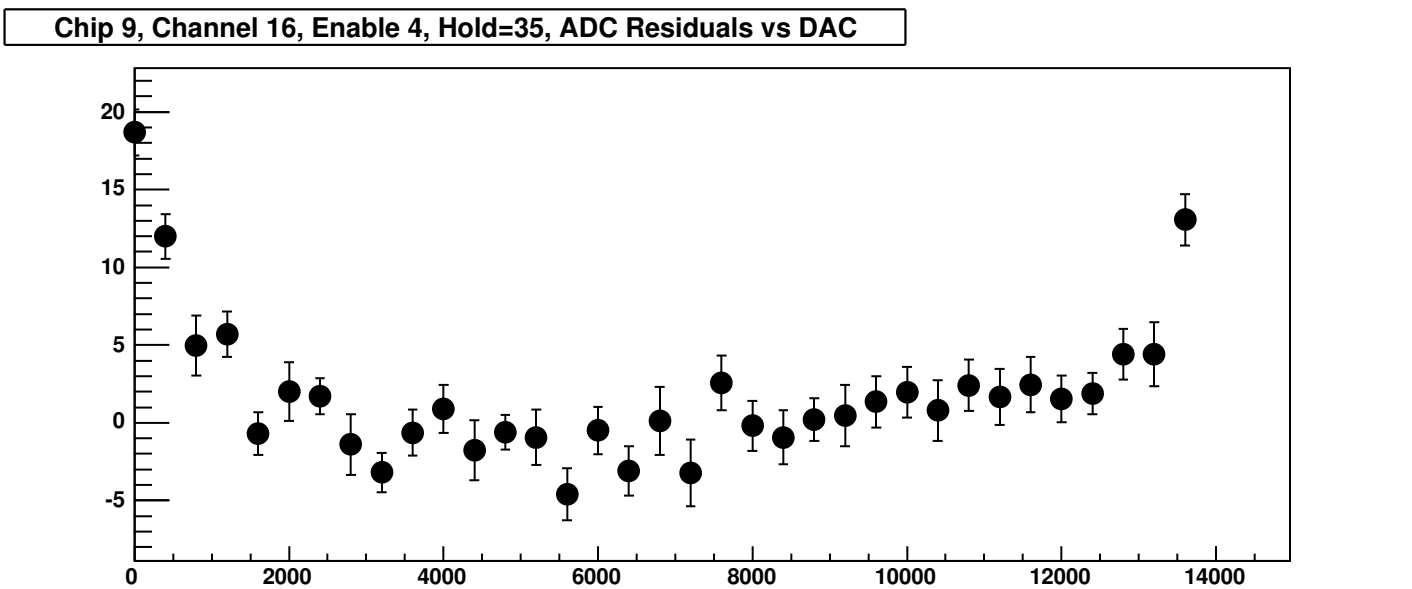

Chip 9, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 49.55 / 23
p0 -500.8 ± 0.7664
p1 0.01228 ± 0.0001152
-320
-340
-360
-380
-400
-420
-440
-460
-480
-500
0
2000
4000
6000
8000
10000
12000
14000

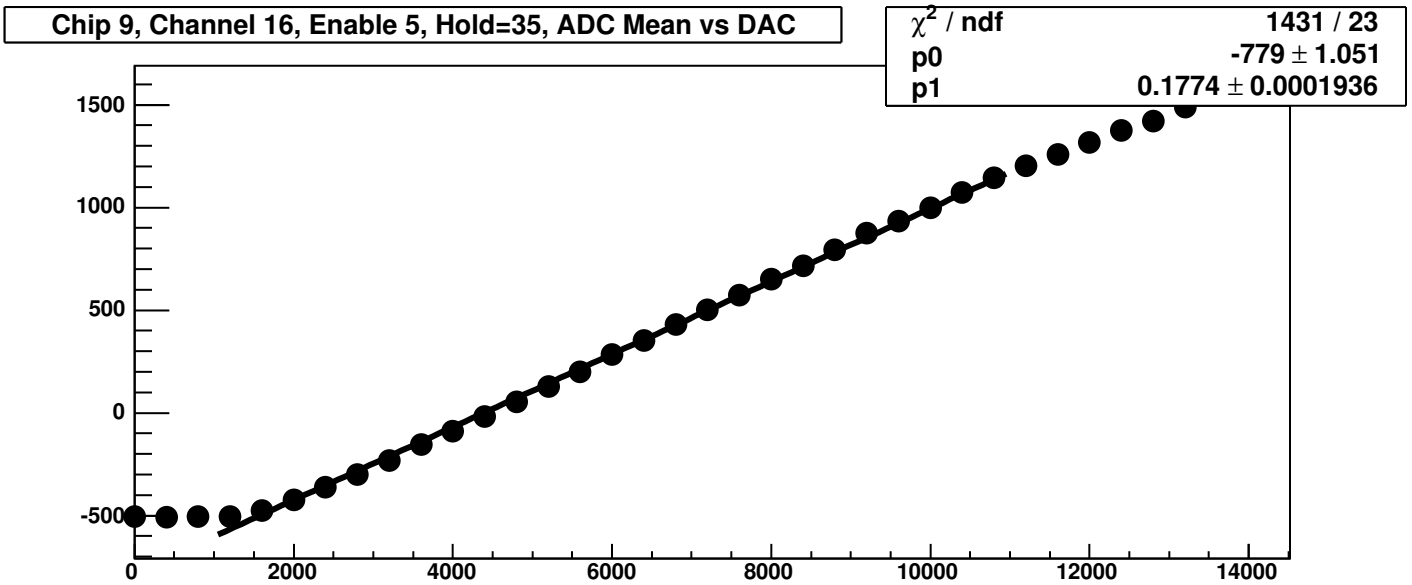

Chip 9, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

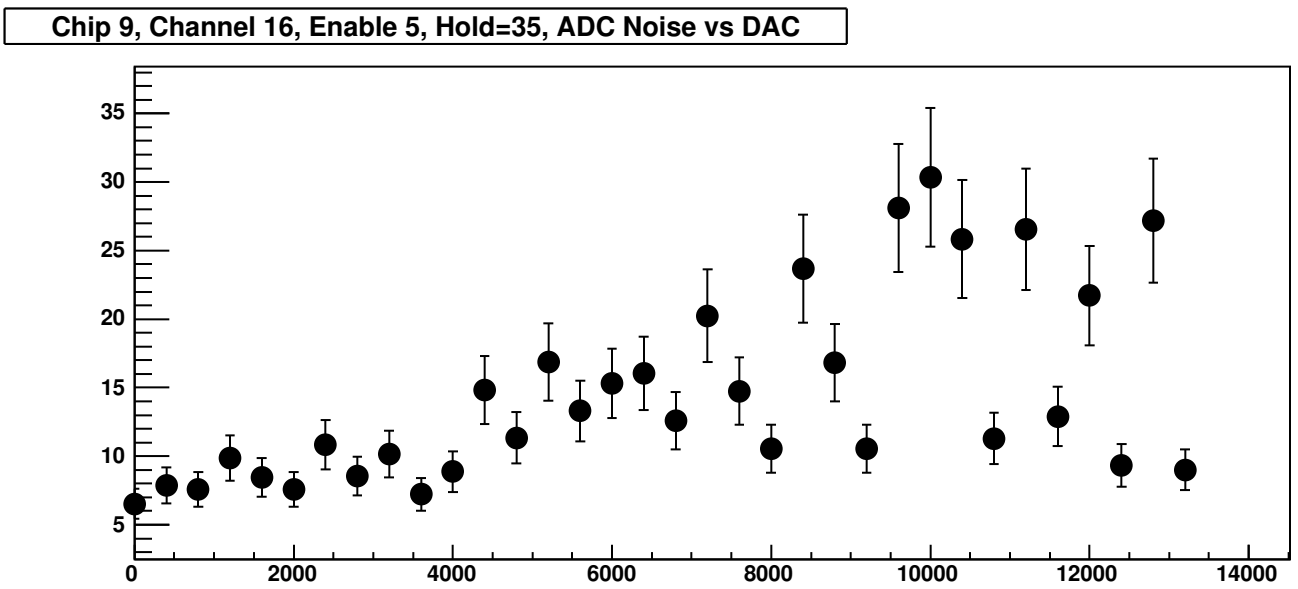

Chip 9, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

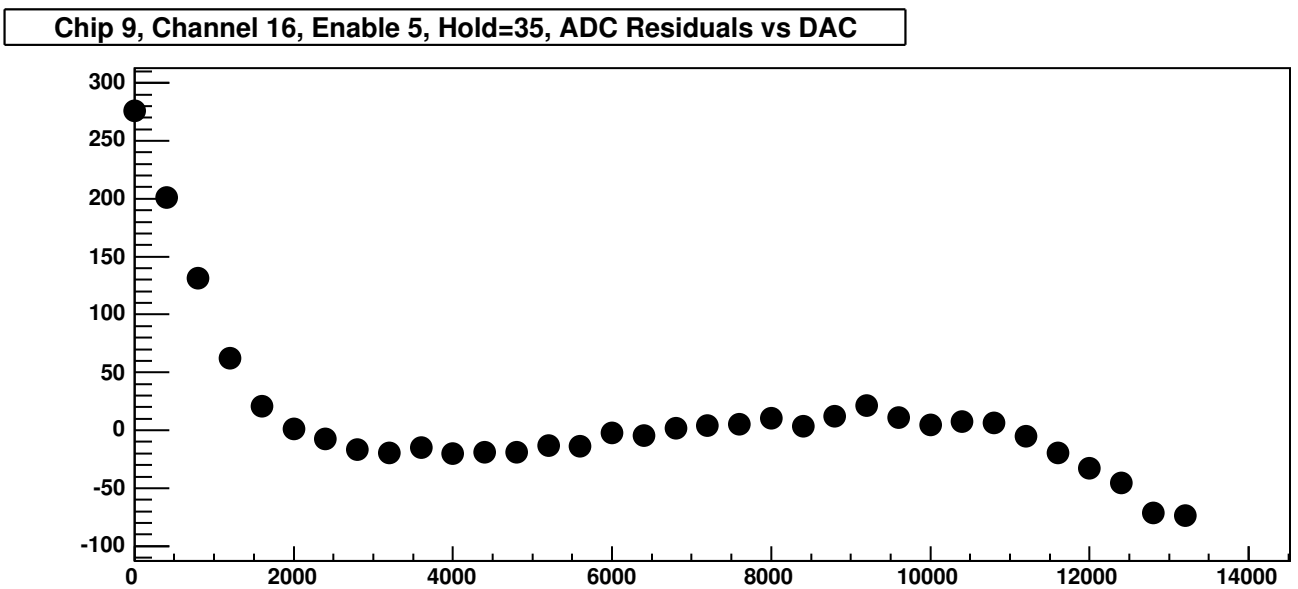

Chip 9, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 48.62 / 23
p0 -500.4 ± 0.6879
p1 0.01582 ± 0.0001093
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

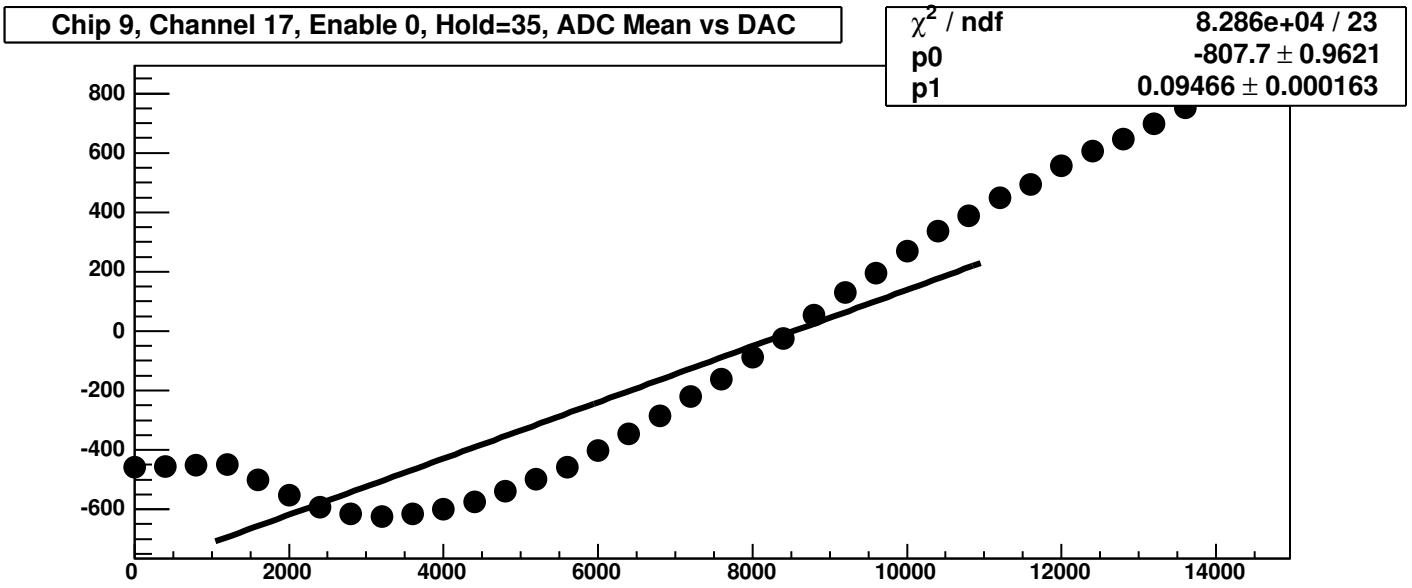

Chip 9, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

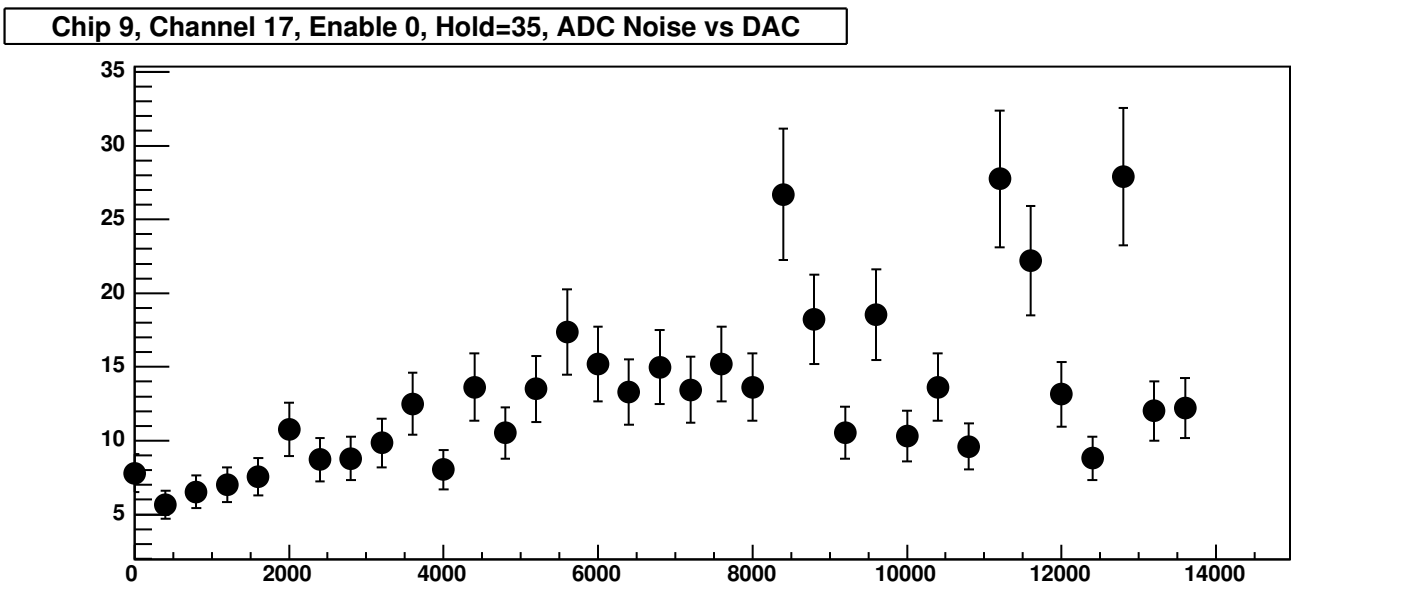

Chip 9, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

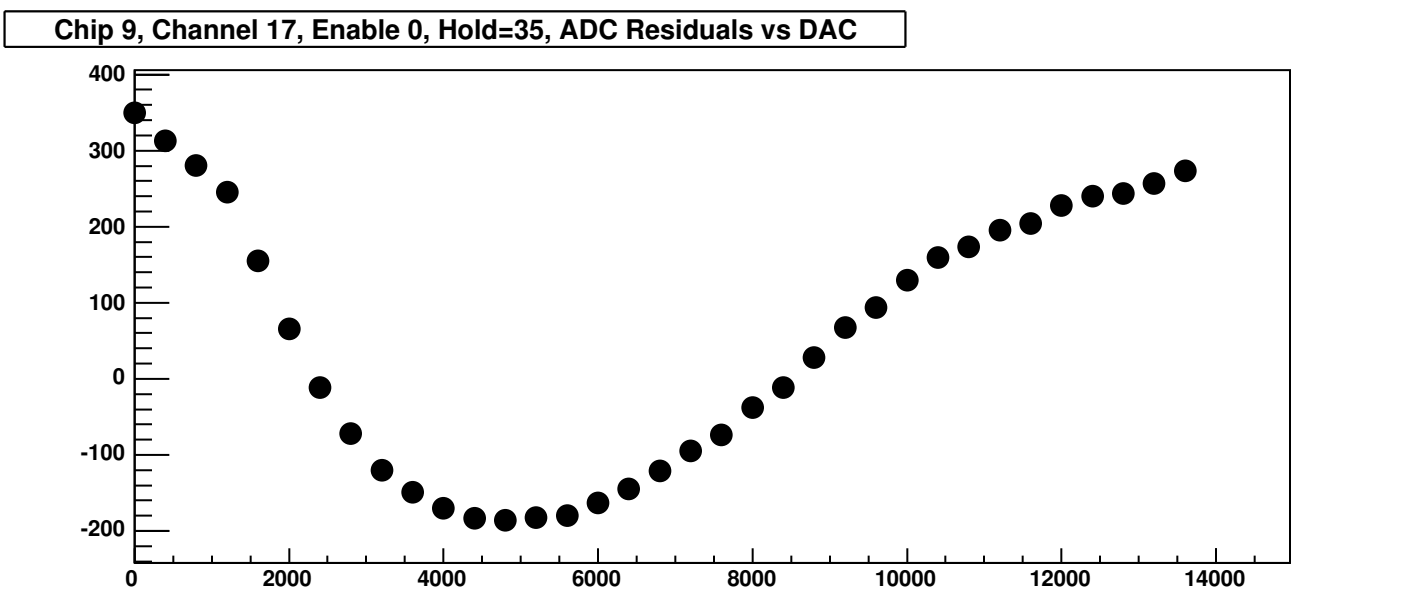

Chip 9, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1431 / 23
p0 -779 ± 1.051
p1 0.1774 ± 0.0001936
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

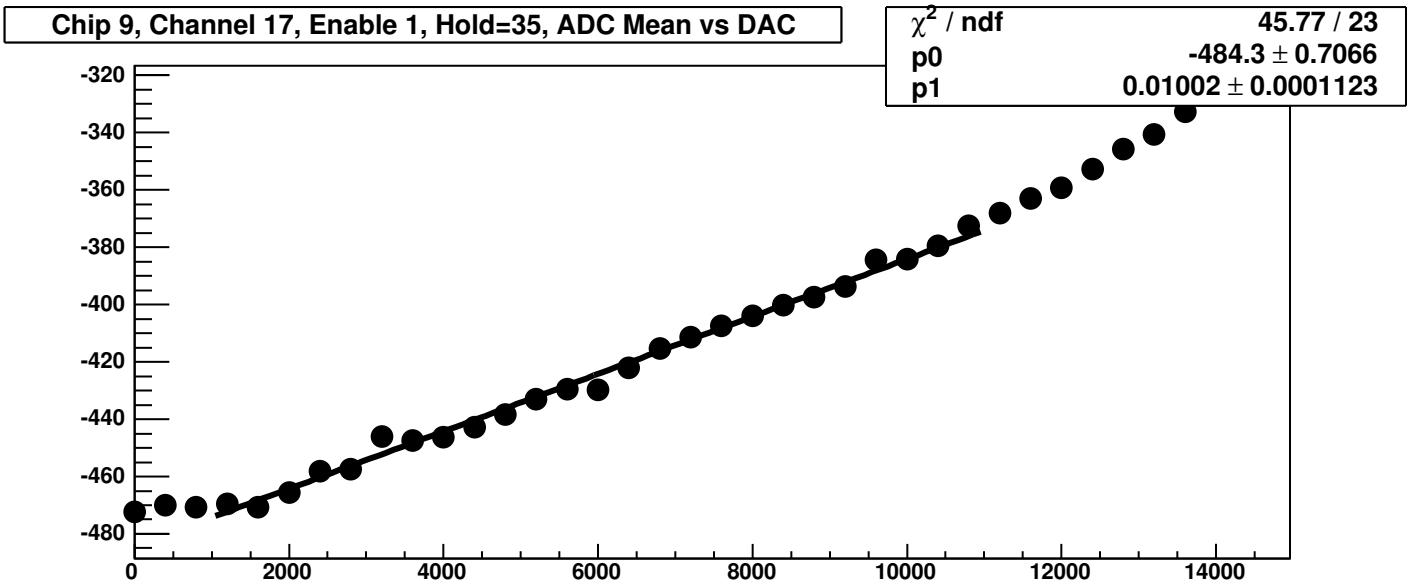

Chip 9, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

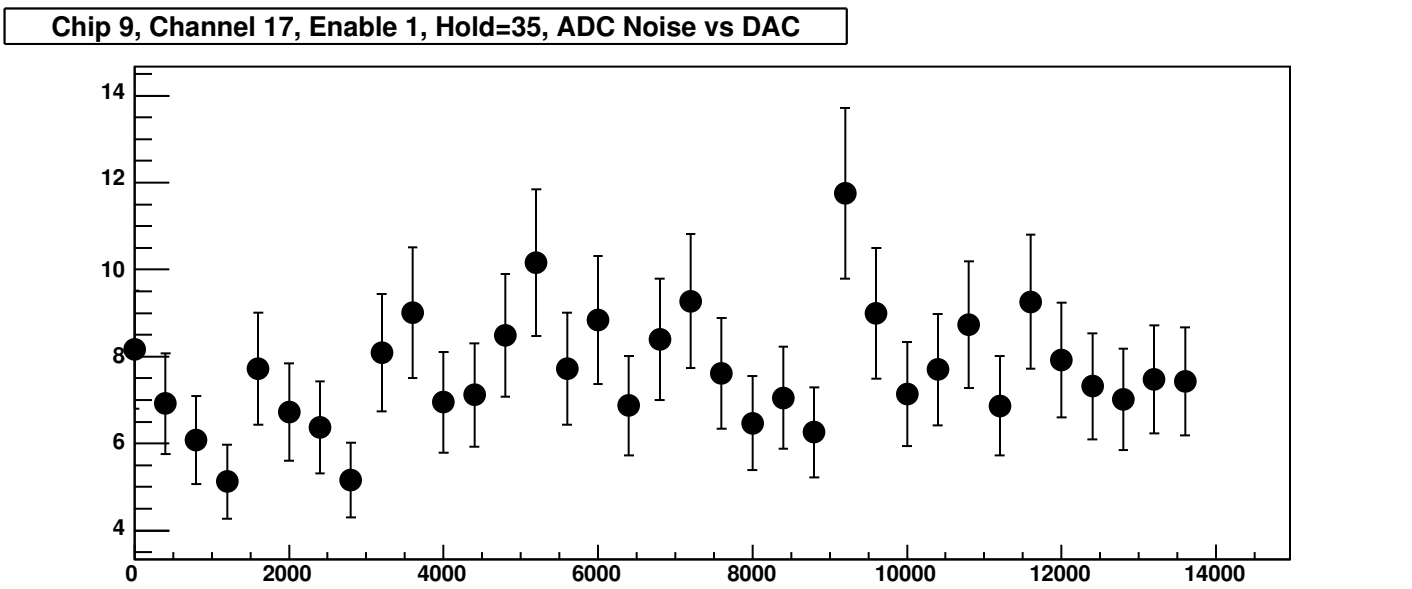

Chip 9, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

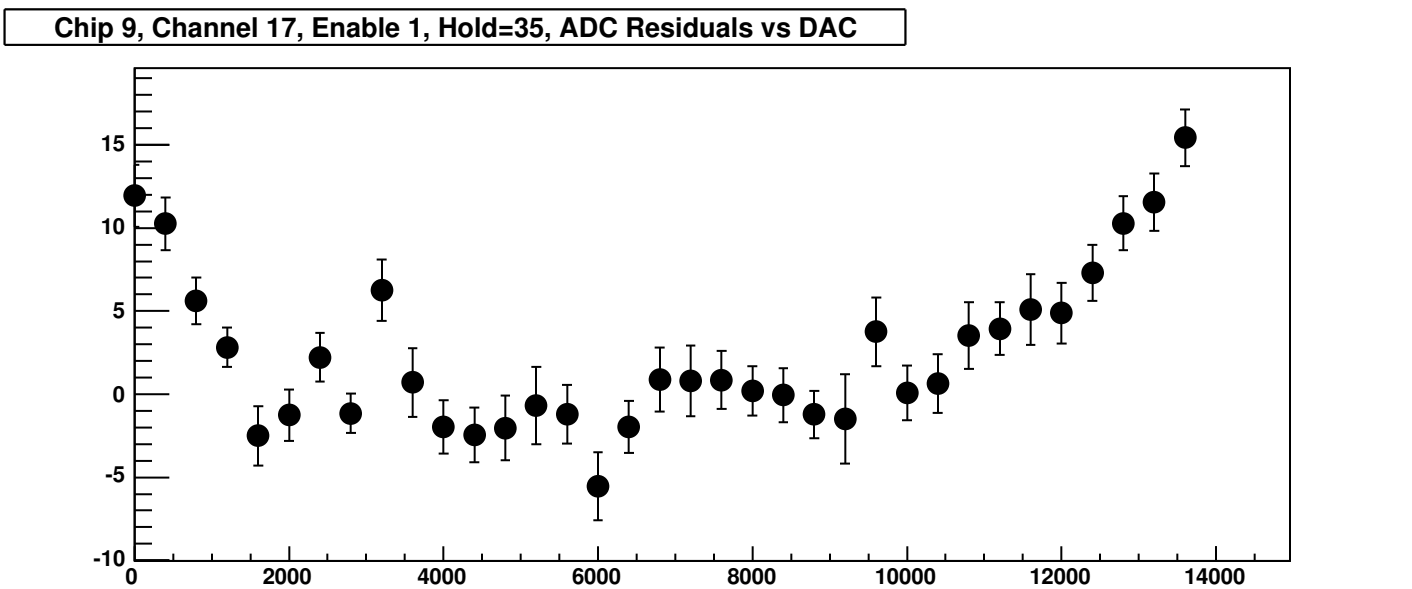

Chip 9, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 8.286e+04 / 23
p0 -807.7 ± 0.9621
p1 0.09466 ± 0.000163
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

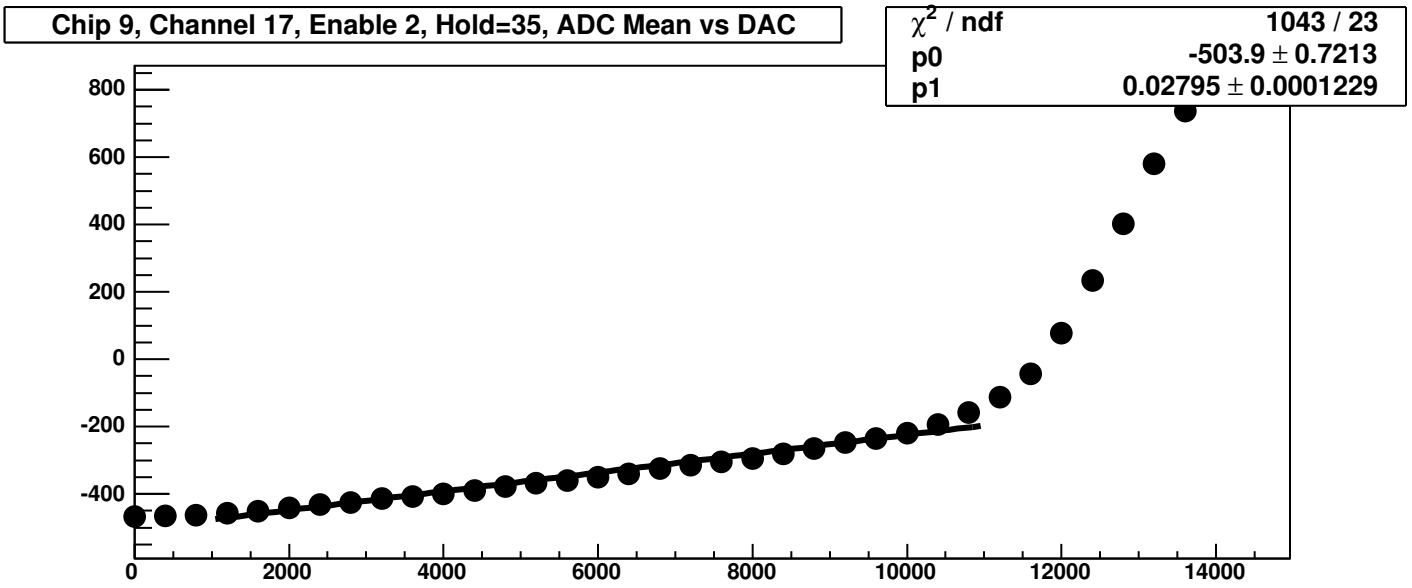

Chip 9, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

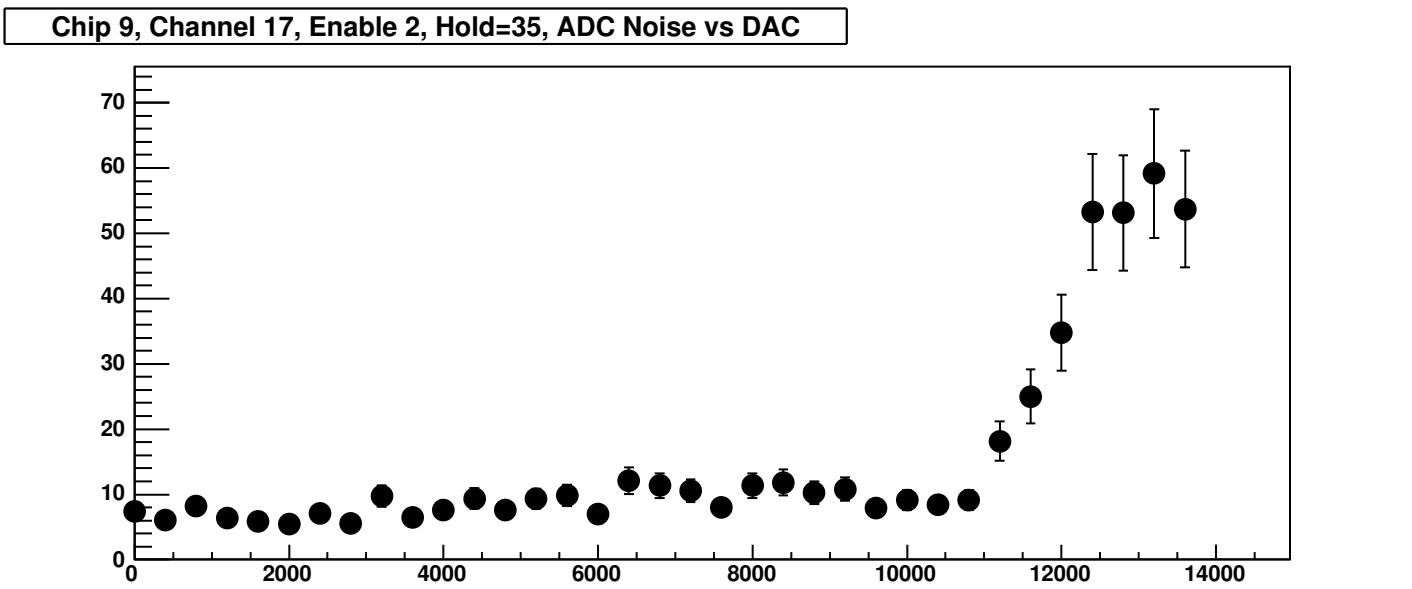

Chip 9, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

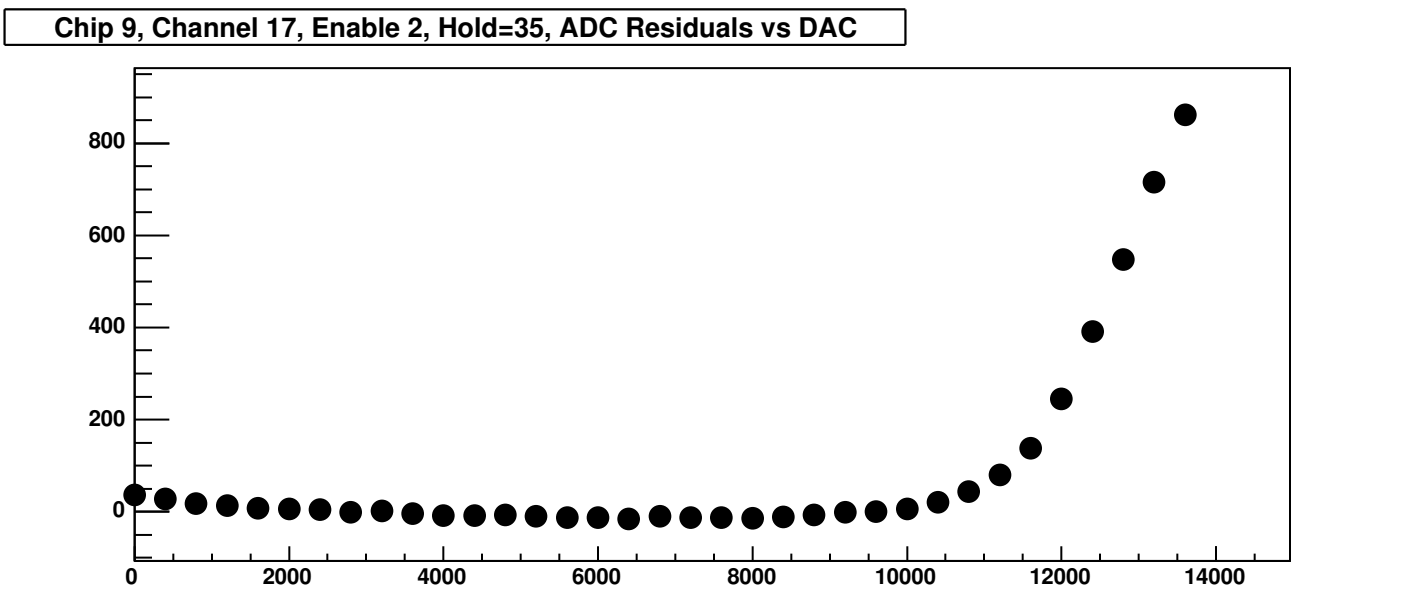

Chip 9, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 45.77 / 23
p0 -484.3 ± 0.7066
p1 0.01002 ± 0.0001123
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
2000
4000
6000
8000
10000
12000
14000

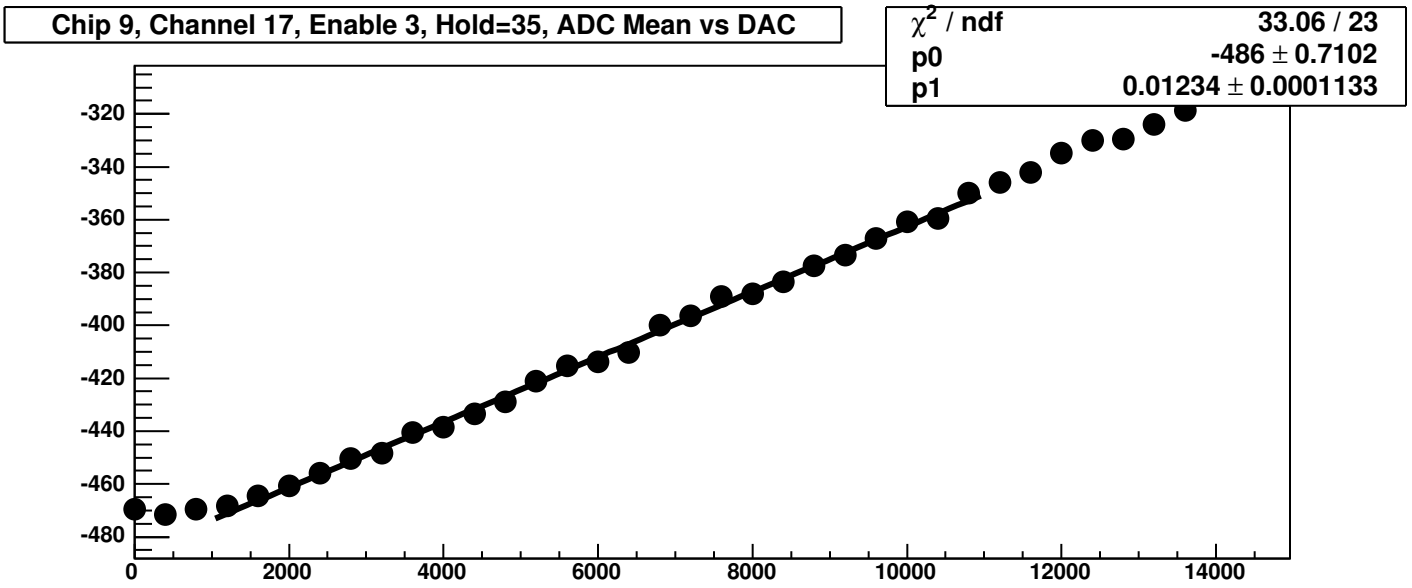

Chip 9, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

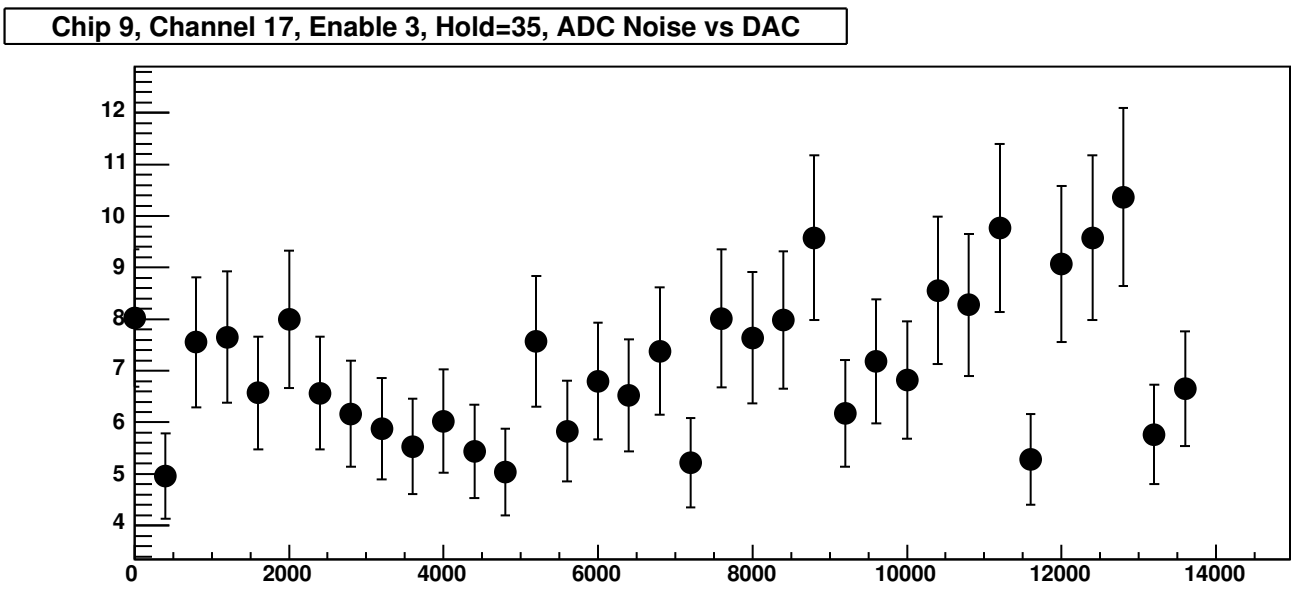

Chip 9, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

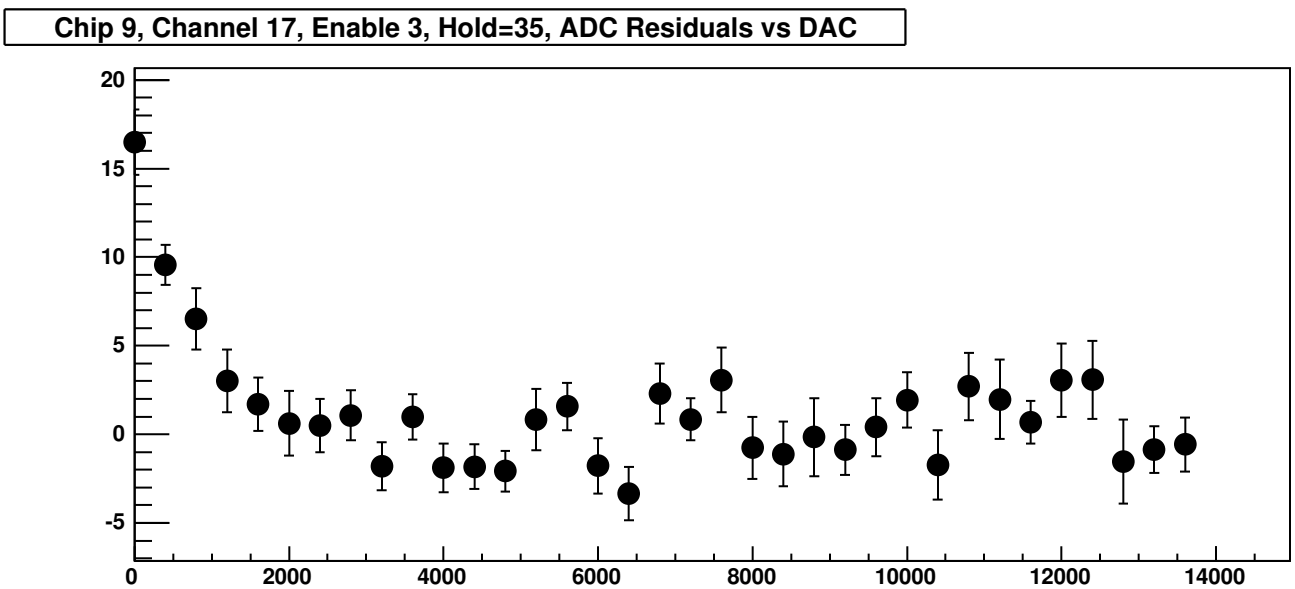

Chip 9, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1043 / 23
p0 -503.9 ± 0.7213
p1 0.02795 ± 0.0001229
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

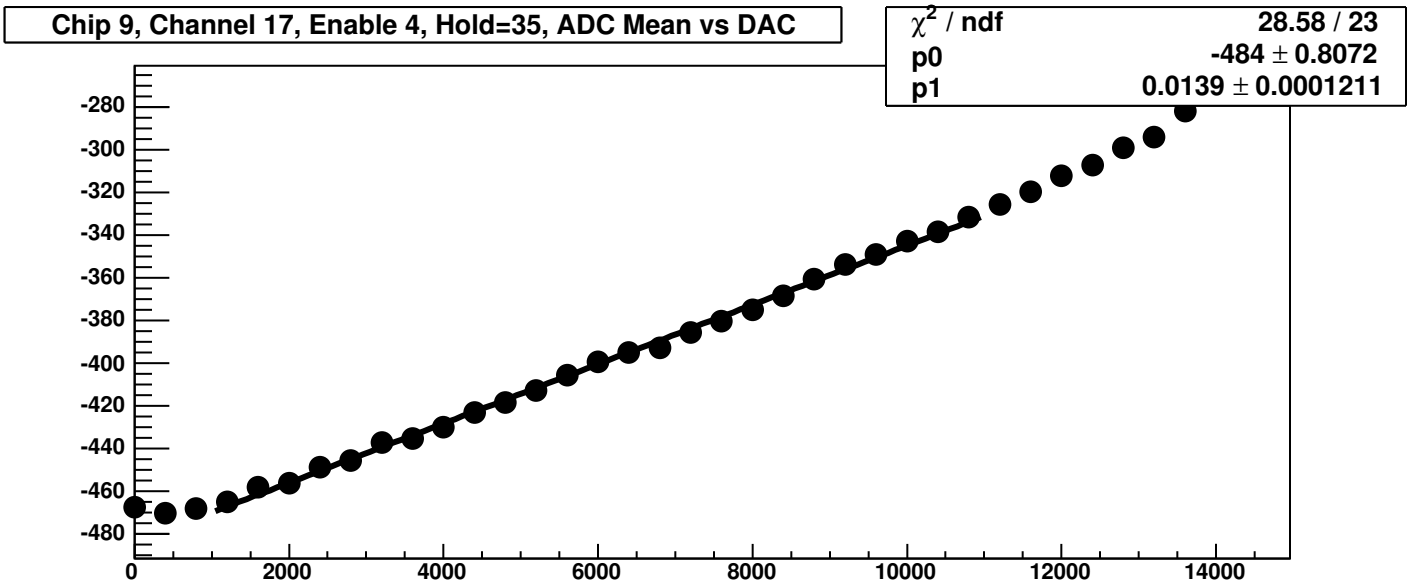

Chip 9, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

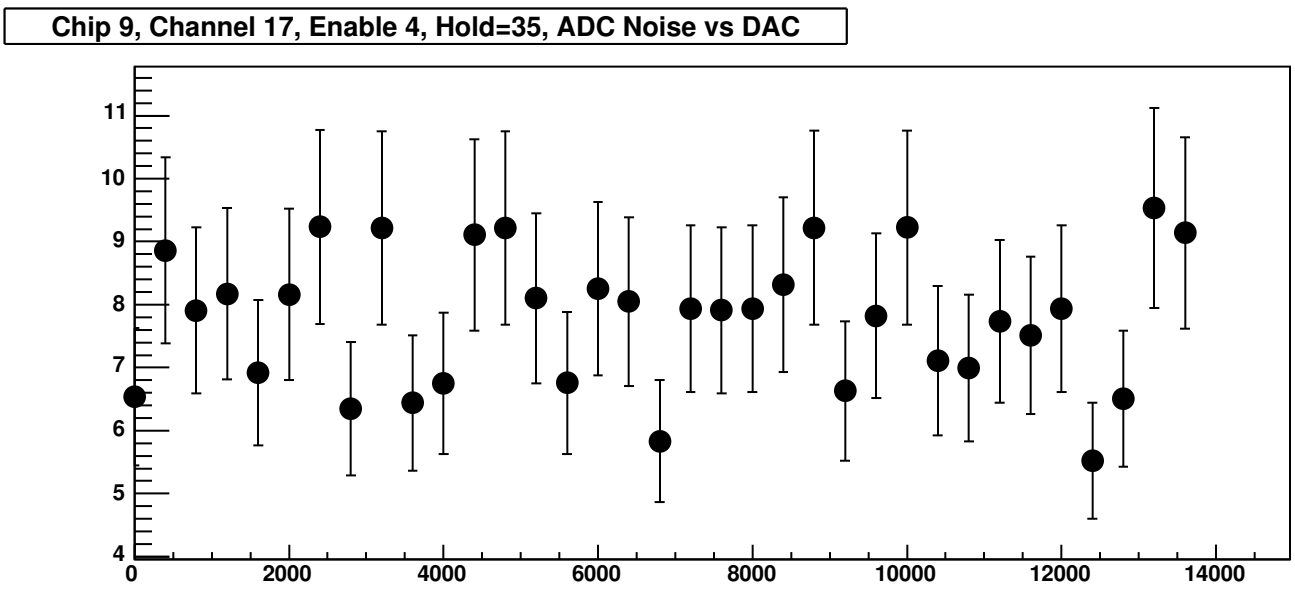

Chip 9, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

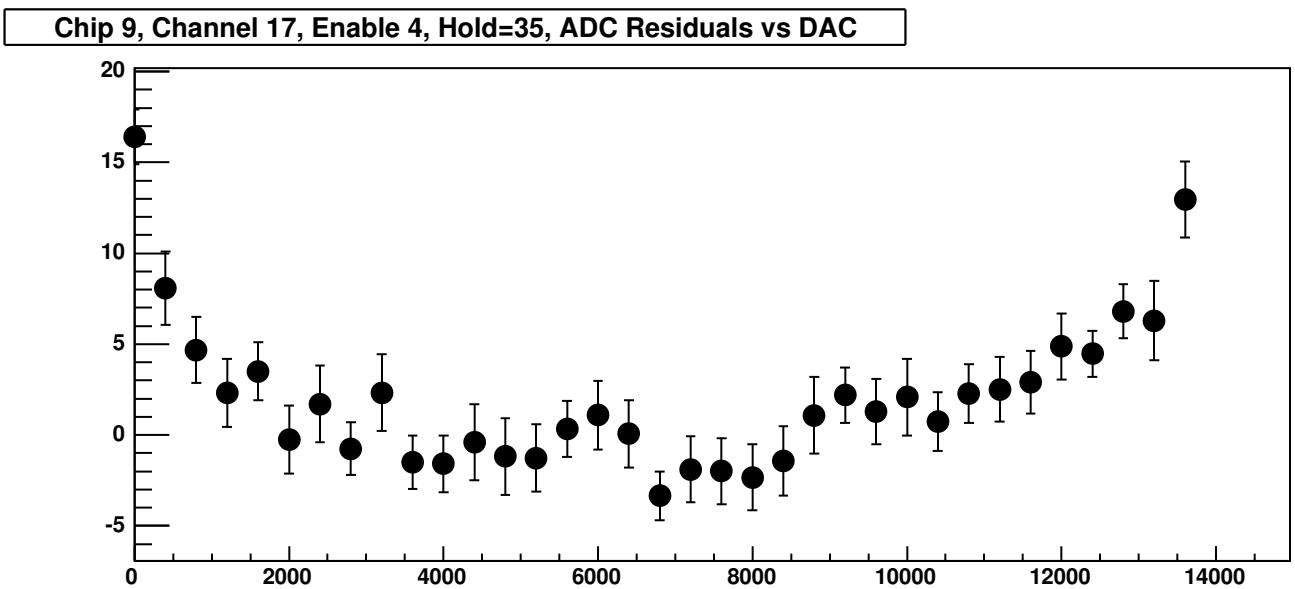

Chip 9, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 33.06 / 23
p0 -486 ± 0.7102
p1 0.01234 ± 0.0001133
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
2000
4000
6000
8000
10000
12000
14000

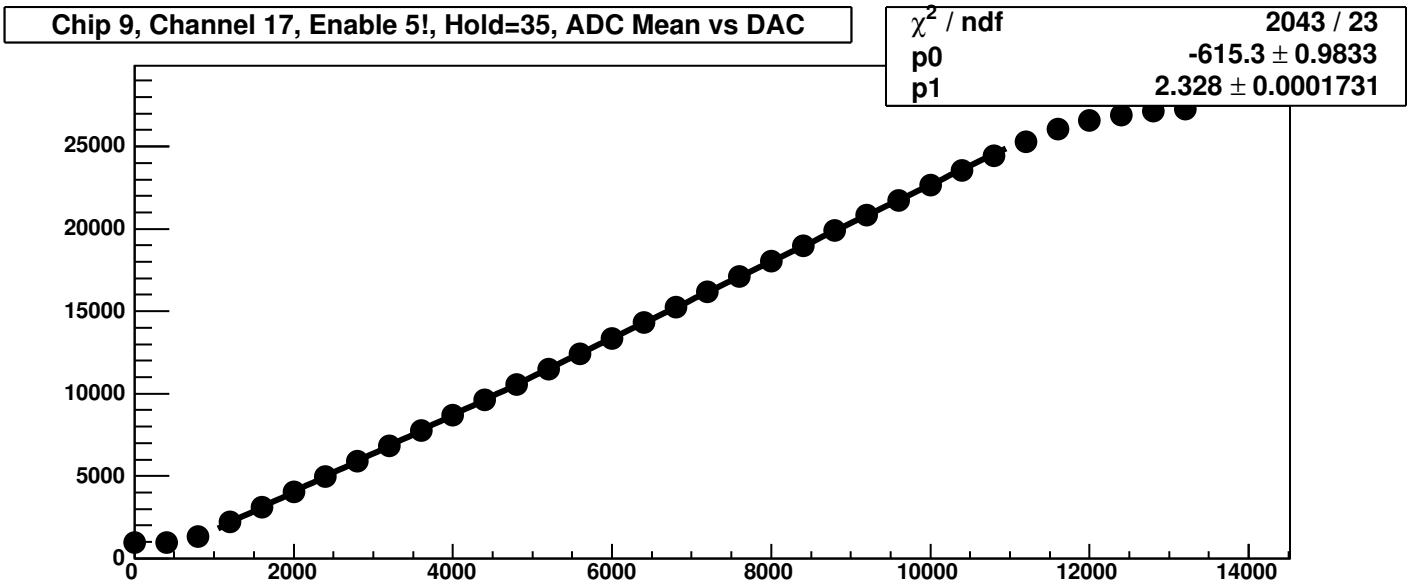

Chip 9, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

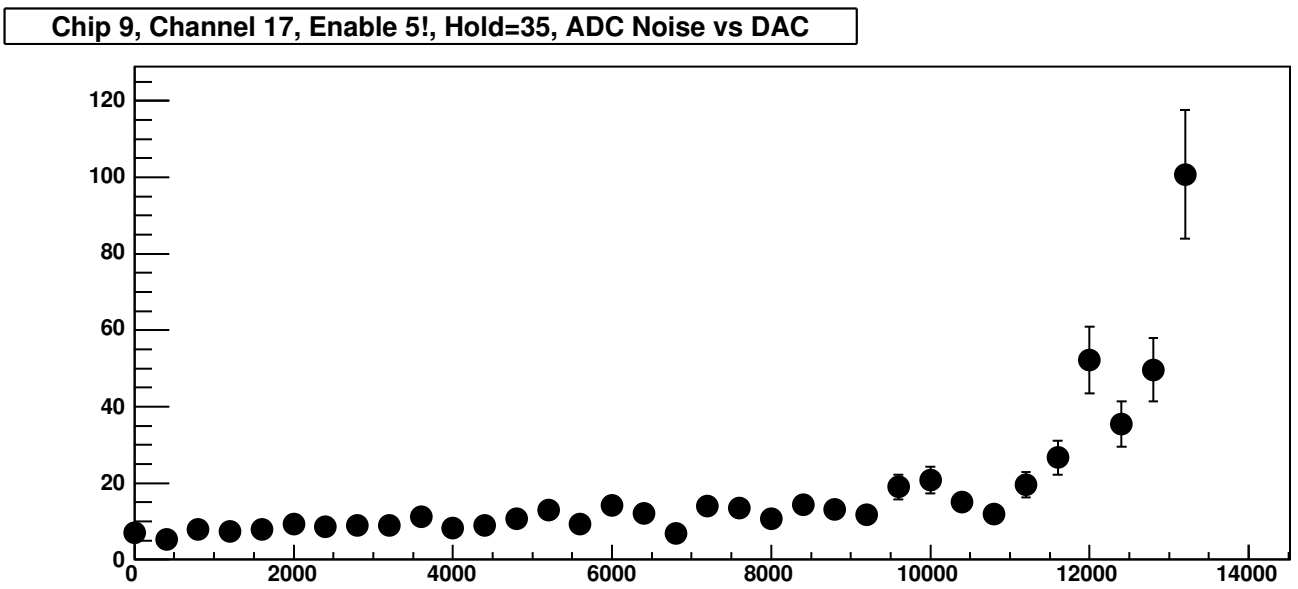

Chip 9, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

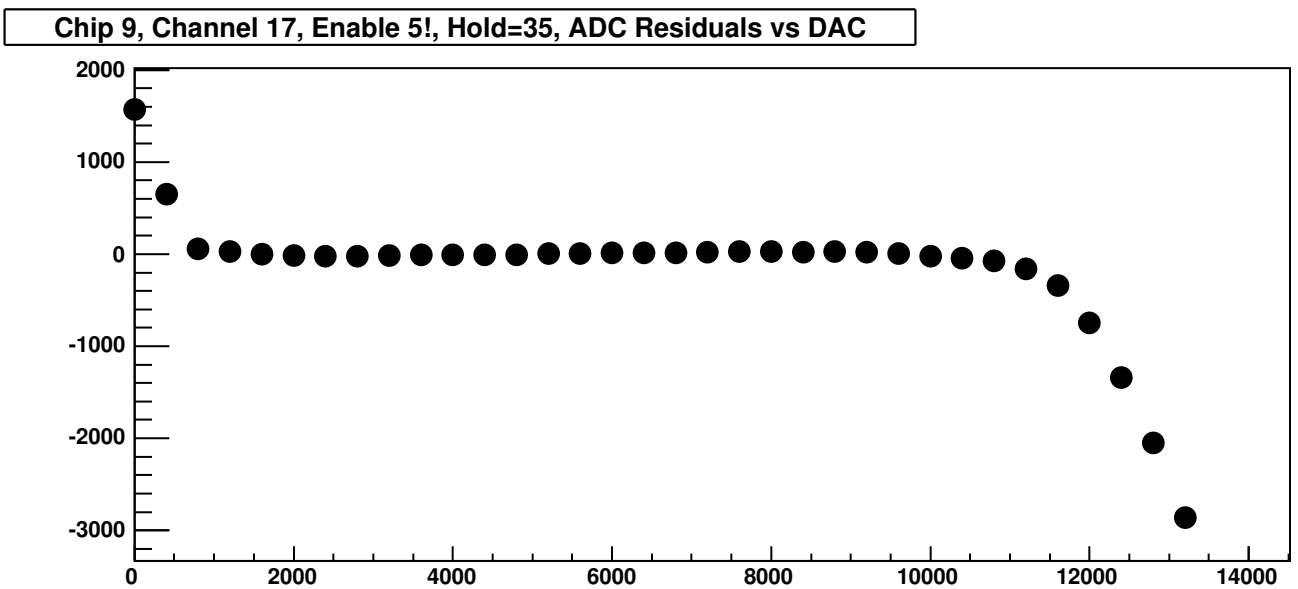

Chip 9, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 28.58 / 23
p0 -484 ± 0.8072
p1 0.0139 ± 0.0001211
-280
-300
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
2000
4000
6000
8000
10000
12000
14000

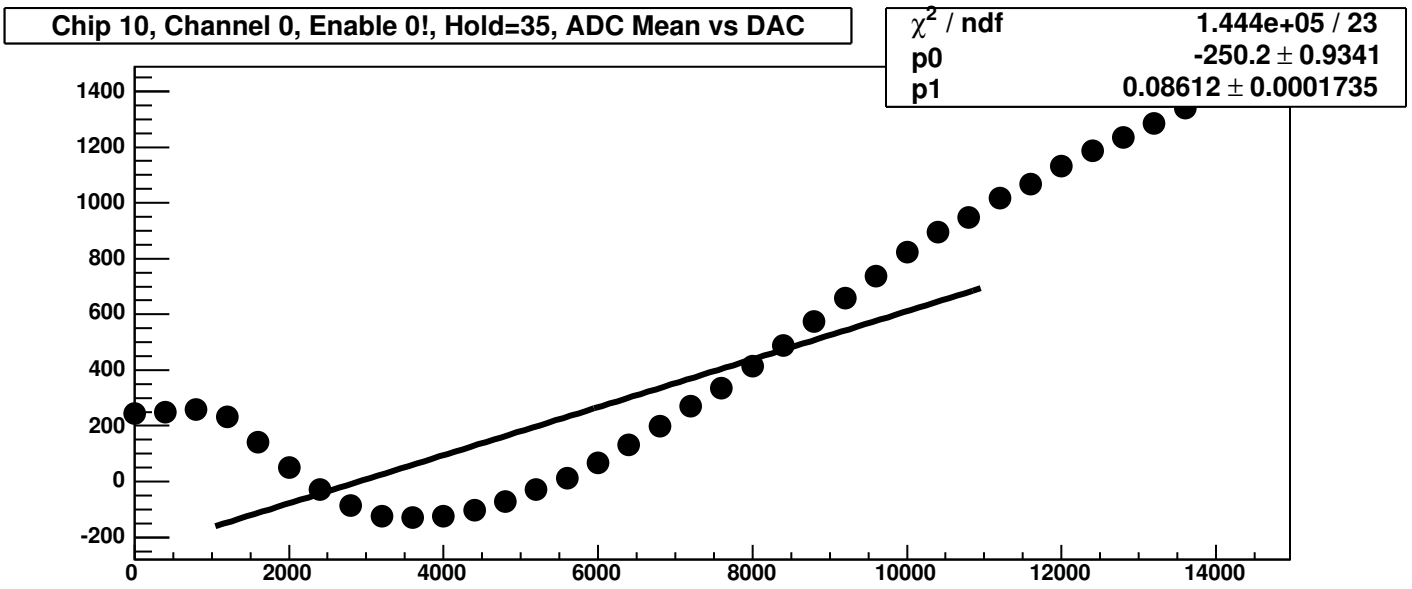

Chip 9, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

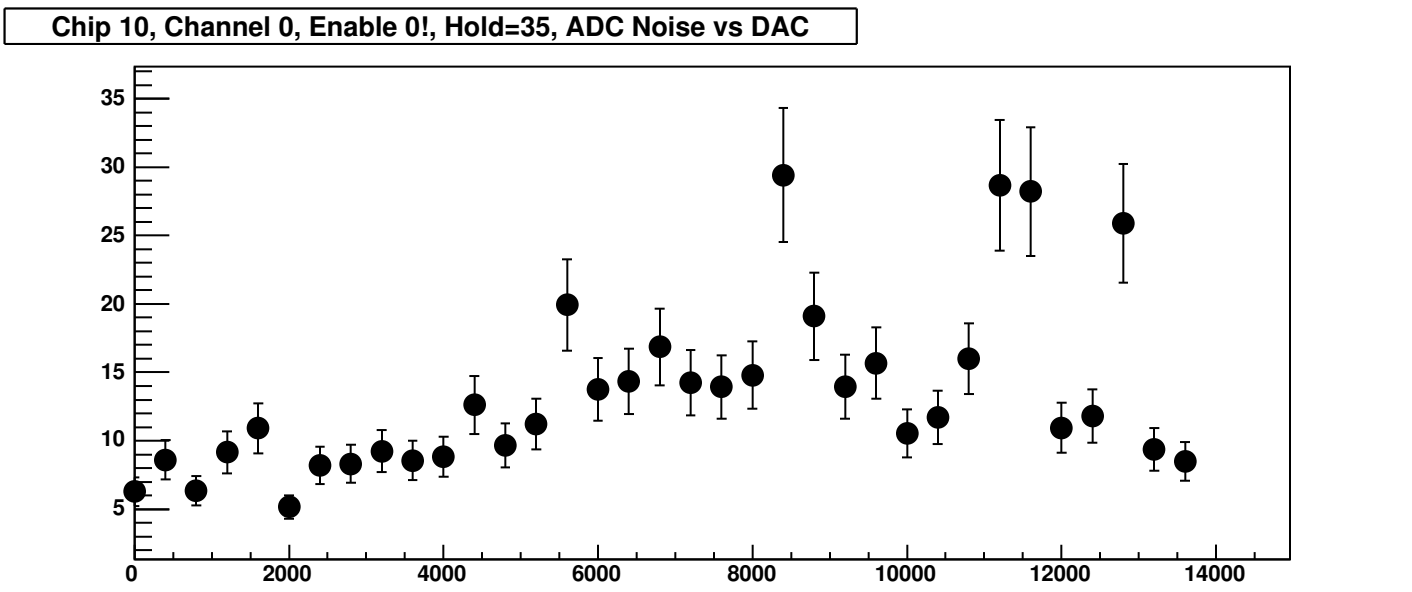

Chip 9, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

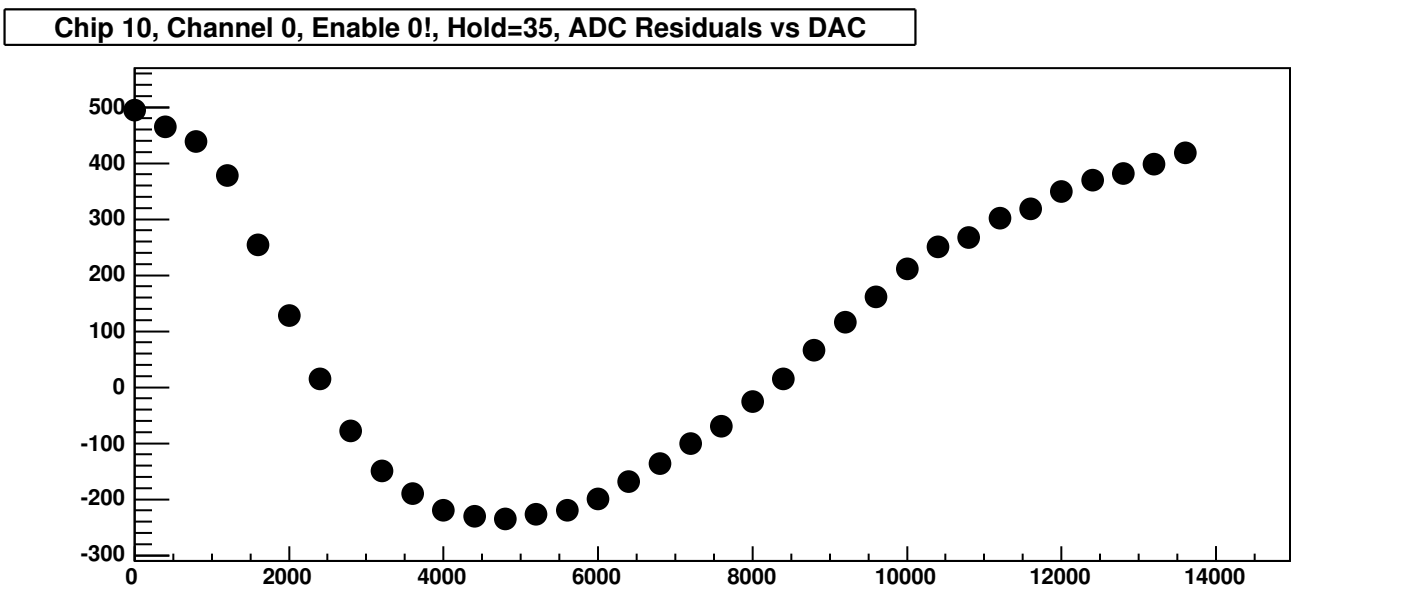

Chip 9, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2043 / 23
p0 -615.3 ± 0.9833
p1 2.328 ± 0.0001731
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

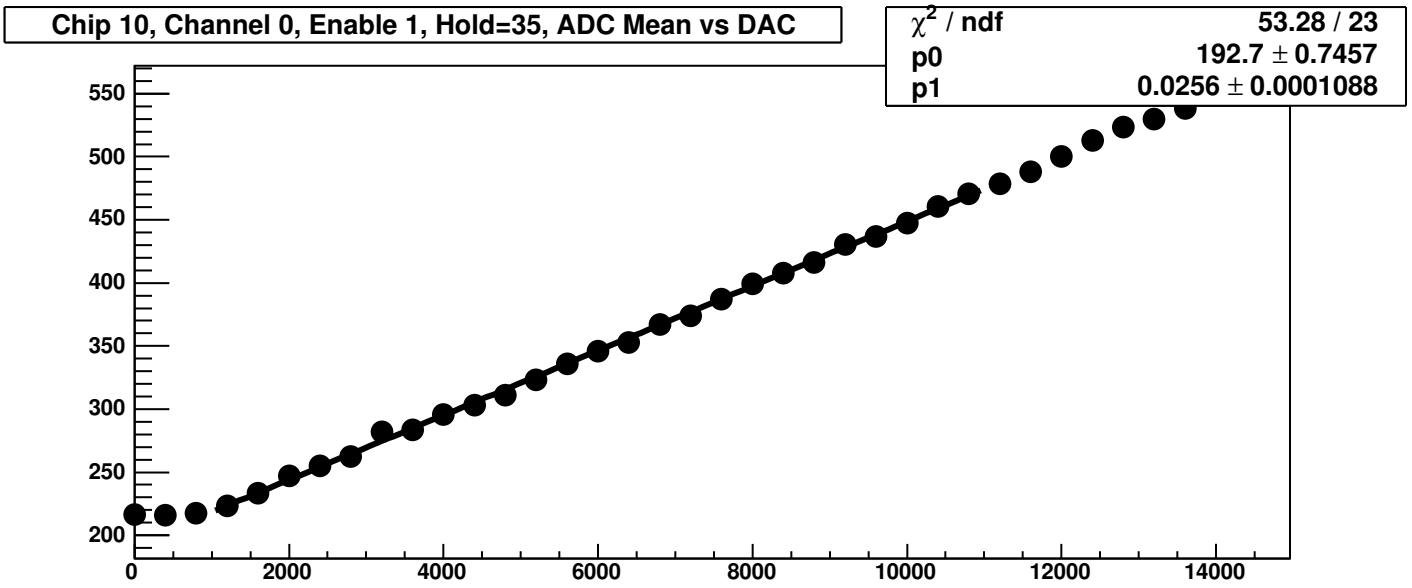

Chip 9, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

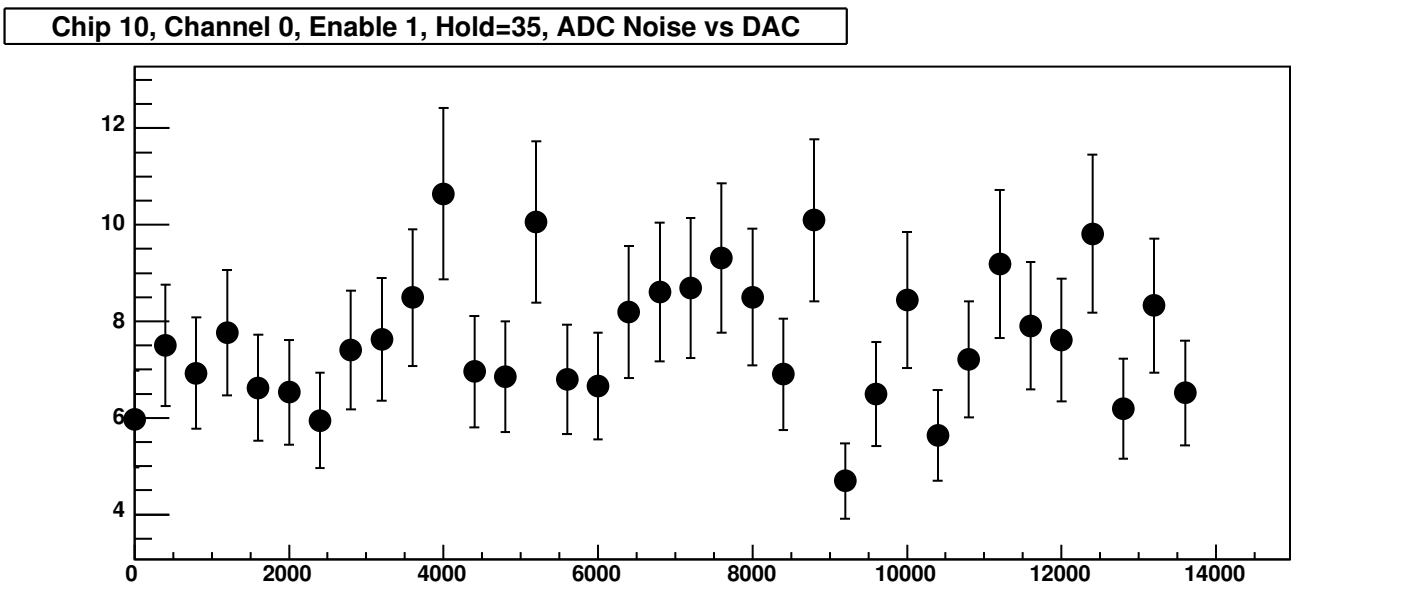

Chip 9, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

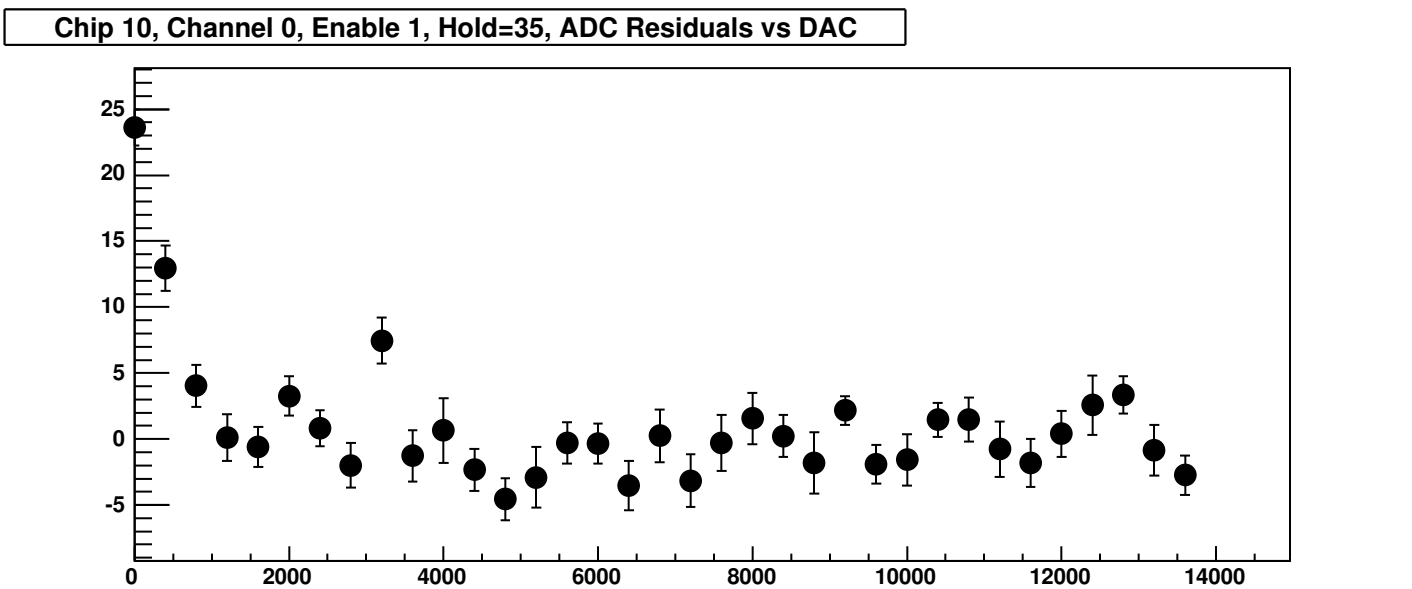

Chip 10, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1.444e+05 / 23
p0 -250.2 ± 0.9341
p1 0.08612 ± 0.0001735
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

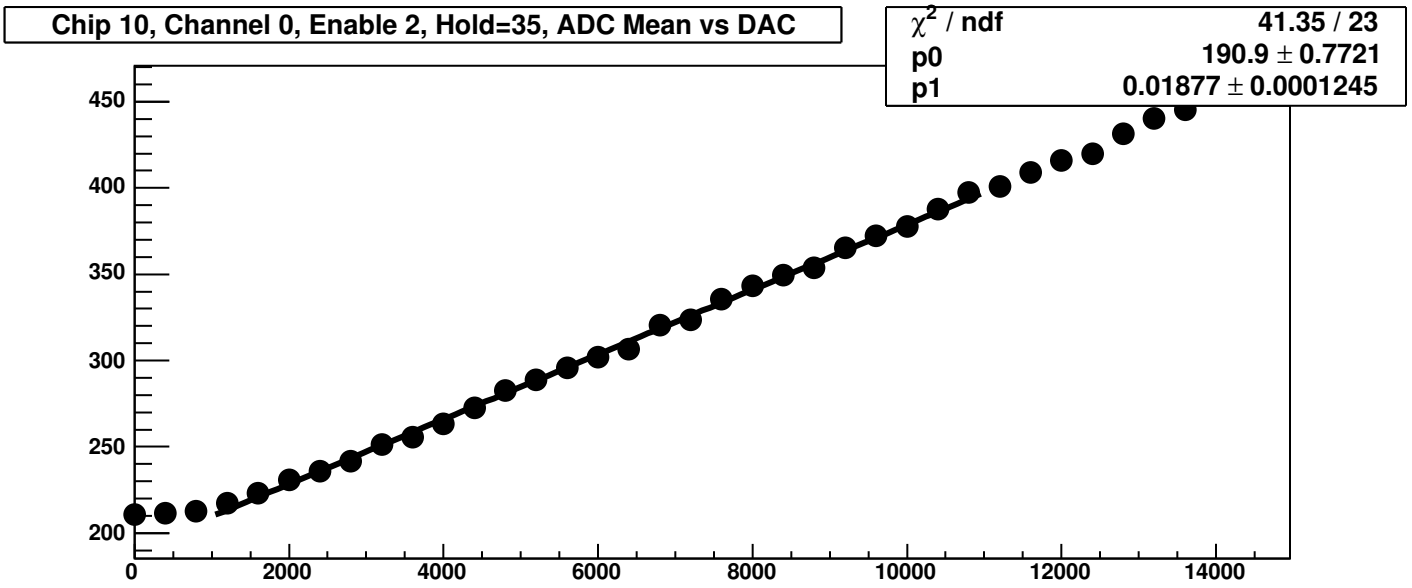

Chip 10, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

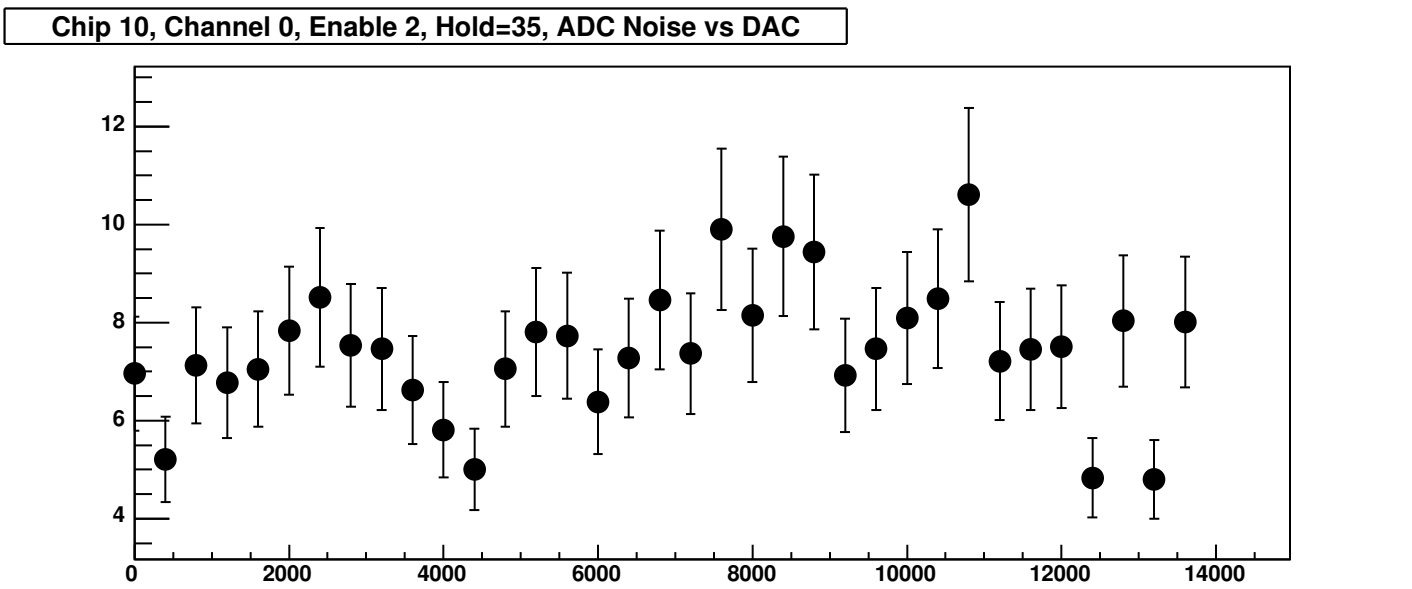

Chip 10, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

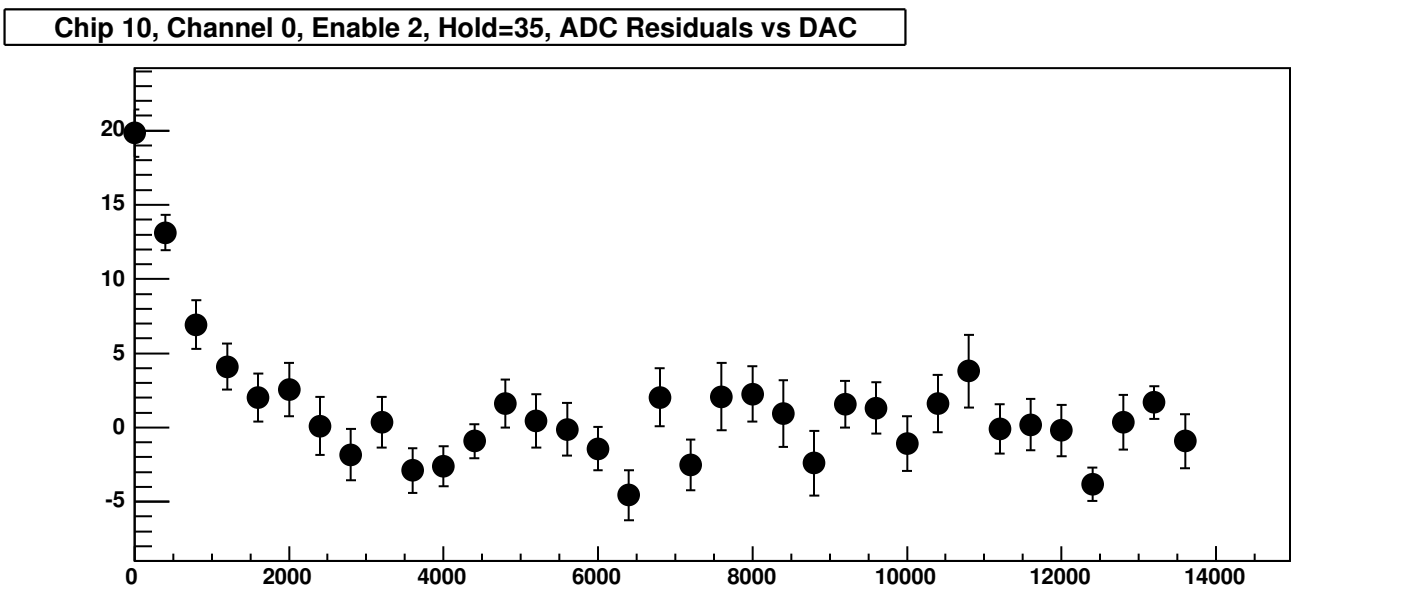

Chip 10, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 53.28 / 23
p0 192.7 ± 0.7457
p1 0.0256 ± 0.0001088
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

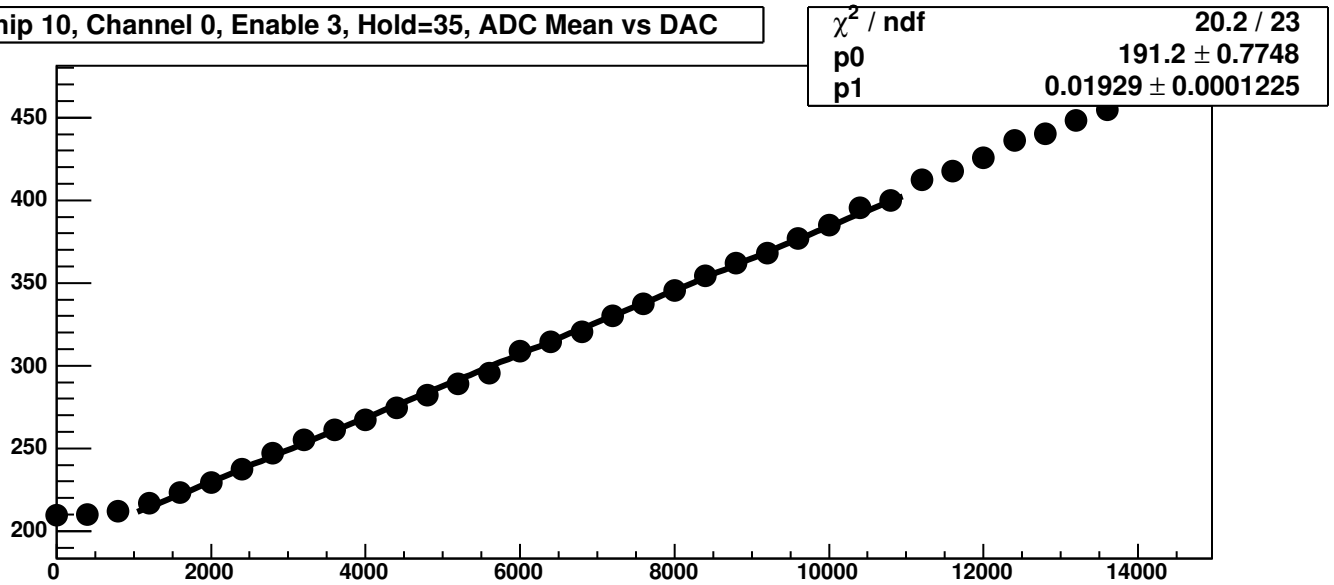

Chip 10, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 41.35 / 23
p0 190.9 ± 0.7721
p1 0.01877 ± 0.0001245
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

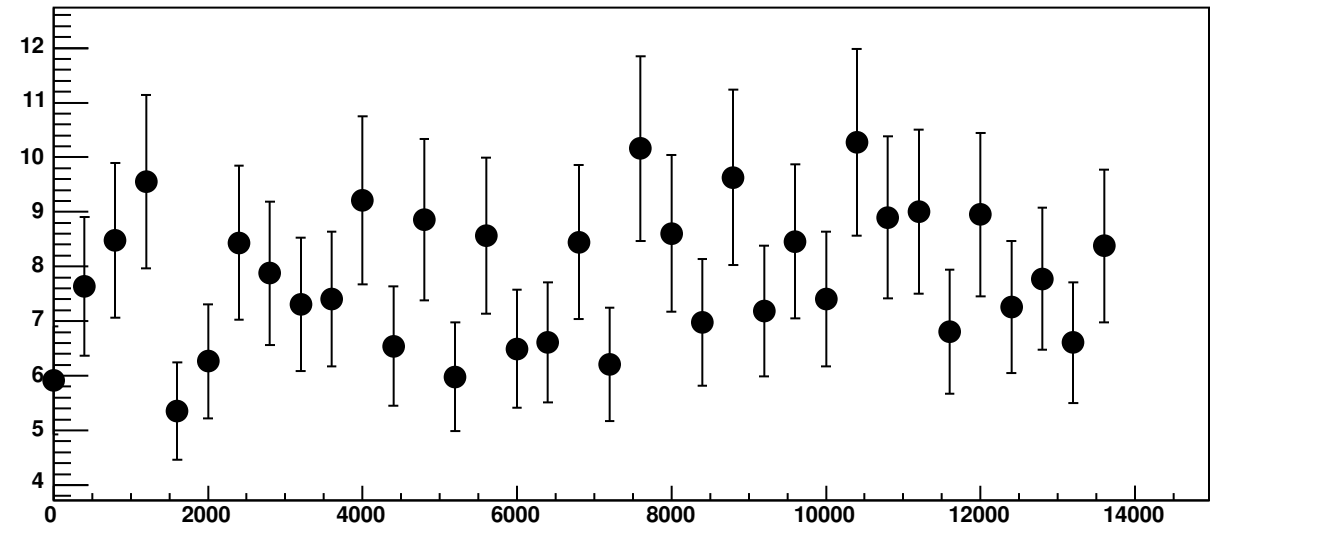

Chip 10, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

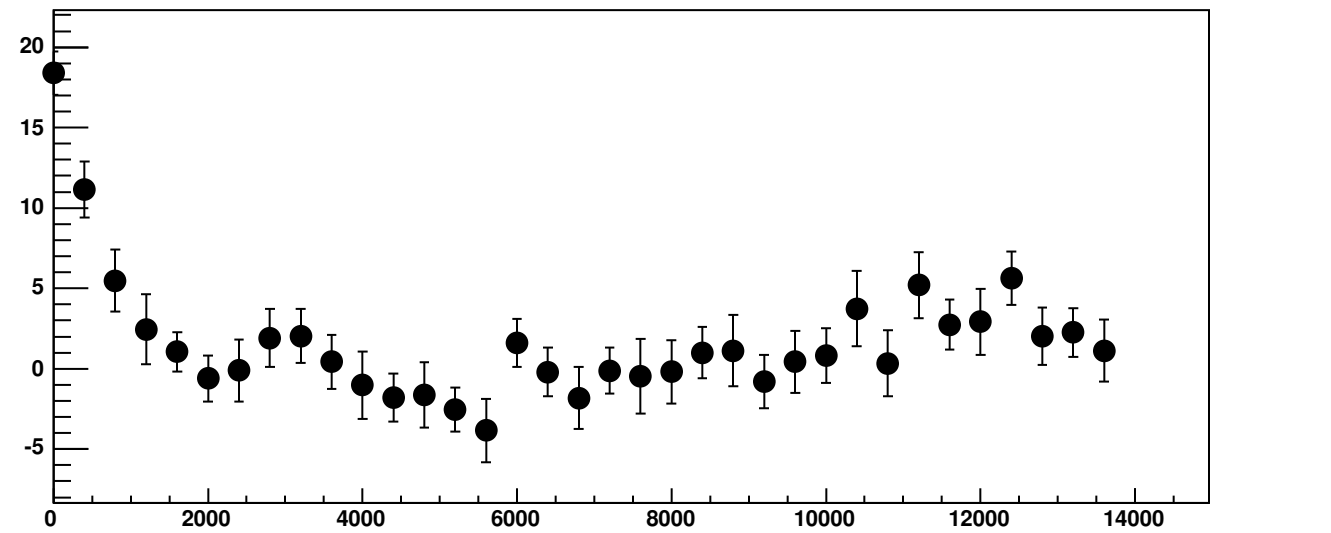
Chip 10, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

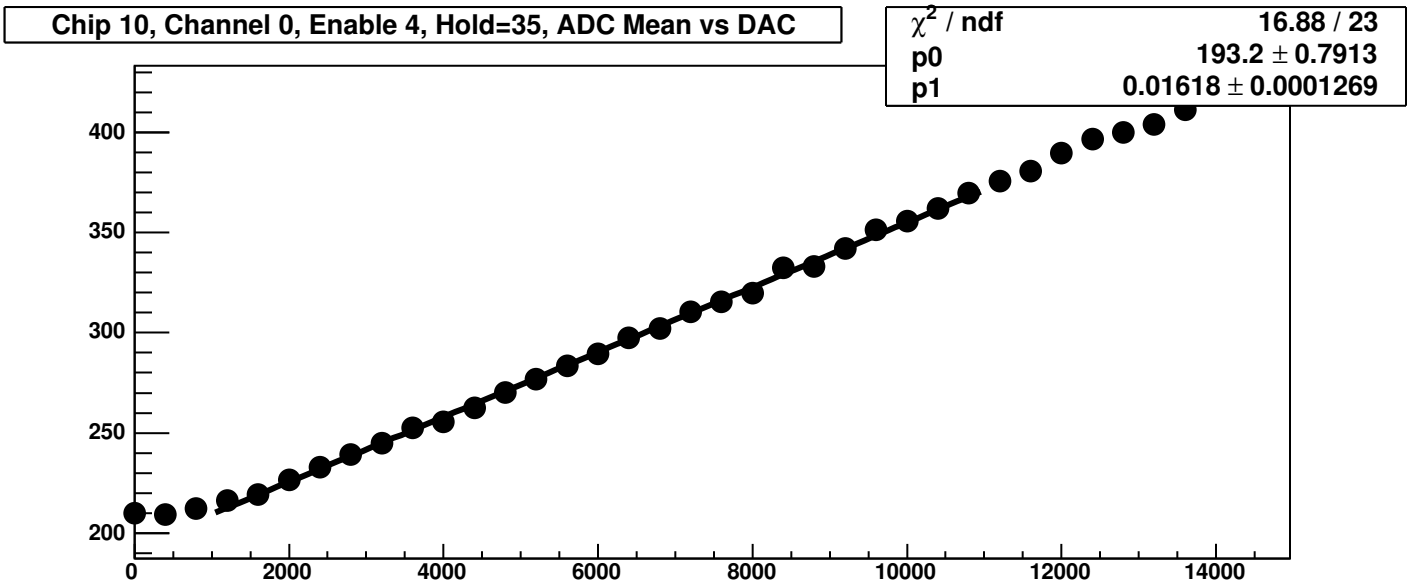


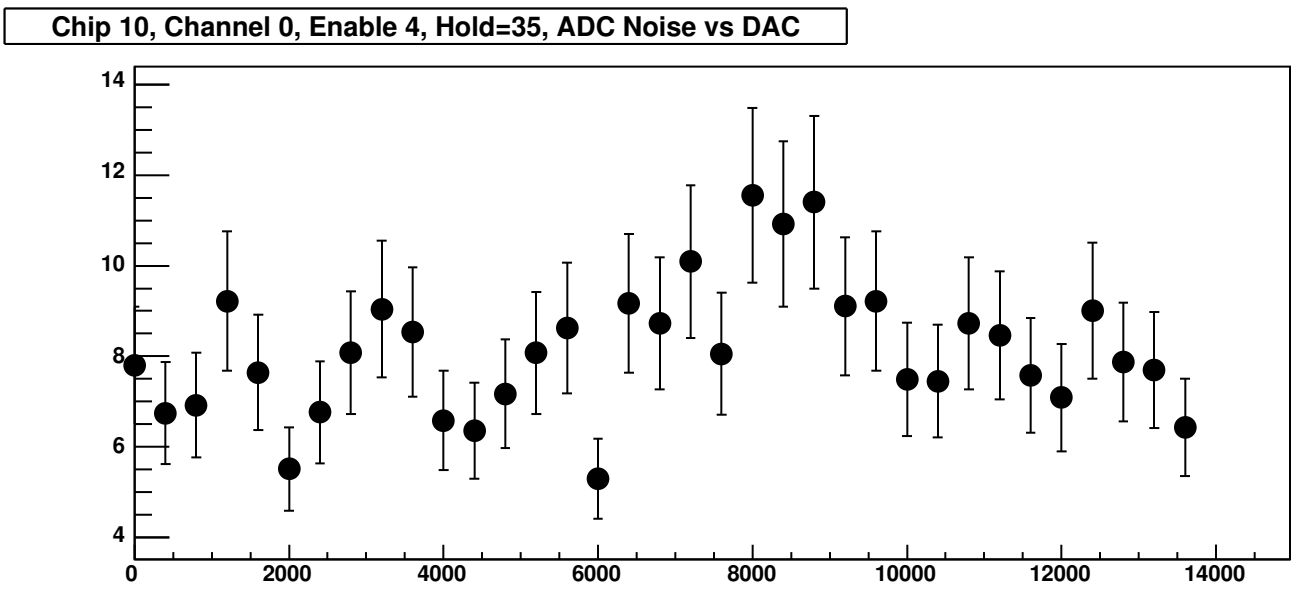
Chip 10, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC

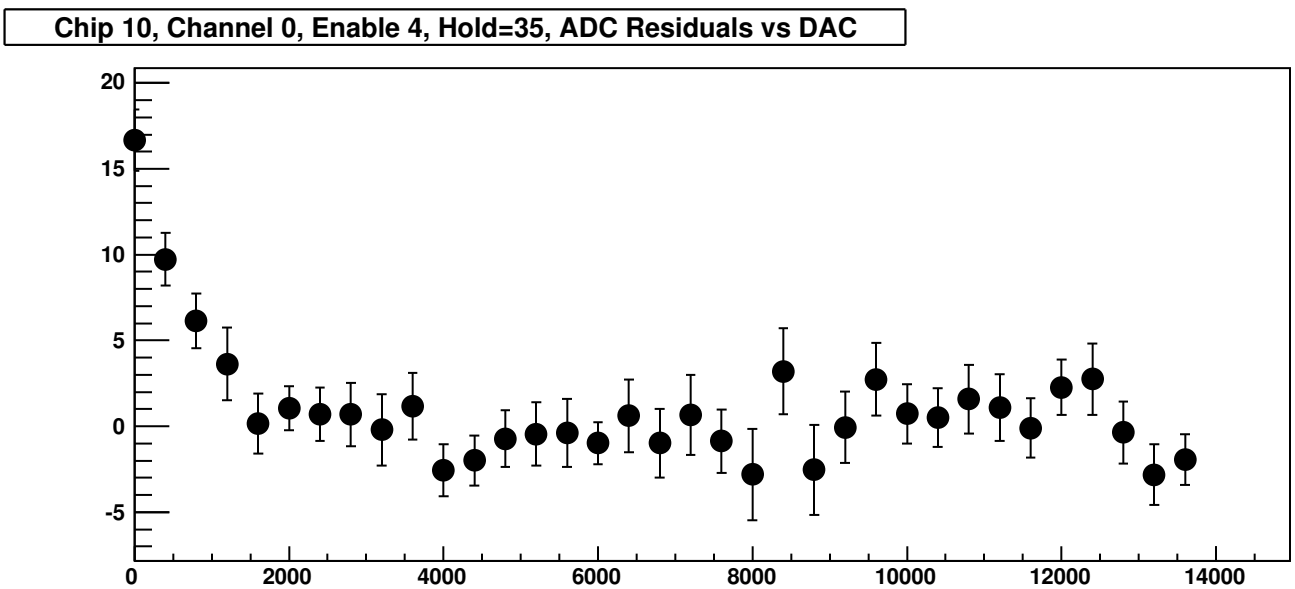


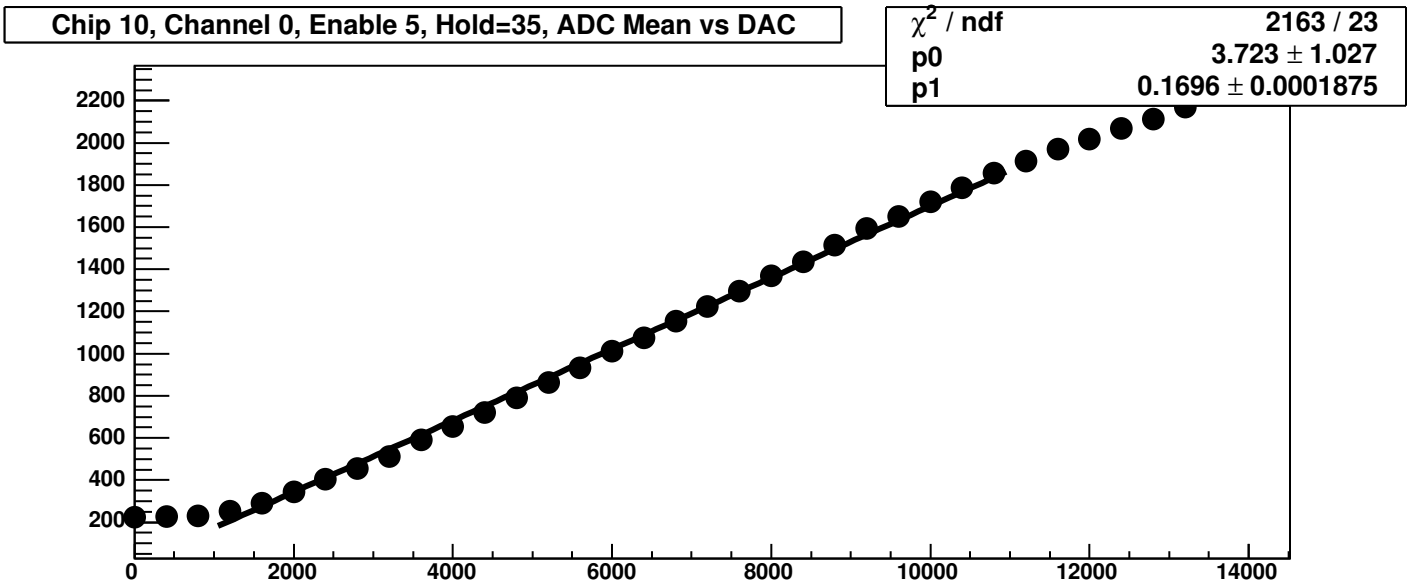
Chip 10, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC

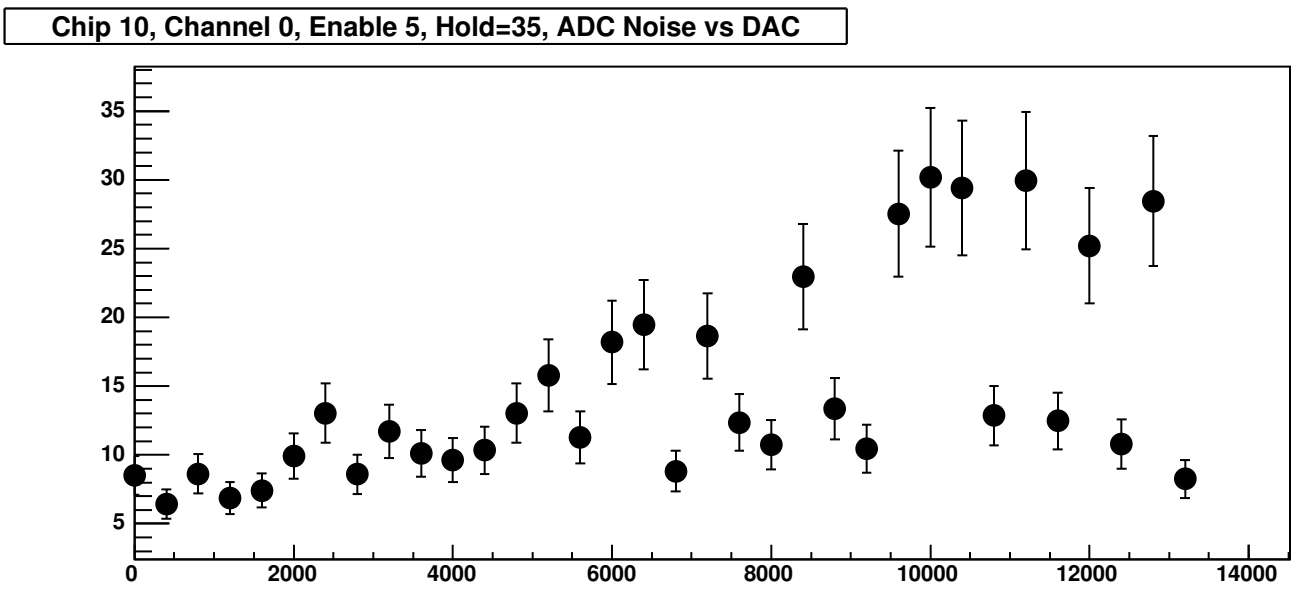


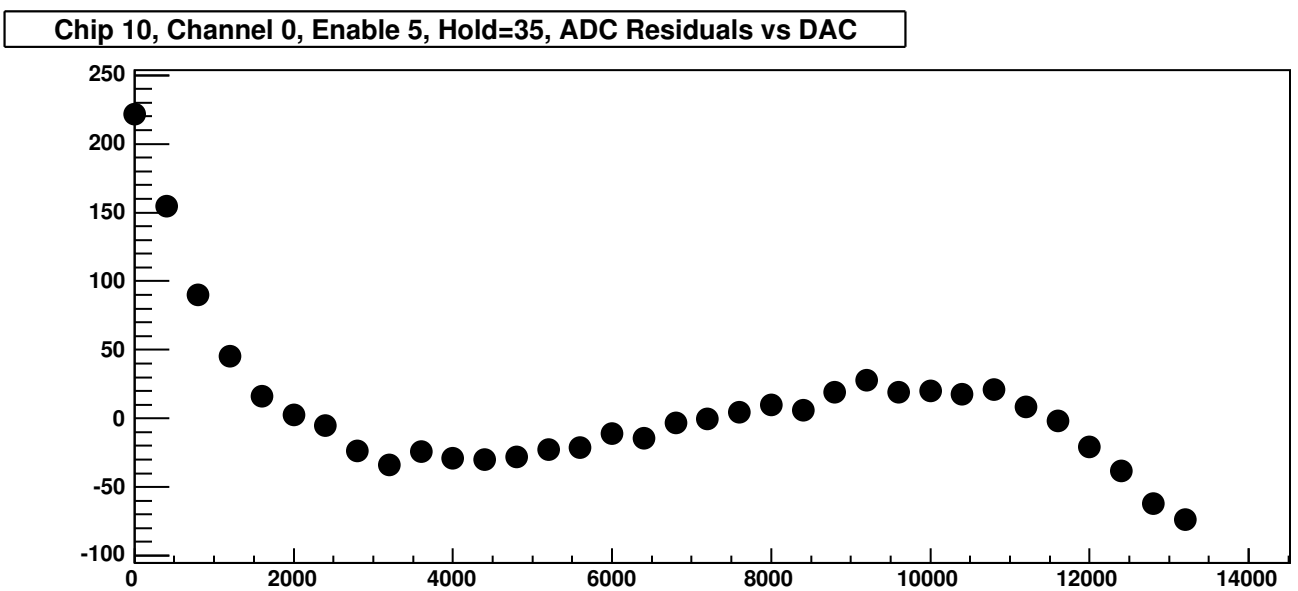

Chip 10, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 16.88 / 23
p0 193.2 ± 0.7913
p1 0.01618 ± 0.0001269
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

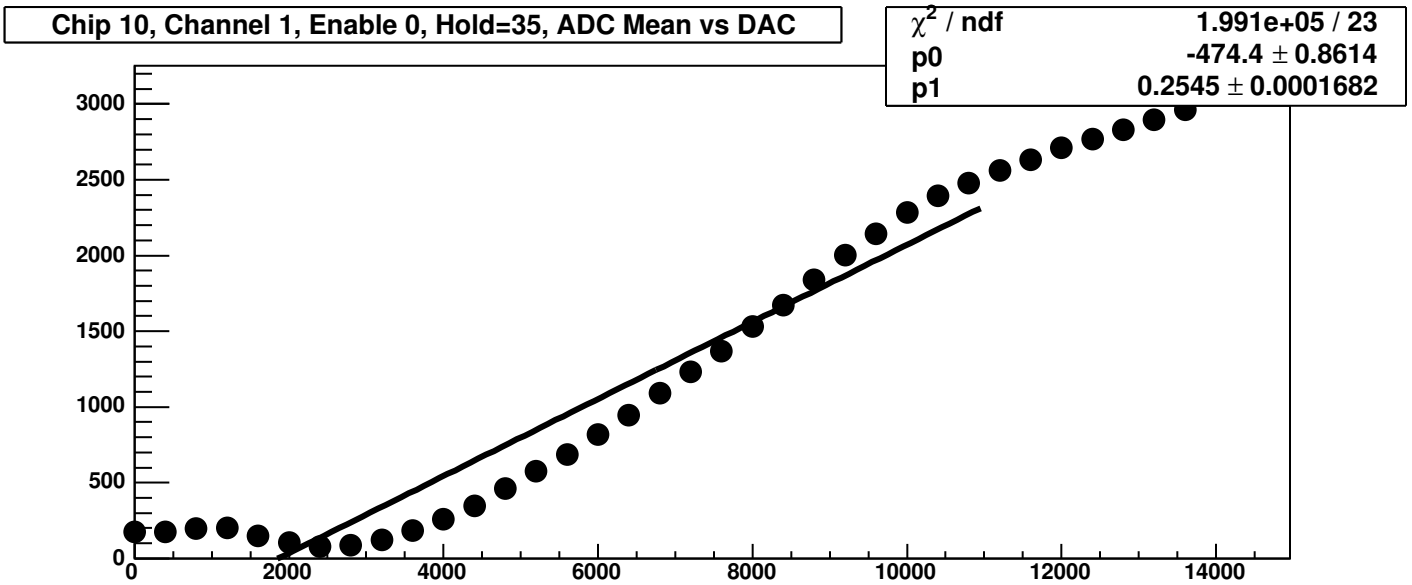

Chip 10, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

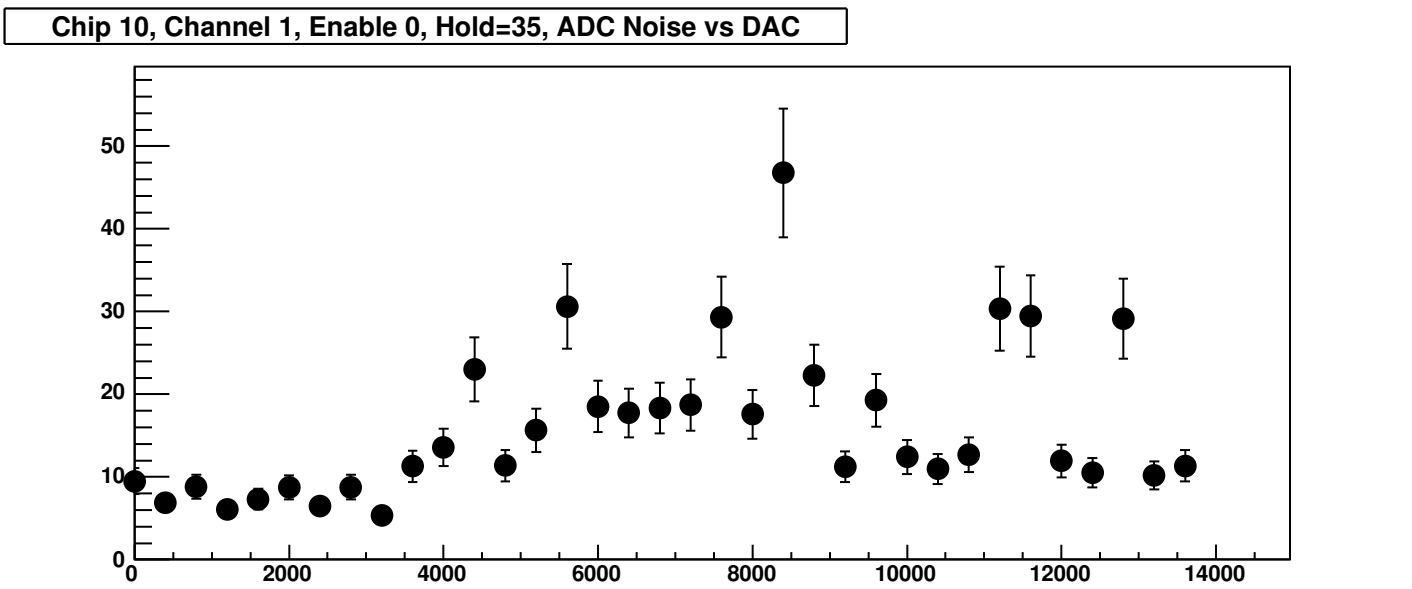

Chip 10, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

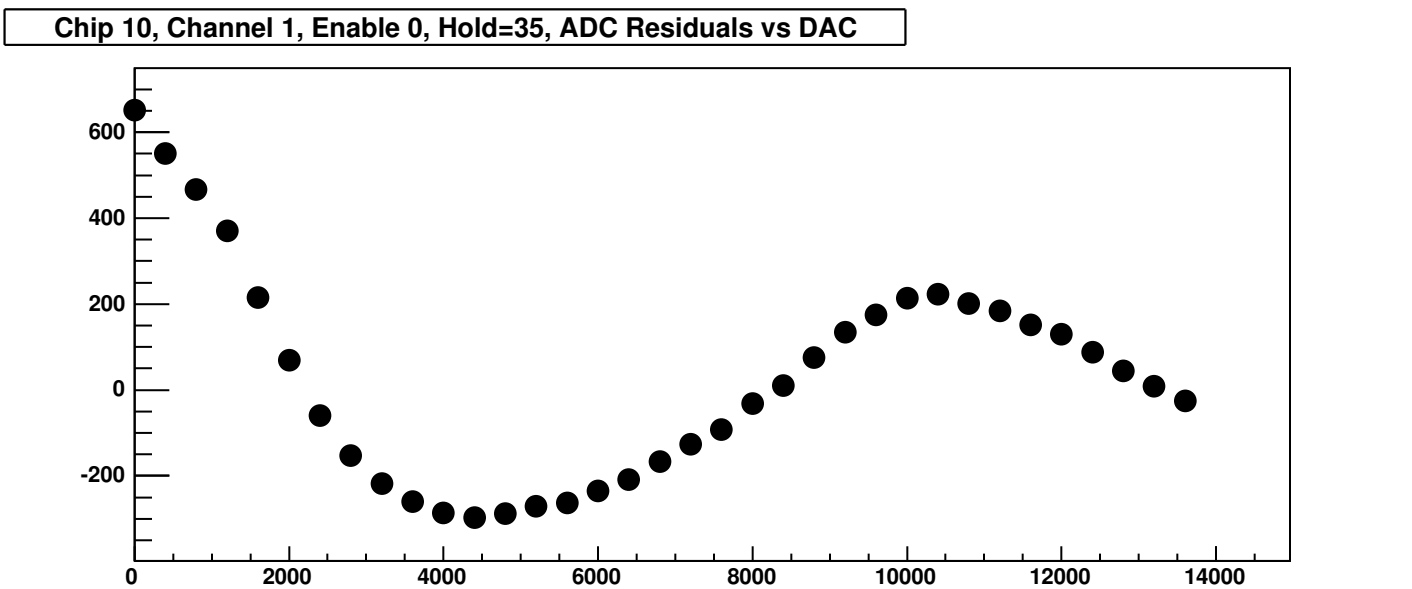

Chip 10, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2163 / 23
p0 3.723 ± 1.027
p1 0.1696 ± 0.0001875
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

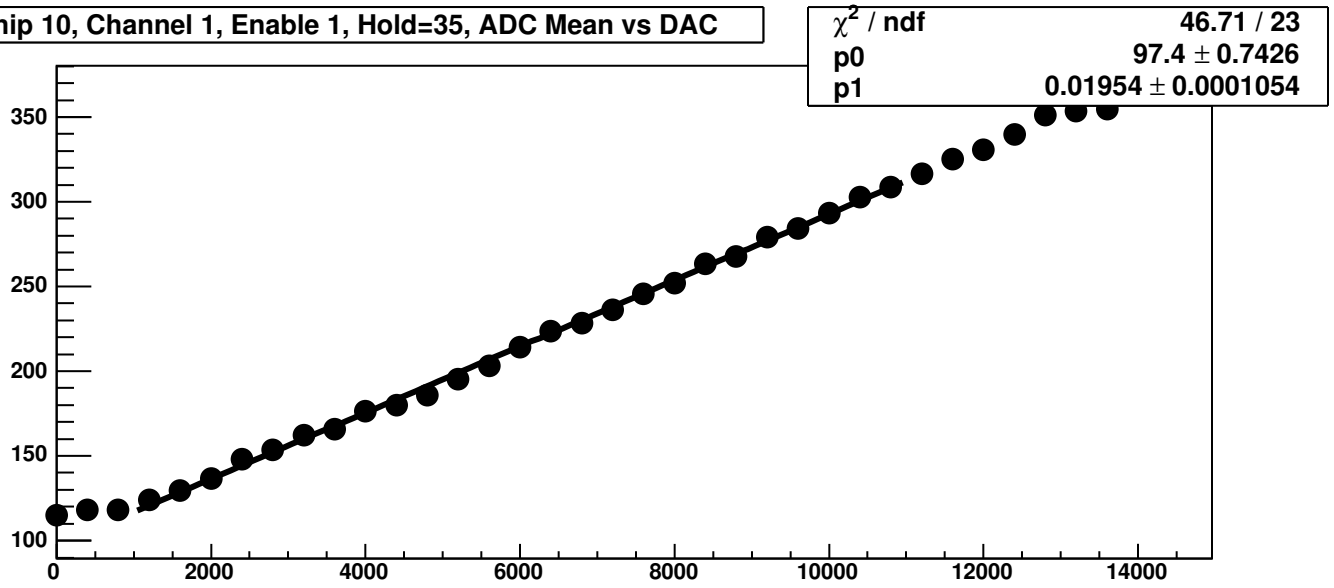

Chip 10, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.991e+05 / 23
p0 -474.4 ± 0.8614
p1 0.2545 ± 0.0001682
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

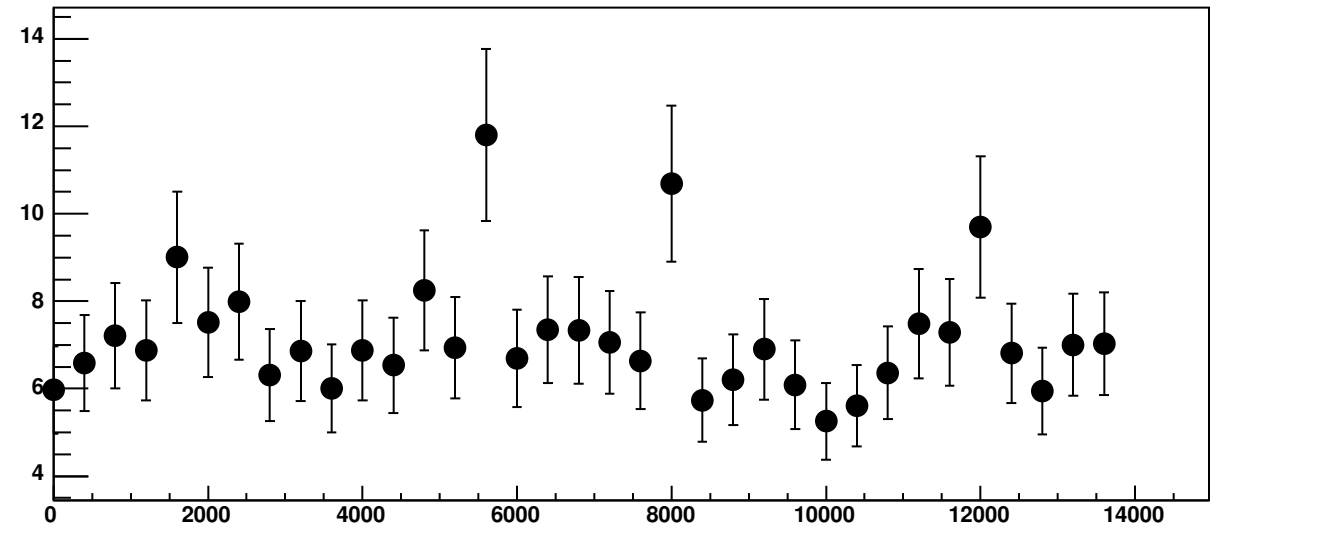

Chip 10, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

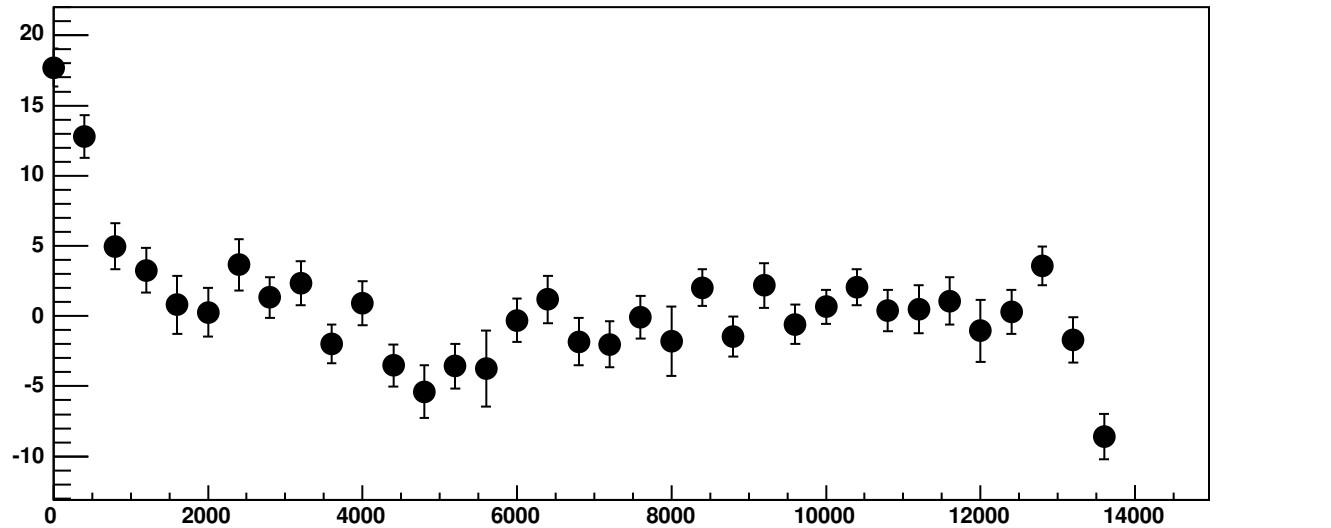
Chip 10, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

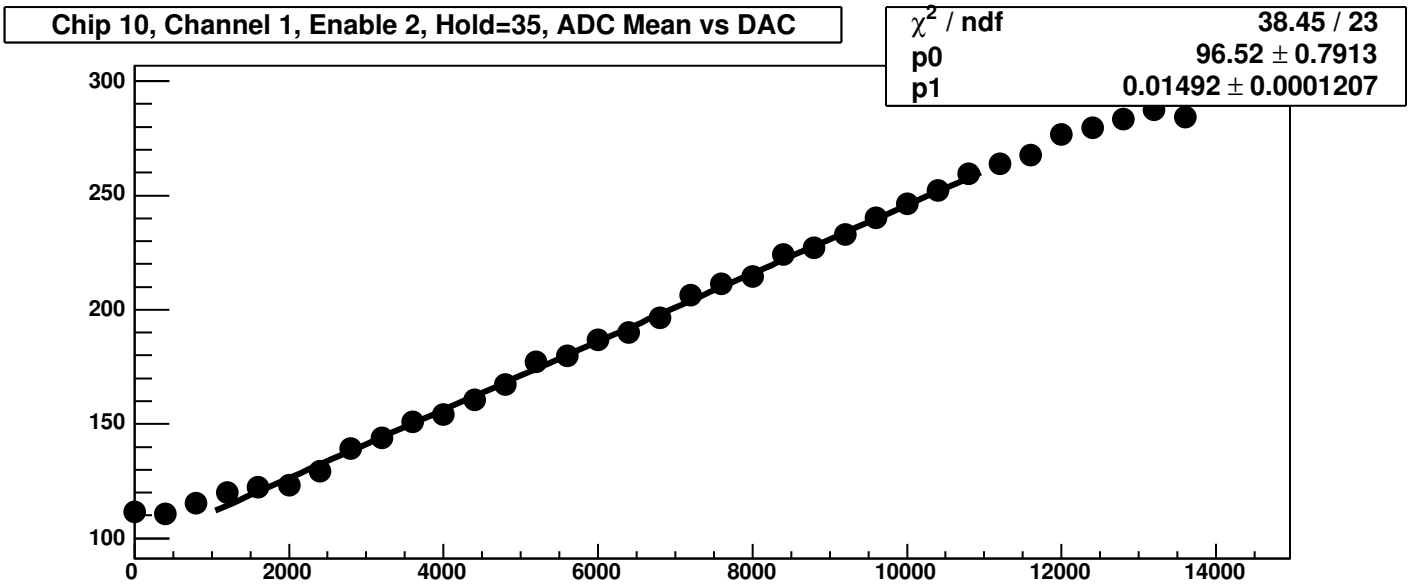


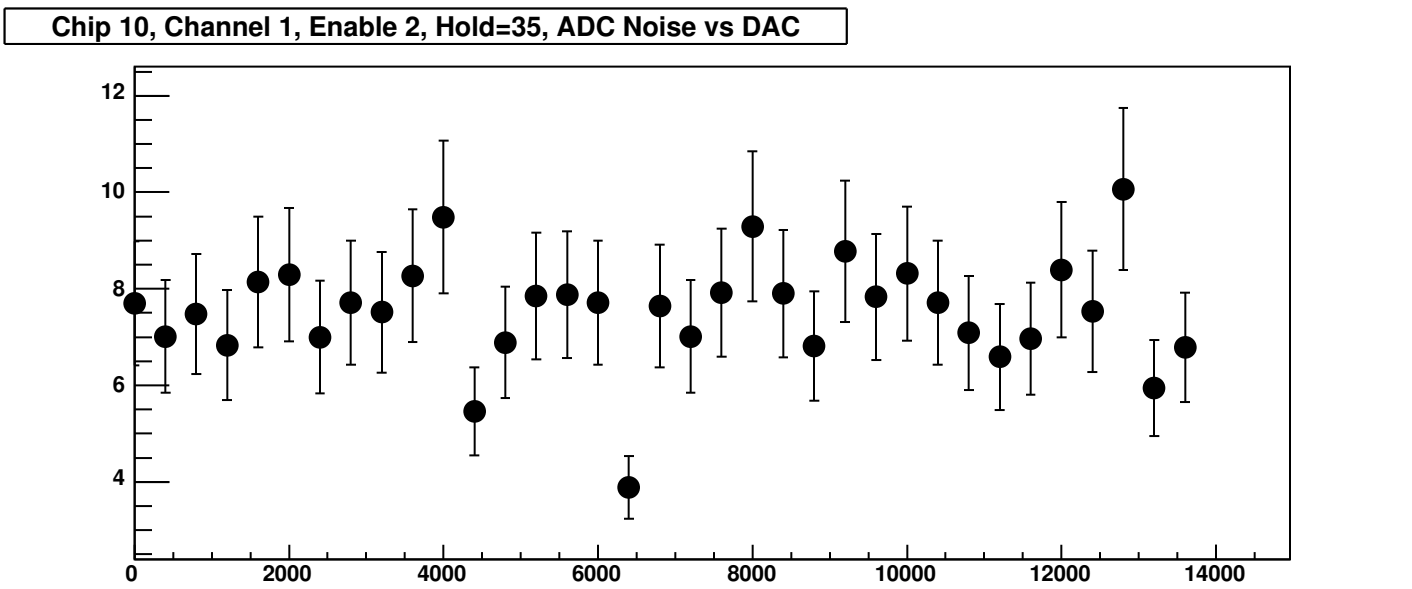
Chip 10, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC

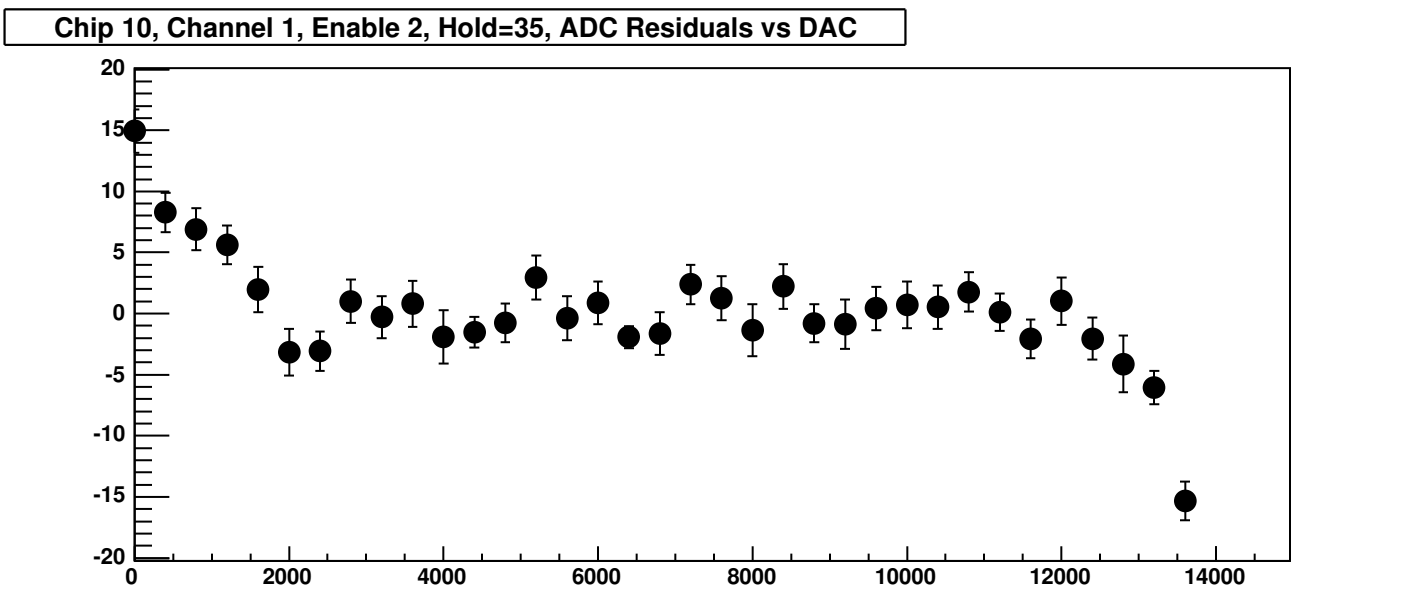


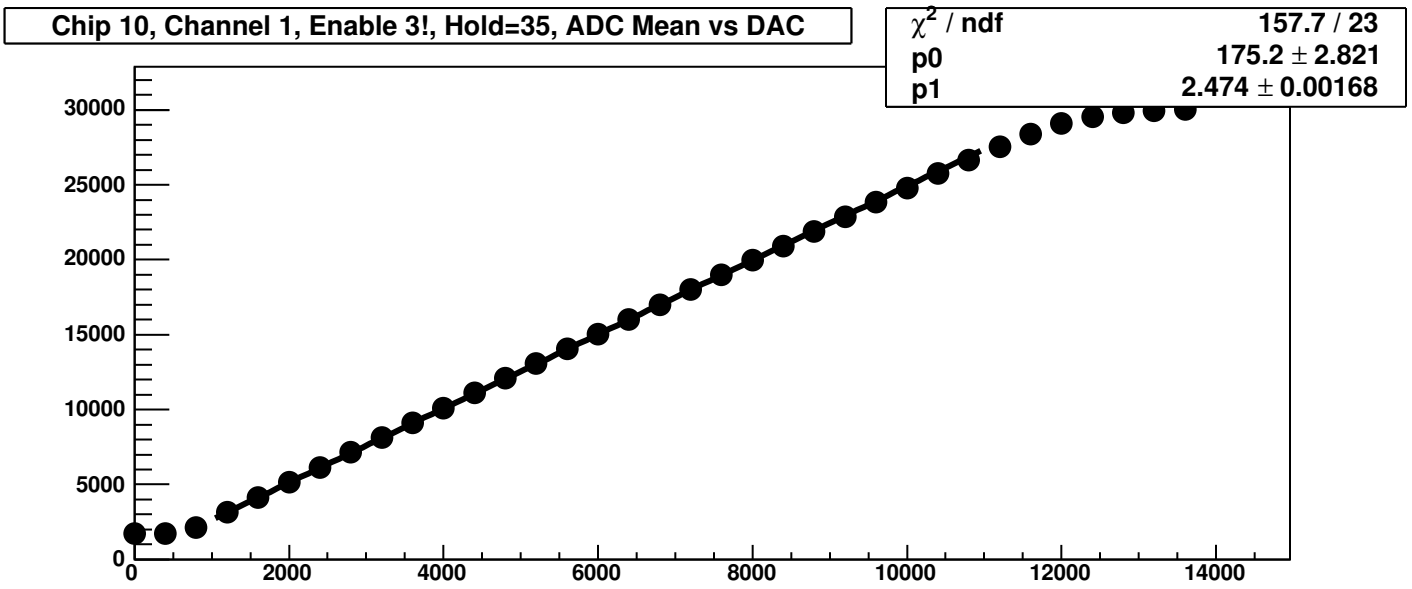
Chip 10, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

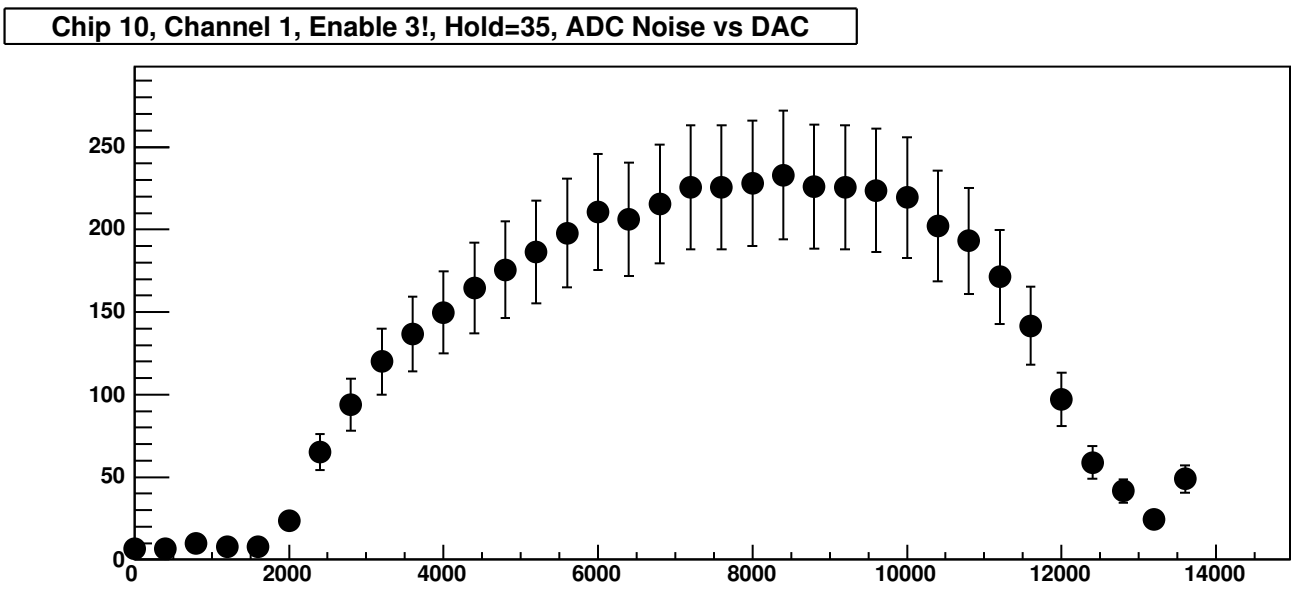


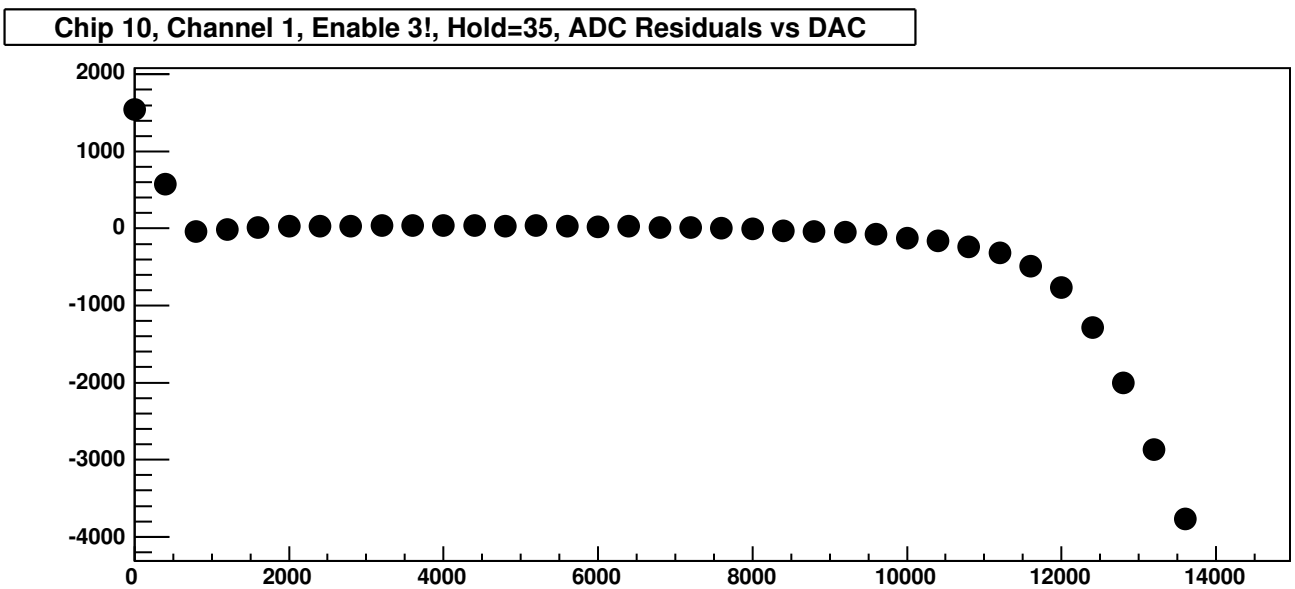

Chip 10, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 38.45 / 23
p0 96.52 ± 0.7913
p1 0.01492 ± 0.0001207
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

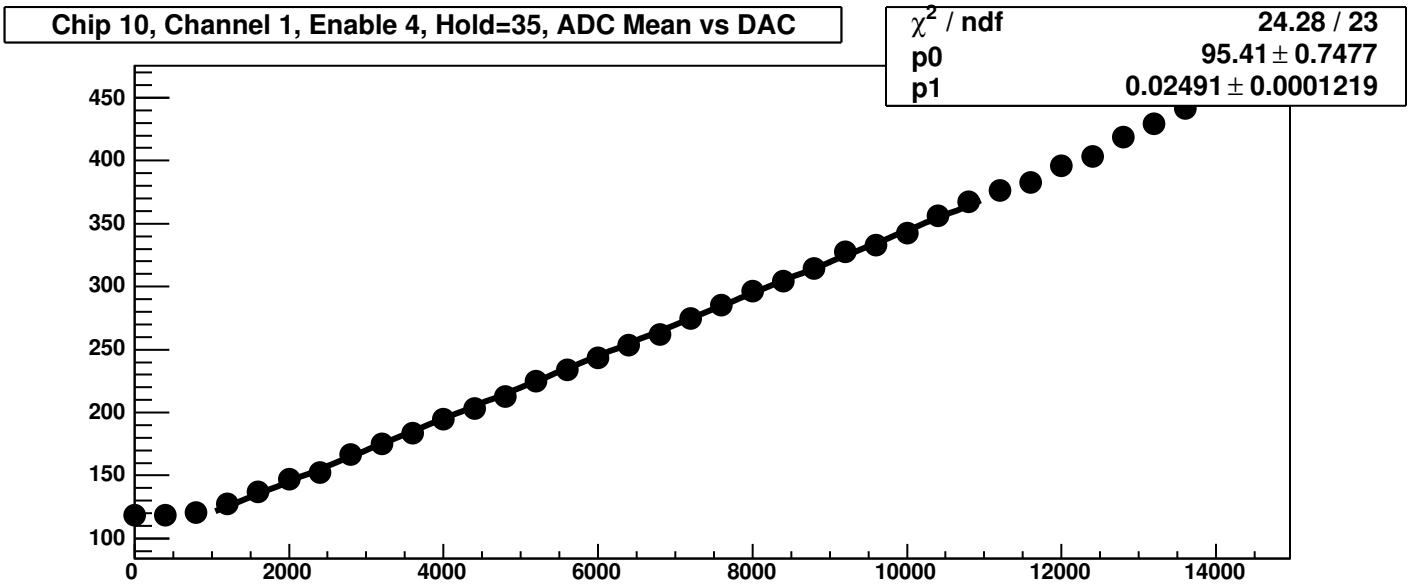

Chip 10, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

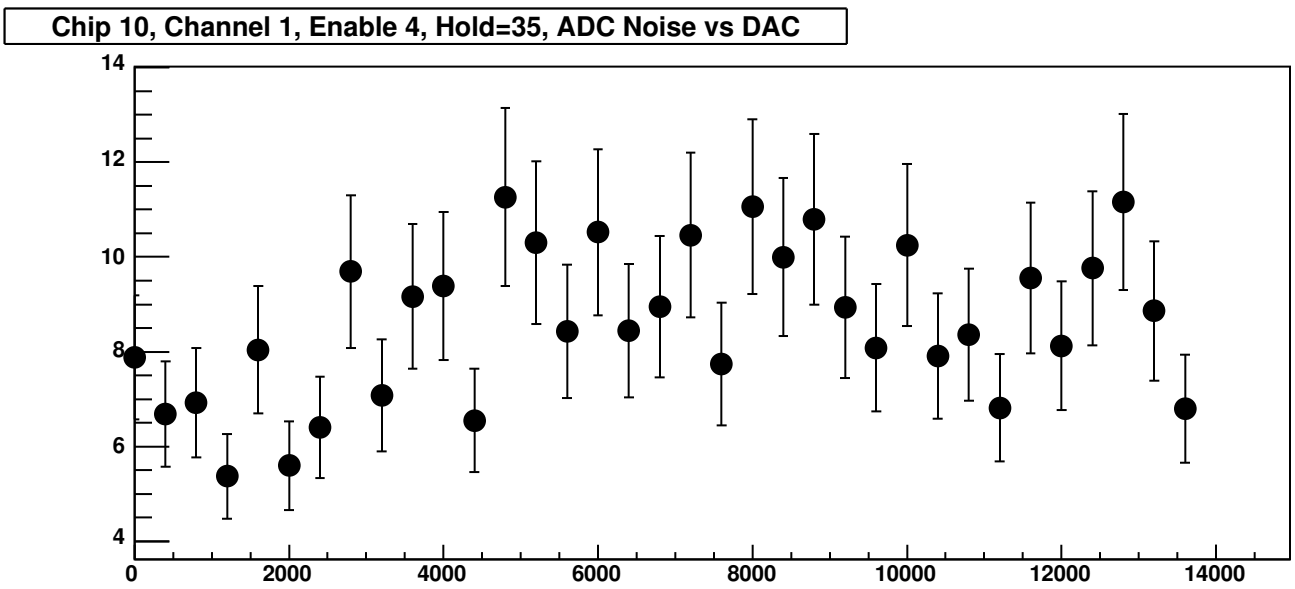

Chip 10, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

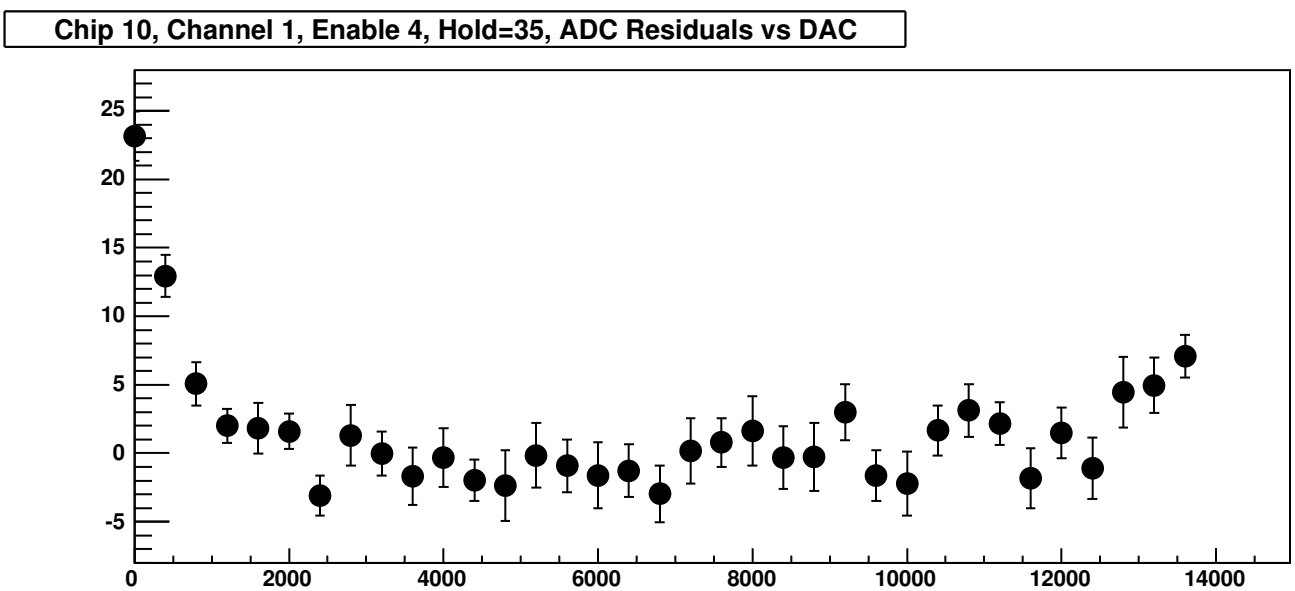

Chip 10, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 157.7 / 23
p0 175.2 ± 2.821
p1 2.474 ± 0.00168
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

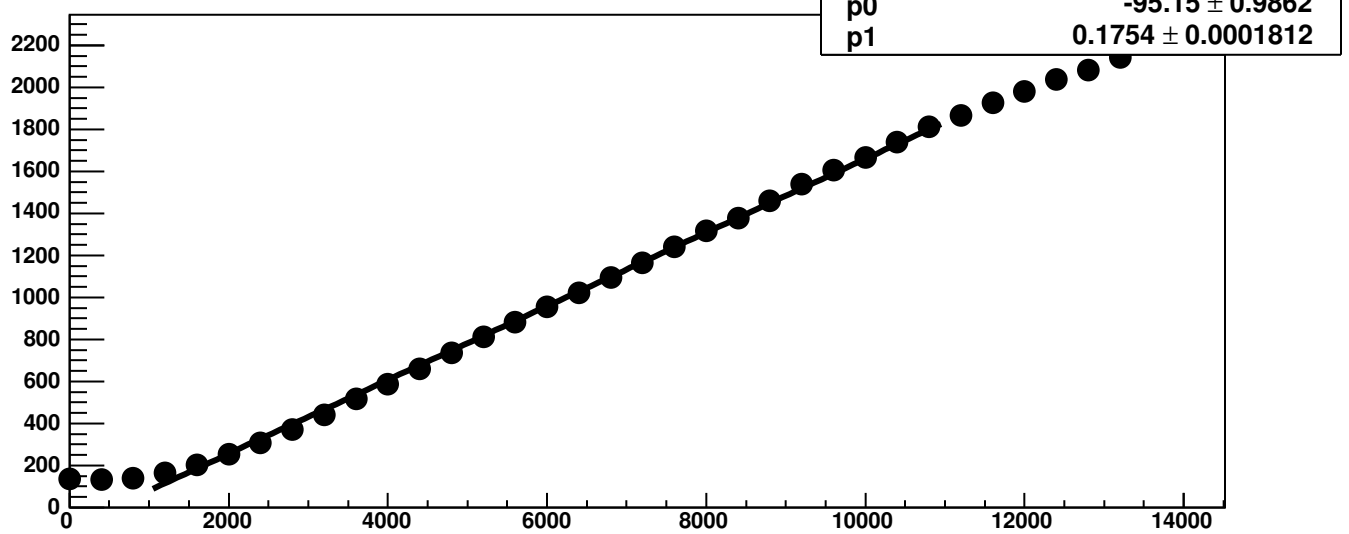

Chip 10, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.28 / 23
p0 95.41 ± 0.7477
p1 0.02491 ± 0.0001219
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

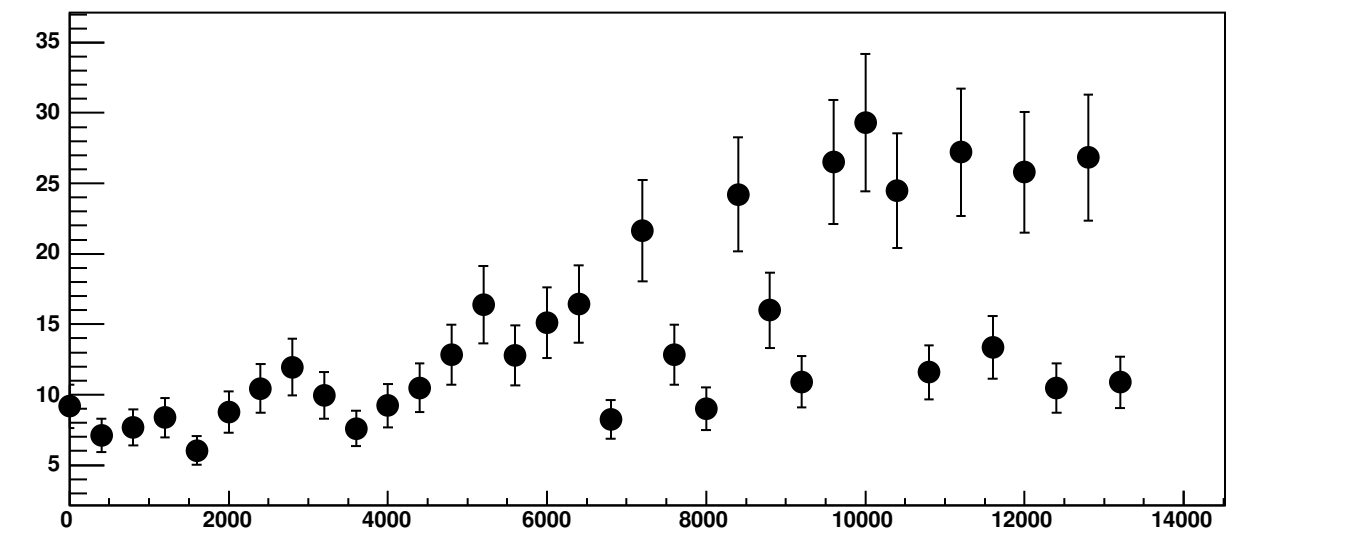

Chip 10, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

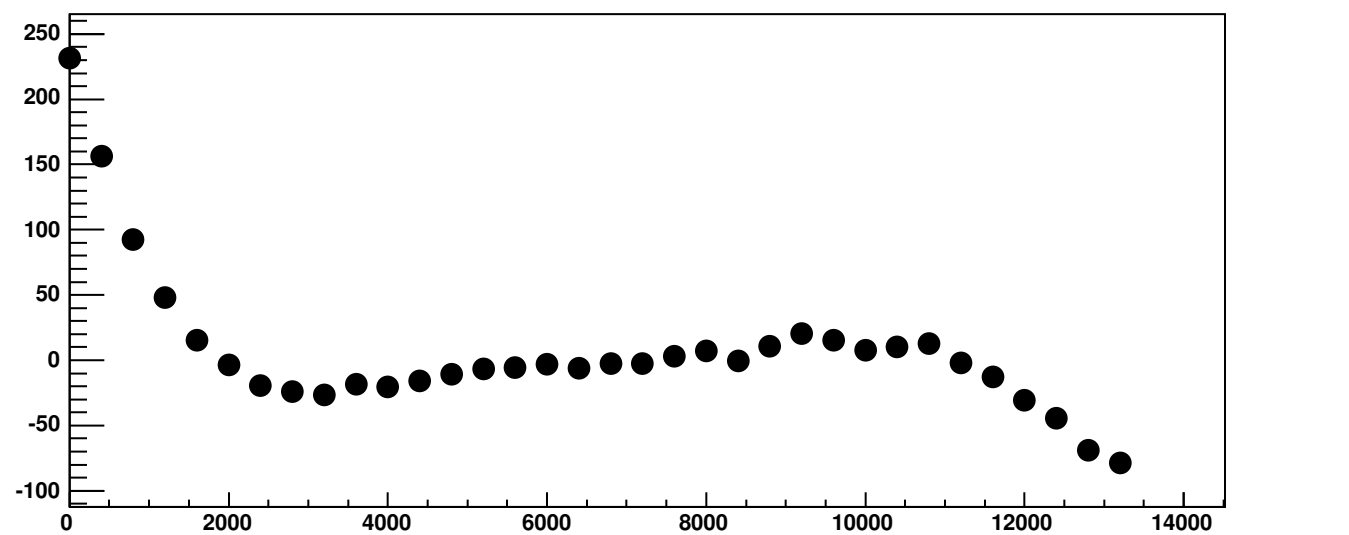
Chip 10, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

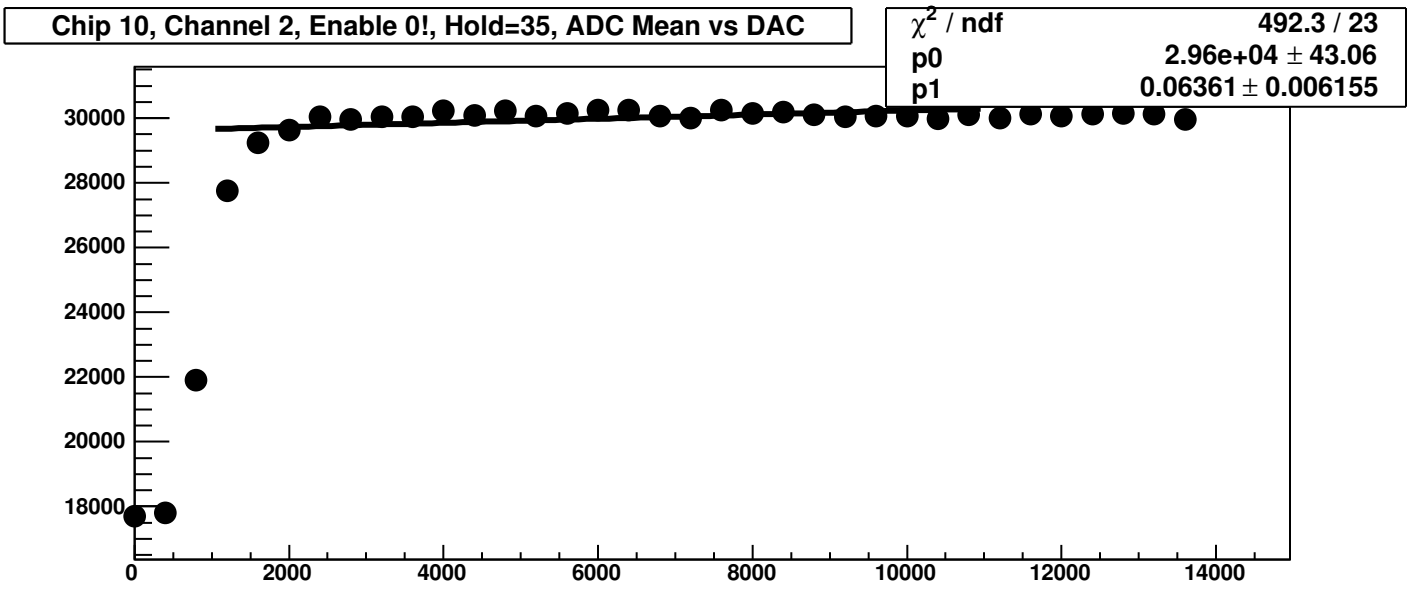


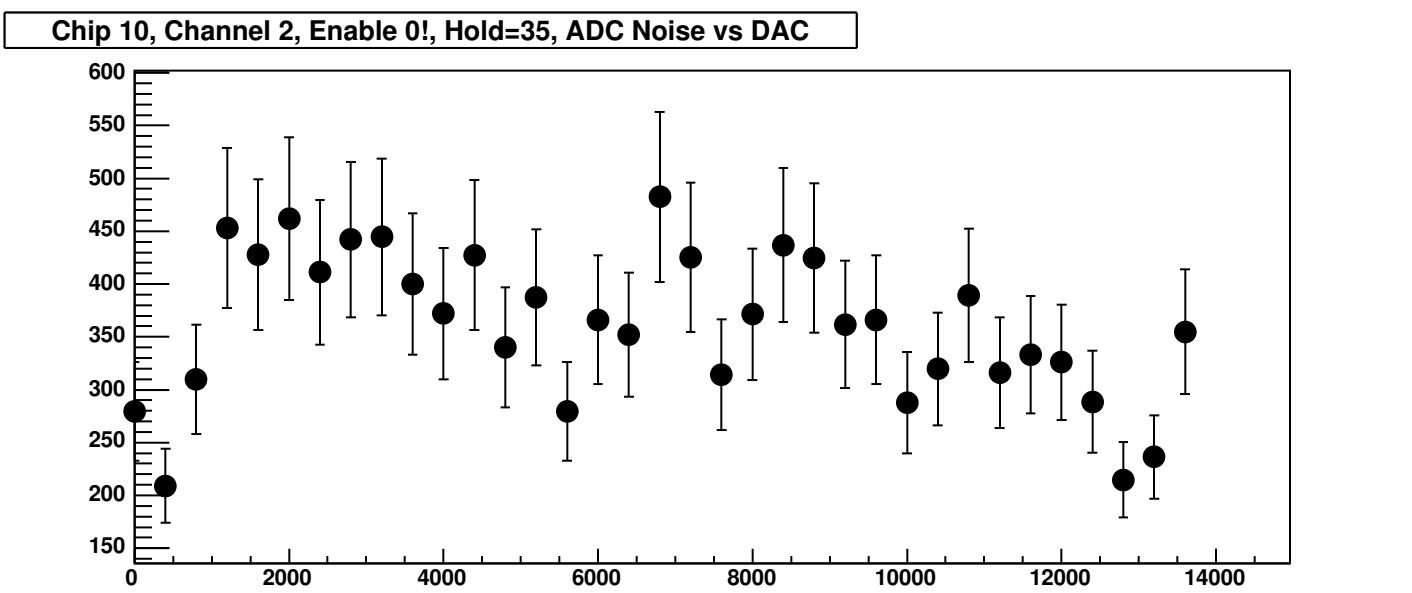
Chip 10, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC

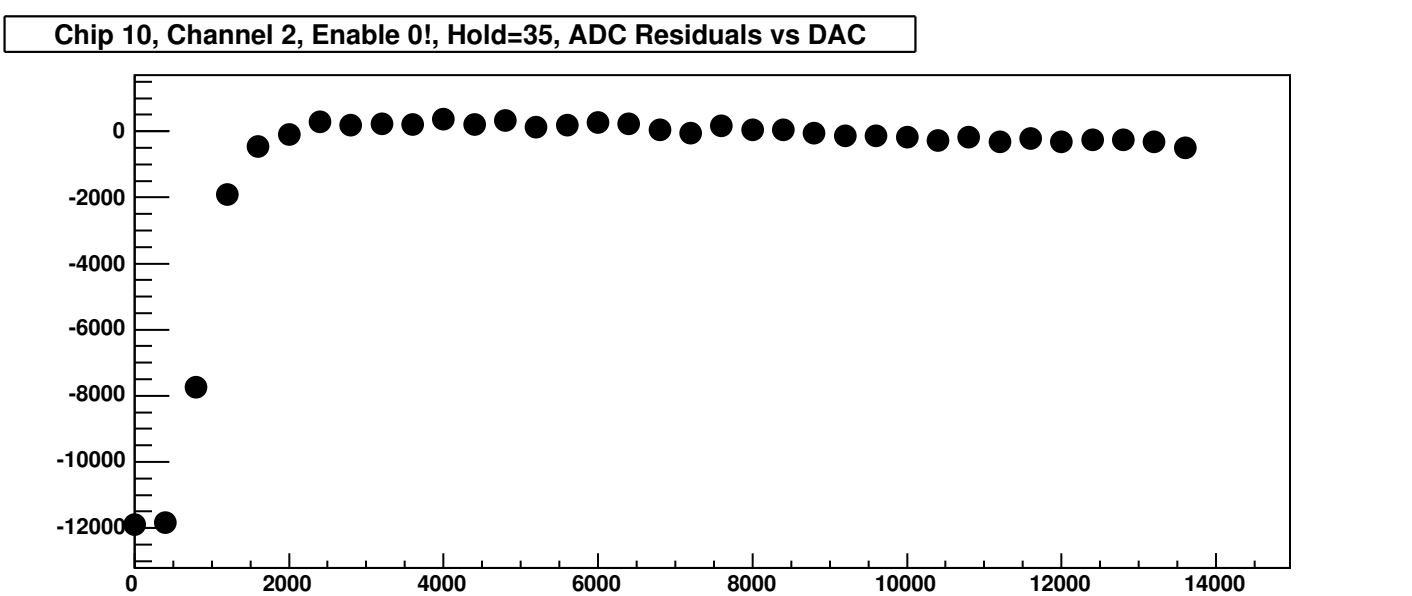


Chip 10, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

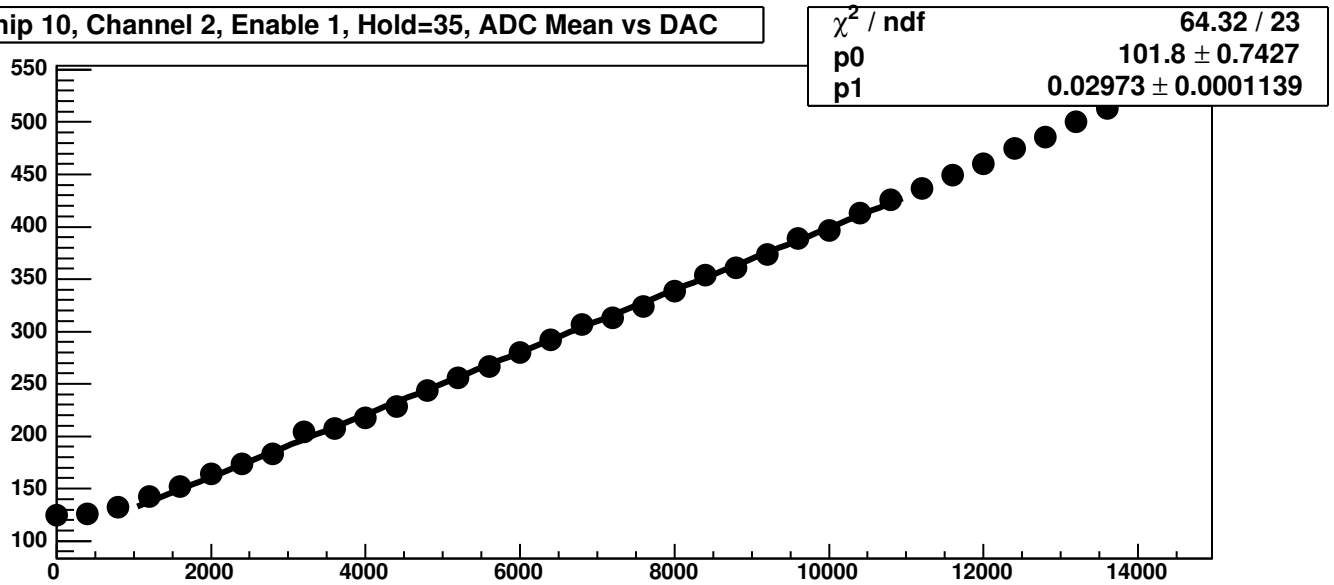



Chip 10, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 492.3 / 23
p0 2.96e+04 ± 43.06
p1 0.06361 ± 0.006155
30000
28000
26000
24000
22000
20000
18000
0
2000
4000
6000
8000
10000
12000
14000

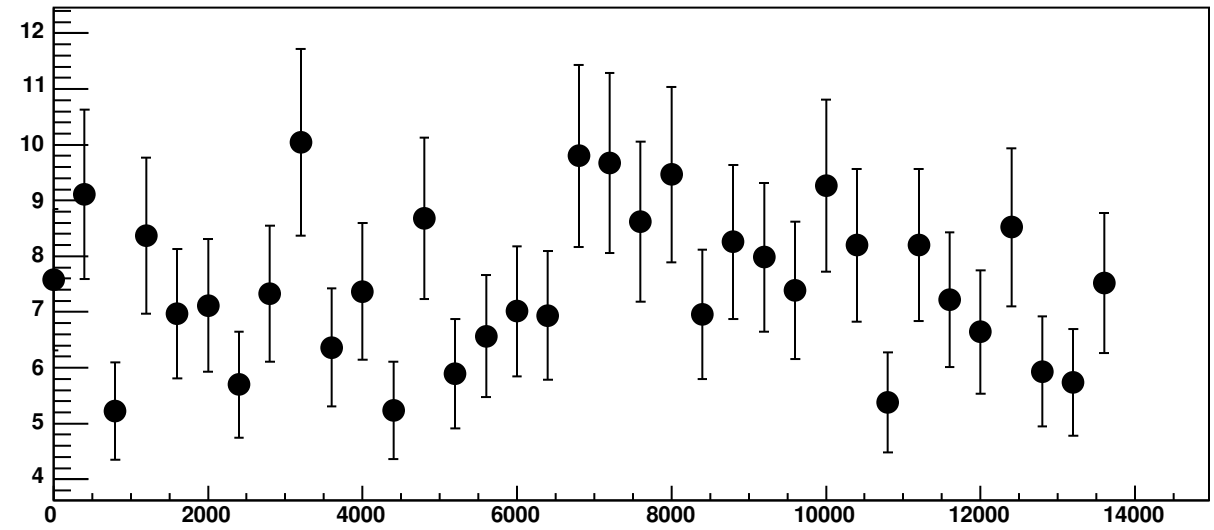

Chip 10, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
600
550
500
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
2000
4000
6000
8000
10000
12000
14000

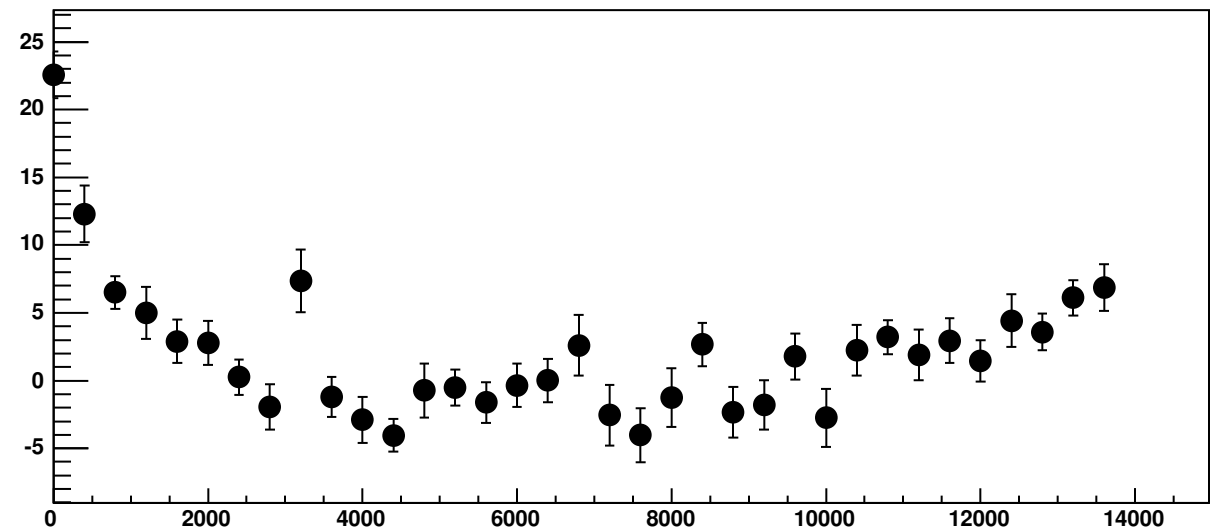
Chip 10, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



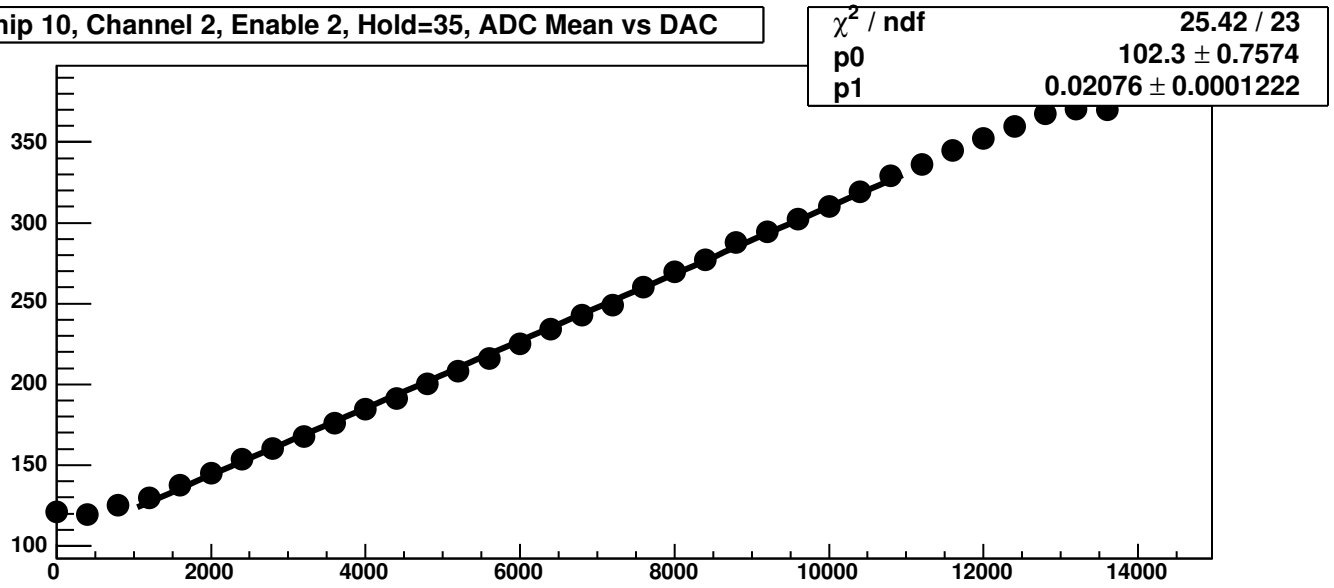
Chip 10, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



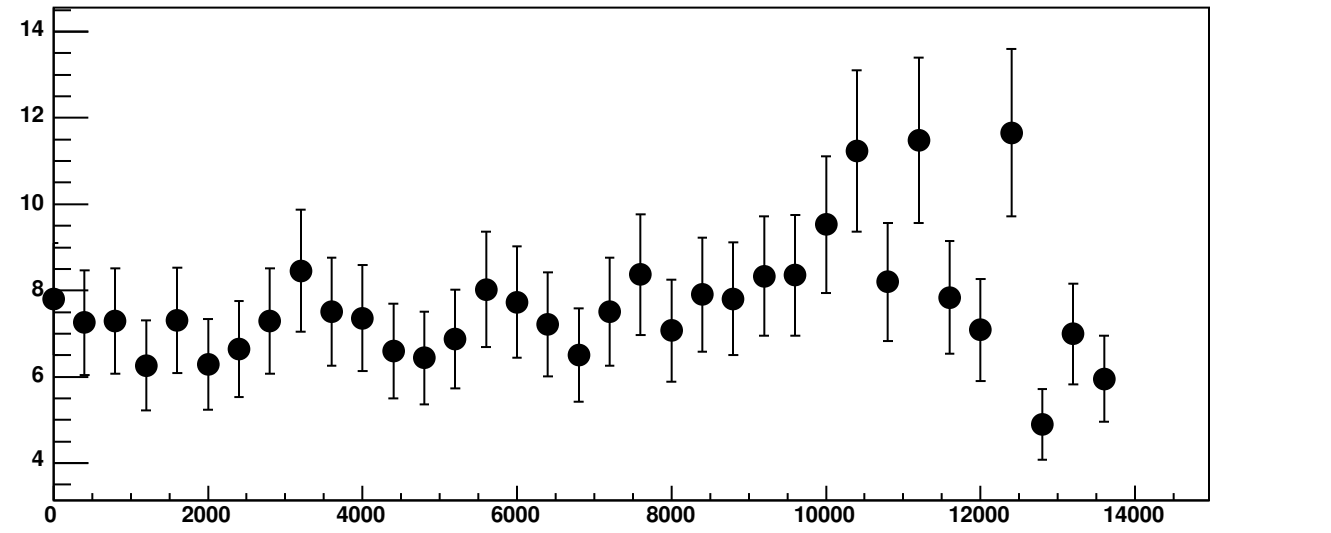
Chip 10, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



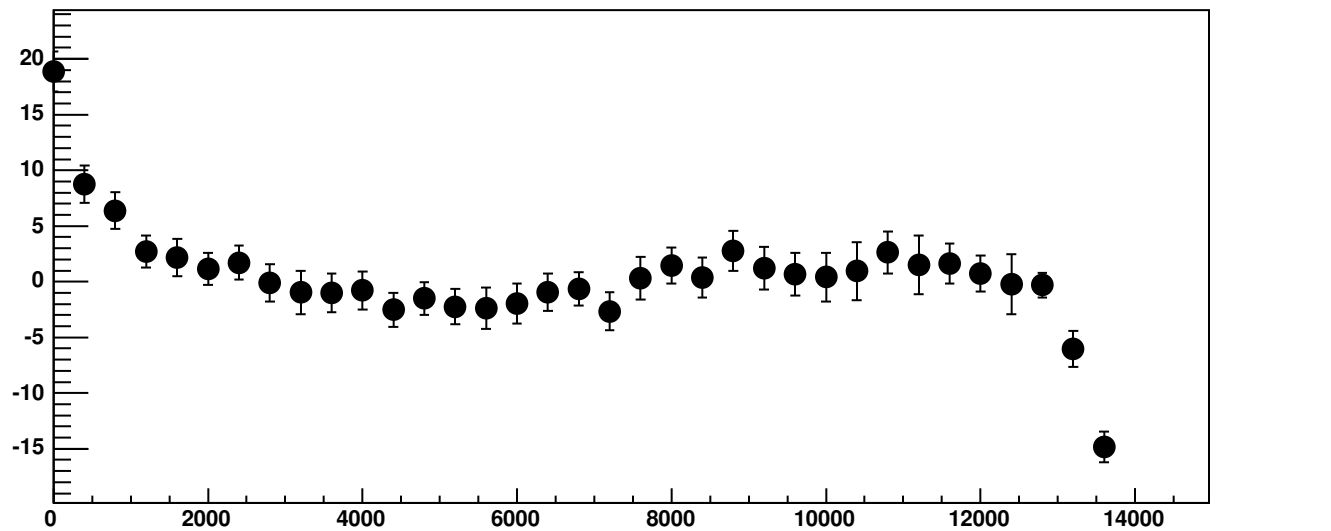
Chip 10, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

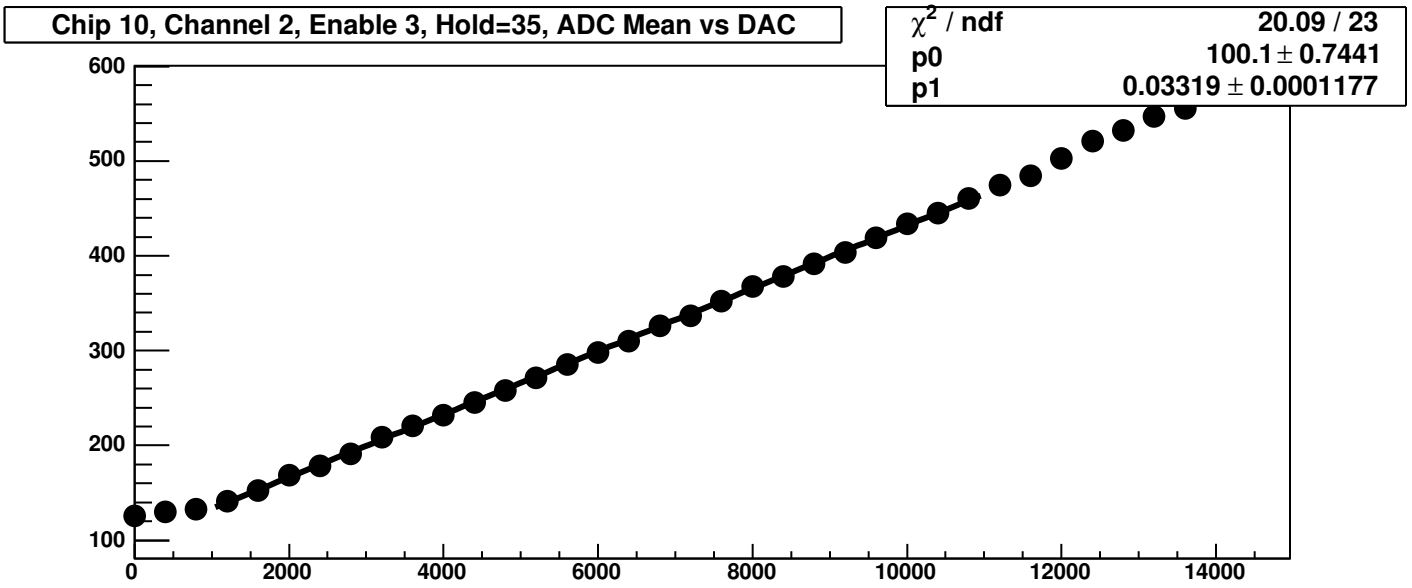


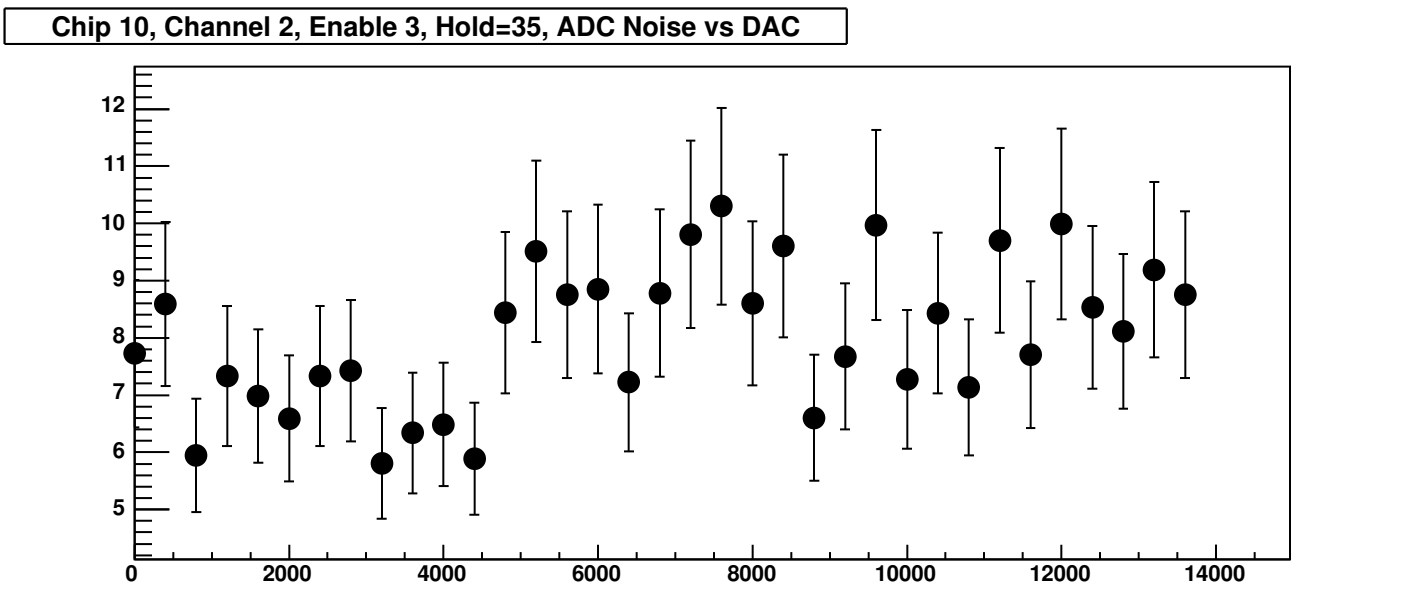
Chip 10, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

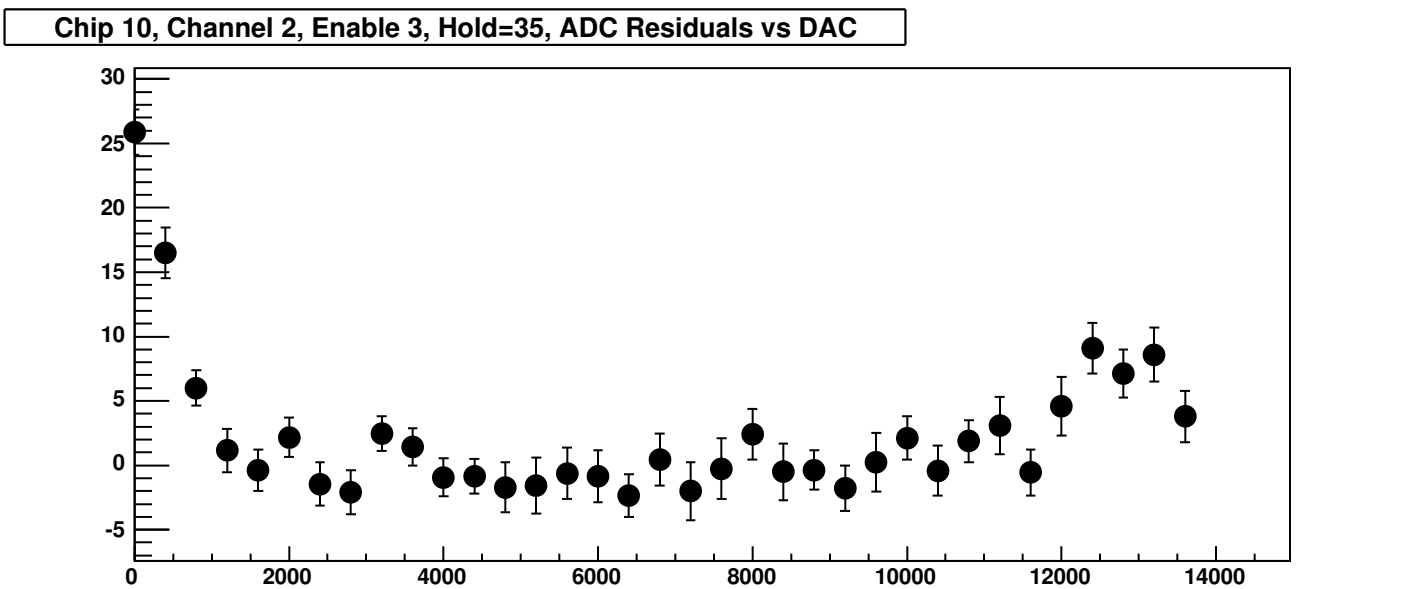


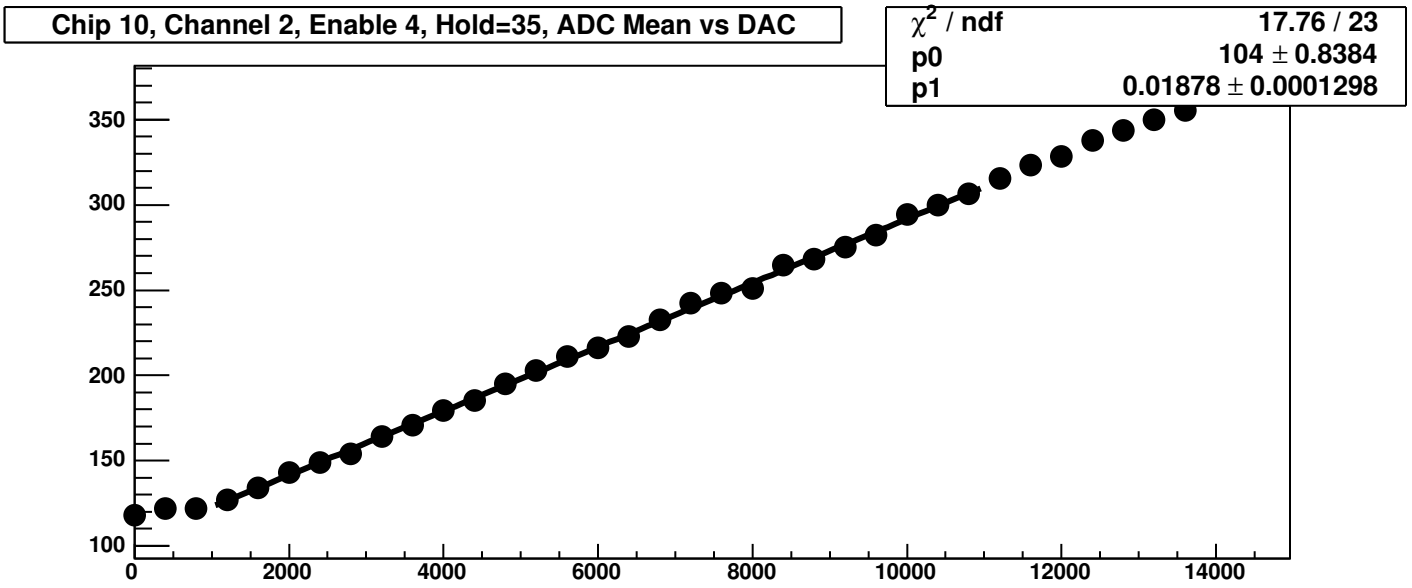
Chip 10, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

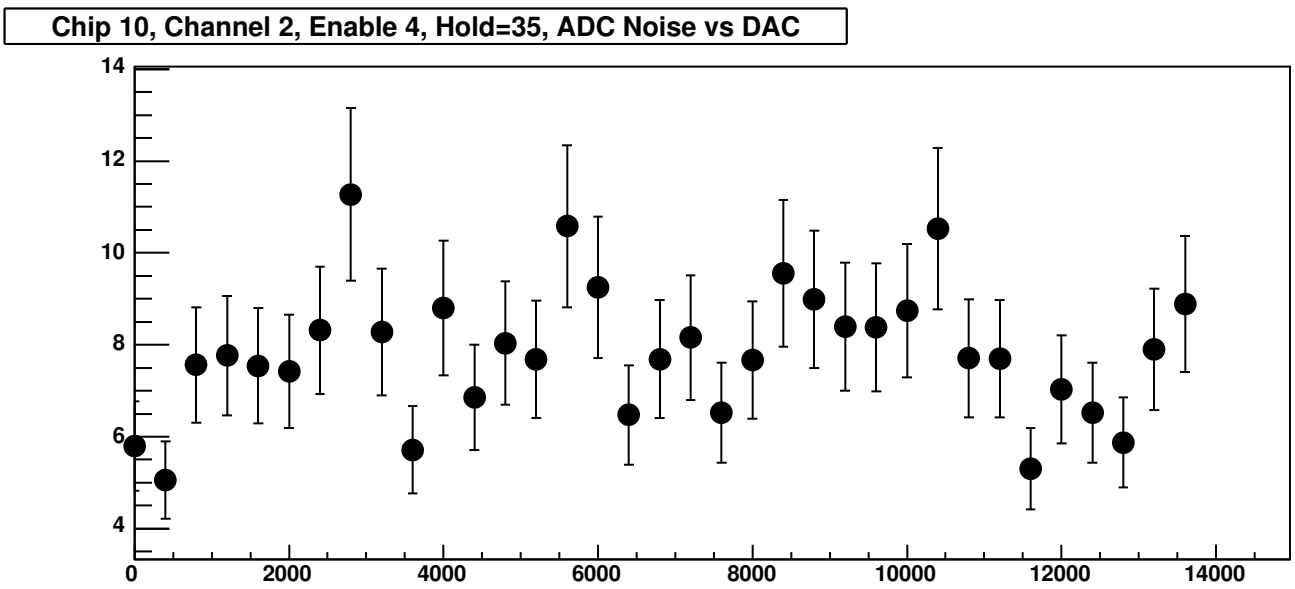


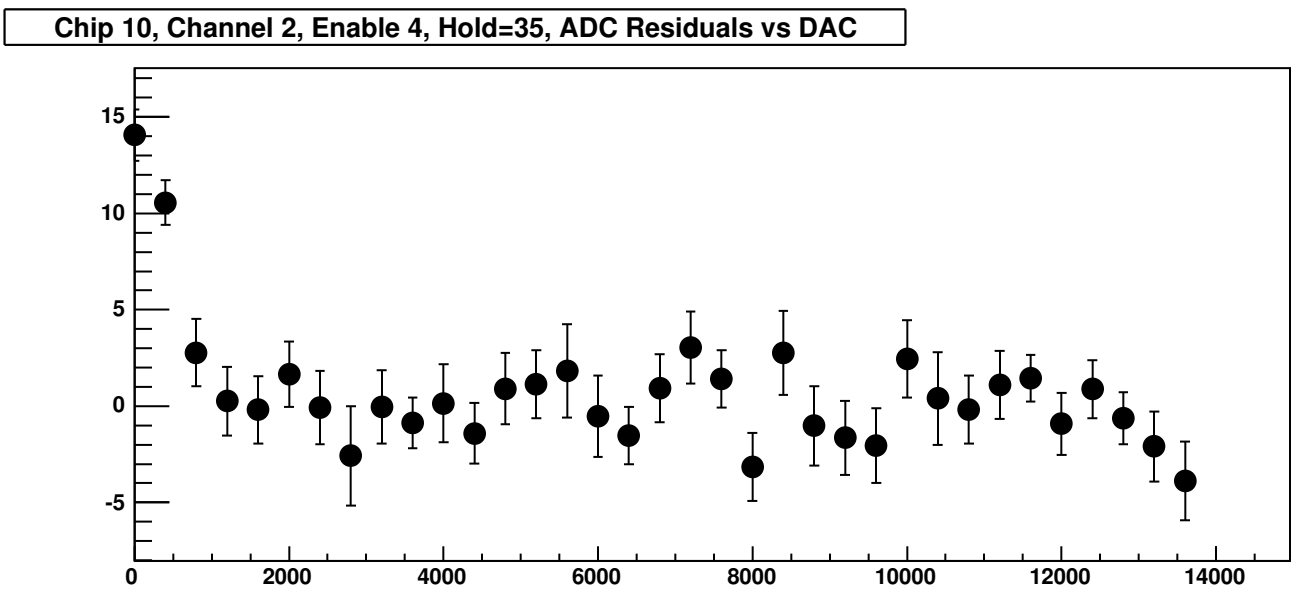

Chip 10, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 20.09 / 23
p0 100.1 ± 0.7441
p1 0.03319 ± 0.0001177
600
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000

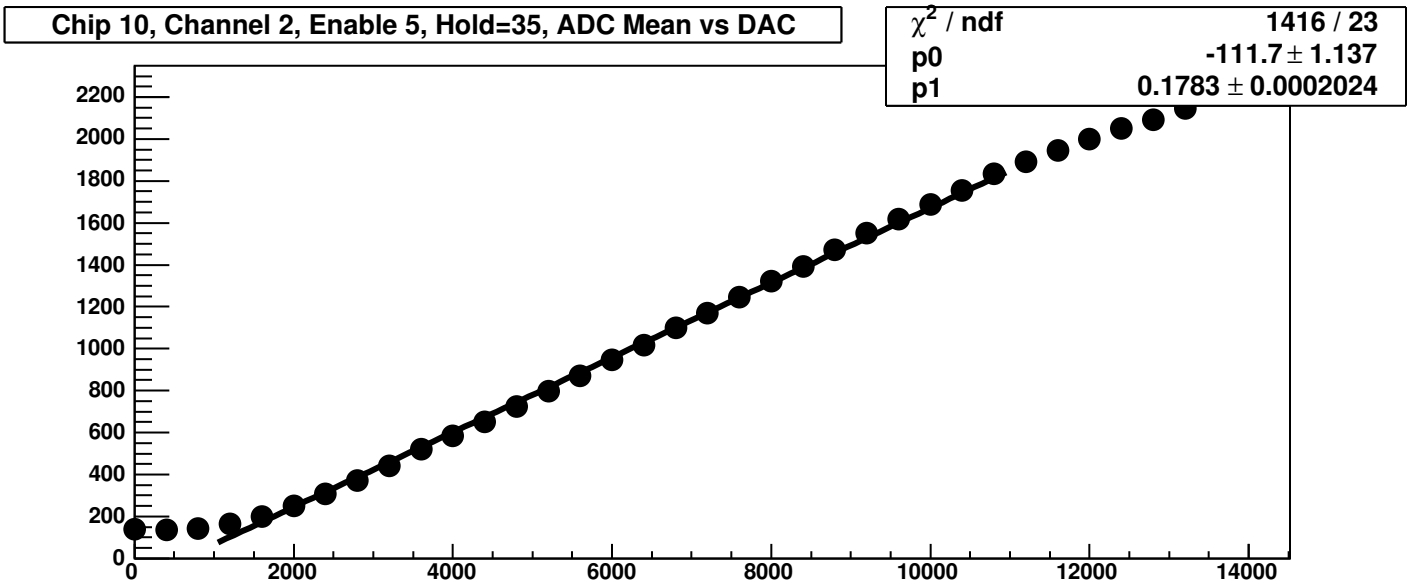

Chip 10, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

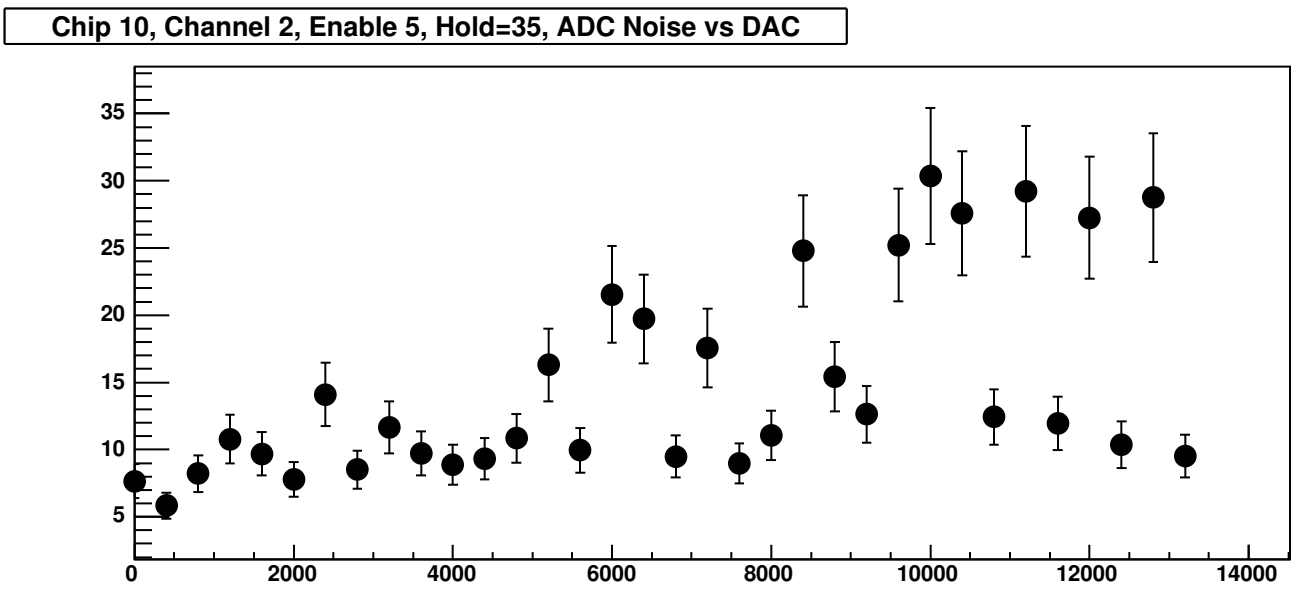

Chip 10, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

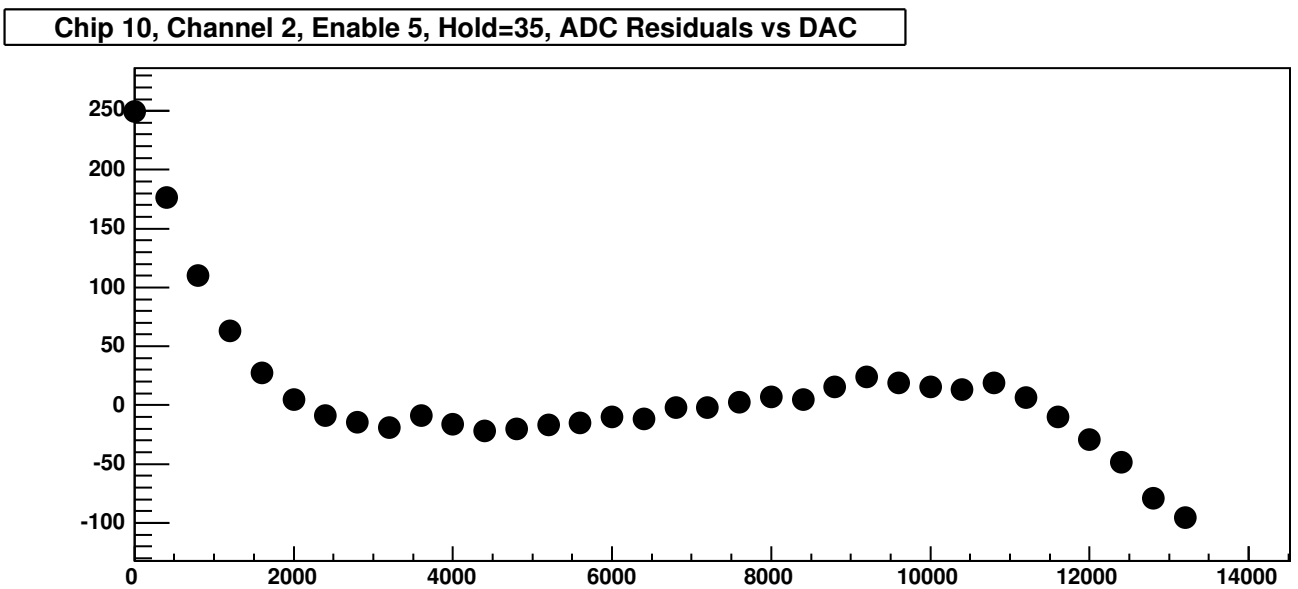

Chip 10, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 17.76 / 23
p0 104 ± 0.8384
p1 0.01878 ± 0.0001298
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

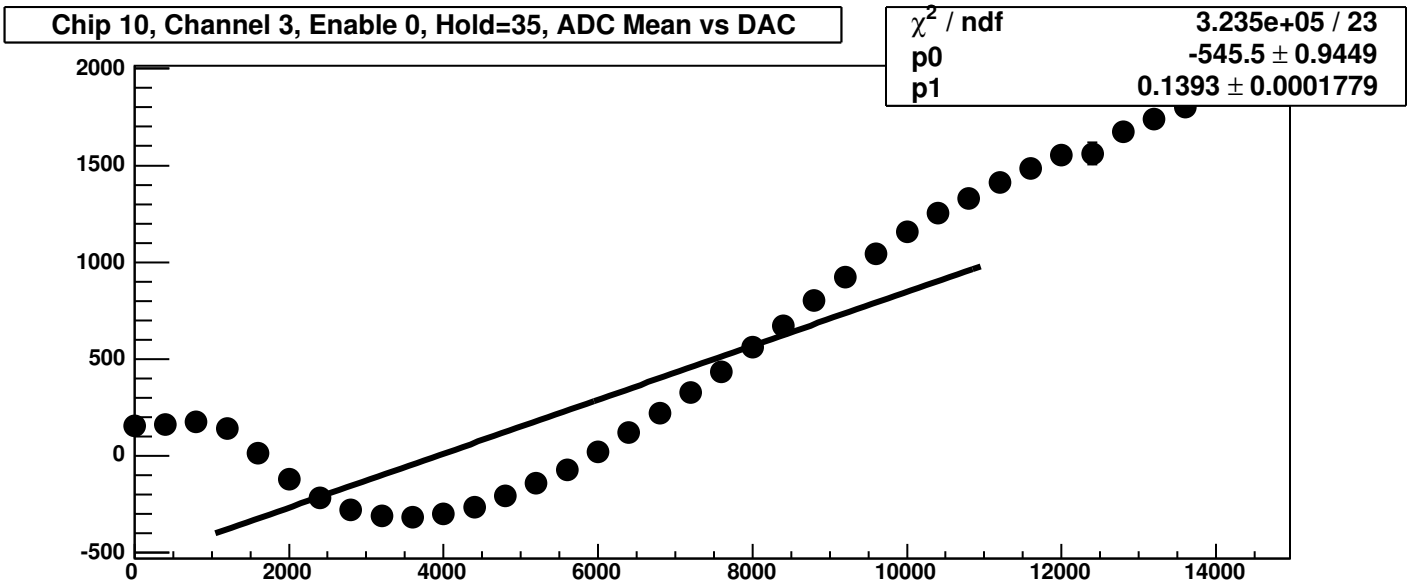

Chip 10, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

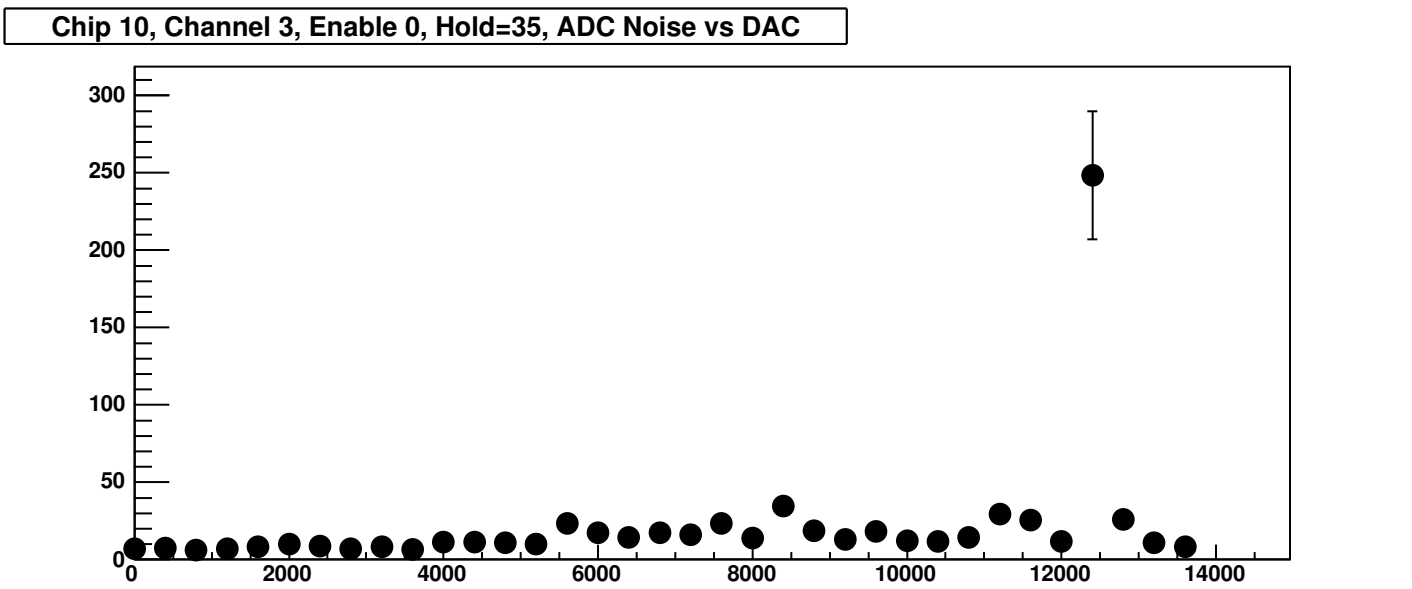

Chip 10, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

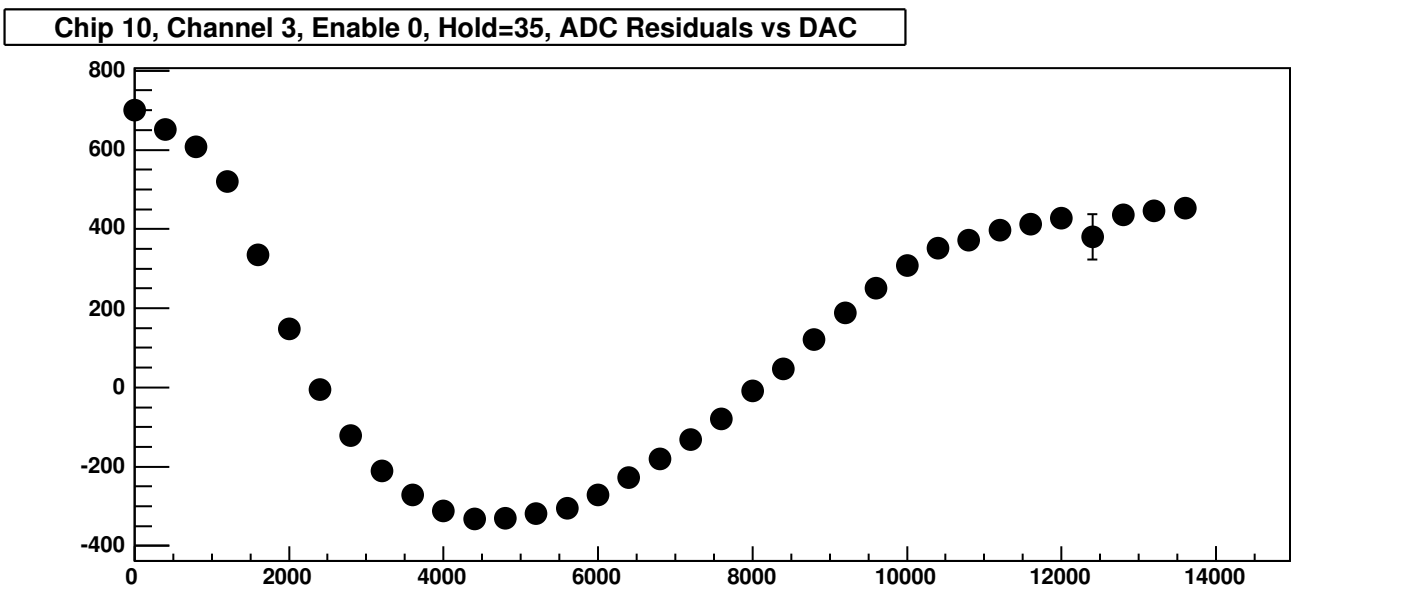

Chip 10, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1416 / 23
p0 -111.7 ± 1.137
p1 0.1783 ± 0.0002024
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

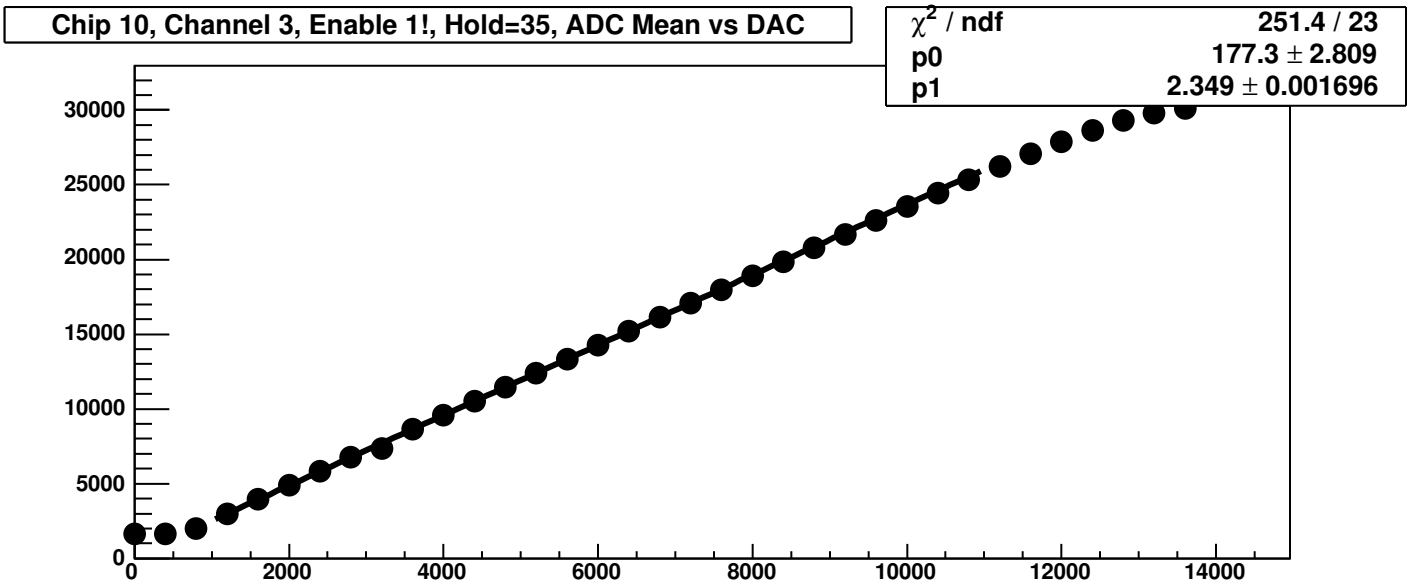

Chip 10, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

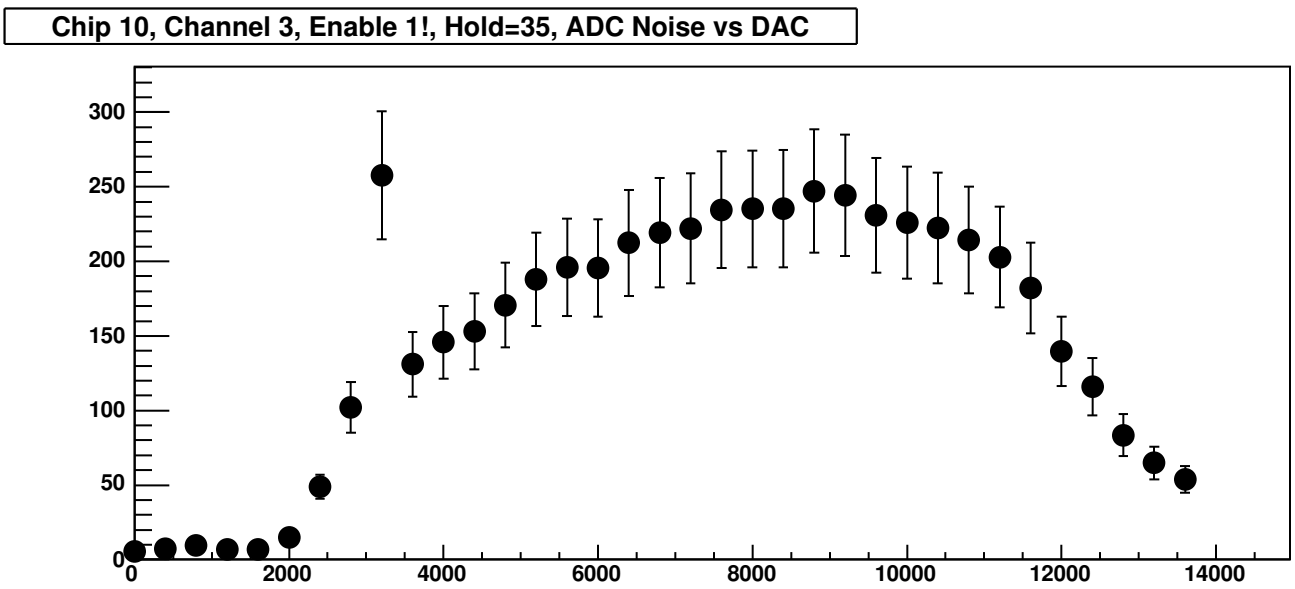

Chip 10, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

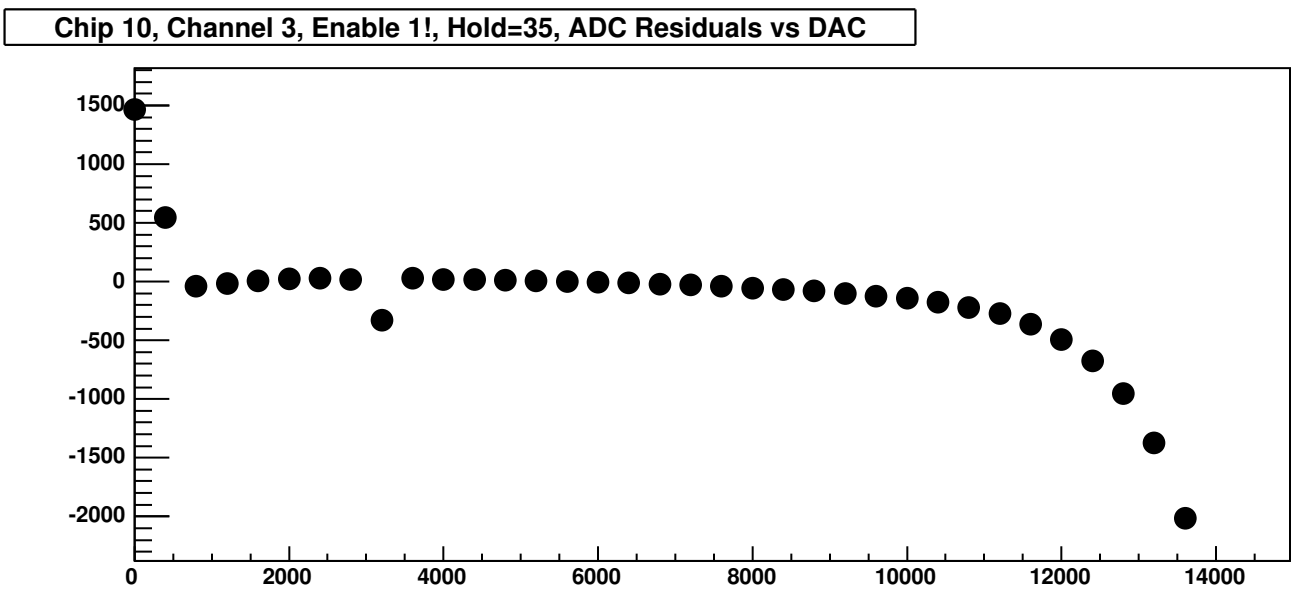

Chip 10, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.235e+05 / 23
p0 -545.5 ± 0.9449
p1 0.1393 ± 0.0001779
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

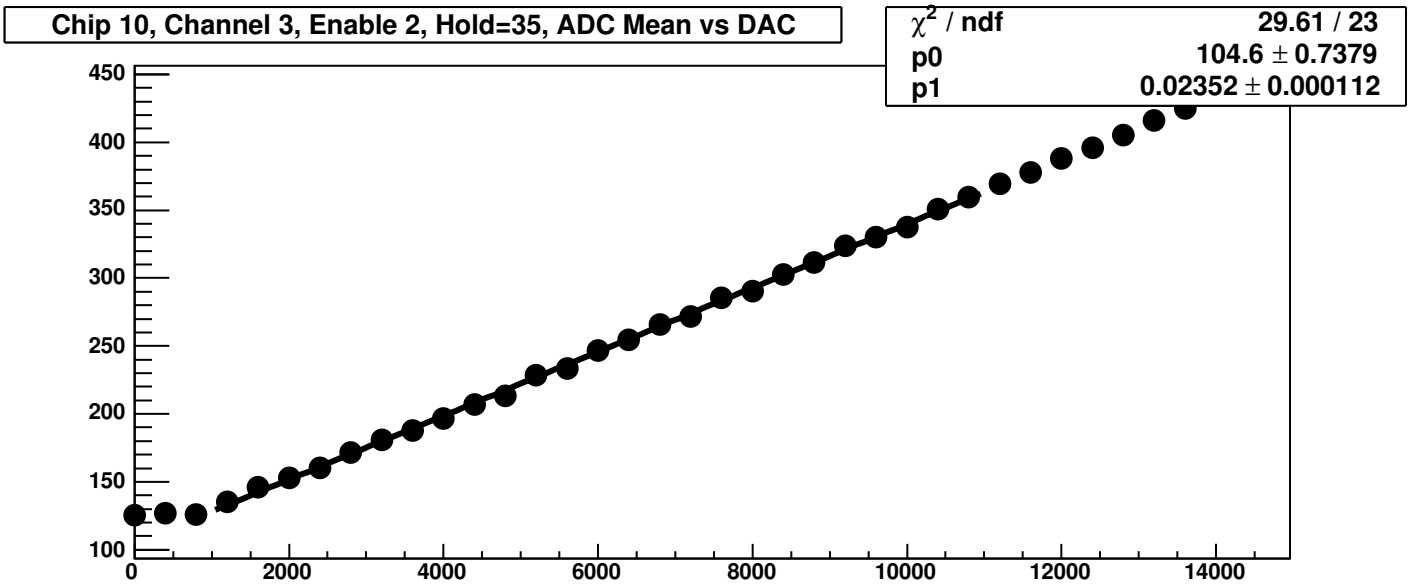

Chip 10, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

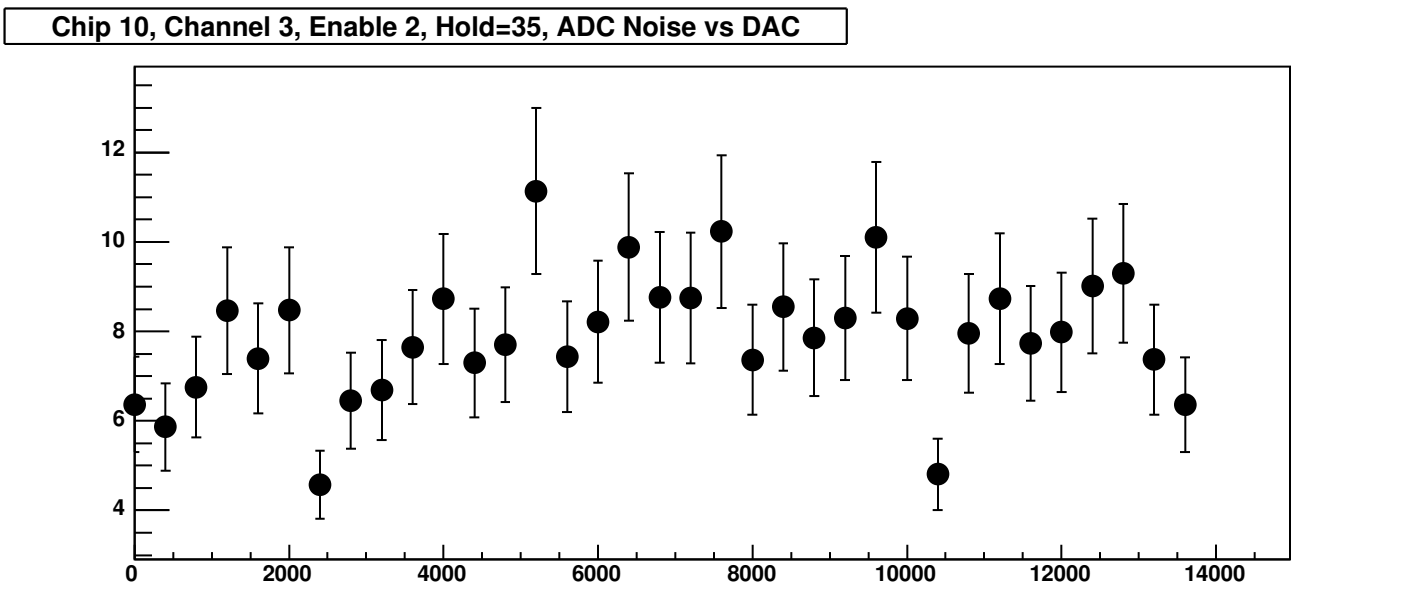

Chip 10, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

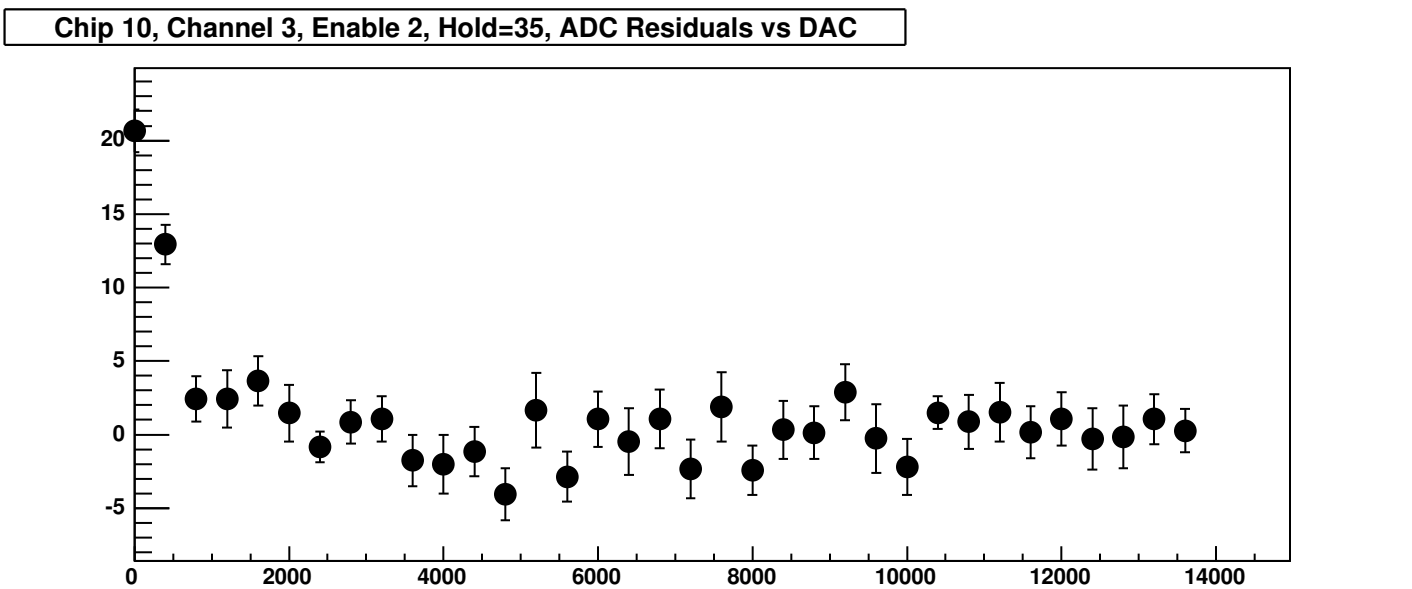

Chip 10, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 251.4 / 23
p0 177.3 ± 2.809
p1 2.349 ± 0.001696
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

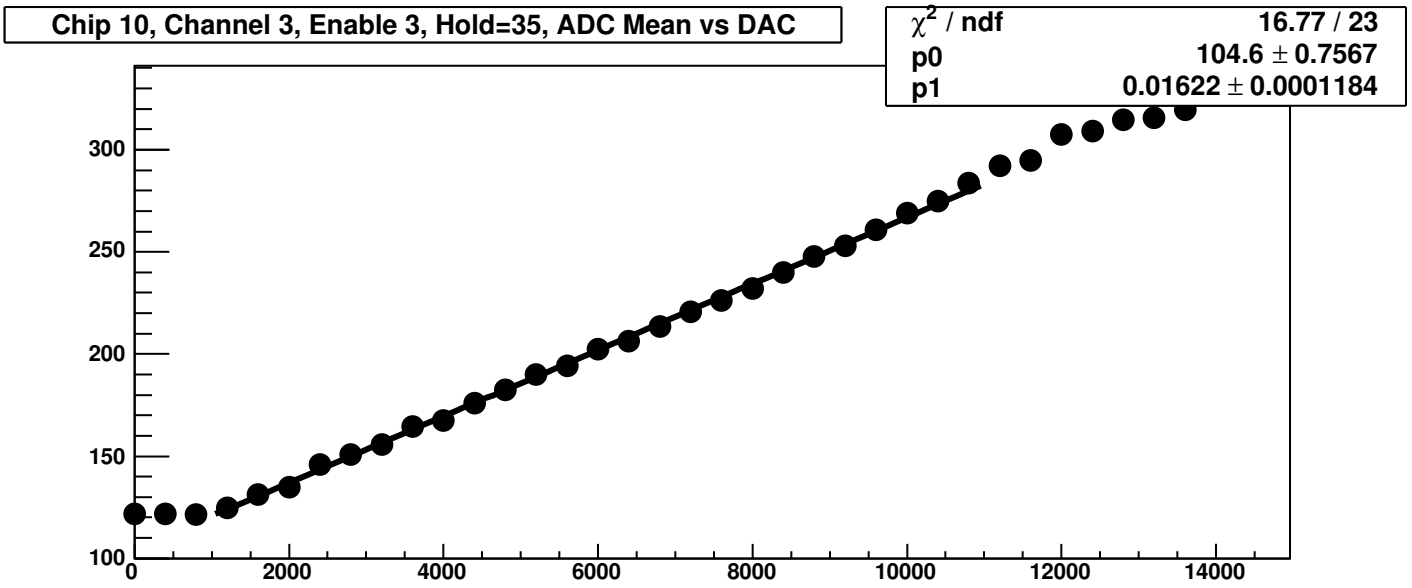

Chip 10, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

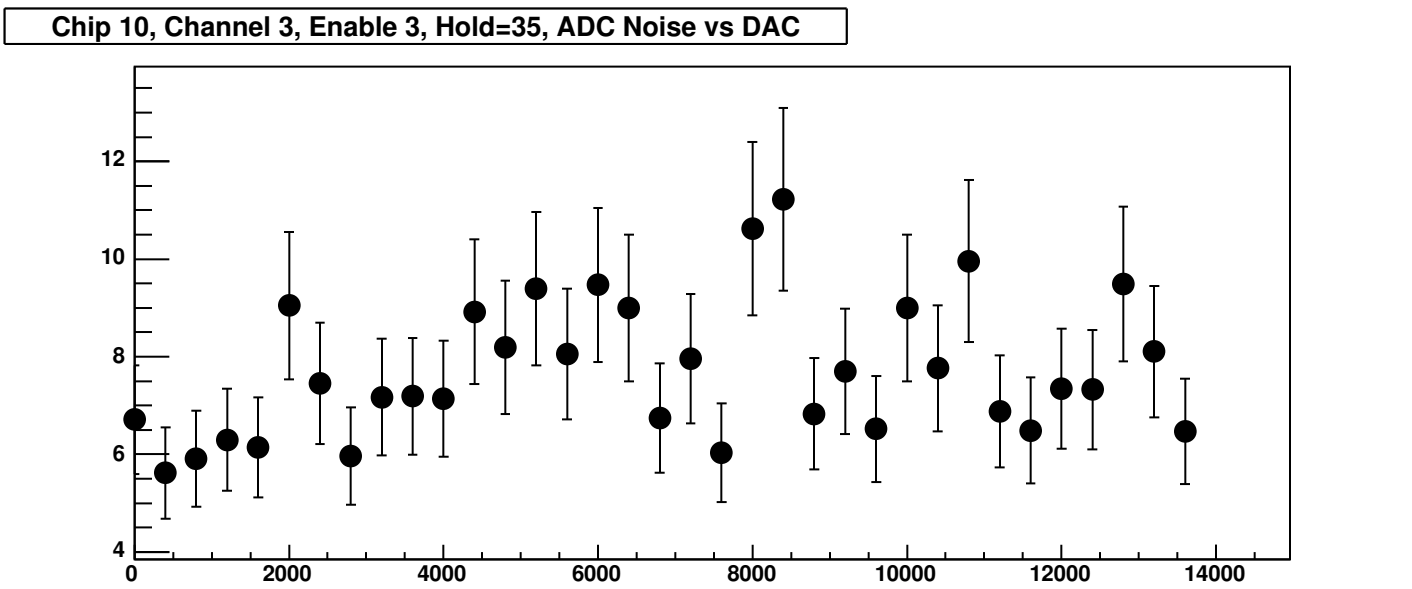

Chip 10, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

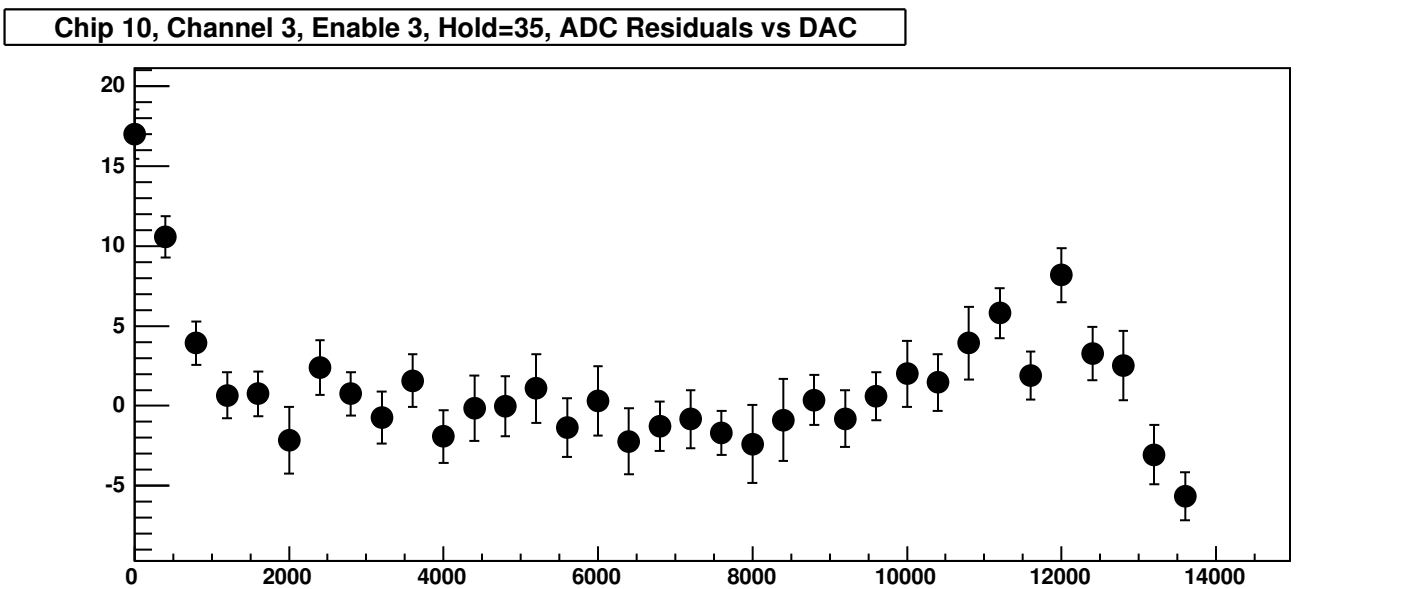

Chip 10, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 29.61 / 23
p0 104.6 ± 0.7379
p1 0.02352 ± 0.000112
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

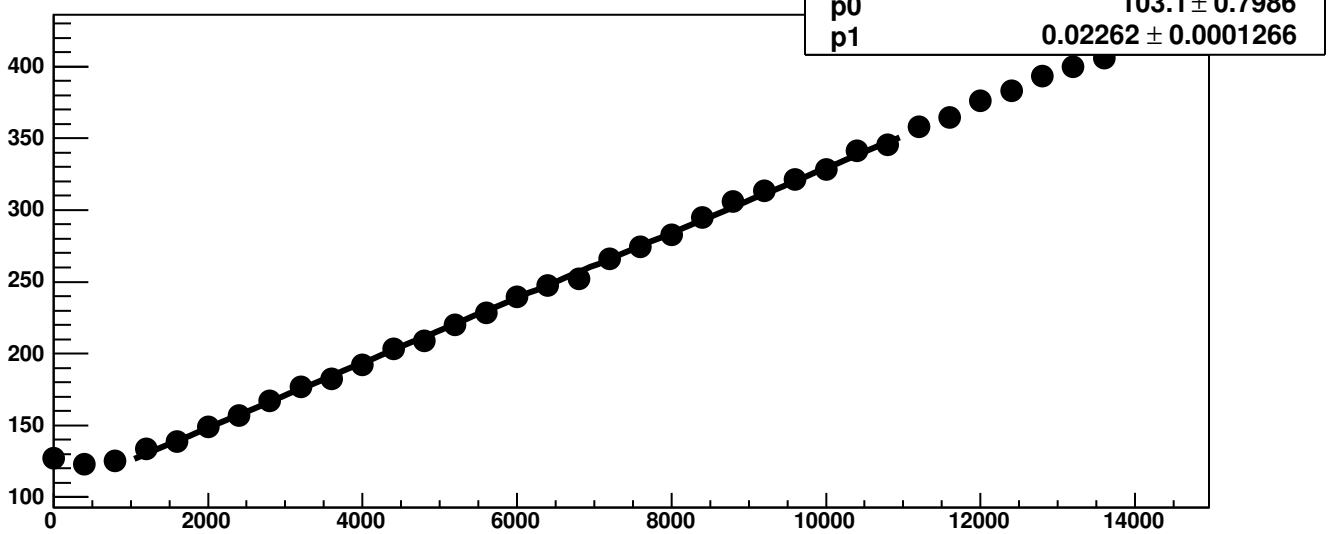

Chip 10, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 16.77 / 23
p0 104.6 ± 0.7567
p1 0.01622 ± 0.0001184
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

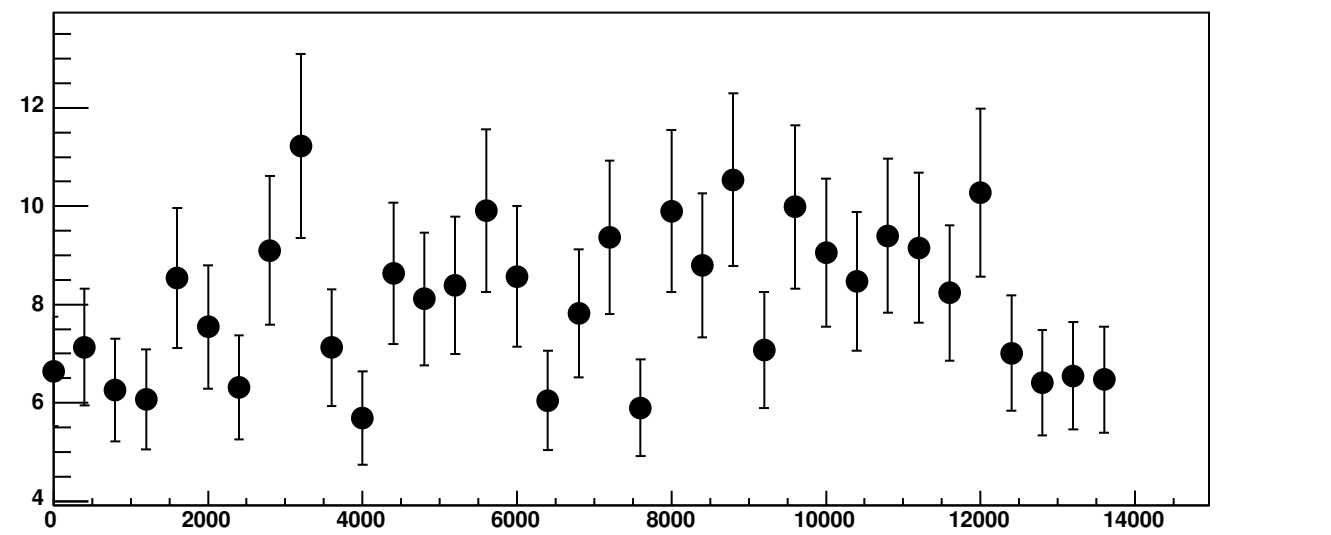

Chip 10, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

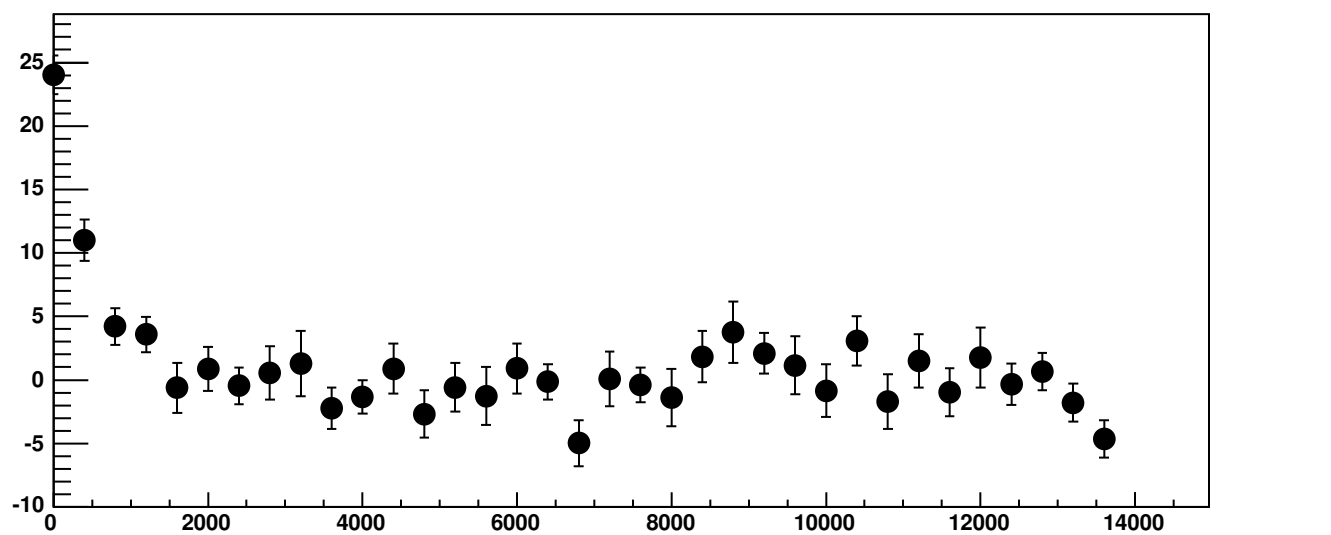
Chip 10, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

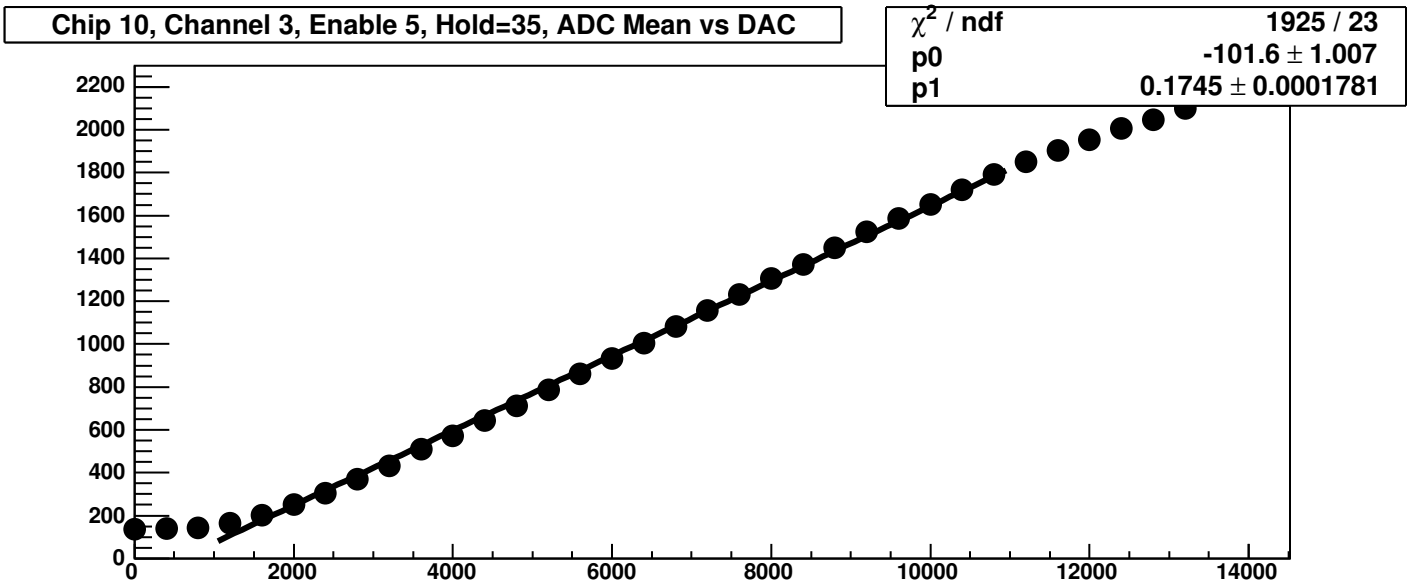


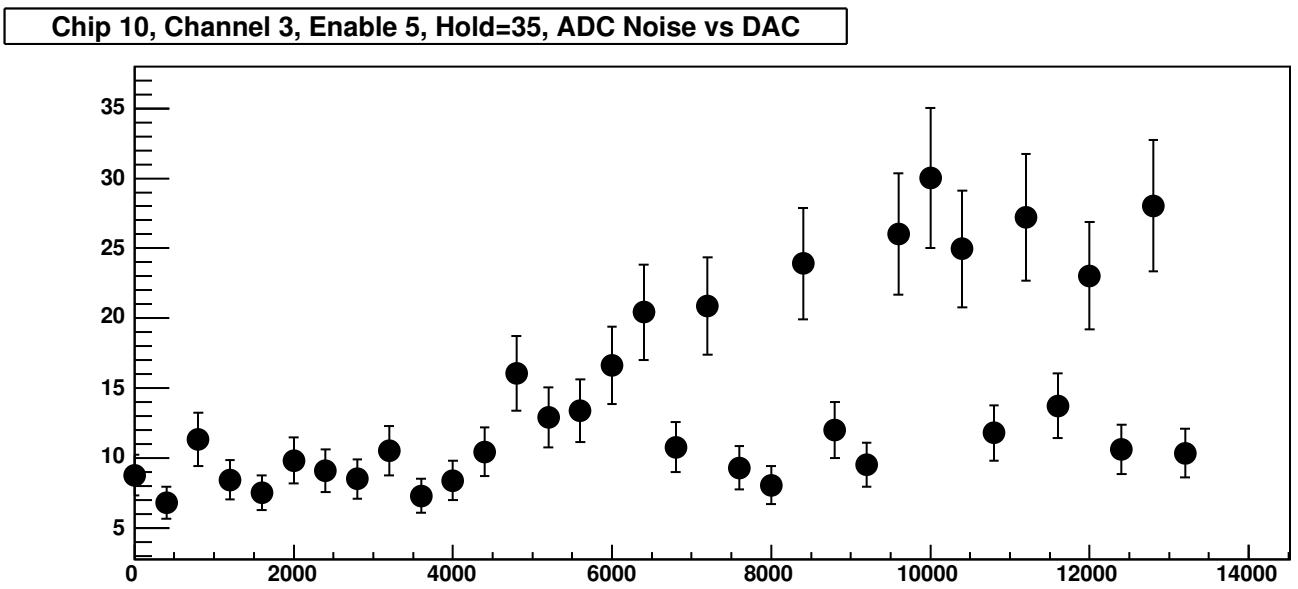
Chip 10, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC

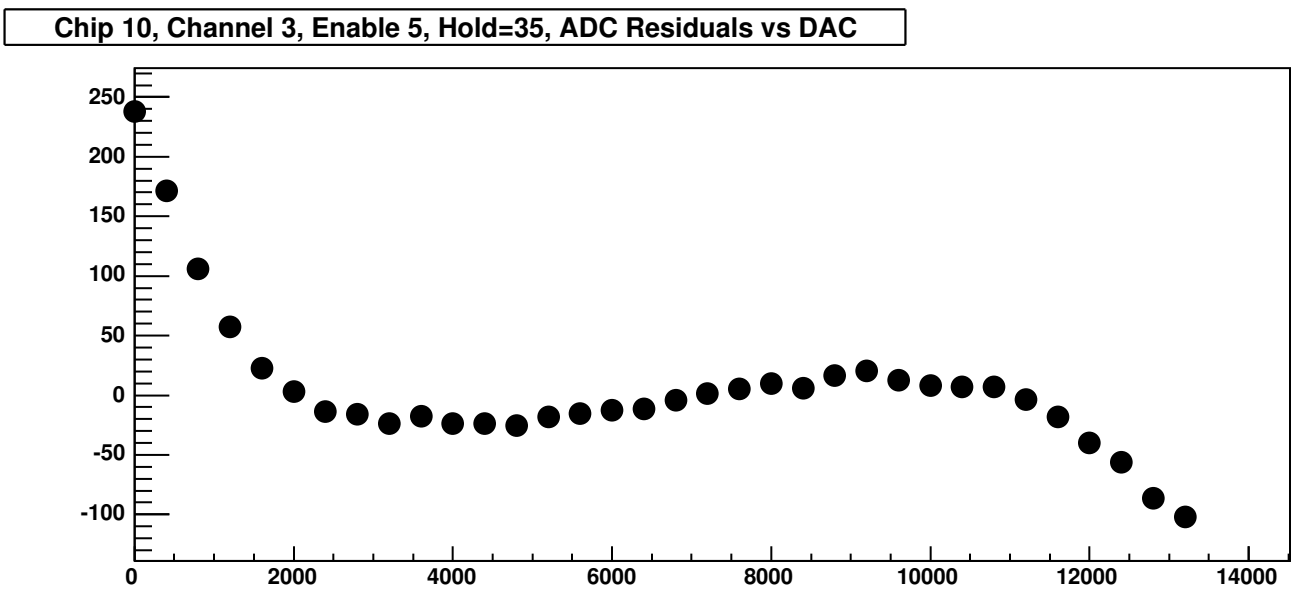


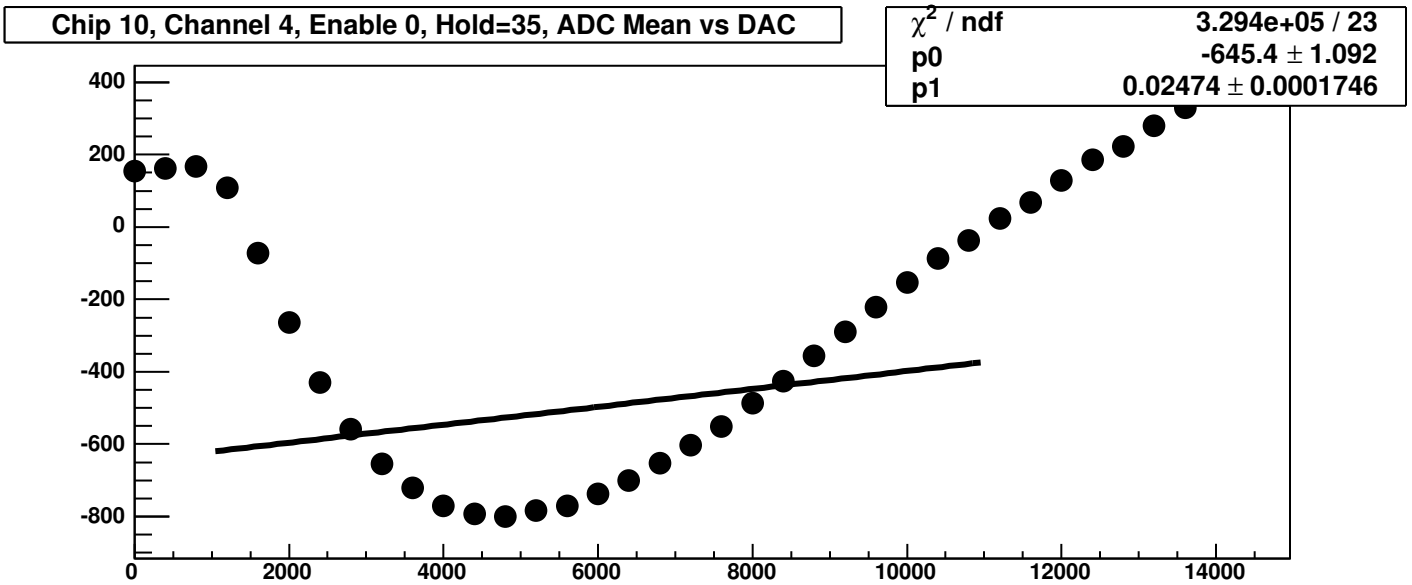
Chip 10, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC

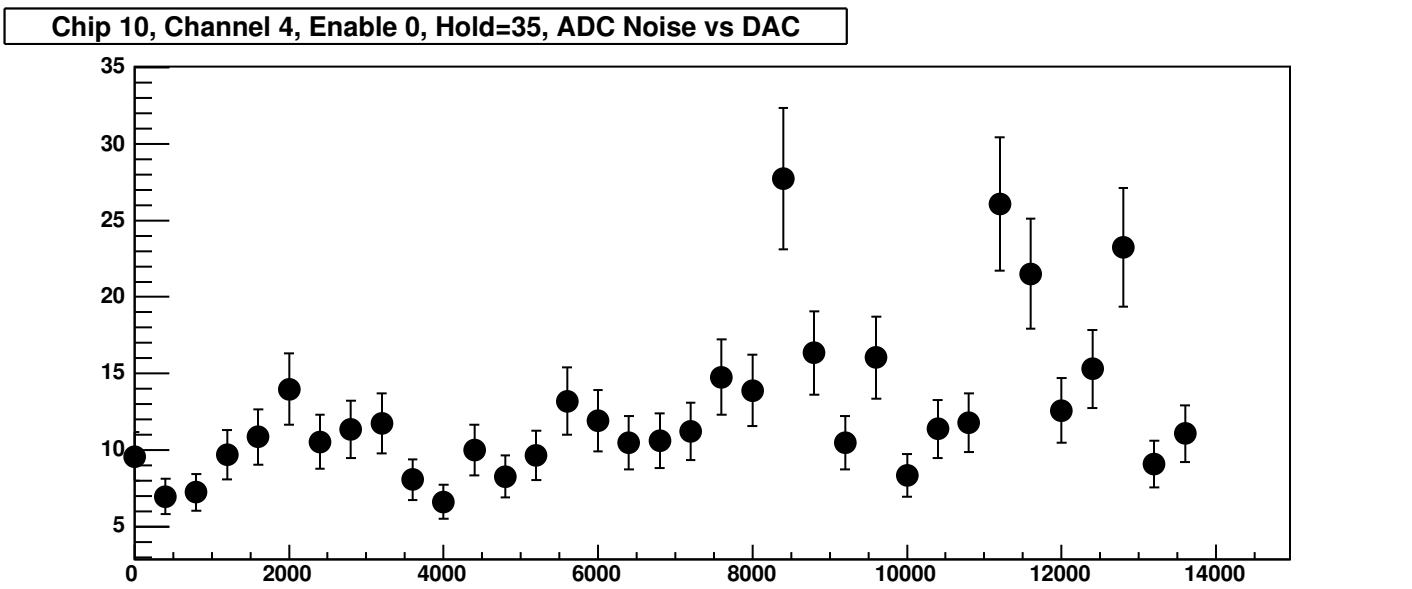


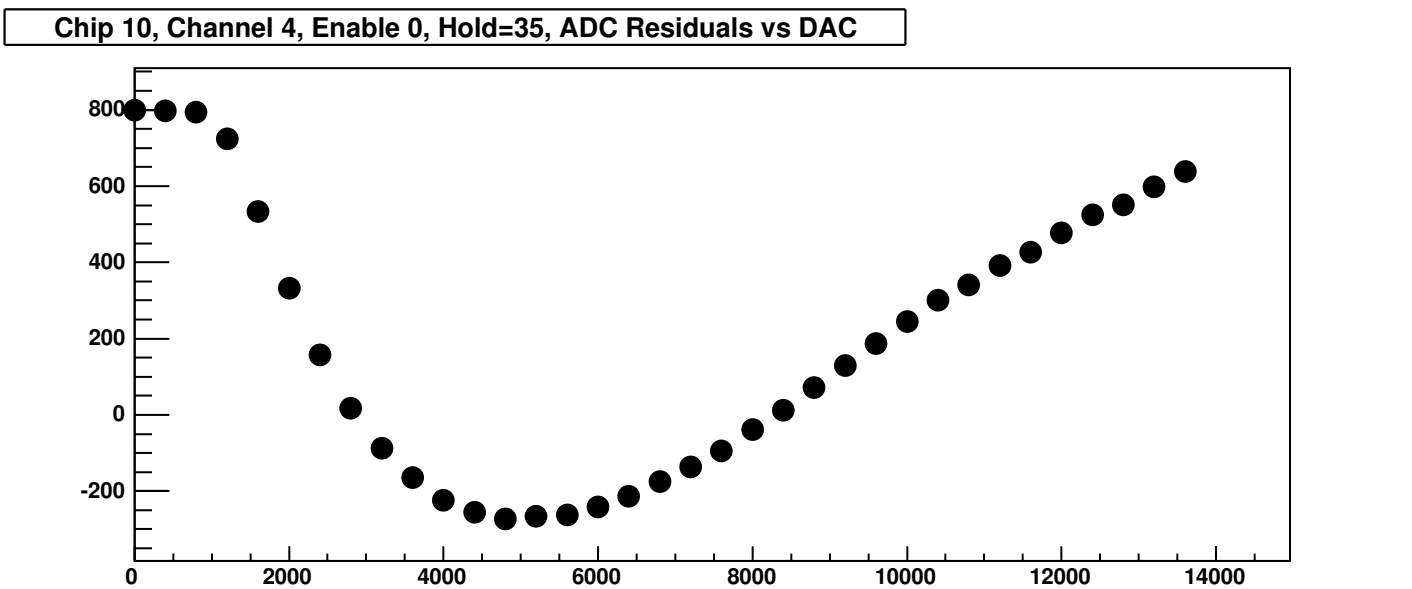

Chip 10, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1925 / 23
p0 -101.6 ± 1.007
p1 0.1745 ± 0.0001781
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

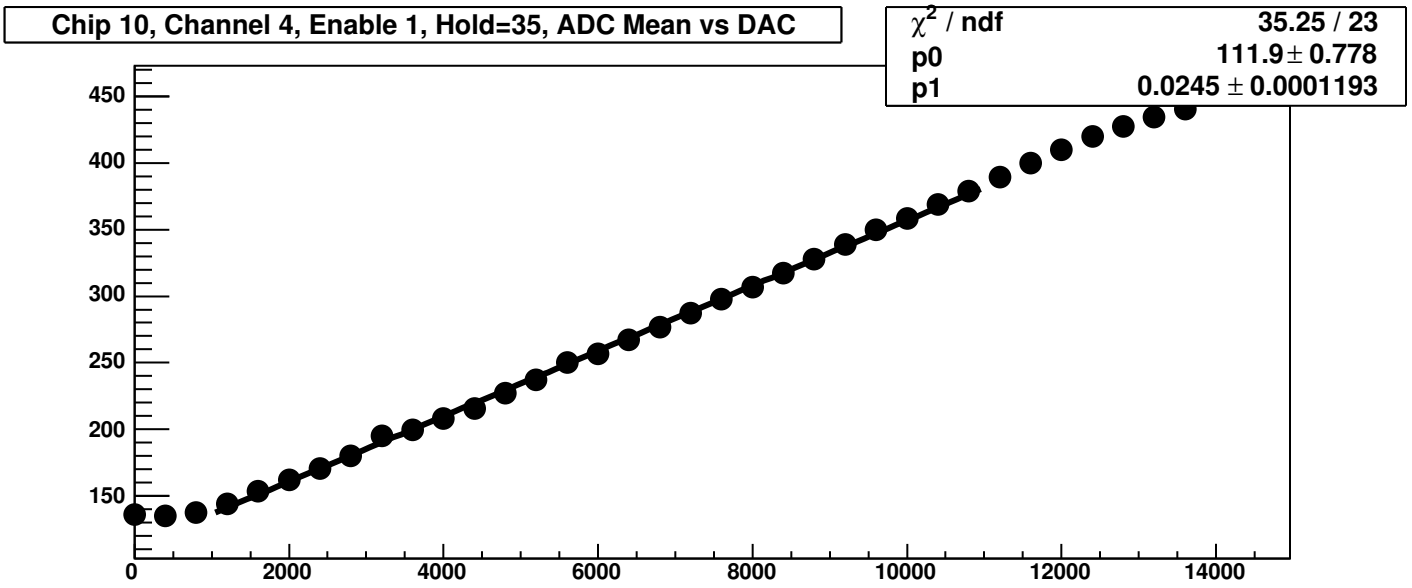

Chip 10, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

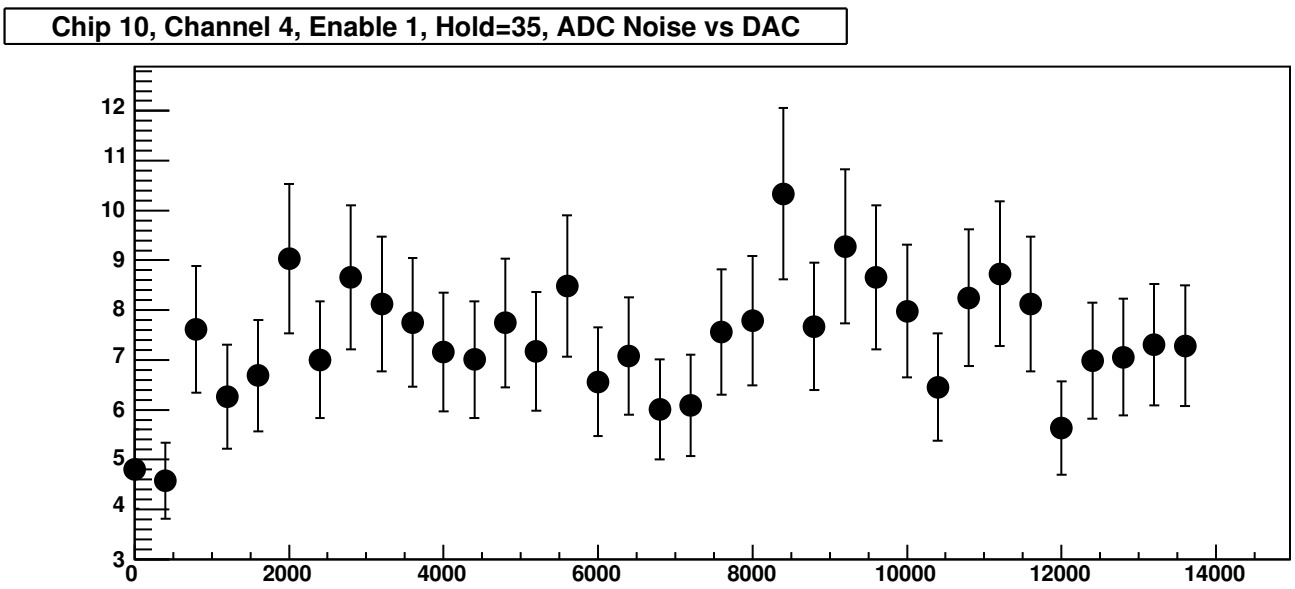

Chip 10, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

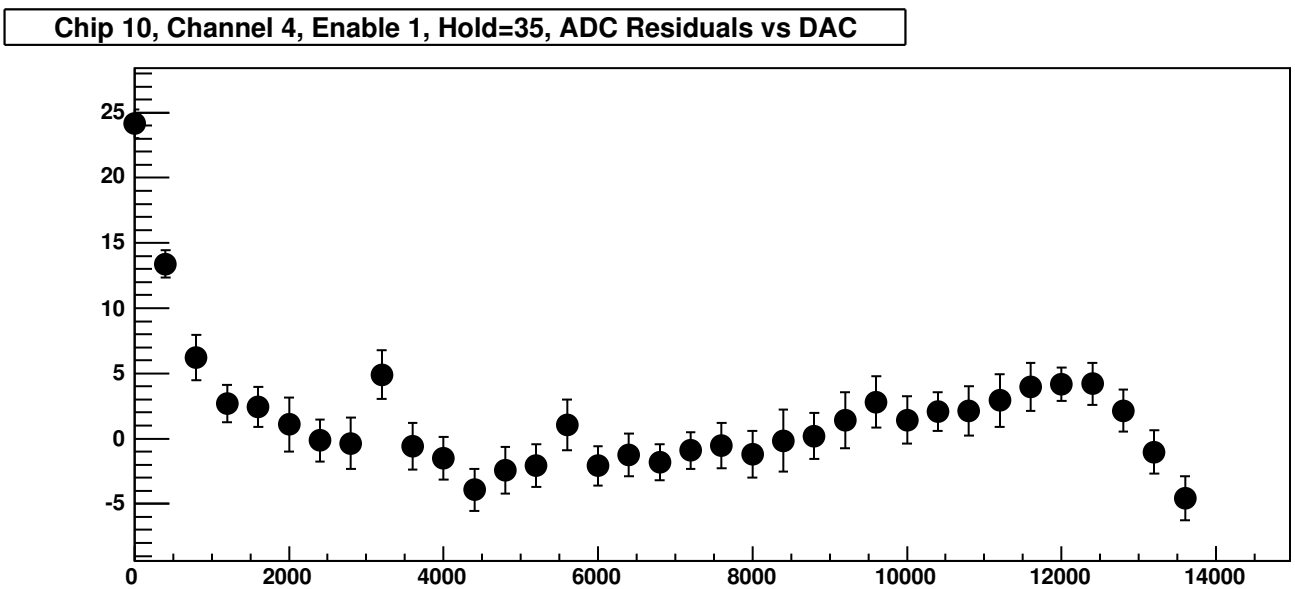

Chip 10, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.294e+05 / 23
p0 -645.4 ± 1.092
p1 0.02474 ± 0.0001746
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

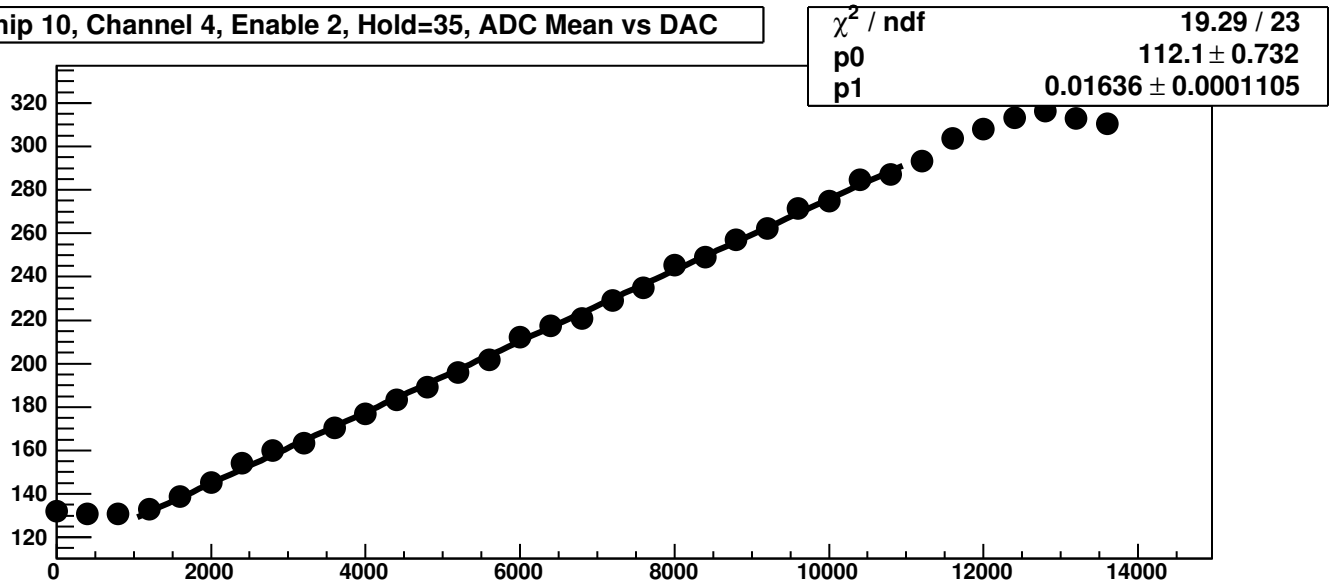

Chip 10, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 35.25 / 23
p0 111.9 ± 0.778
p1 0.0245 ± 0.0001193
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

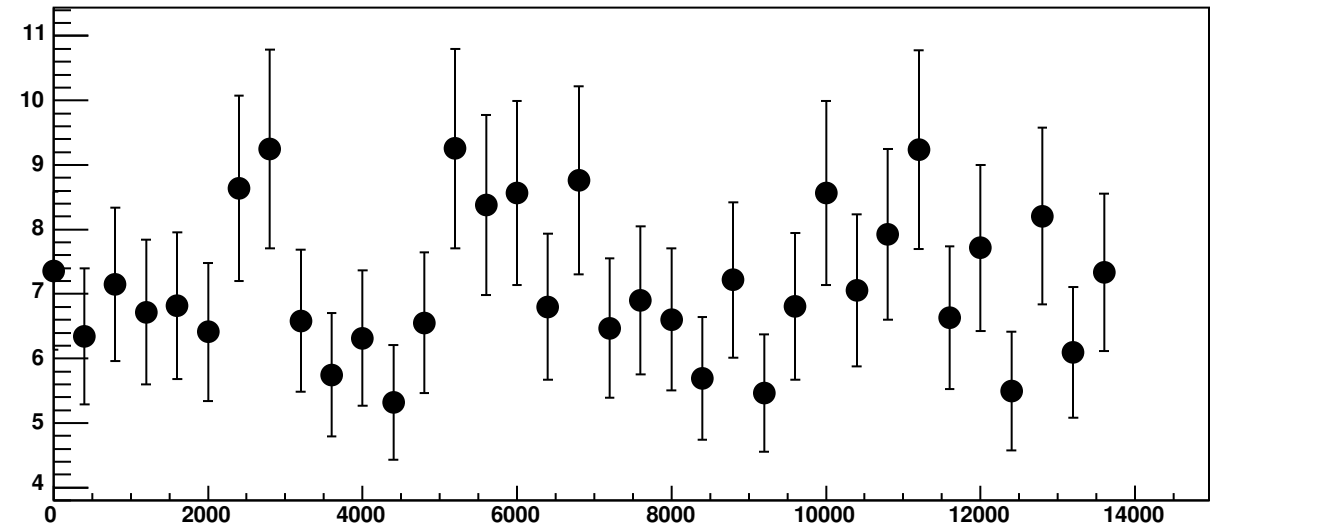

Chip 10, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

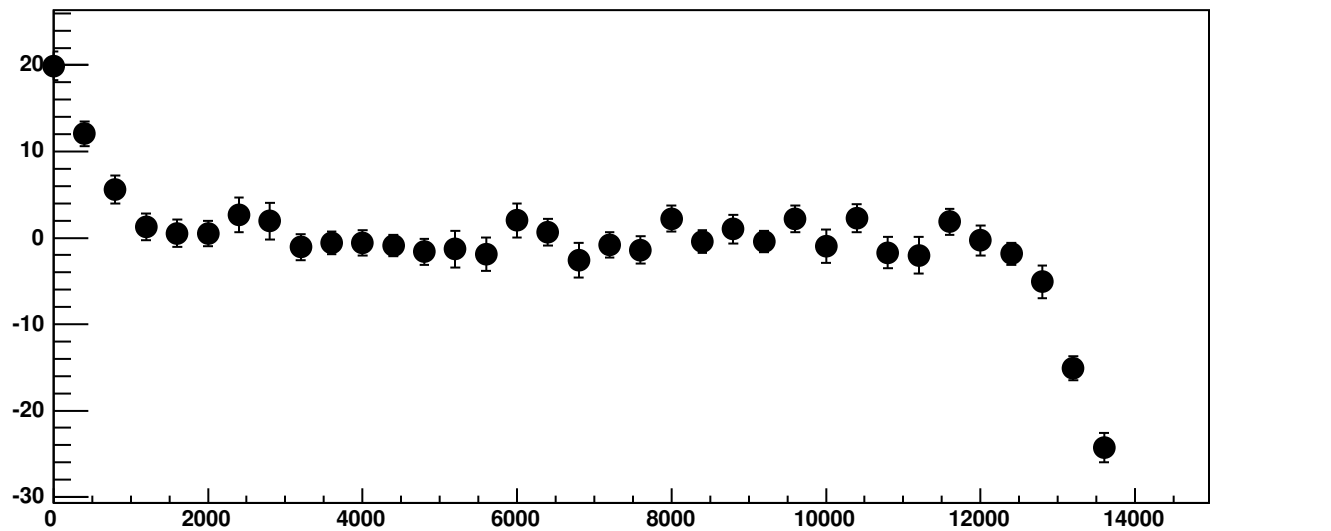
Chip 10, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

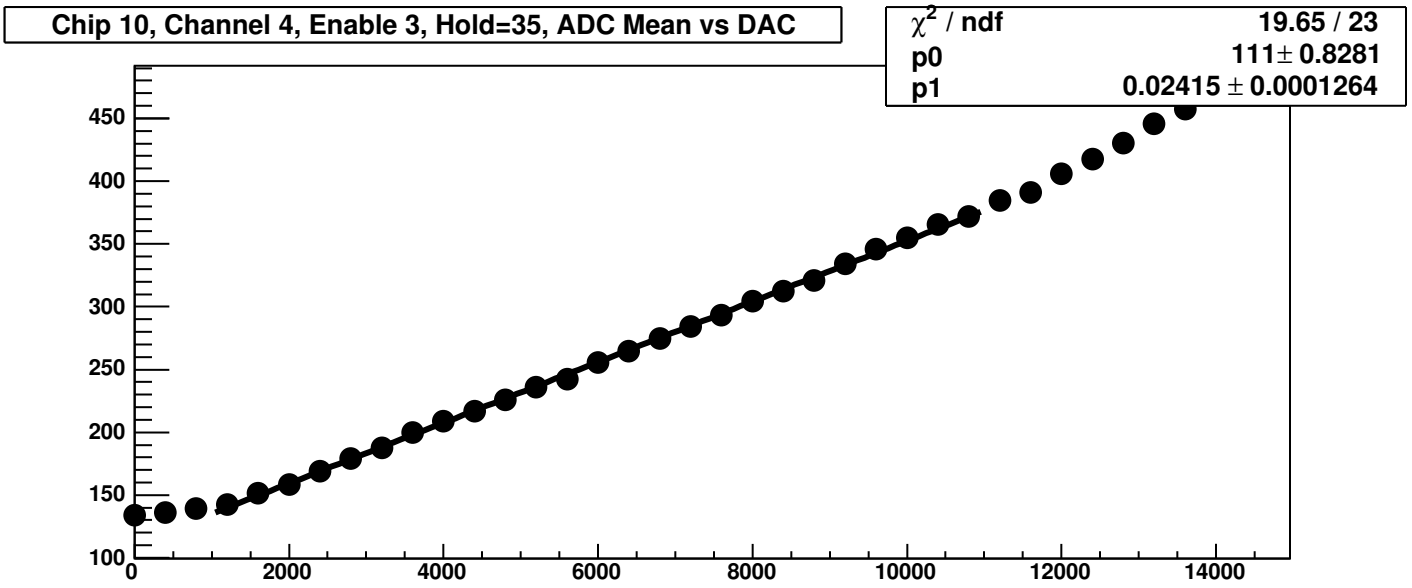


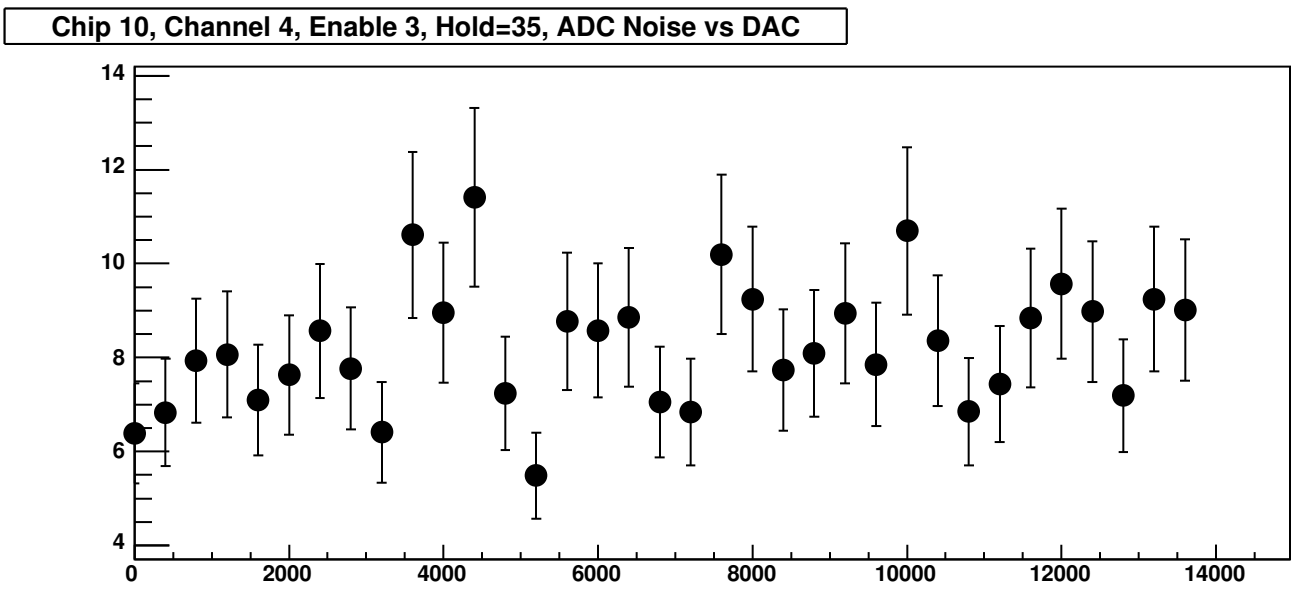
Chip 10, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC

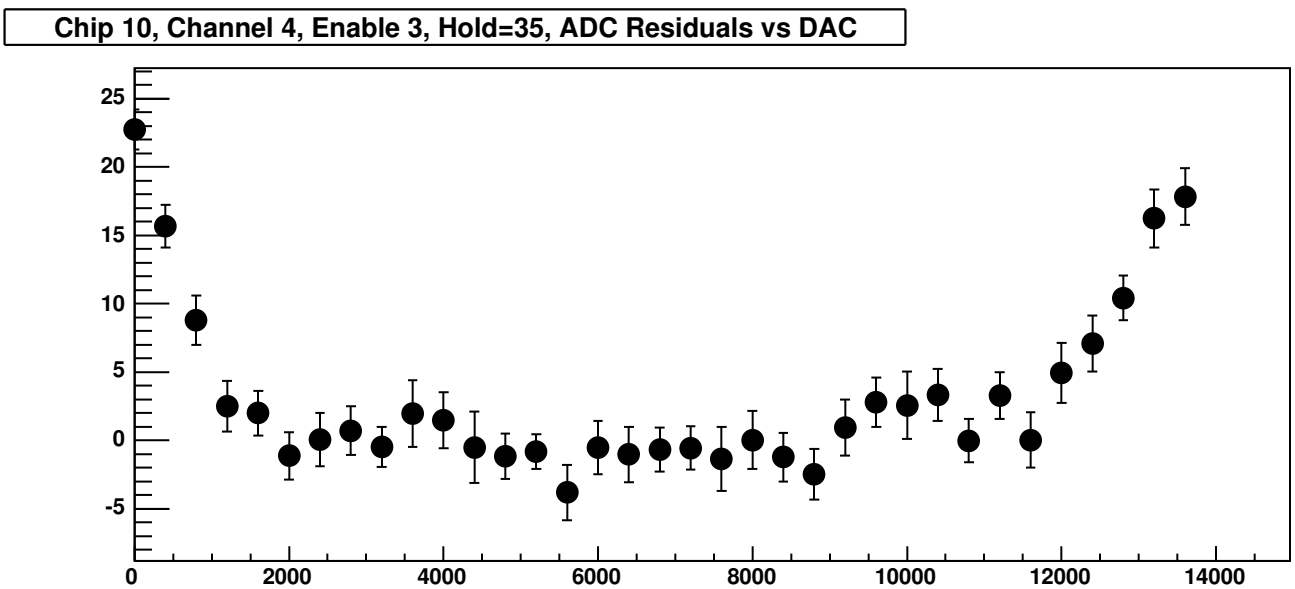


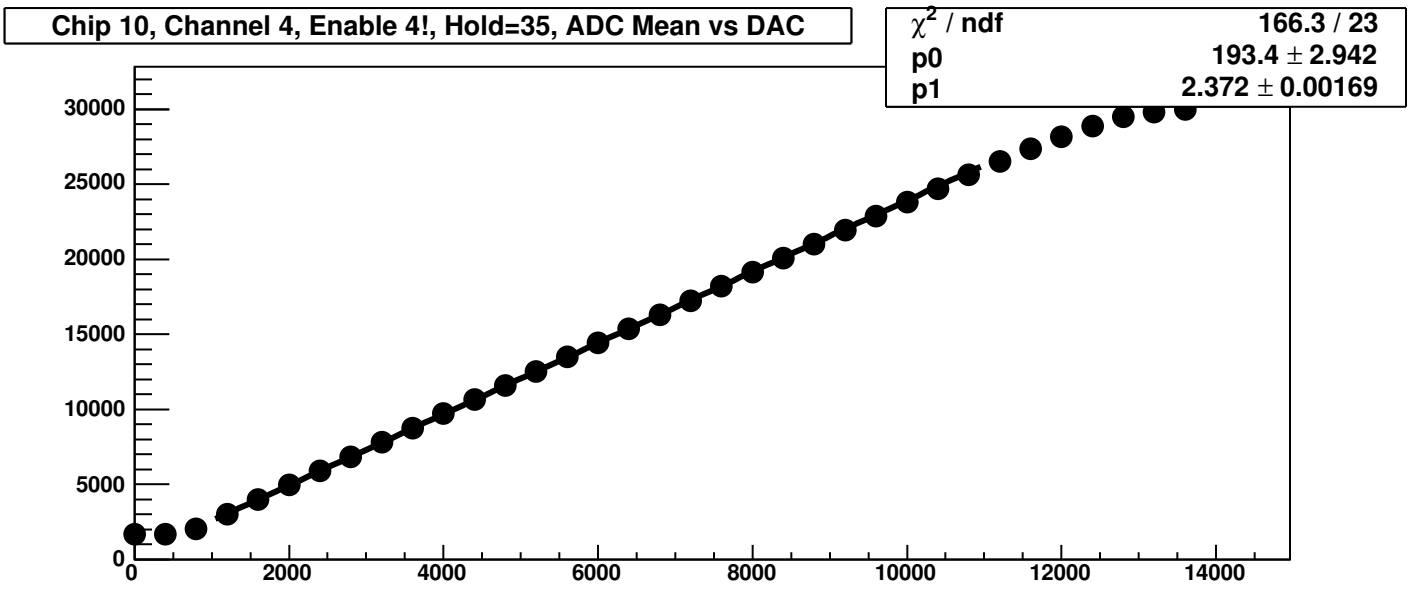
Chip 10, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

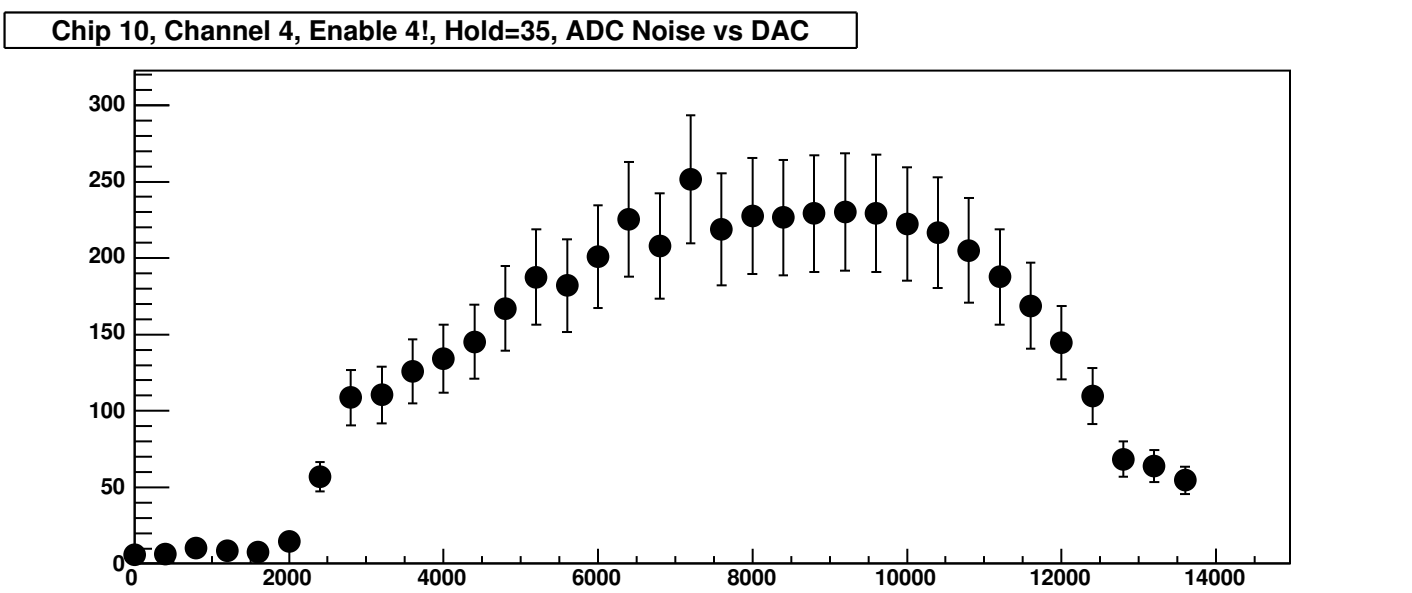


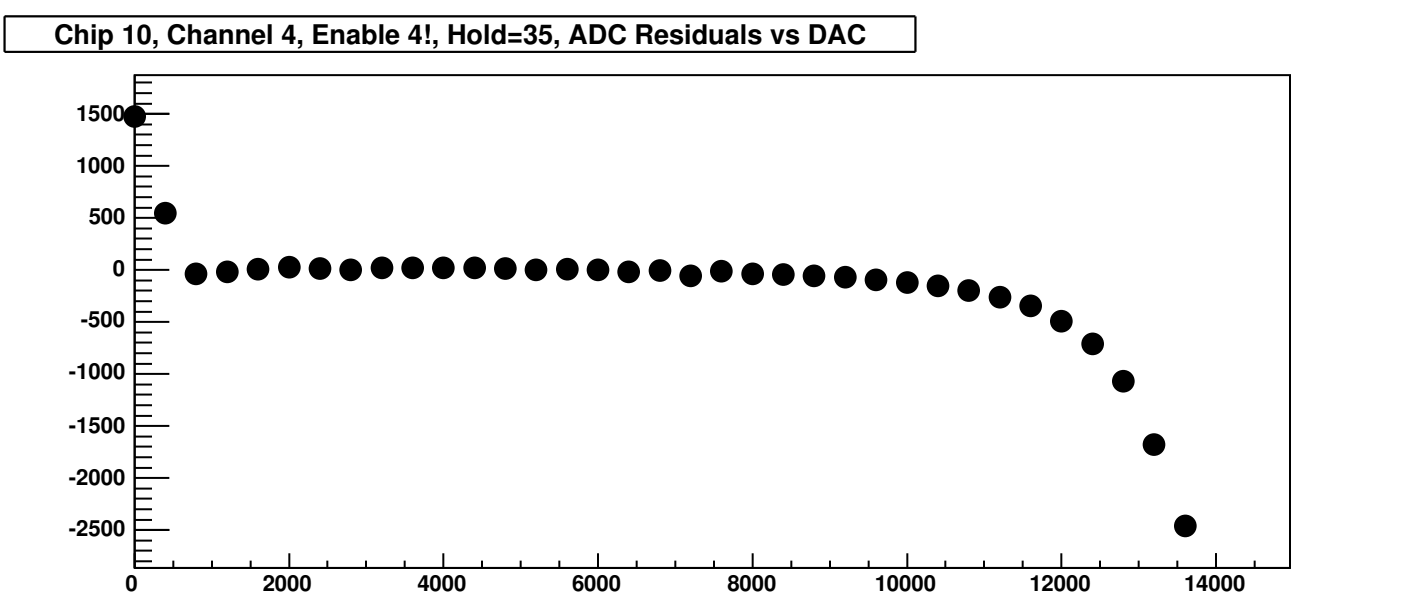

Chip 10, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 19.65 / 23
p0 111 ± 0.8281
p1 0.02415 ± 0.0001264
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

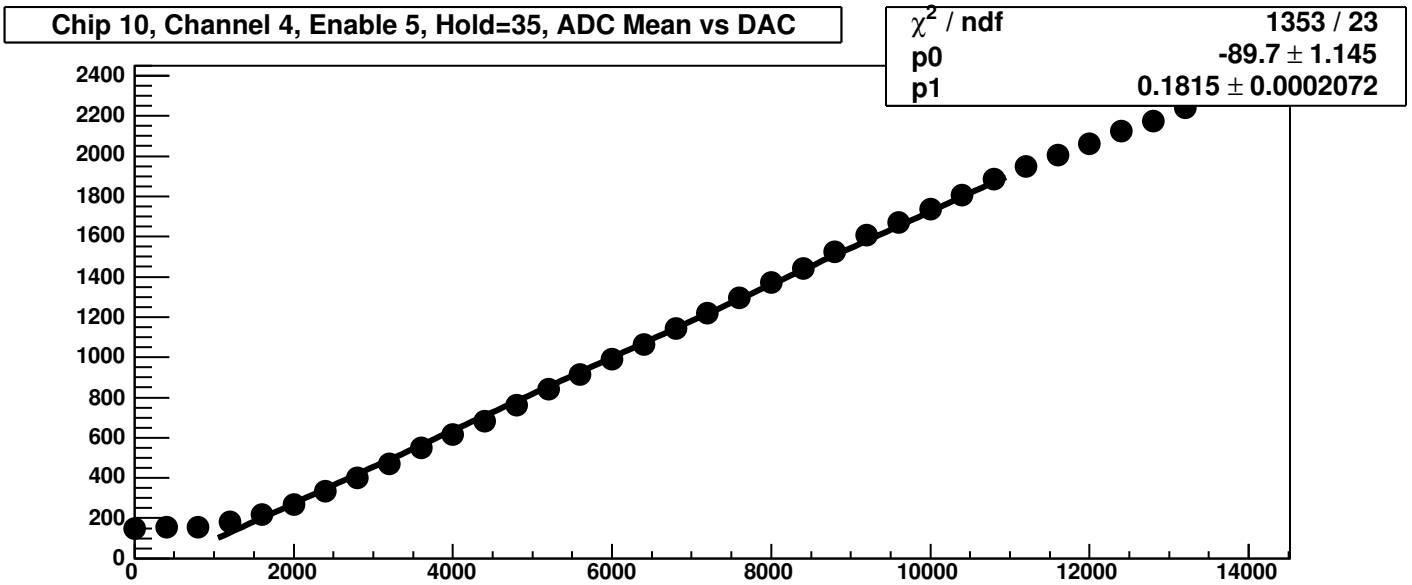

Chip 10, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

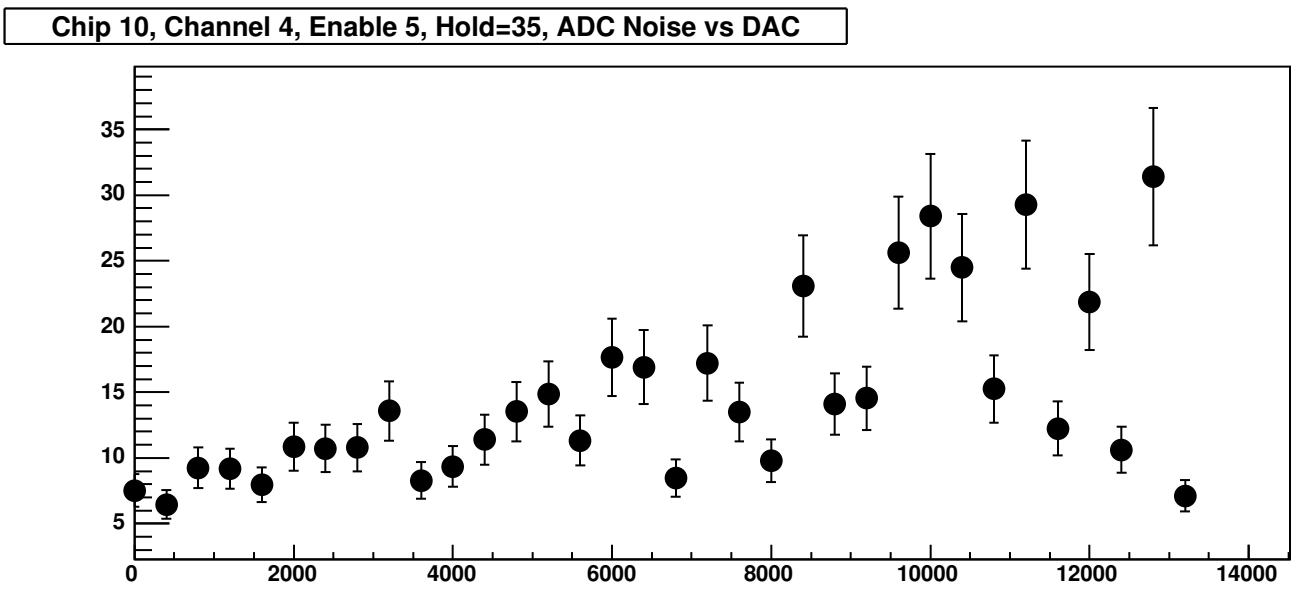

Chip 10, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

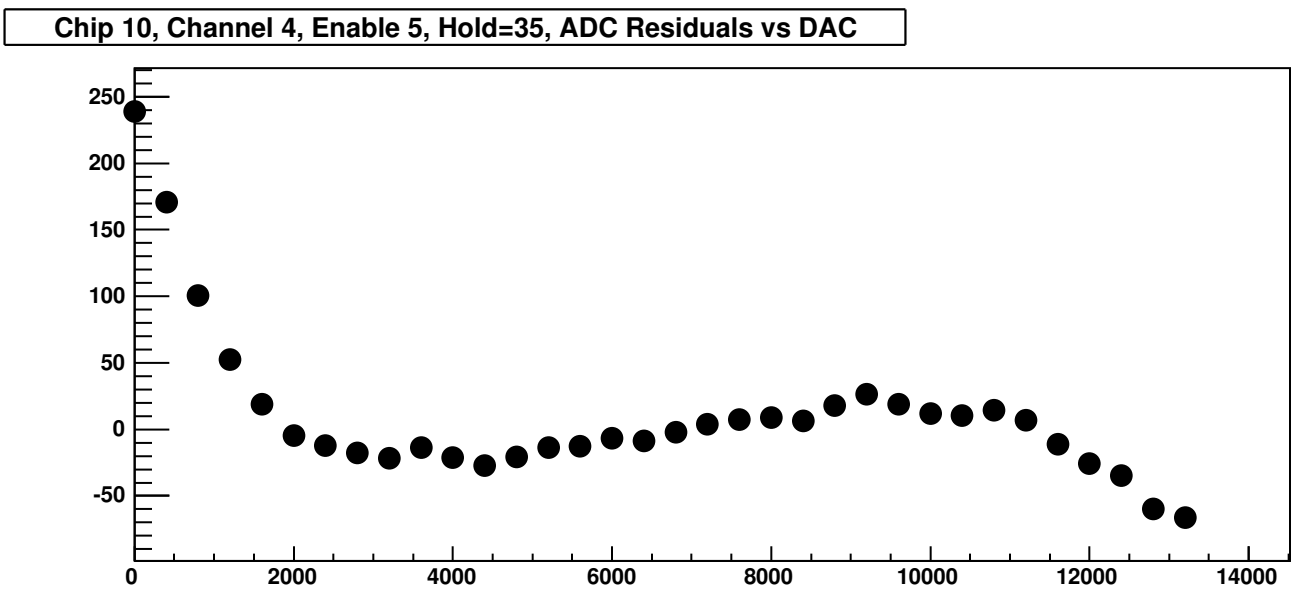

Chip 10, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 166.3 / 23
p0 193.4 ± 2.942
p1 2.372 ± 0.00169
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

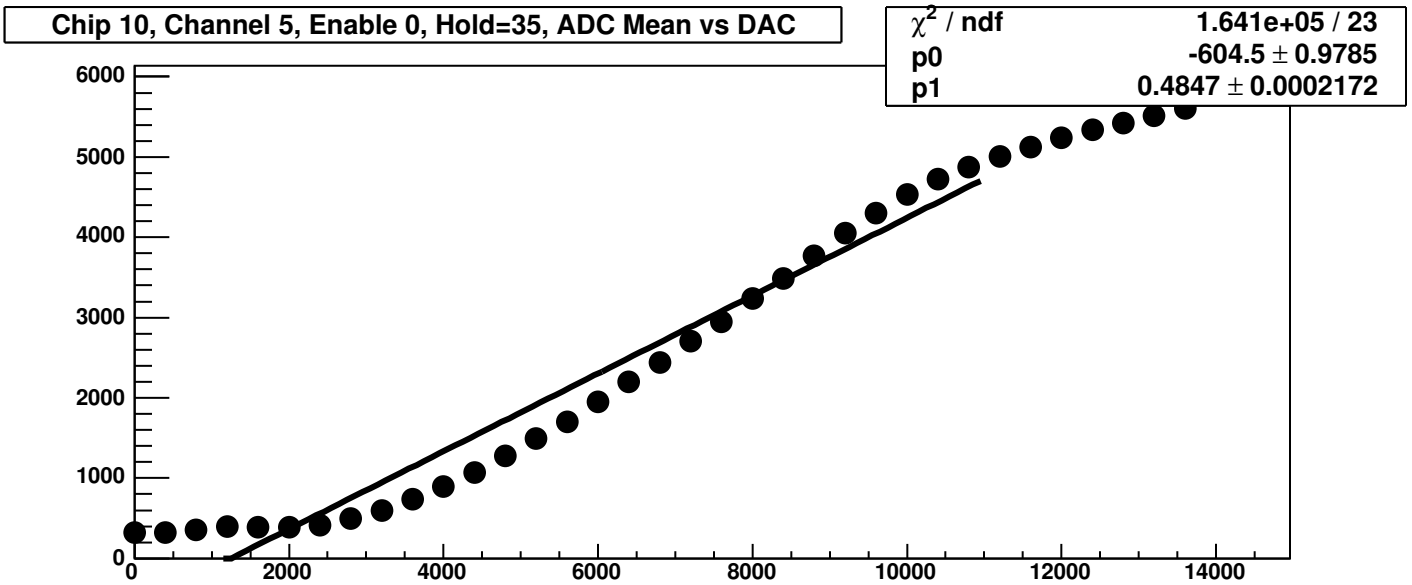

Chip 10, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

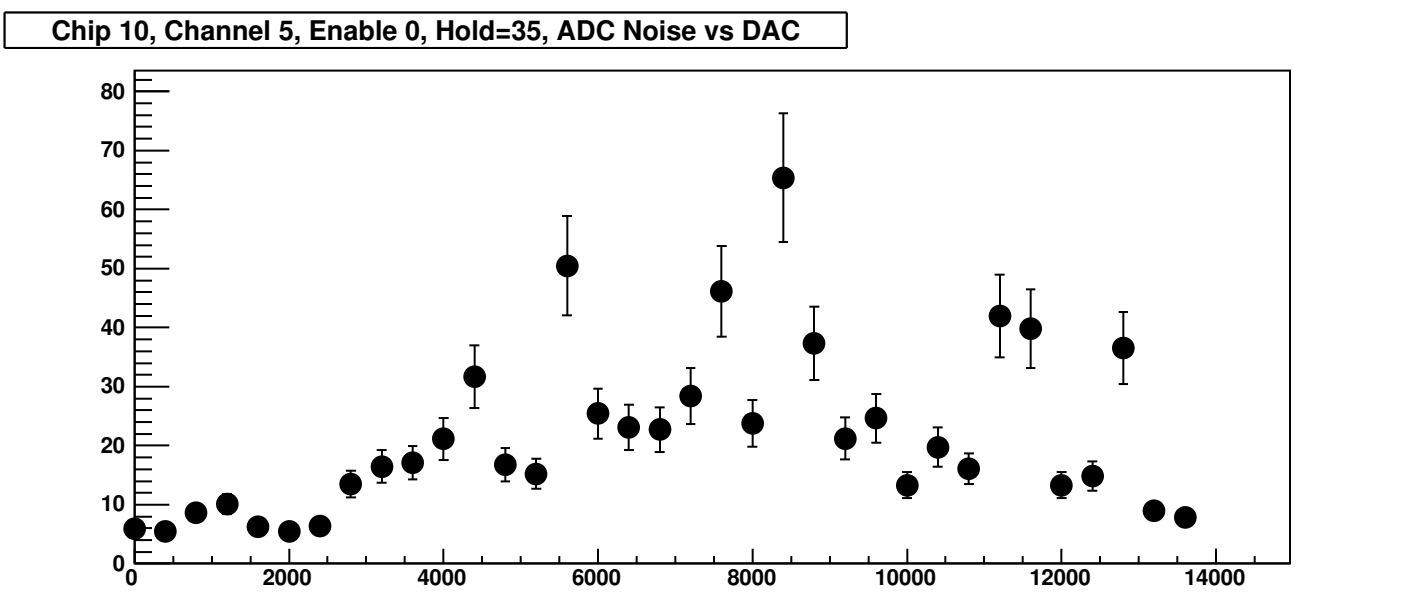

Chip 10, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

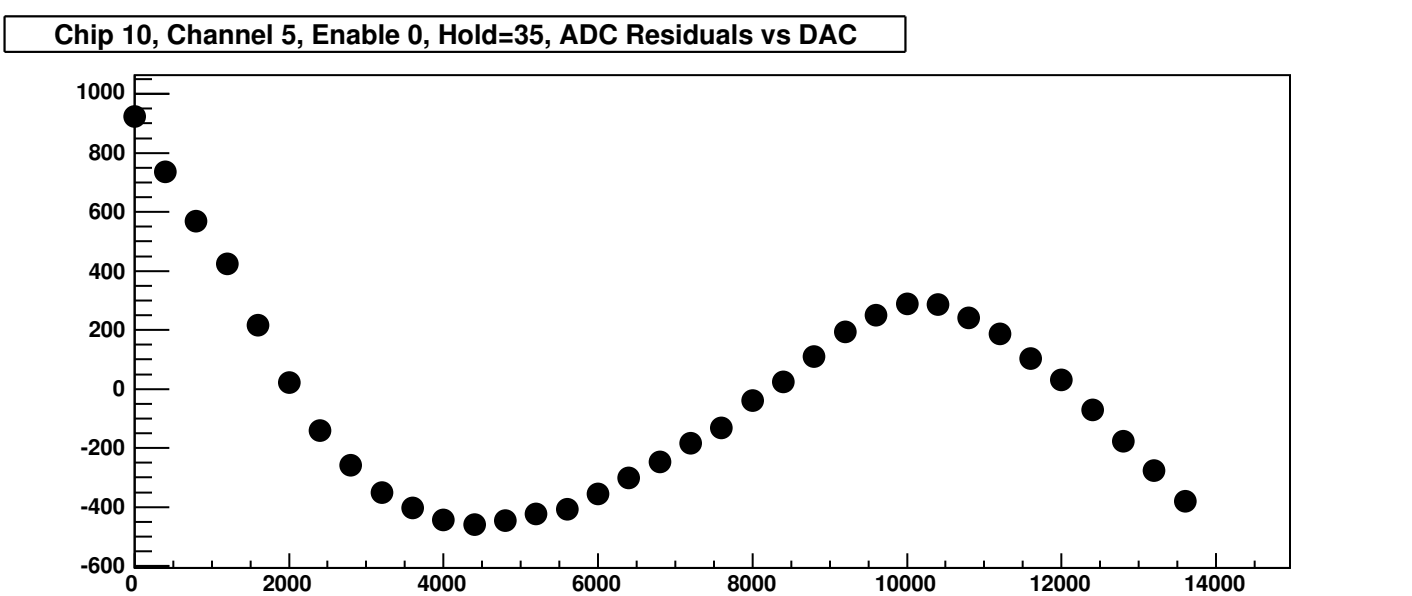

Chip 10, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1353 / 23
p0 -89.7 ± 1.145
p1 0.1815 ± 0.0002072
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

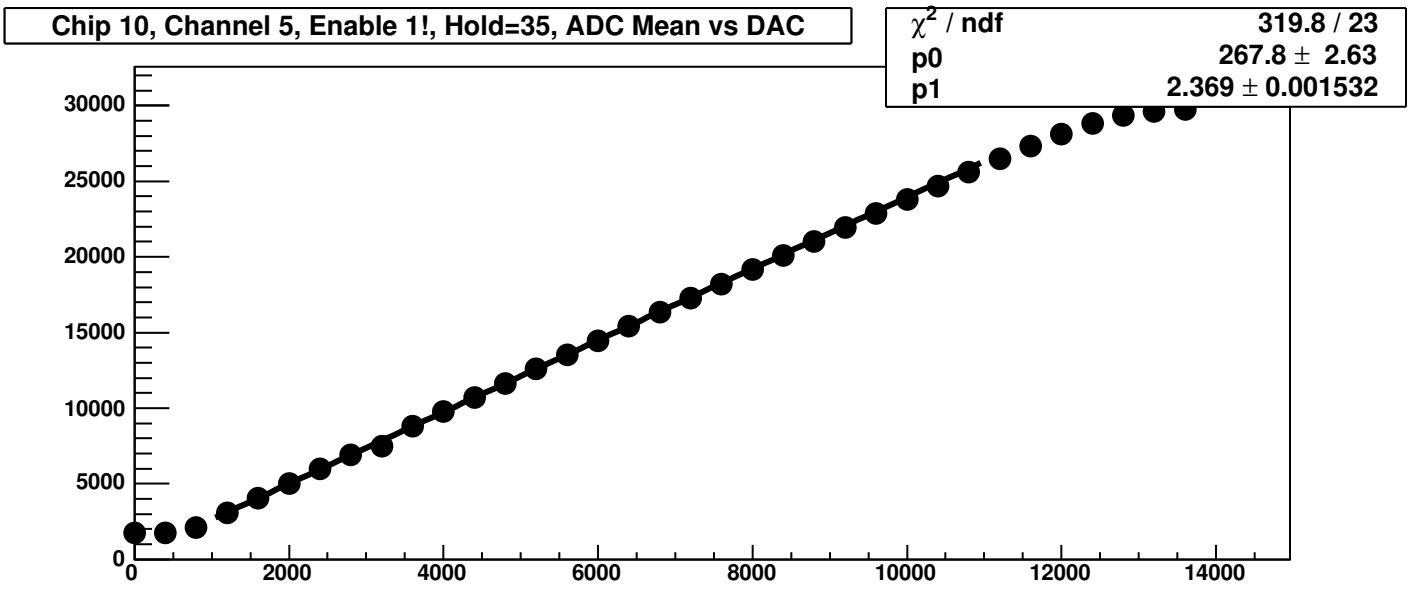

Chip 10, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

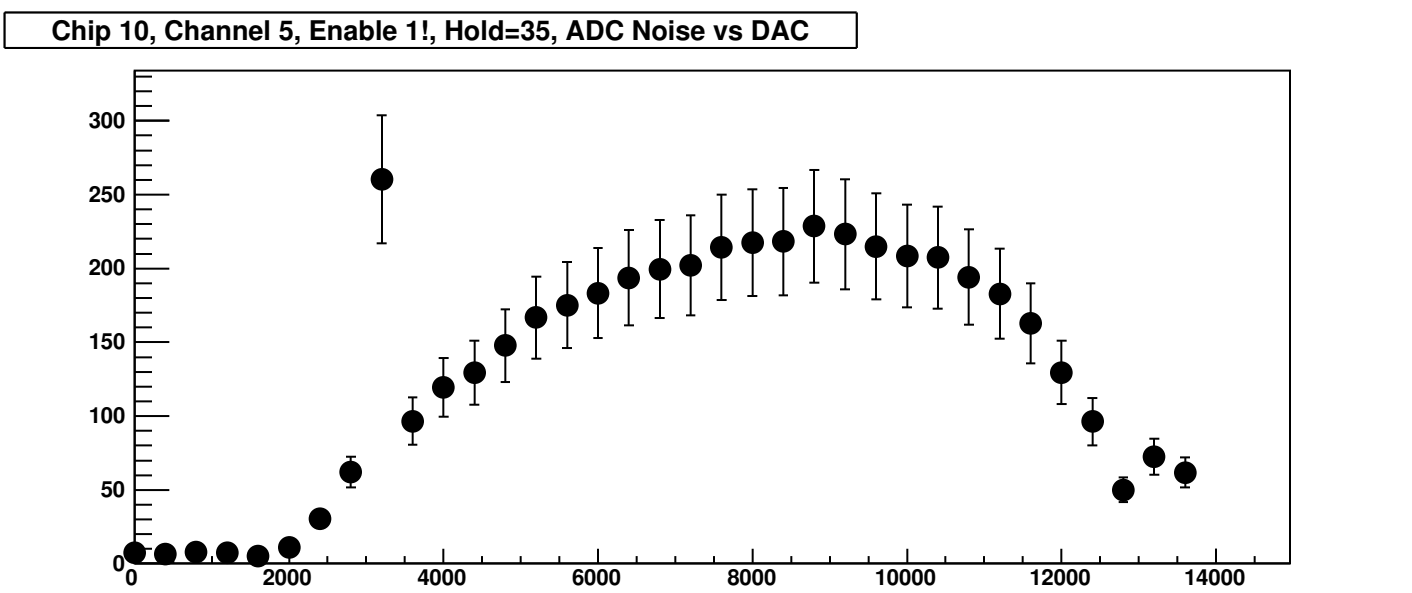

Chip 10, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

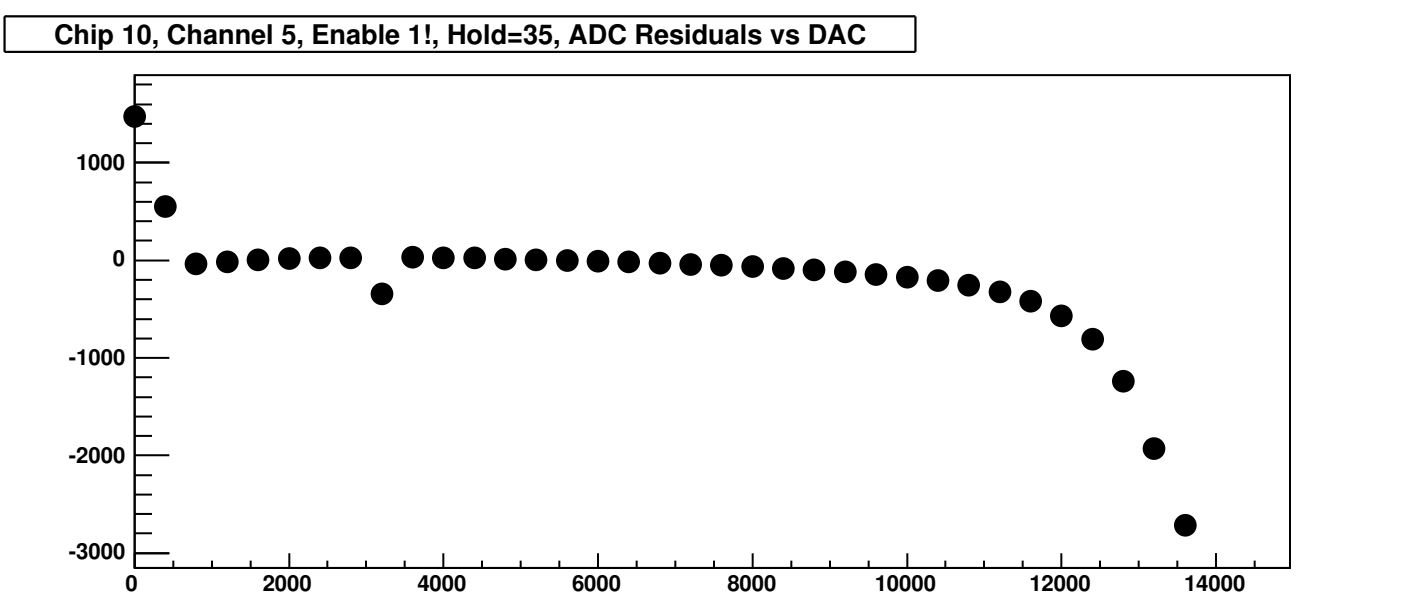

Chip 10, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.641e+05 / 23
p0 -604.5 ± 0.9785
p1 0.4847 ± 0.0002172
6000
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

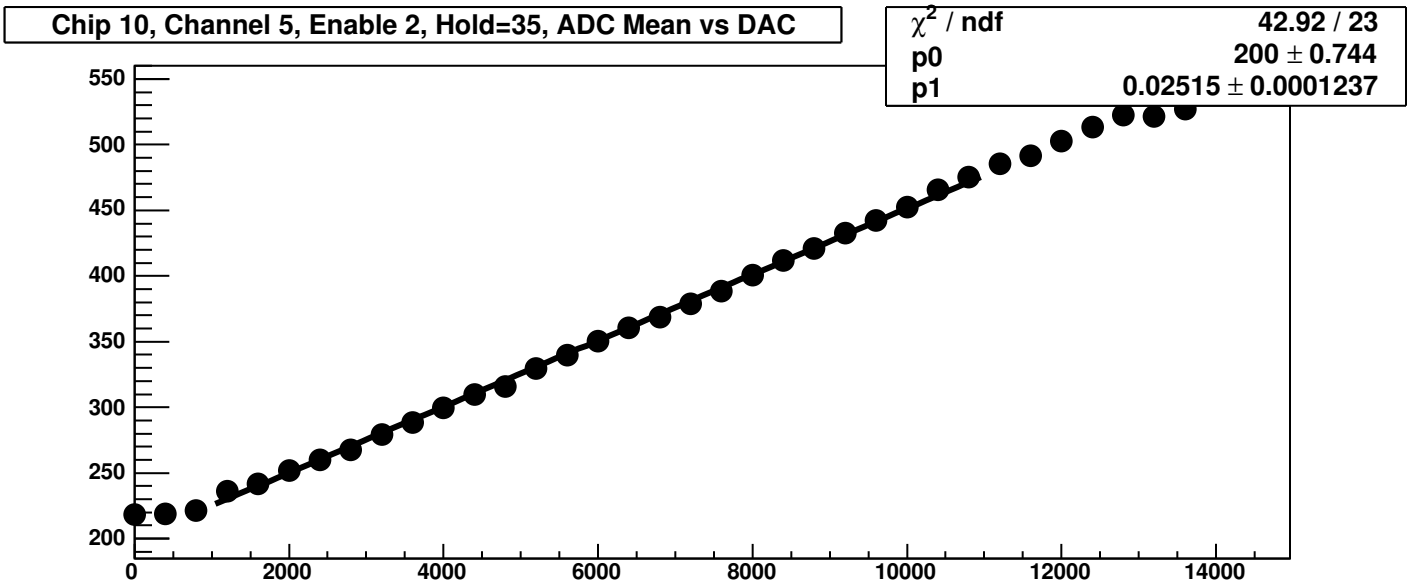

Chip 10, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

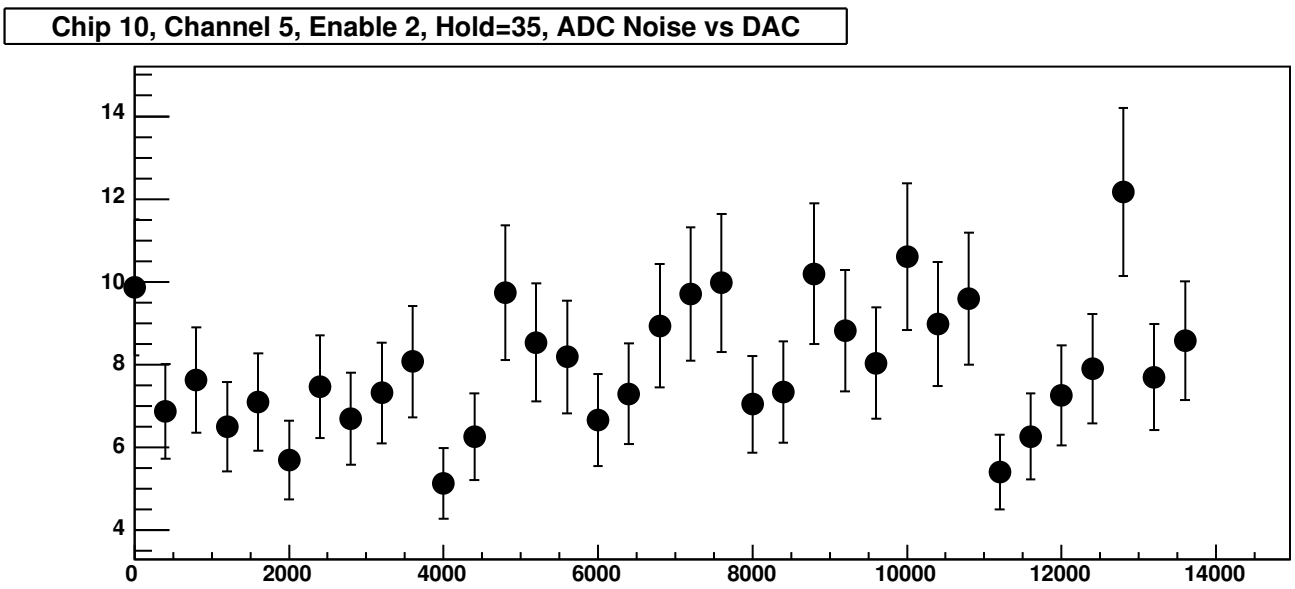

Chip 10, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

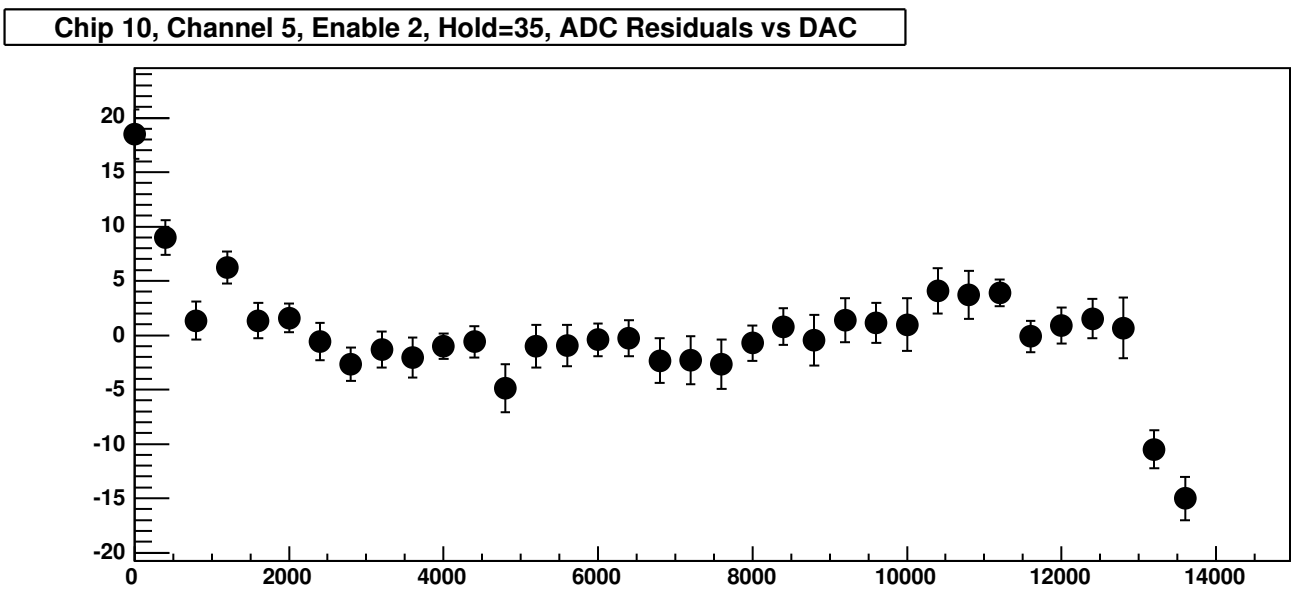

Chip 10, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 319.8 / 23
p0 267.8 ± 2.63
p1 2.369 ± 0.001532
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

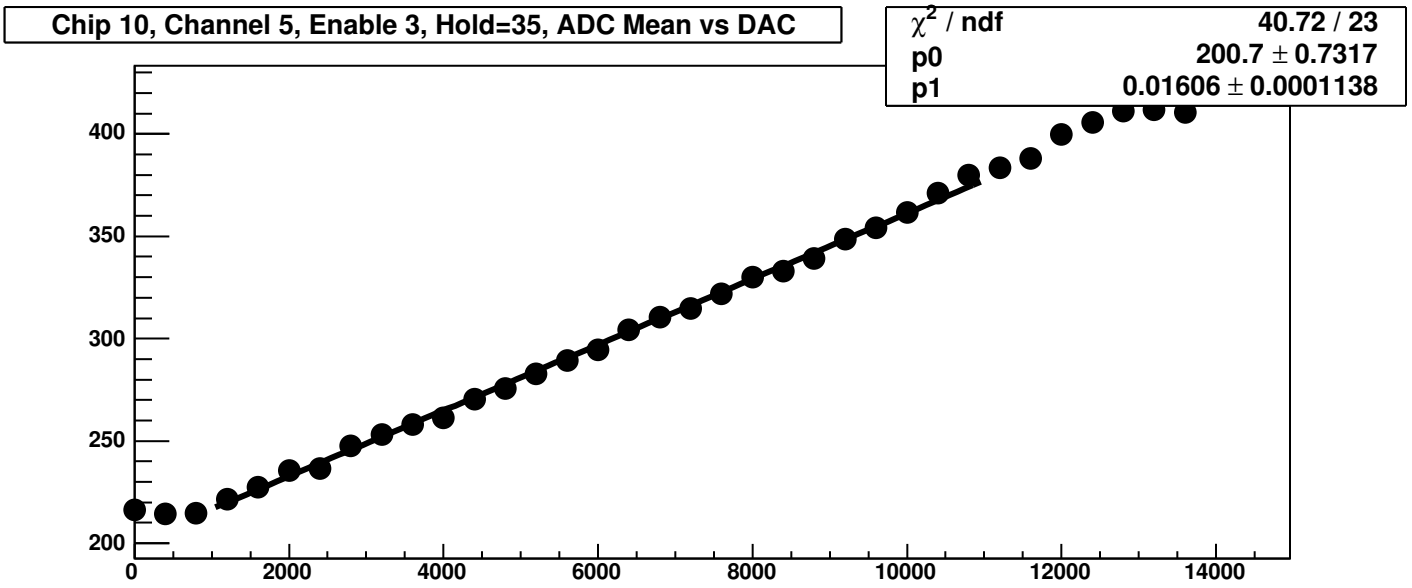

Chip 10, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

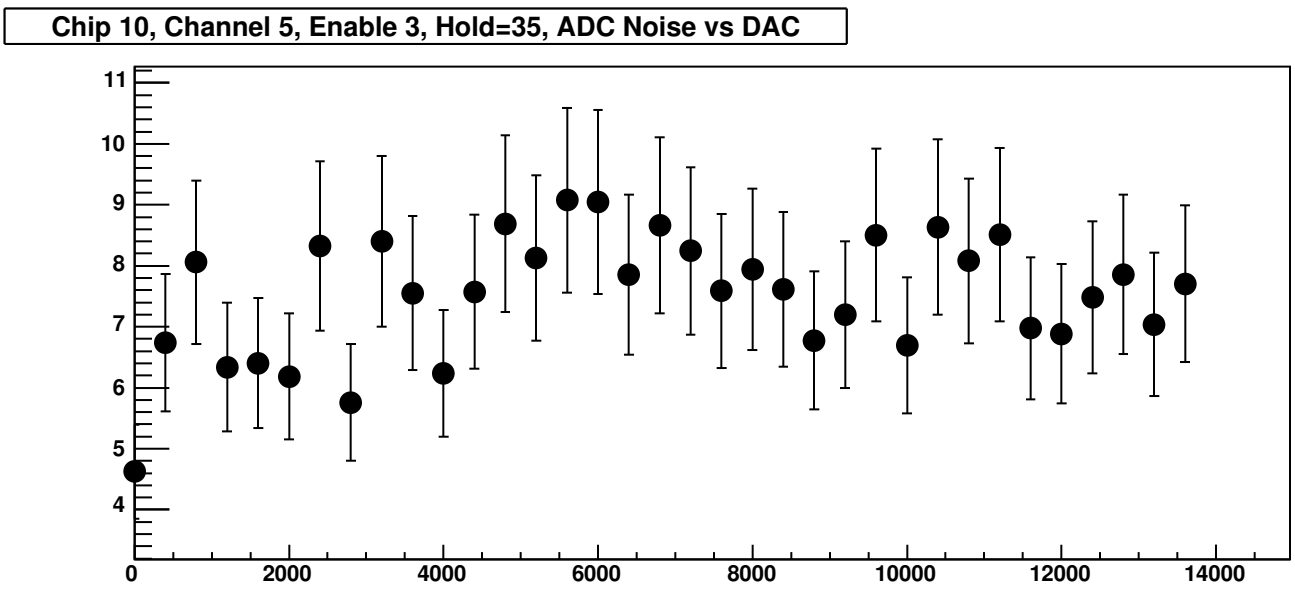

Chip 10, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

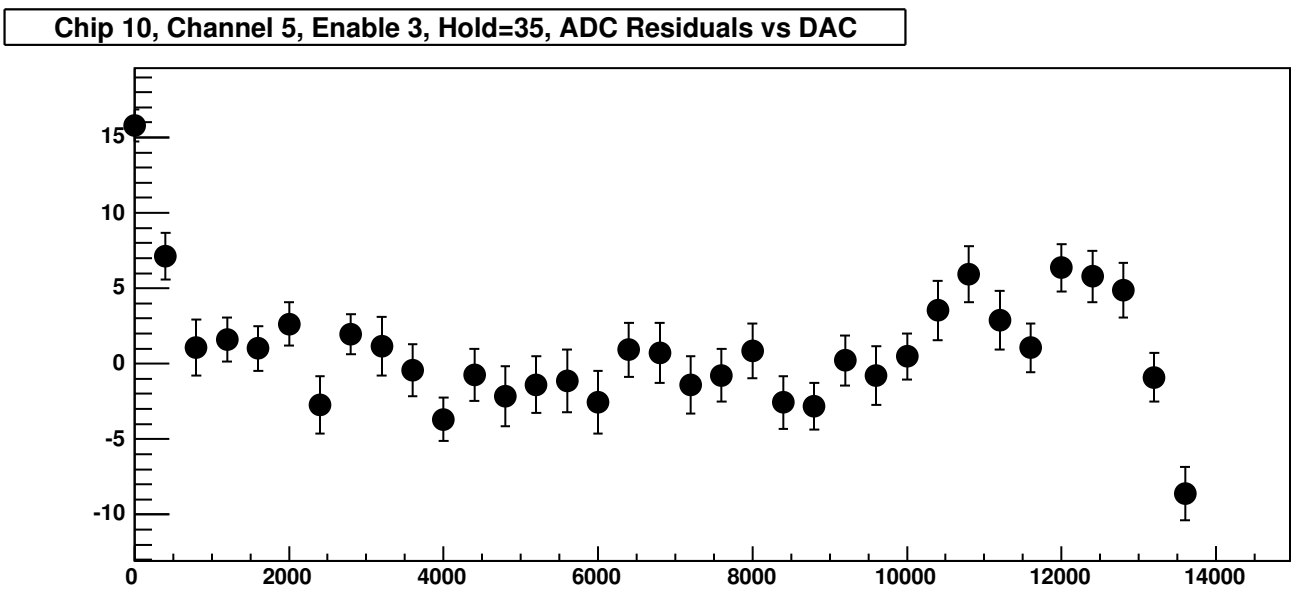

Chip 10, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 42.92 / 23
p0 200 ± 0.744
p1 0.02515 ± 0.0001237
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

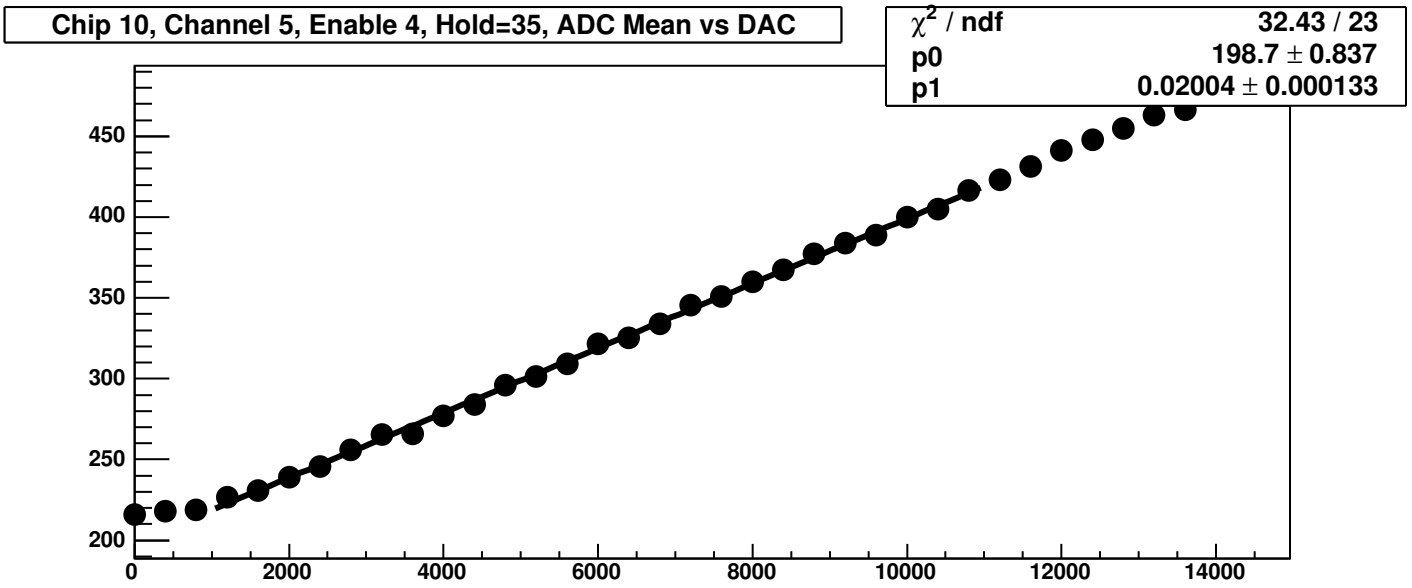

Chip 10, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

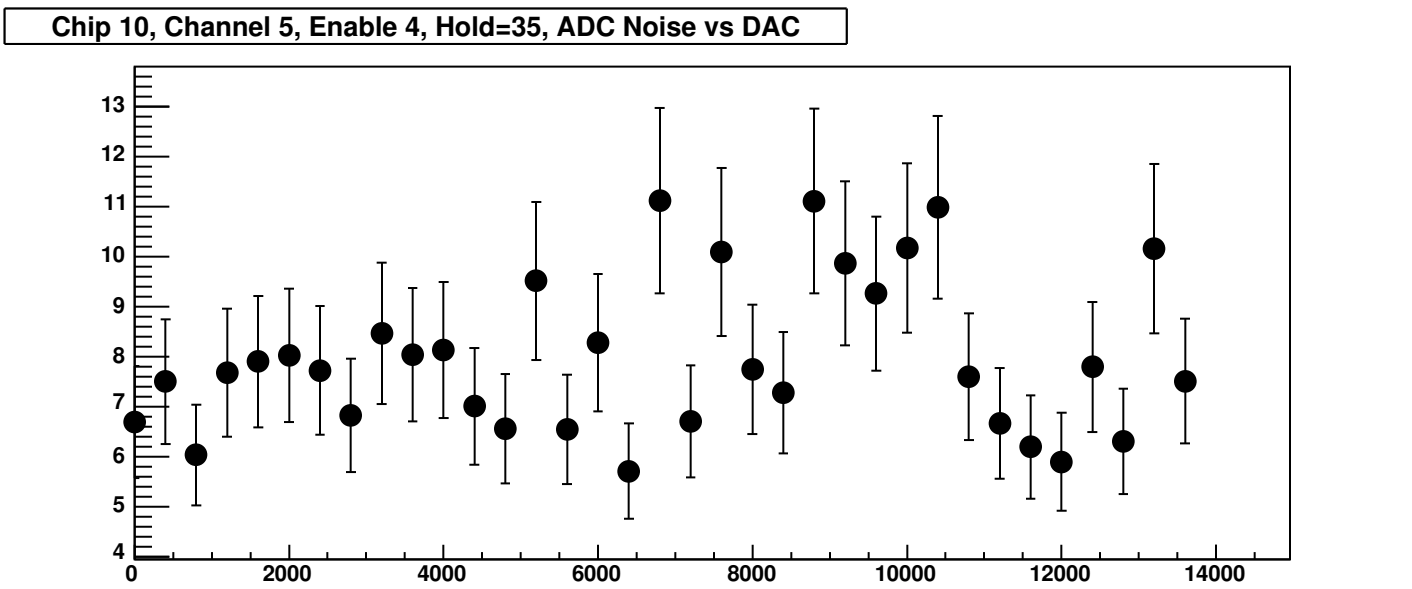

Chip 10, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

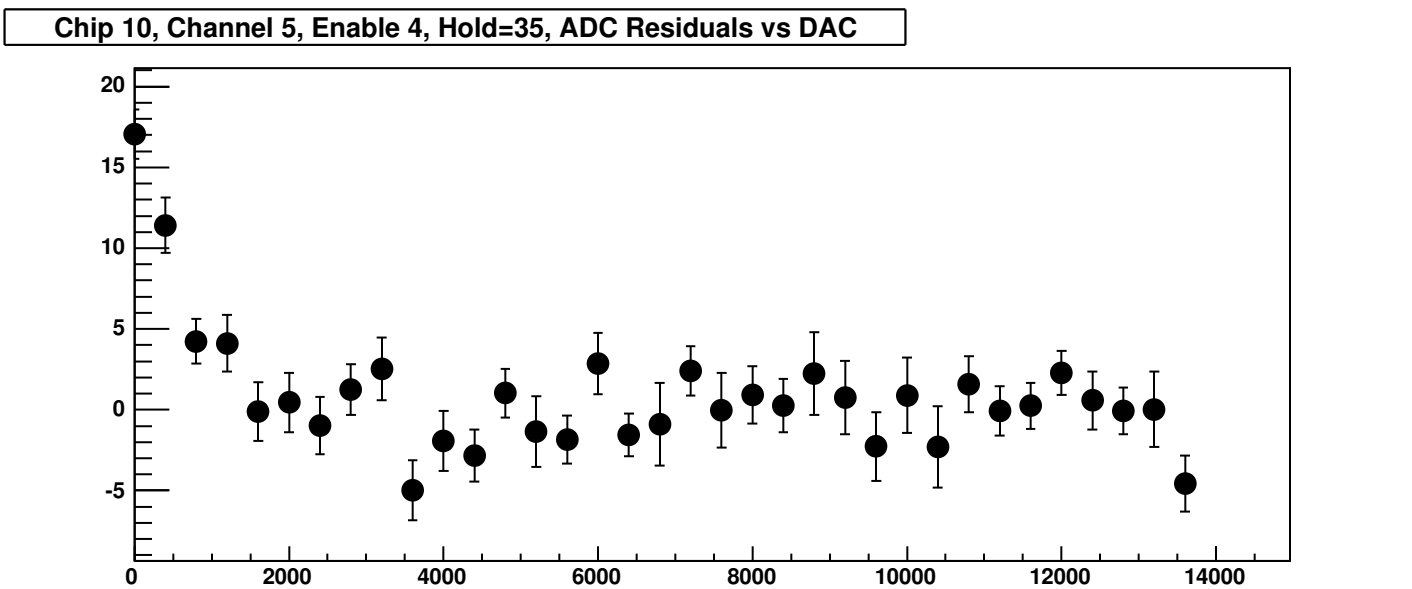

Chip 10, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 40.72 / 23
p0 200.7 ± 0.7317
p1 0.01606 ± 0.0001138
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

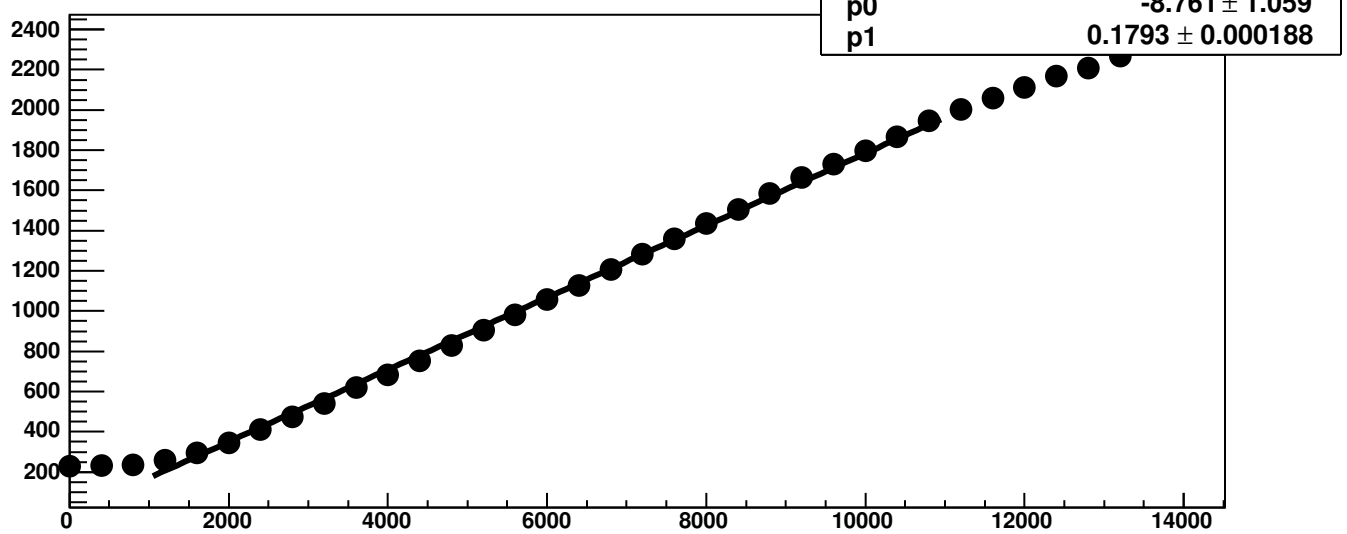

Chip 10, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 32.43 / 23
p0 198.7 ± 0.837
p1 0.02004 ± 0.000133
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

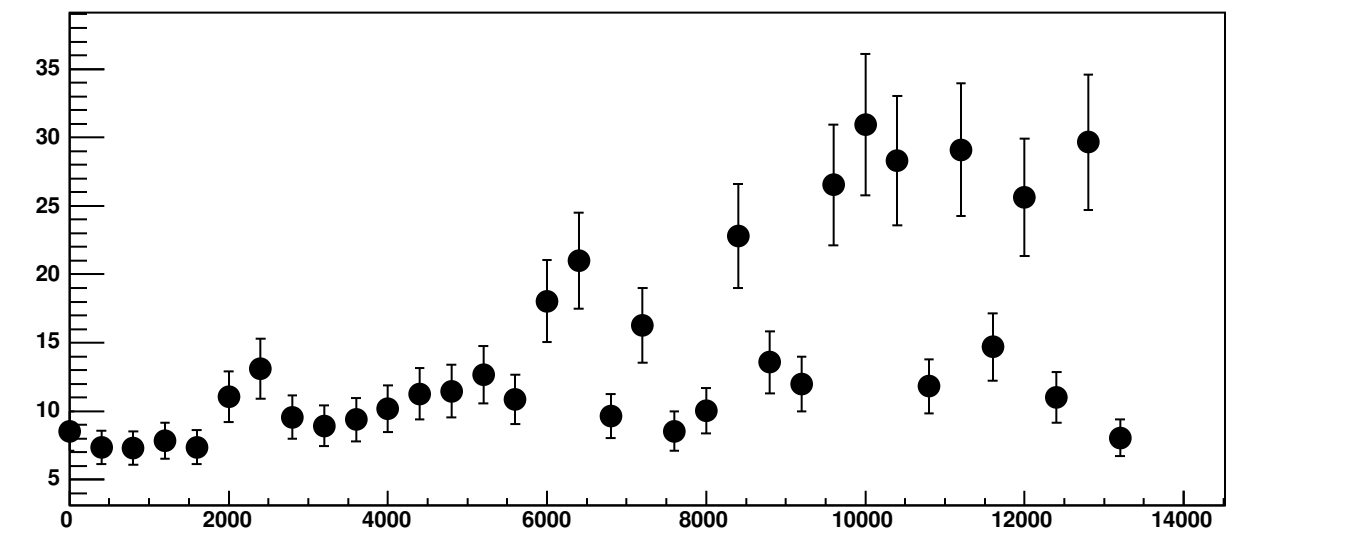

Chip 10, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

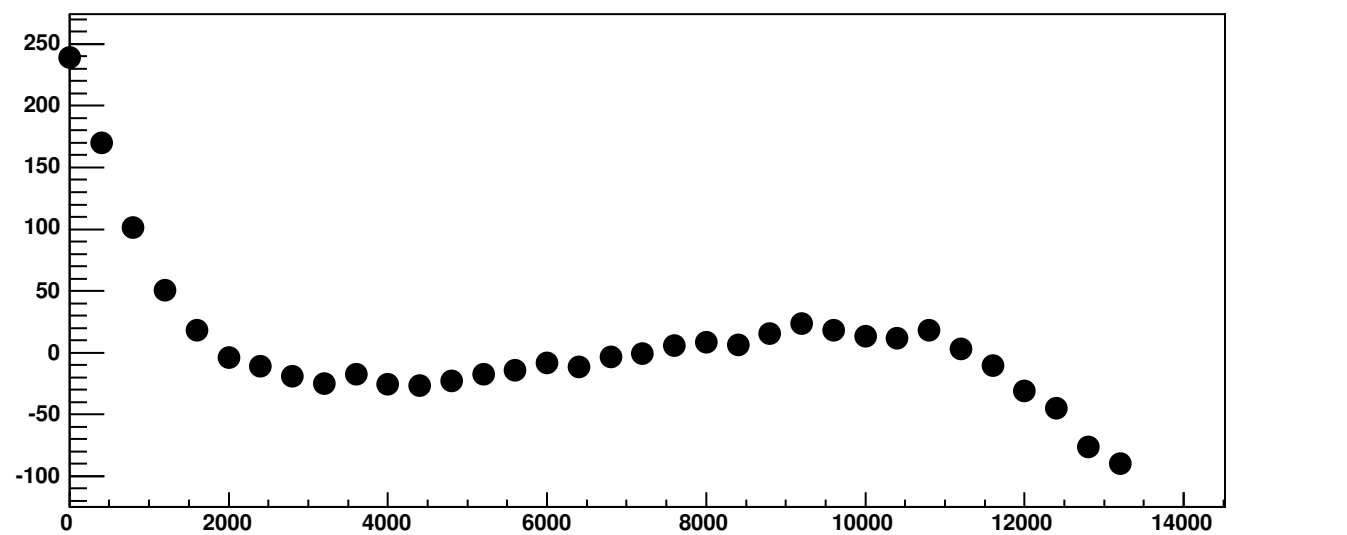
Chip 10, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

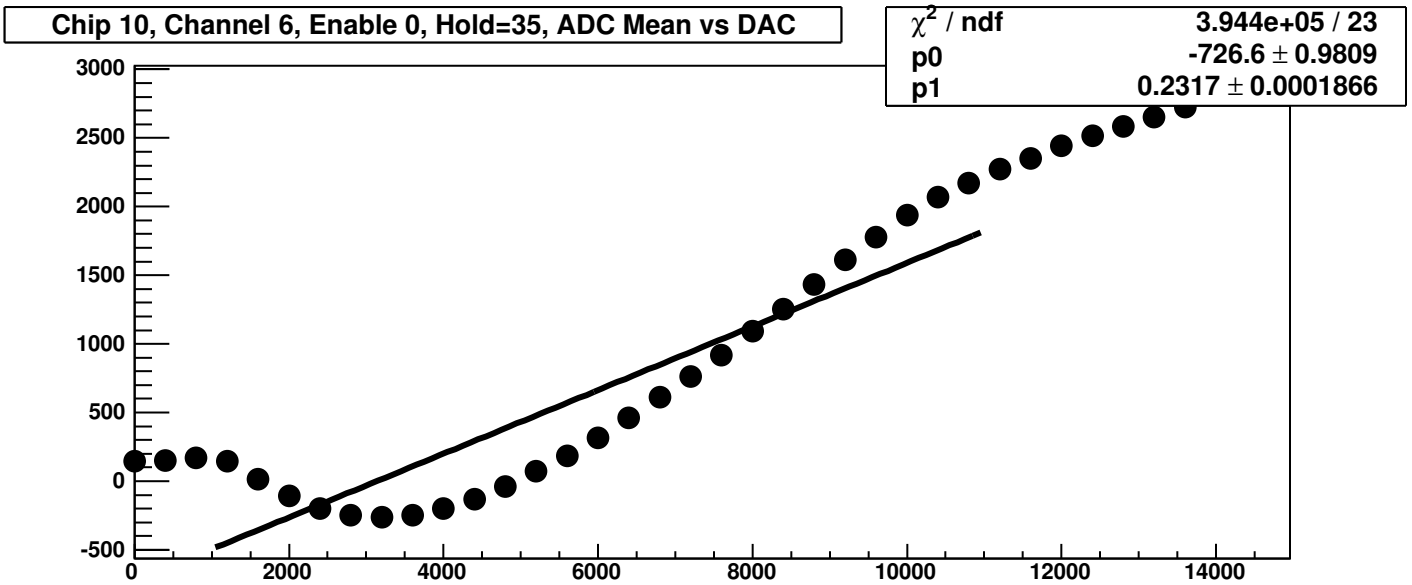


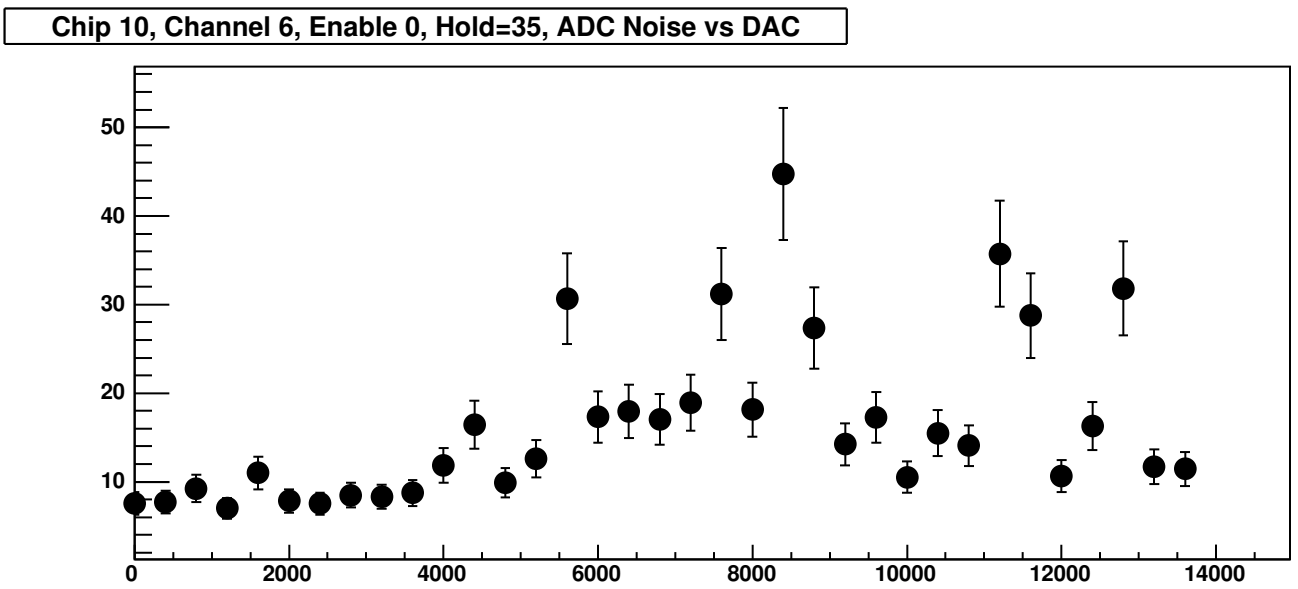
Chip 10, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC

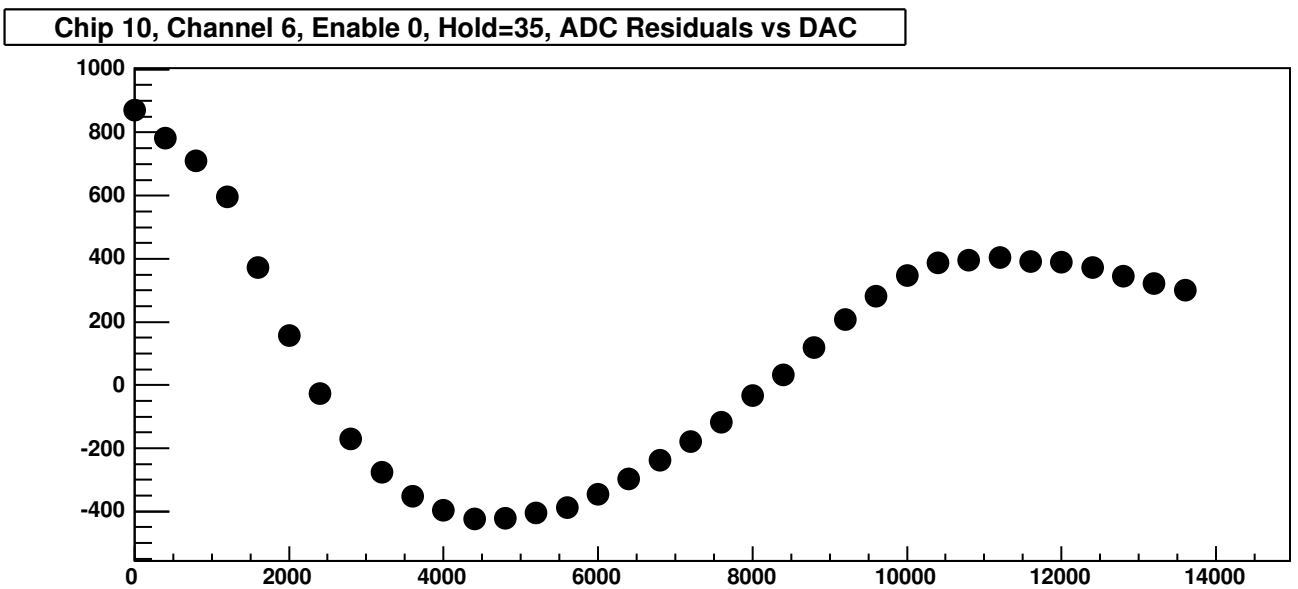


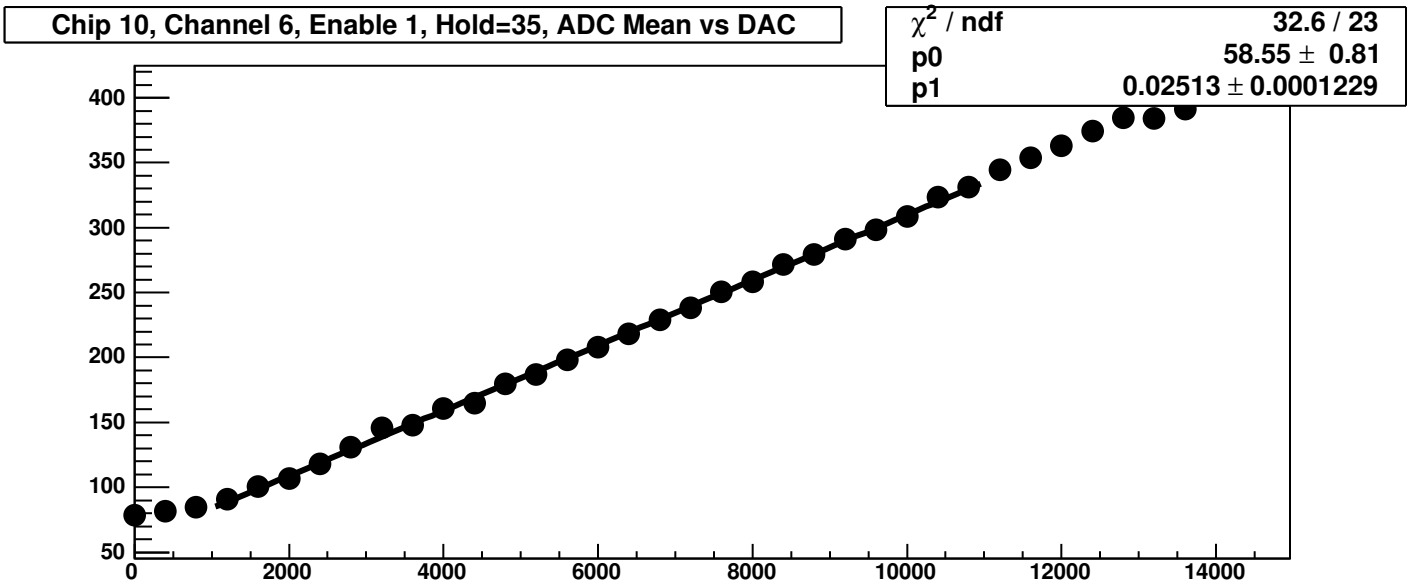
Chip 10, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

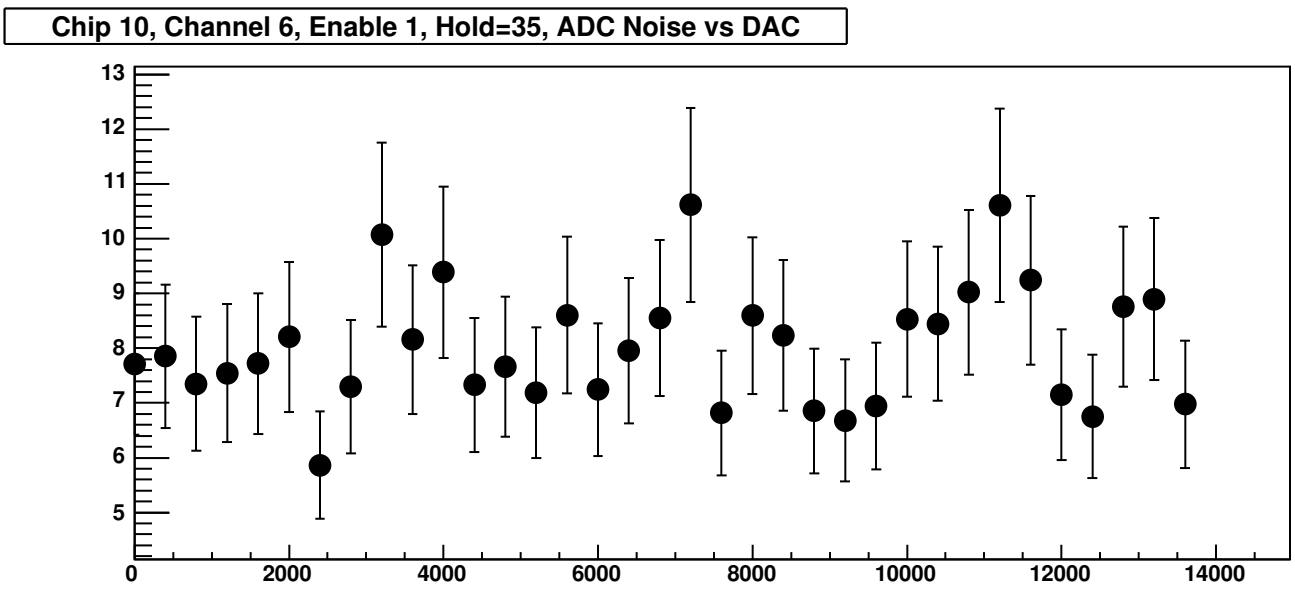


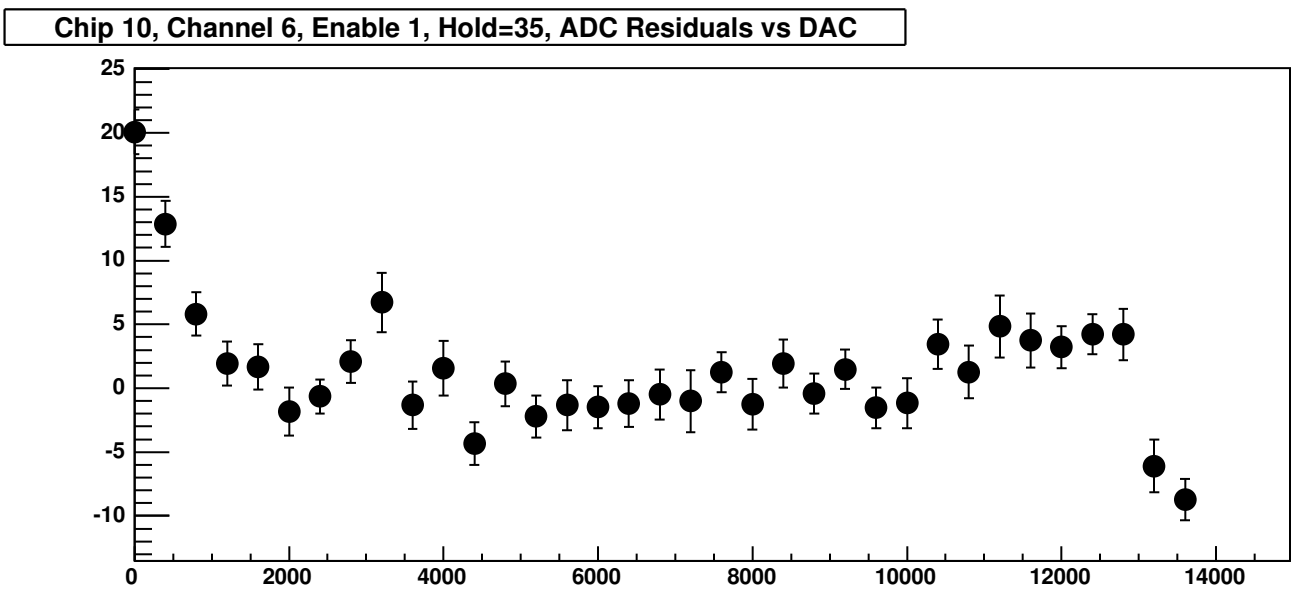

Chip 10, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.944e+05 / 23
p0 -726.6 ± 0.9809
p1 0.2317 ± 0.0001866
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

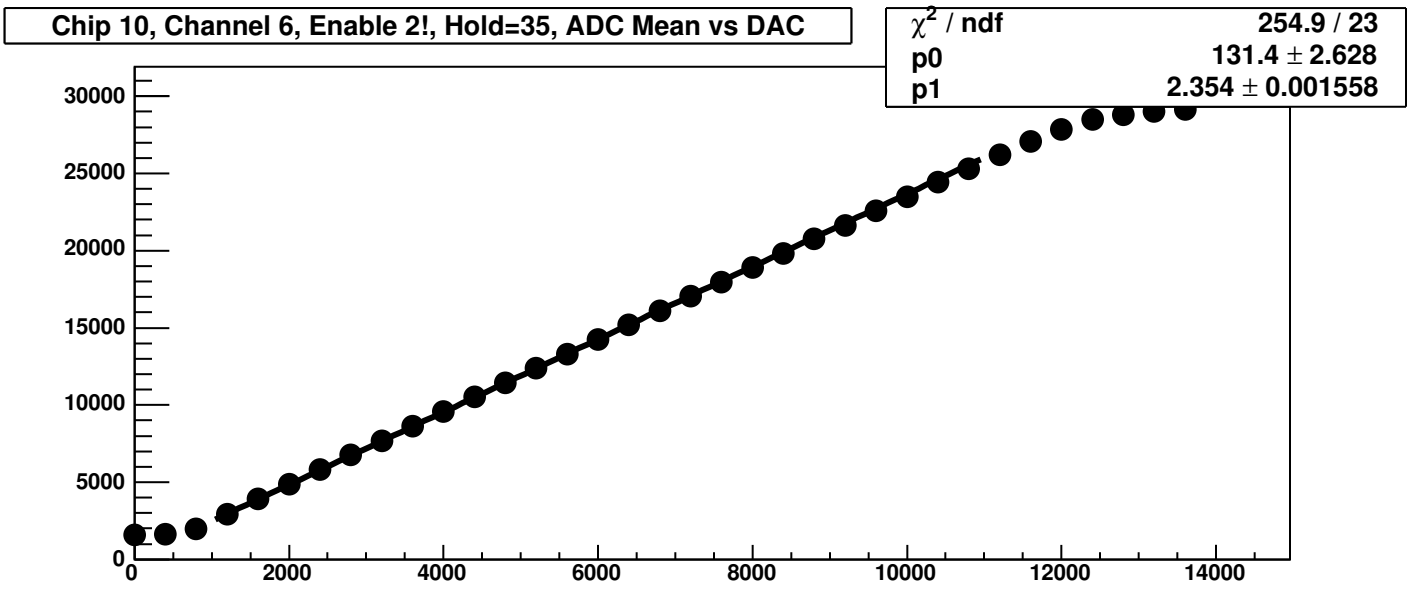

Chip 10, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

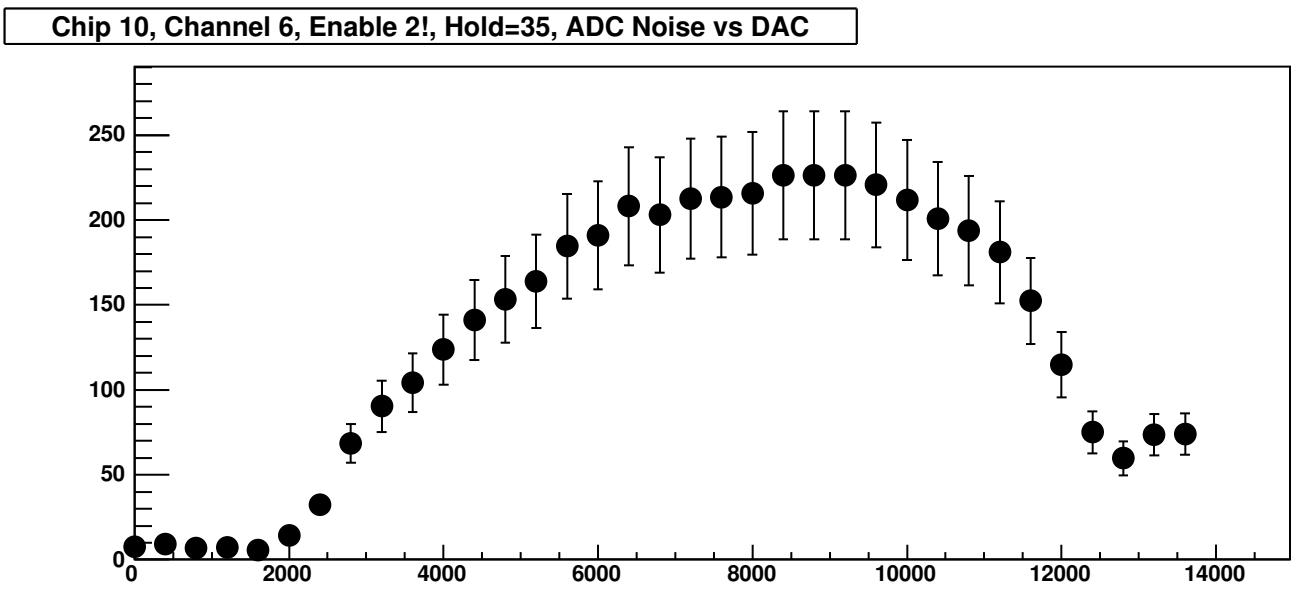

Chip 10, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

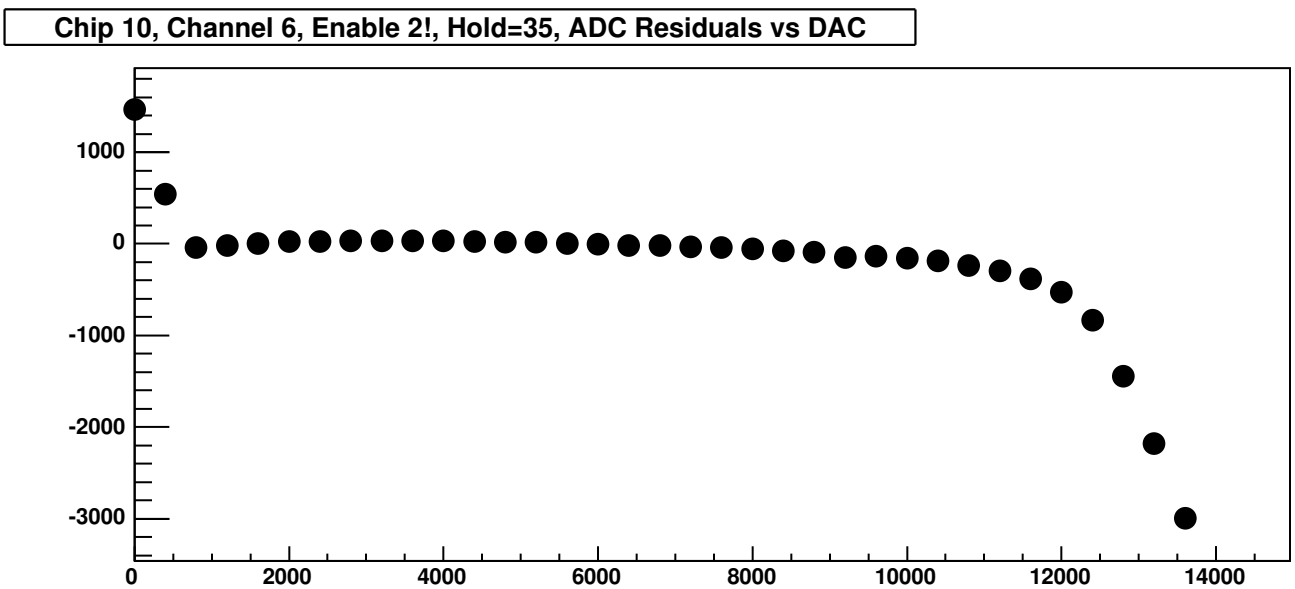

Chip 10, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.6 / 23
p0 58.55 ± 0.81
p1 0.02513 ± 0.0001229
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

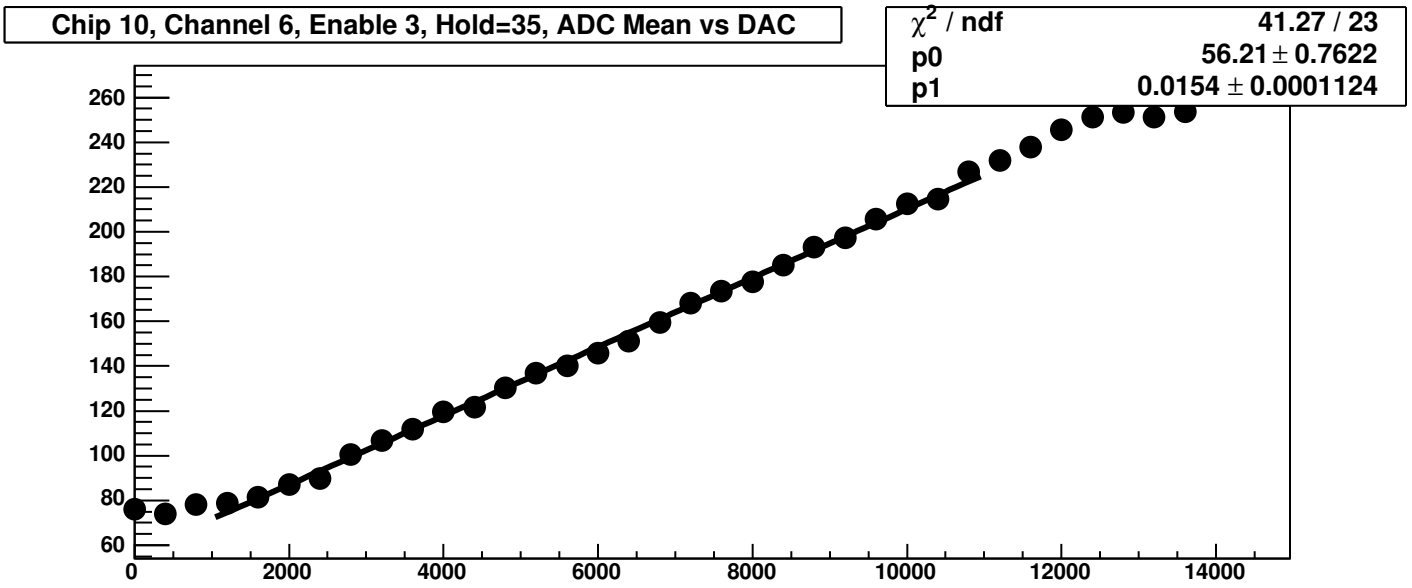

Chip 10, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

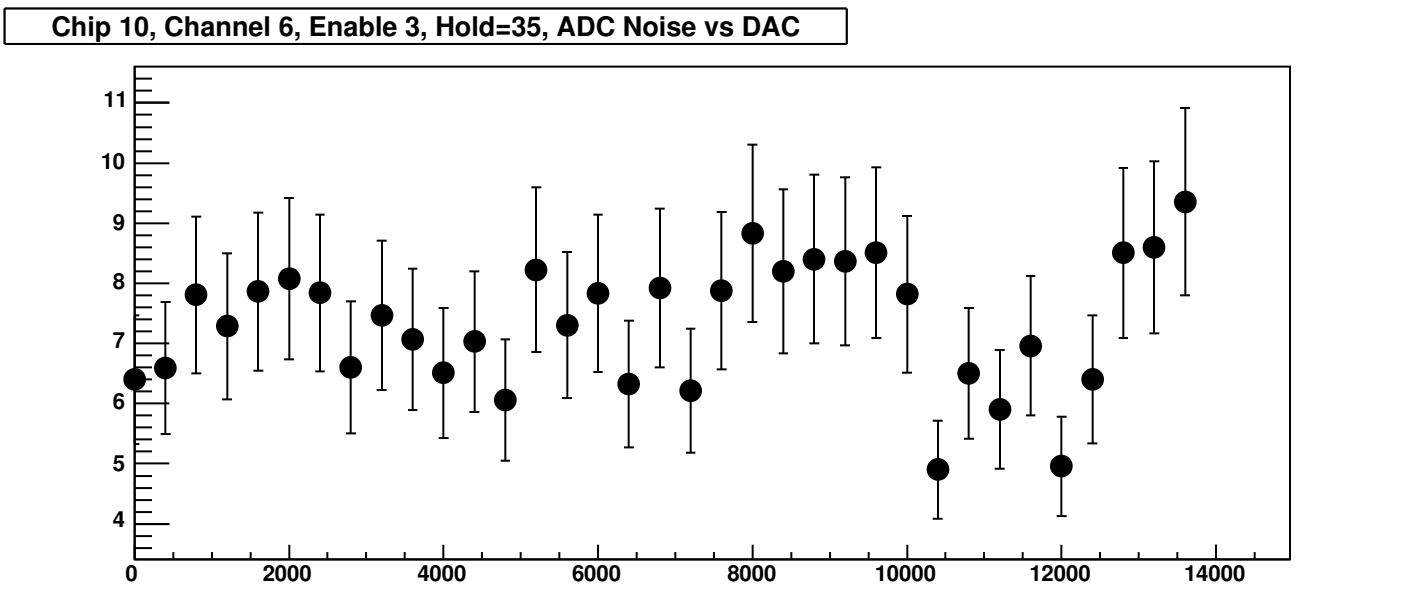

Chip 10, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

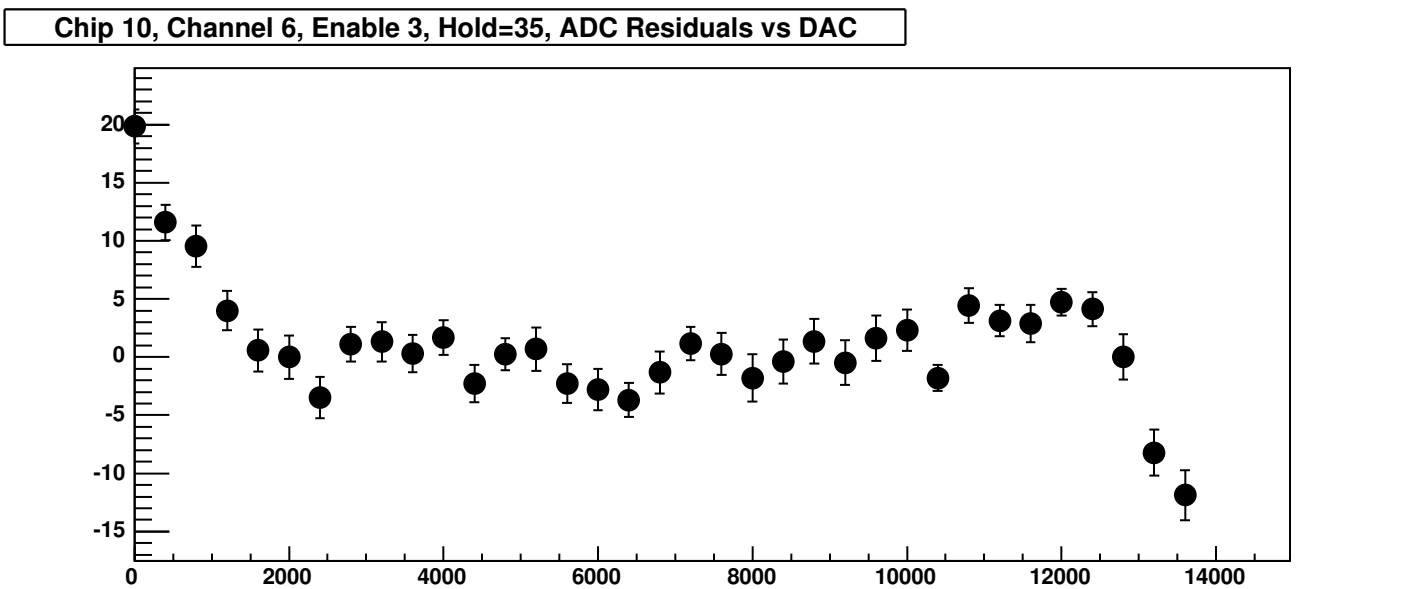

Chip 10, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 254.9 / 23
p0 131.4 ± 2.628
p1 2.354 ± 0.001558
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

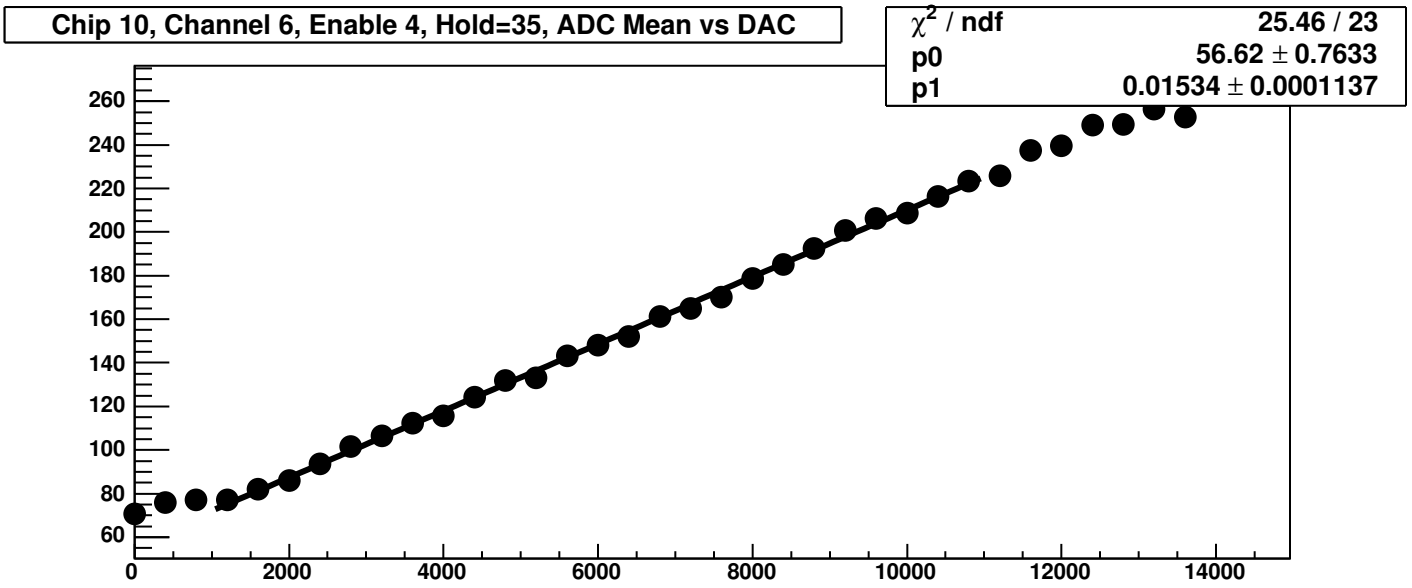

Chip 10, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

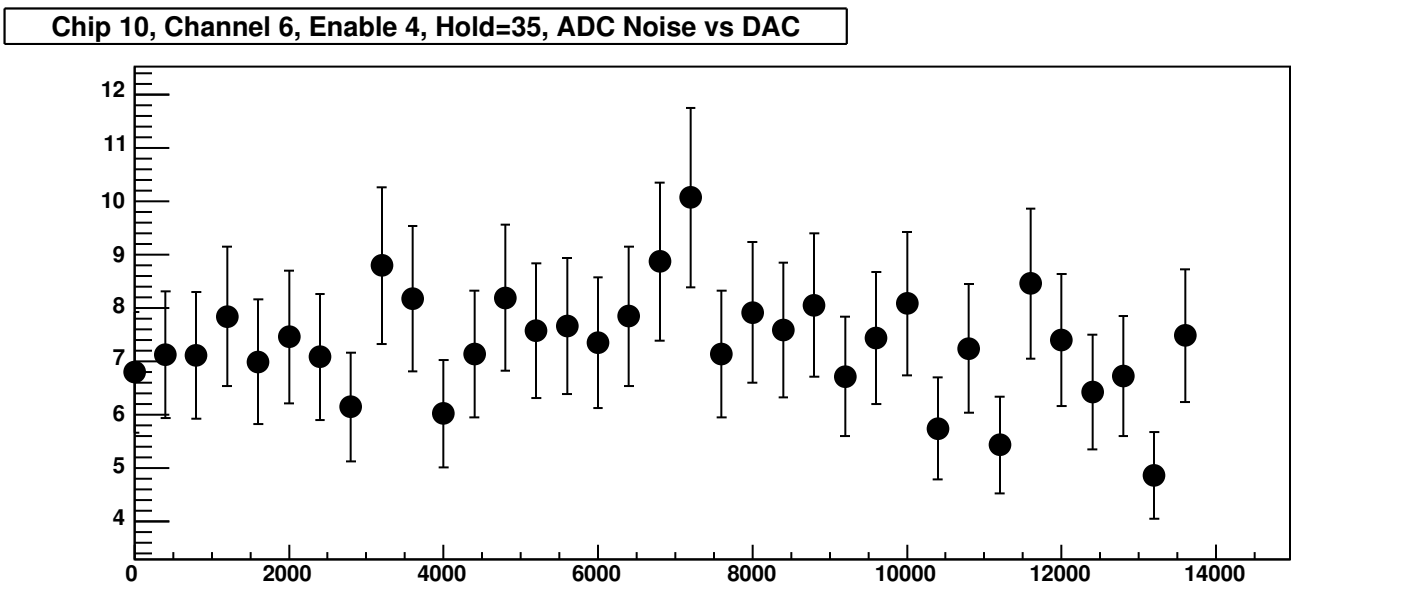

Chip 10, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

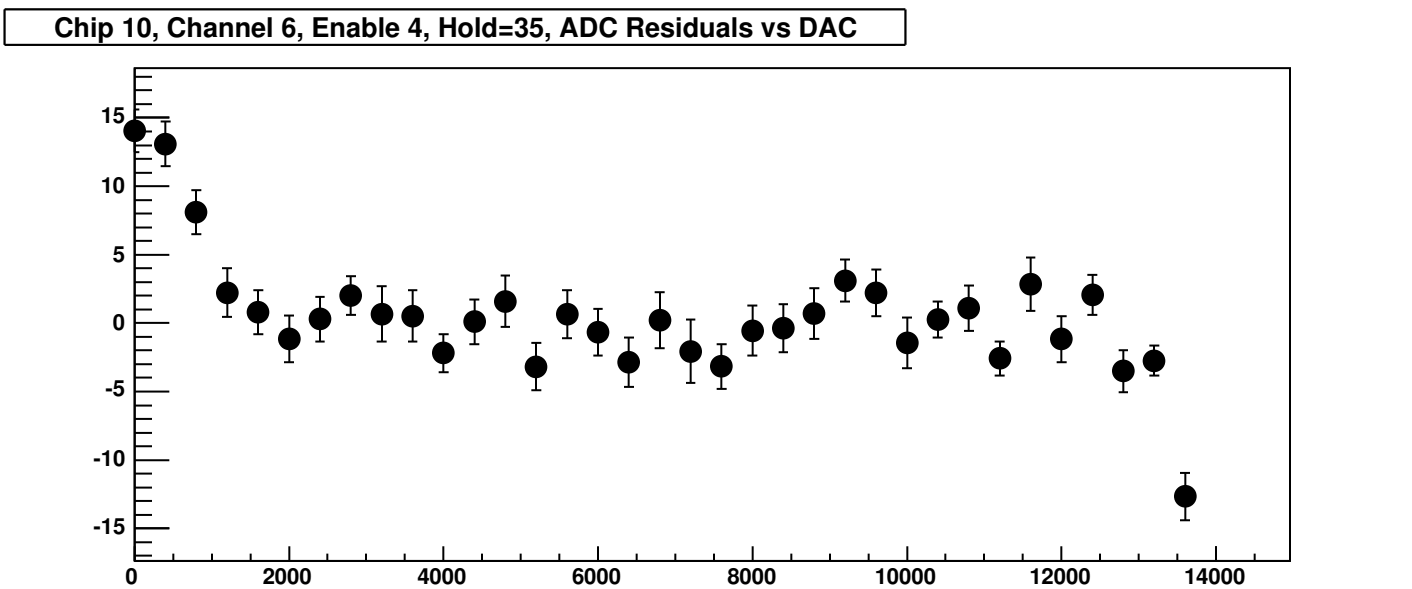

Chip 10, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 41.27 / 23
p0 56.21 ± 0.7622
p1 0.0154 ± 0.0001124
260
240
220
200
180
160
140
120
100
80
60
0
2000
4000
6000
8000
10000
12000
14000

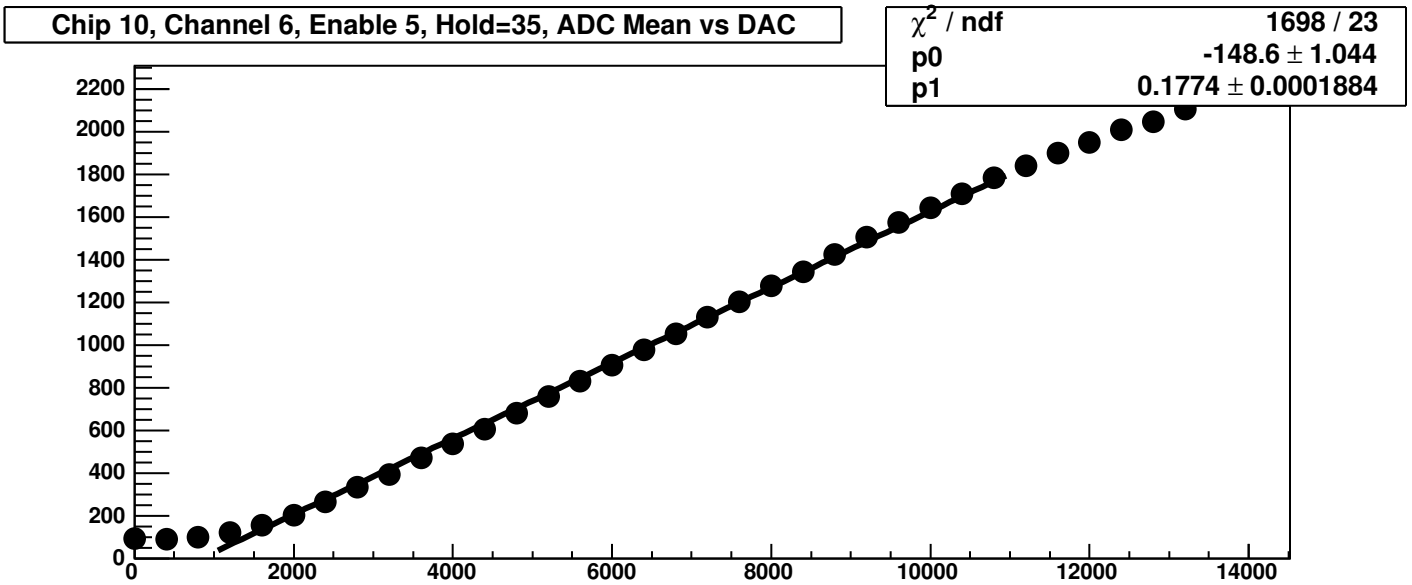

Chip 10, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

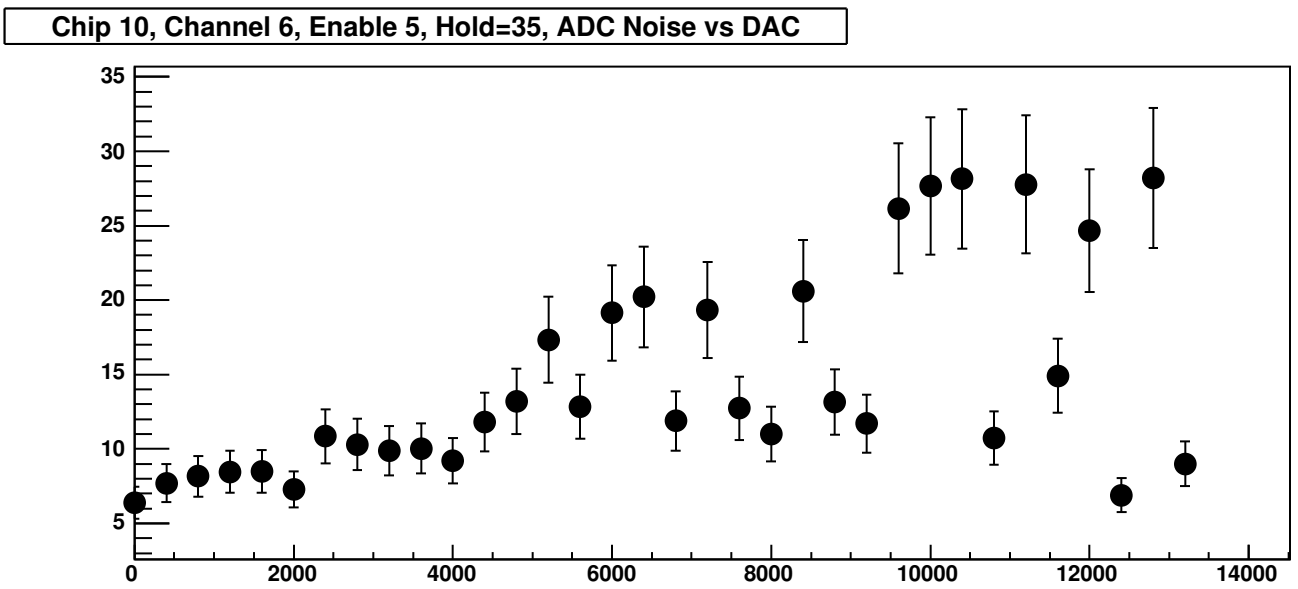

Chip 10, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

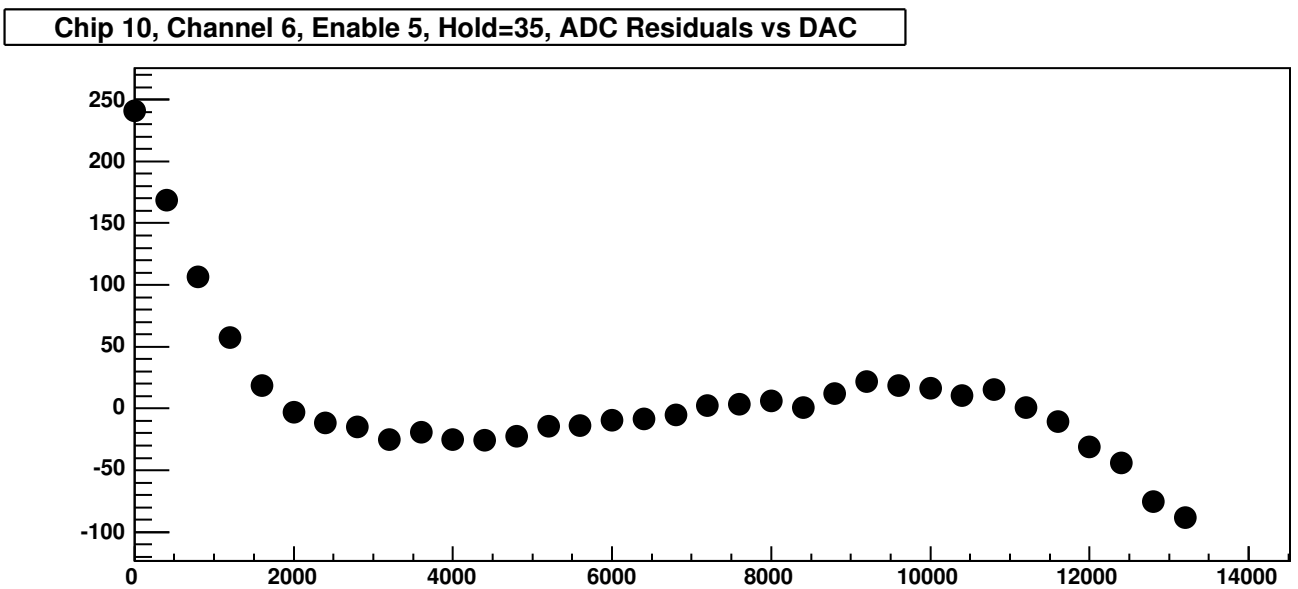

Chip 10, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 25.46 / 23
p0 56.62 ± 0.7633
p1 0.01534 ± 0.0001137
260
240
220
200
180
160
140
120
100
80
60
0
2000
4000
6000
8000
10000
12000
14000

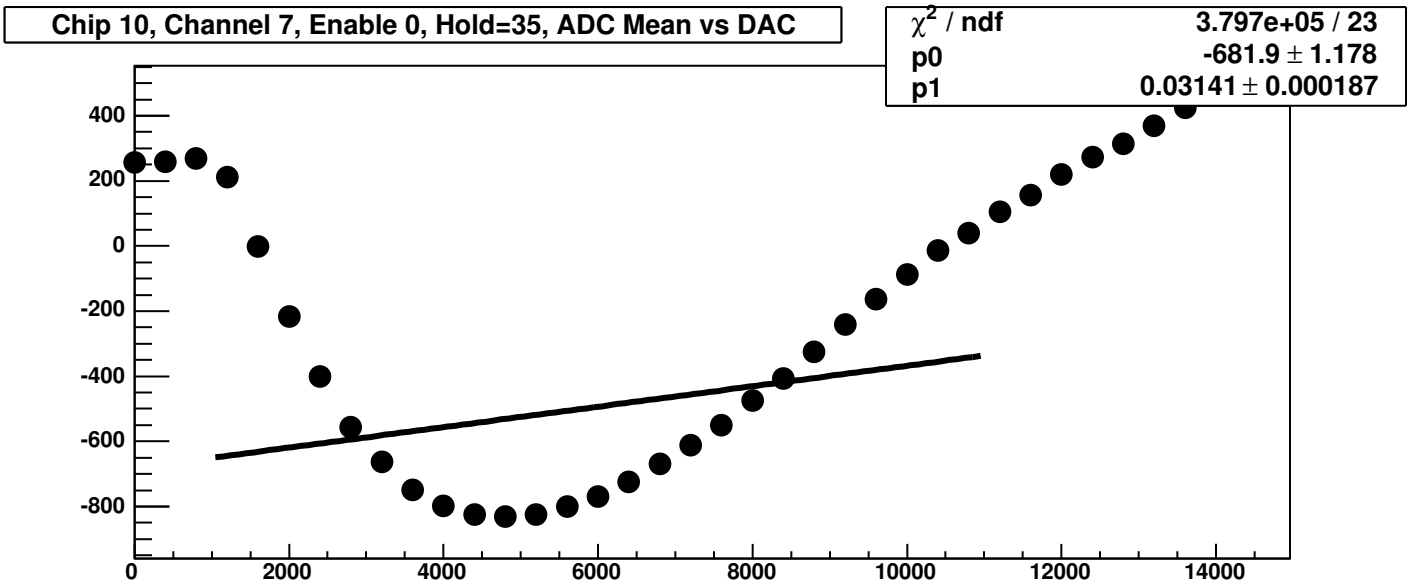

Chip 10, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

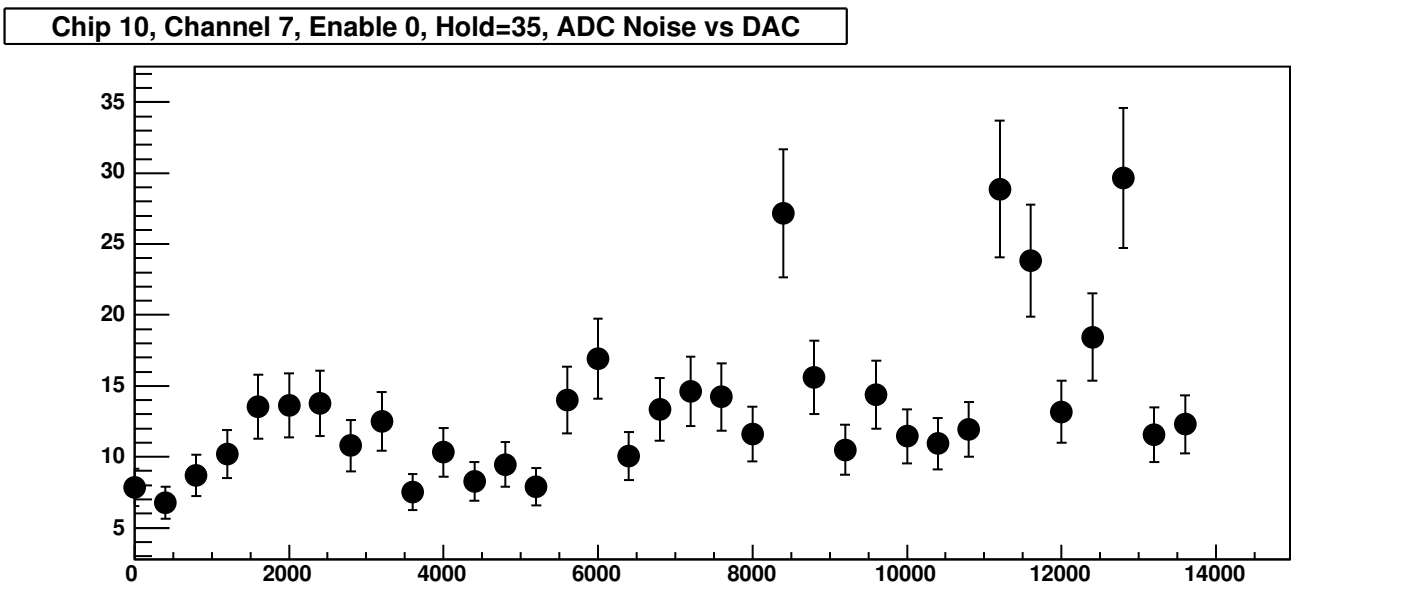

Chip 10, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

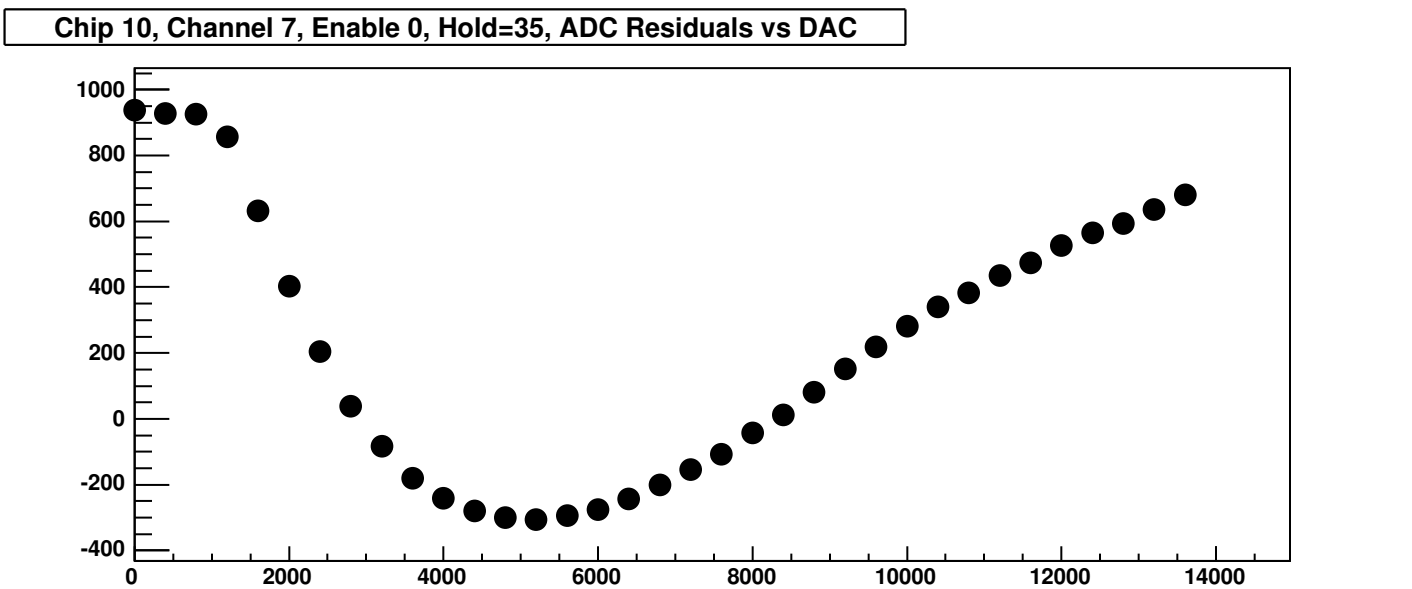

Chip 10, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1698 / 23
p0 -148.6 ± 1.044
p1 0.1774 ± 0.0001884
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

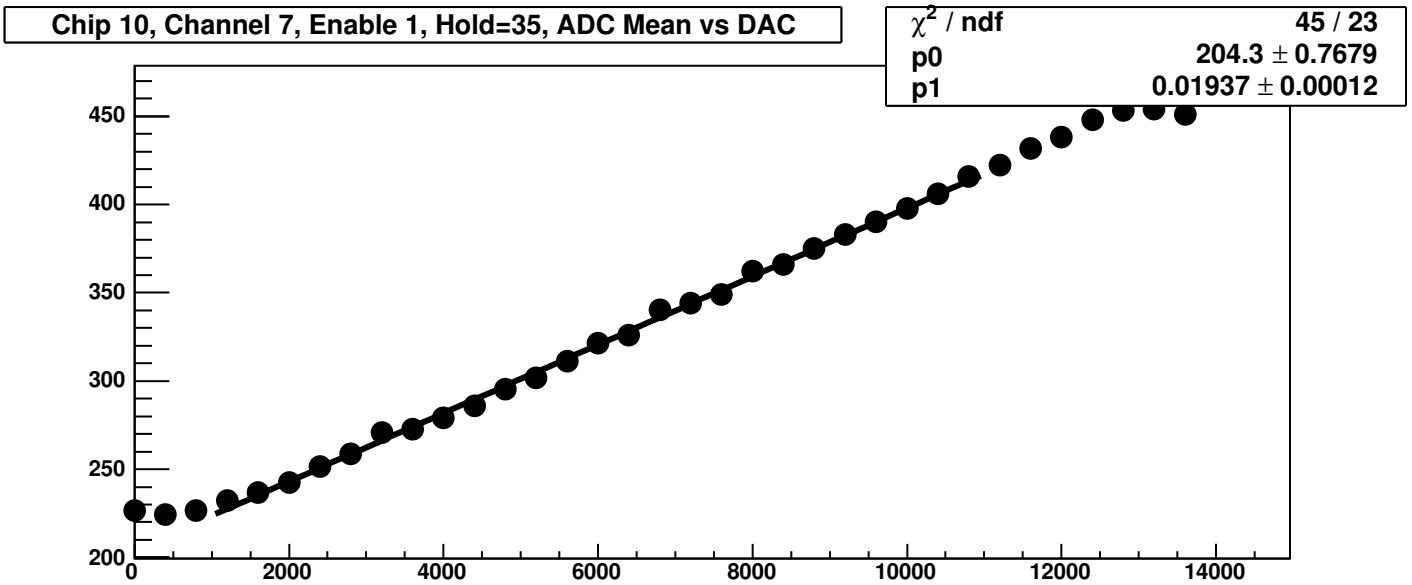

Chip 10, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

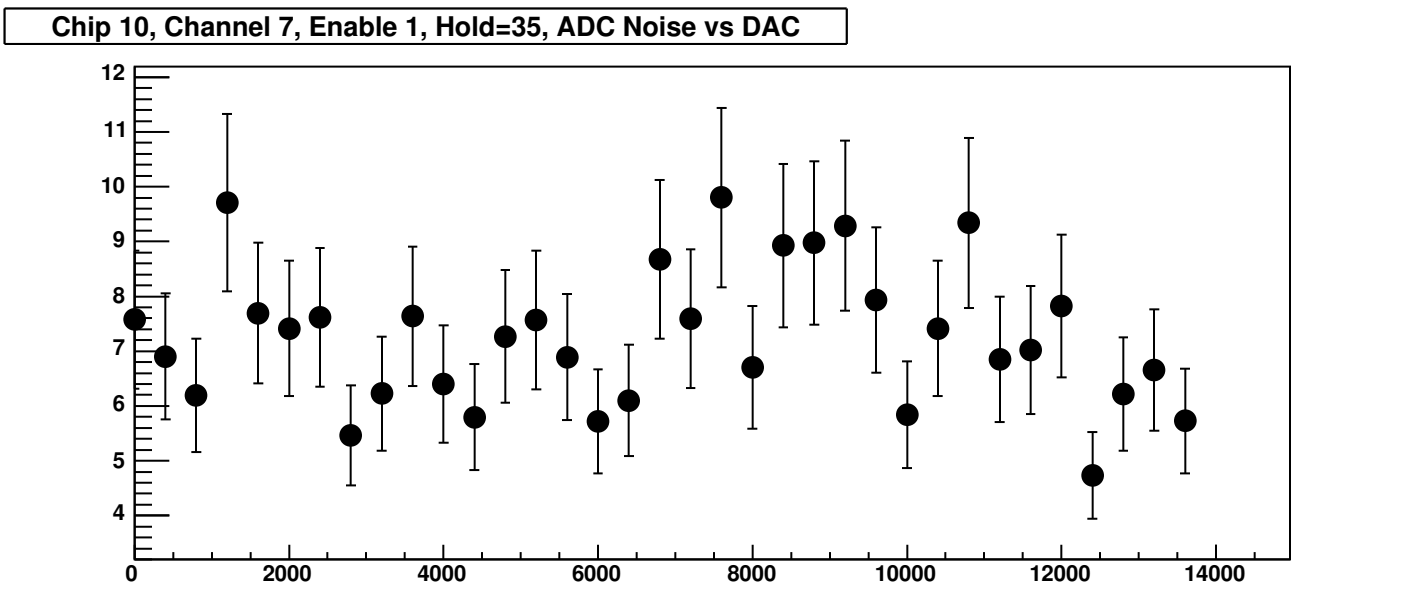

Chip 10, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

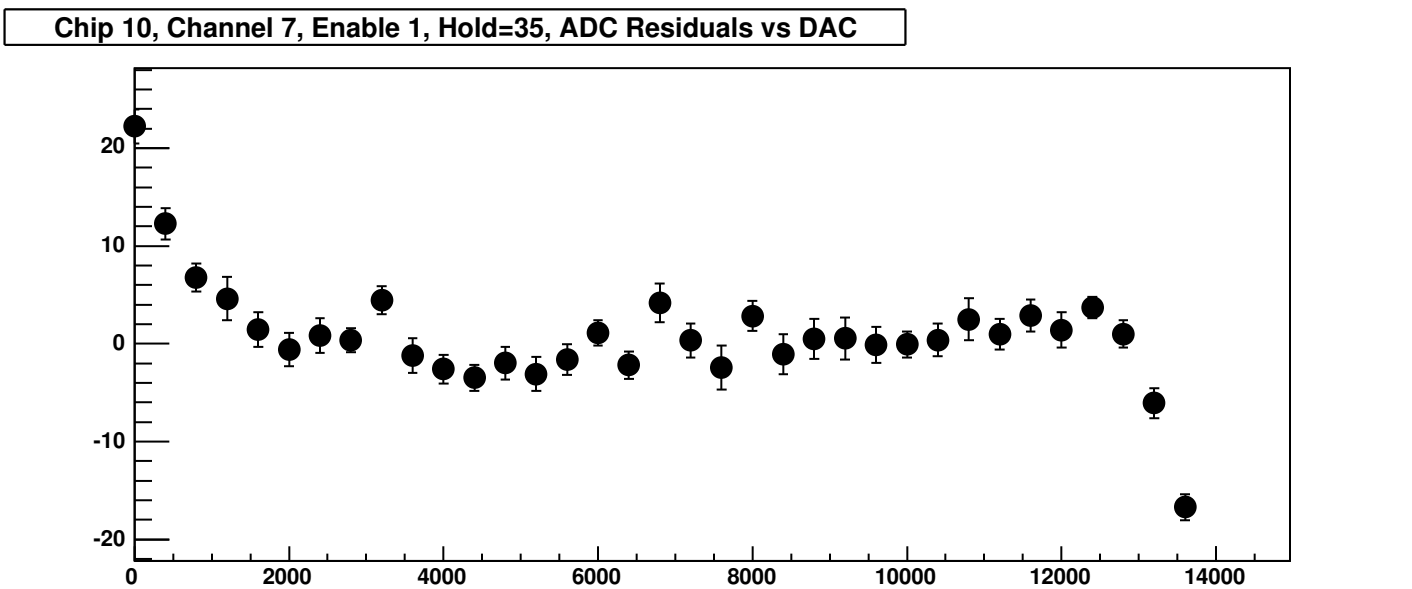

Chip 10, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.797e+05 / 23
p0 -681.9 ± 1.178
p1 0.03141 ± 0.000187
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

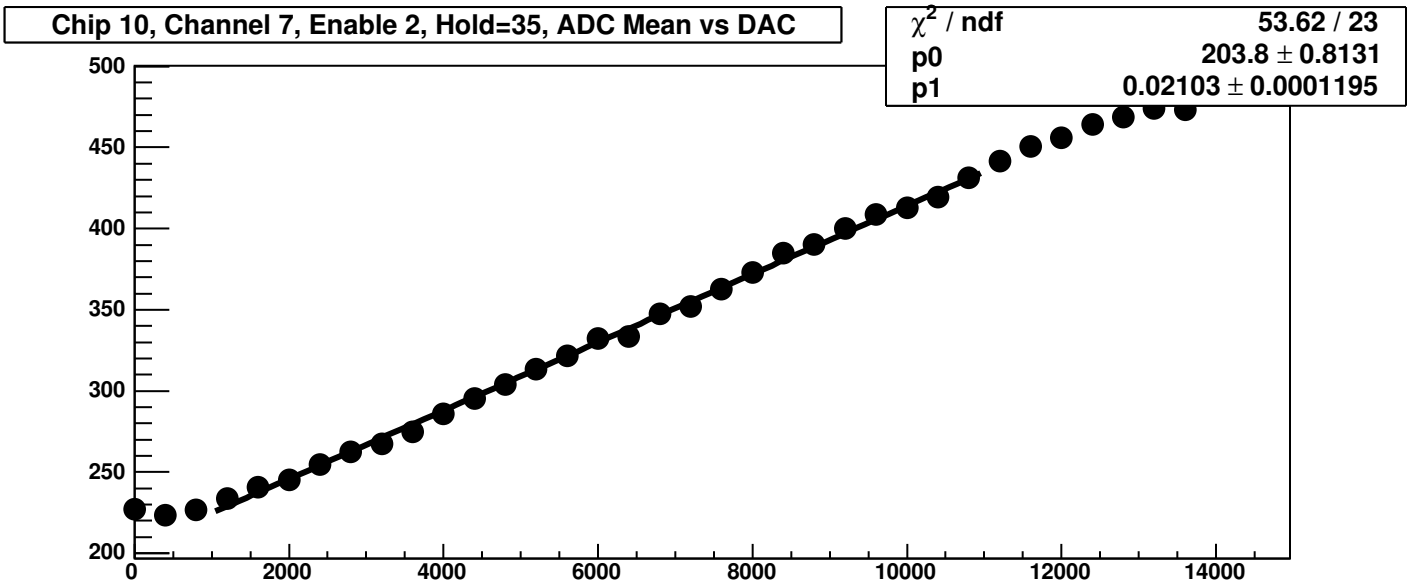

Chip 10, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

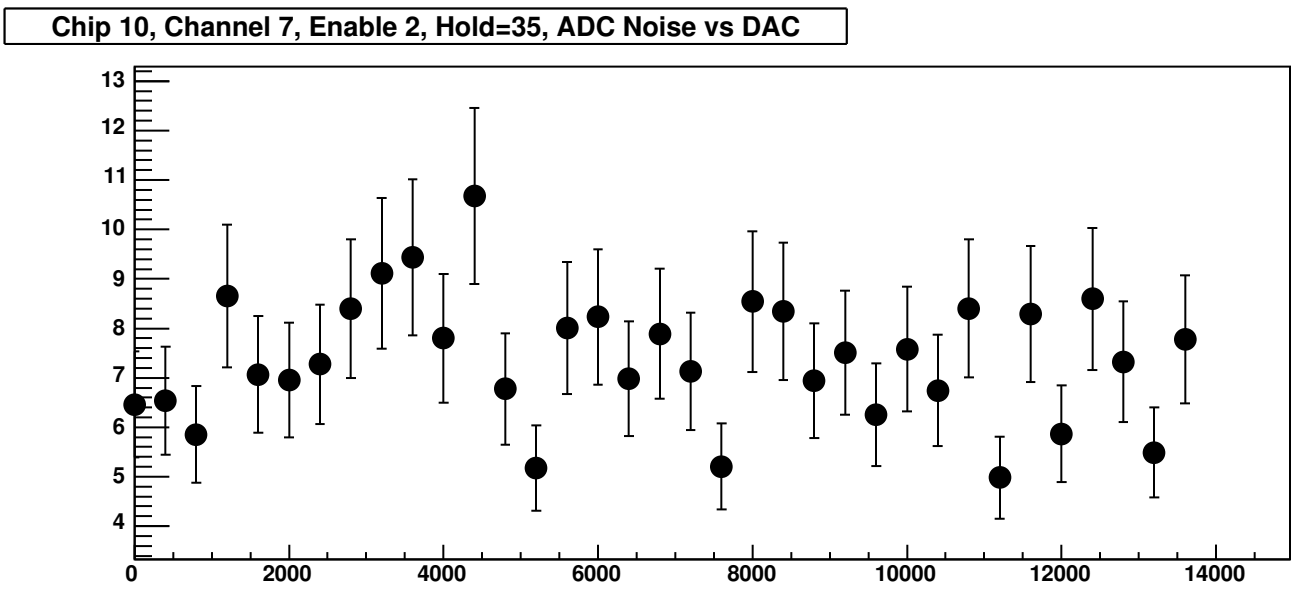

Chip 10, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

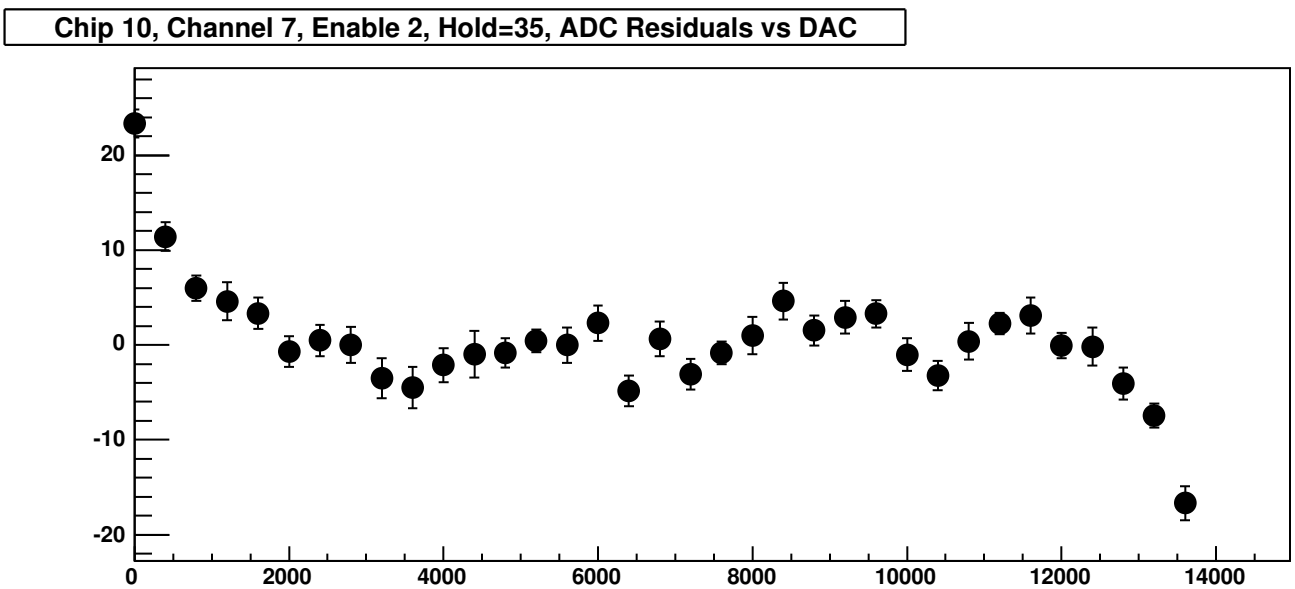

Chip 10, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 45 / 23
p0 204.3 ± 0.7679
p1 0.01937 ± 0.00012
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

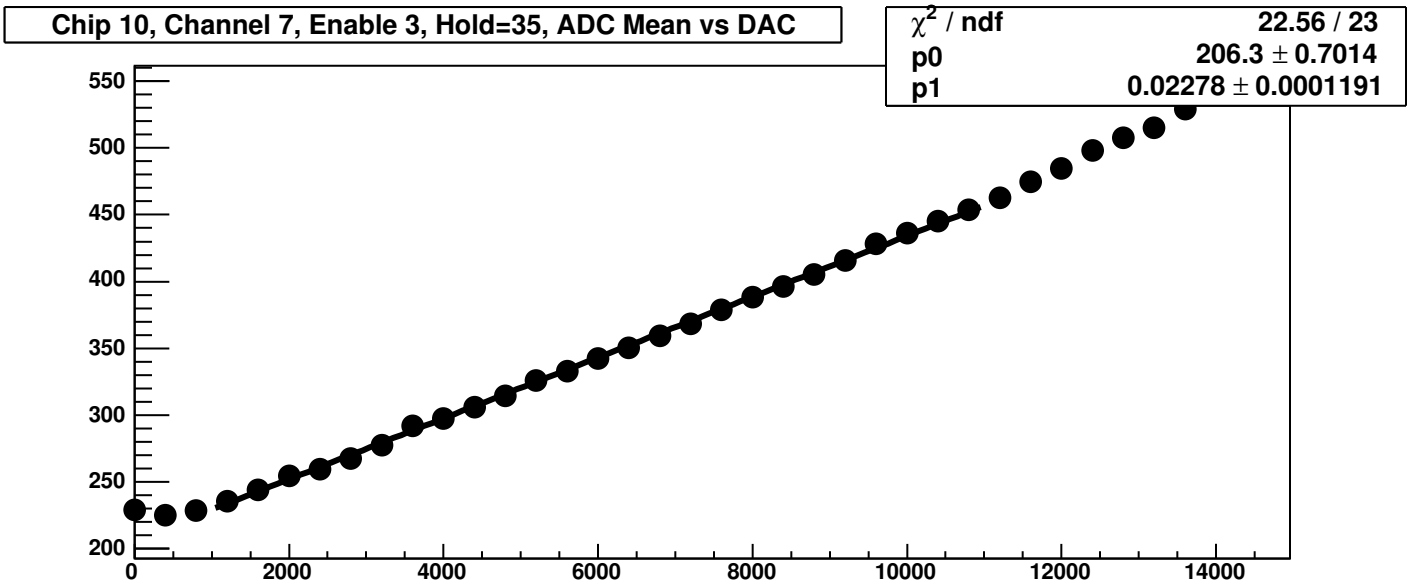

Chip 10, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

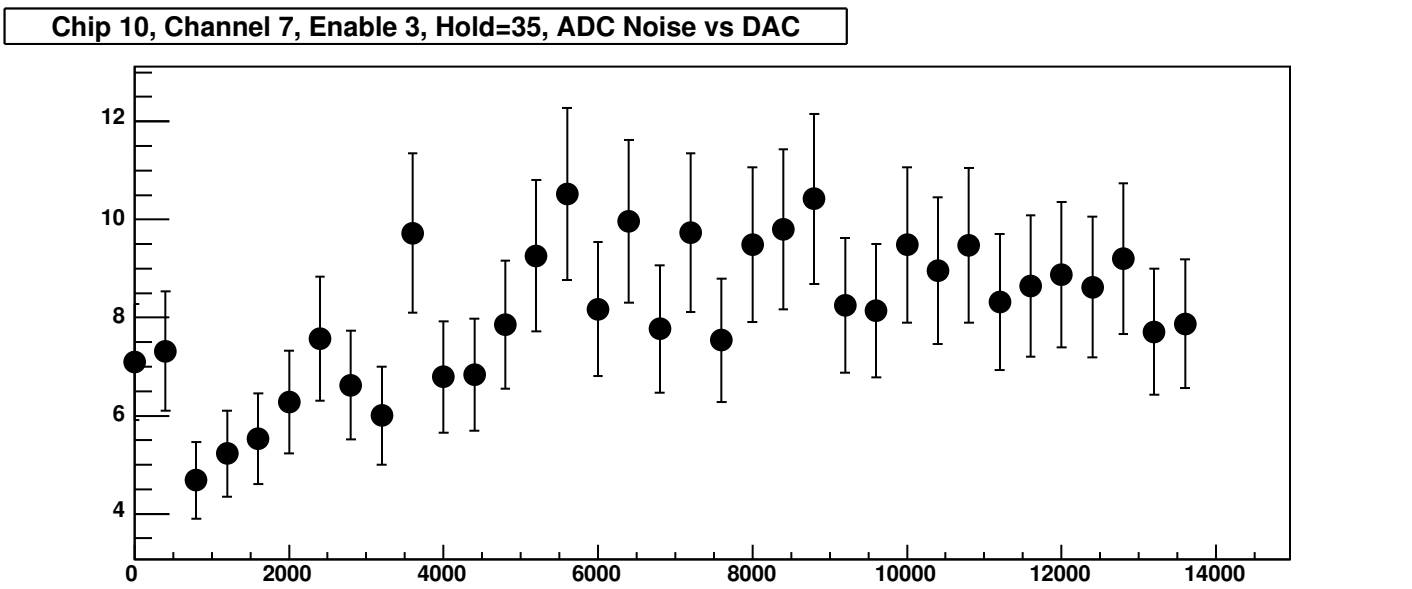

Chip 10, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

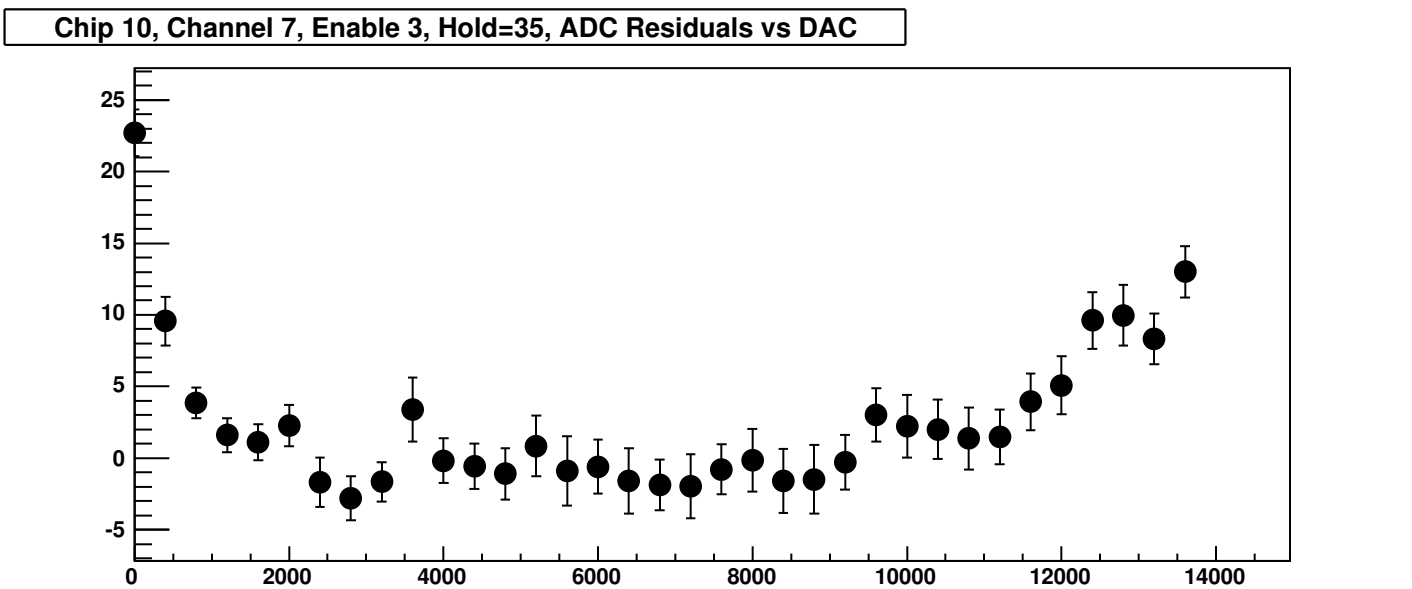

Chip 10, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 53.62 / 23
p0 203.8 ± 0.8131
p1 0.02103 ± 0.0001195
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

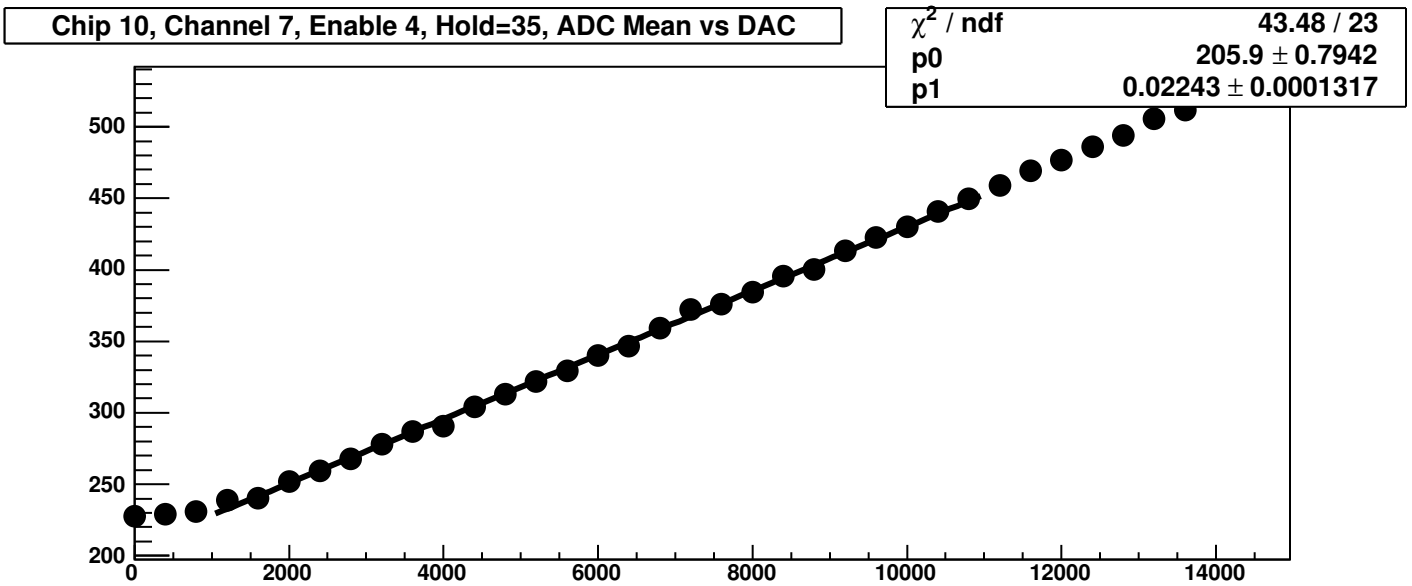

Chip 10, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

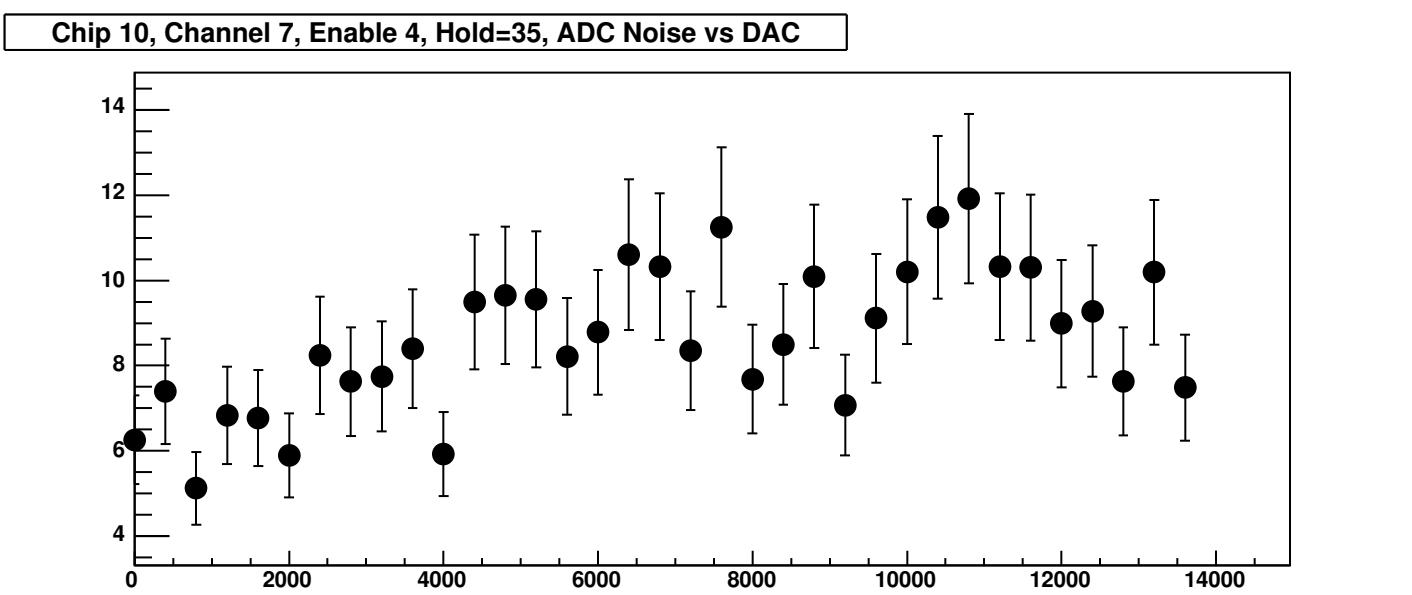

Chip 10, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

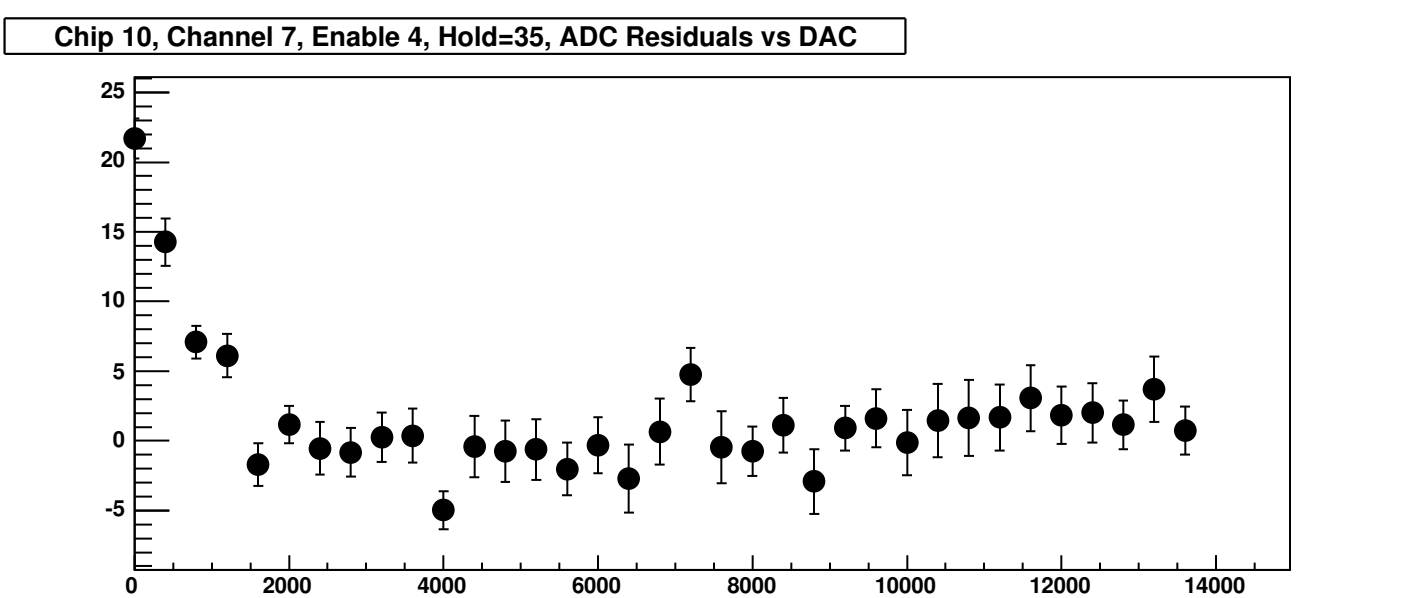

Chip 10, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 22.56 / 23
p0 206.3 ± 0.7014
p1 0.02278 ± 0.0001191
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

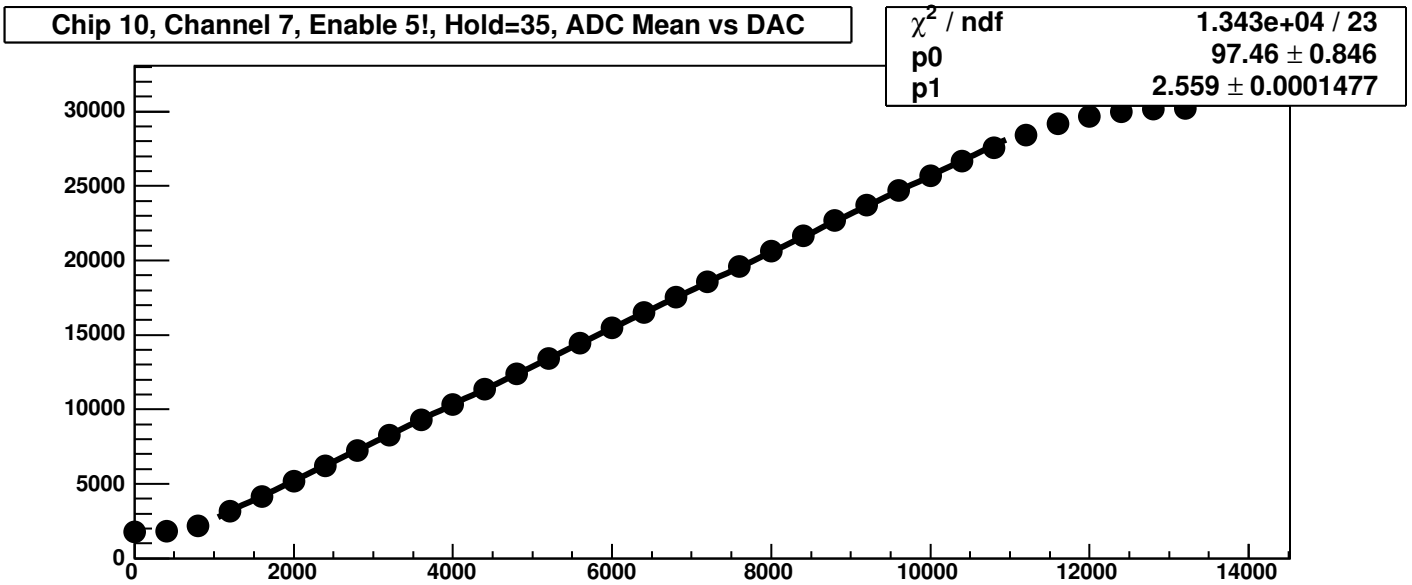

Chip 10, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

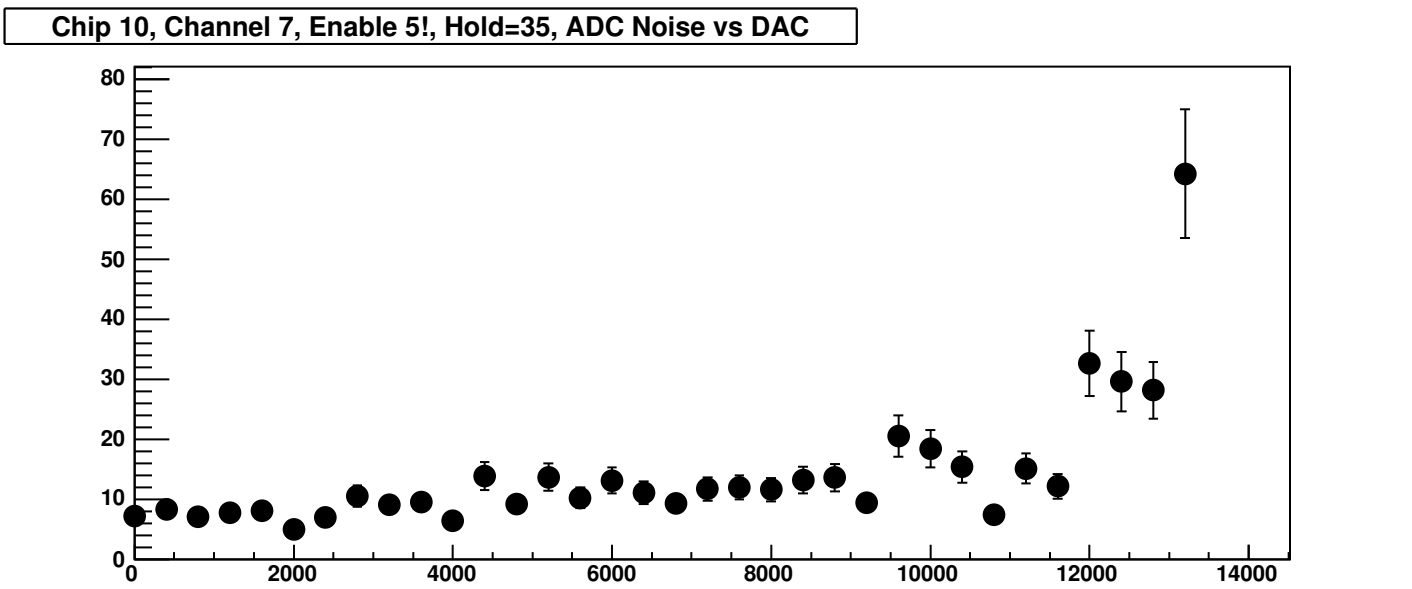

Chip 10, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

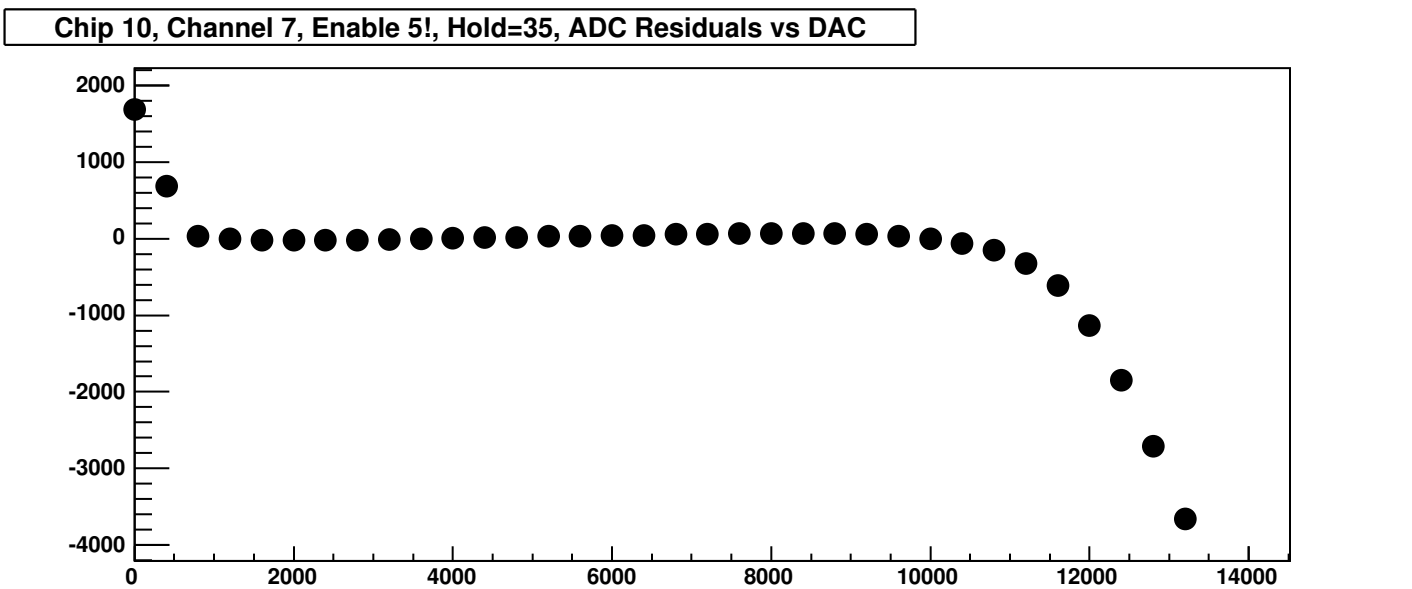

Chip 10, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 43.48 / 23
p0 205.9 ± 0.7942
p1 0.02243 ± 0.0001317
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

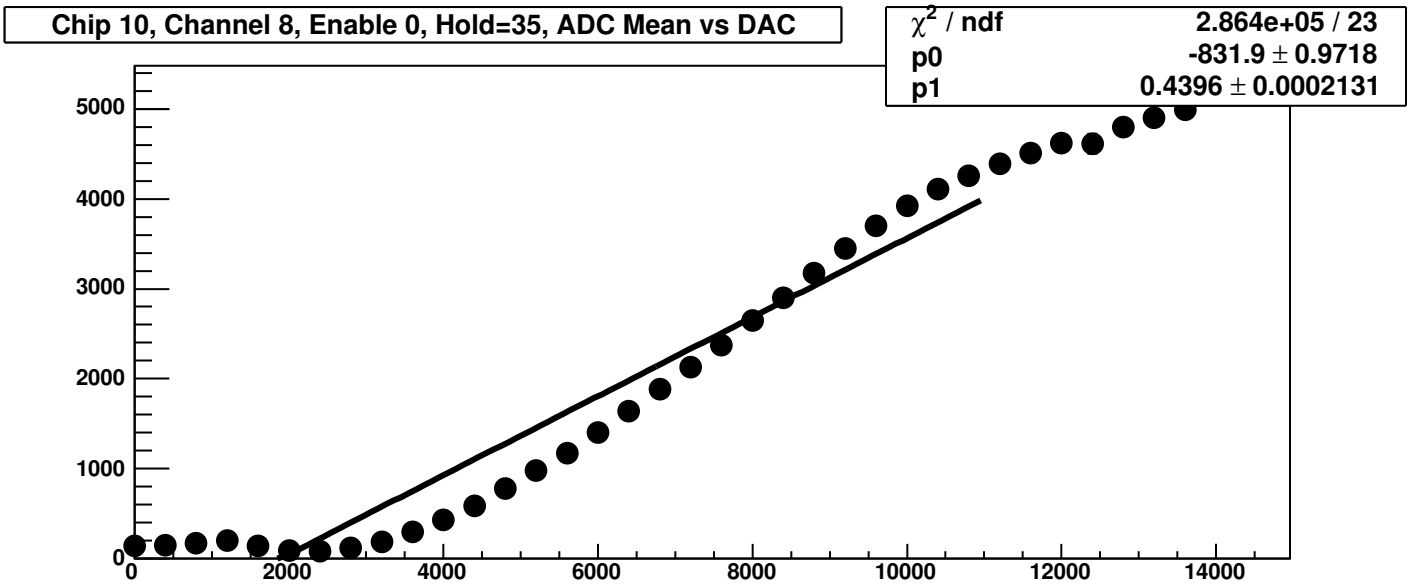

Chip 10, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

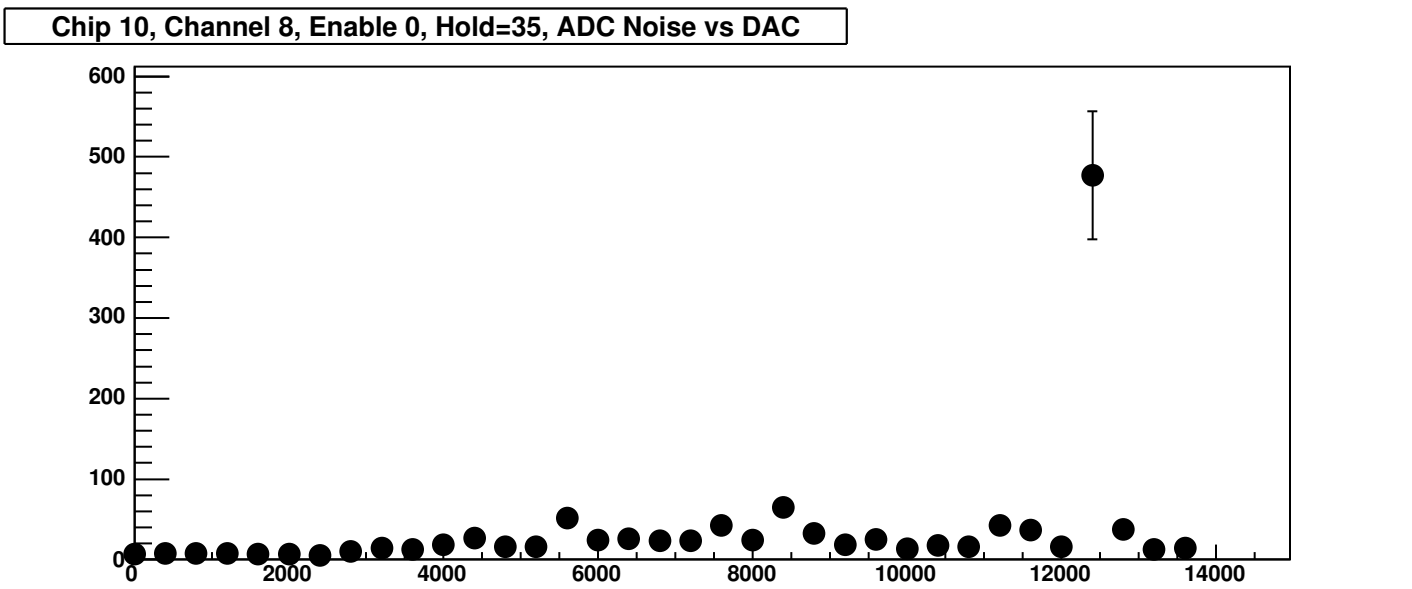

Chip 10, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

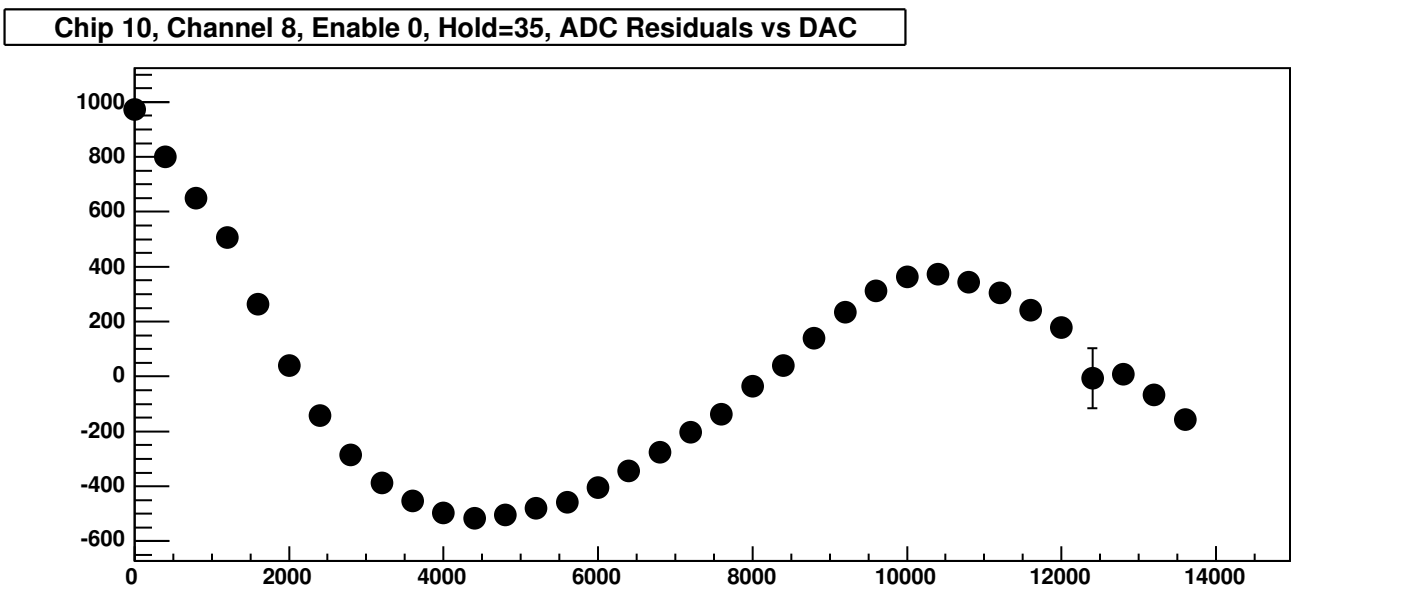

Chip 10, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1.343e+04 / 23
p0 97.46 ± 0.846
p1 2.559 ± 0.0001477
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

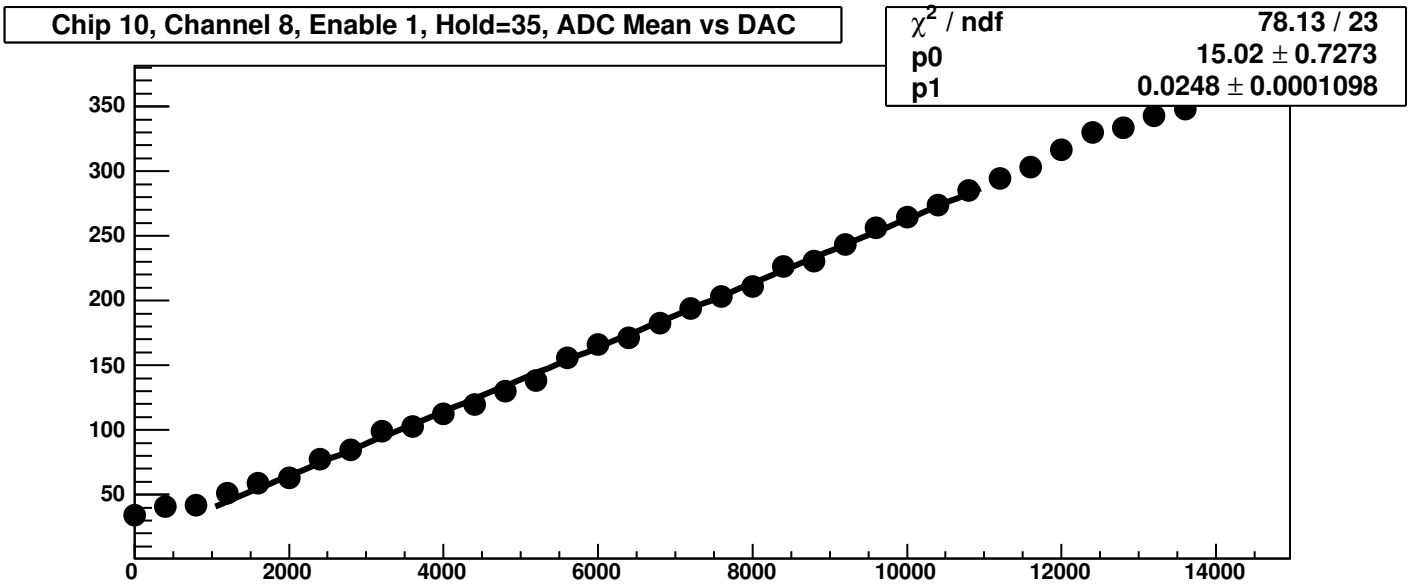

Chip 10, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

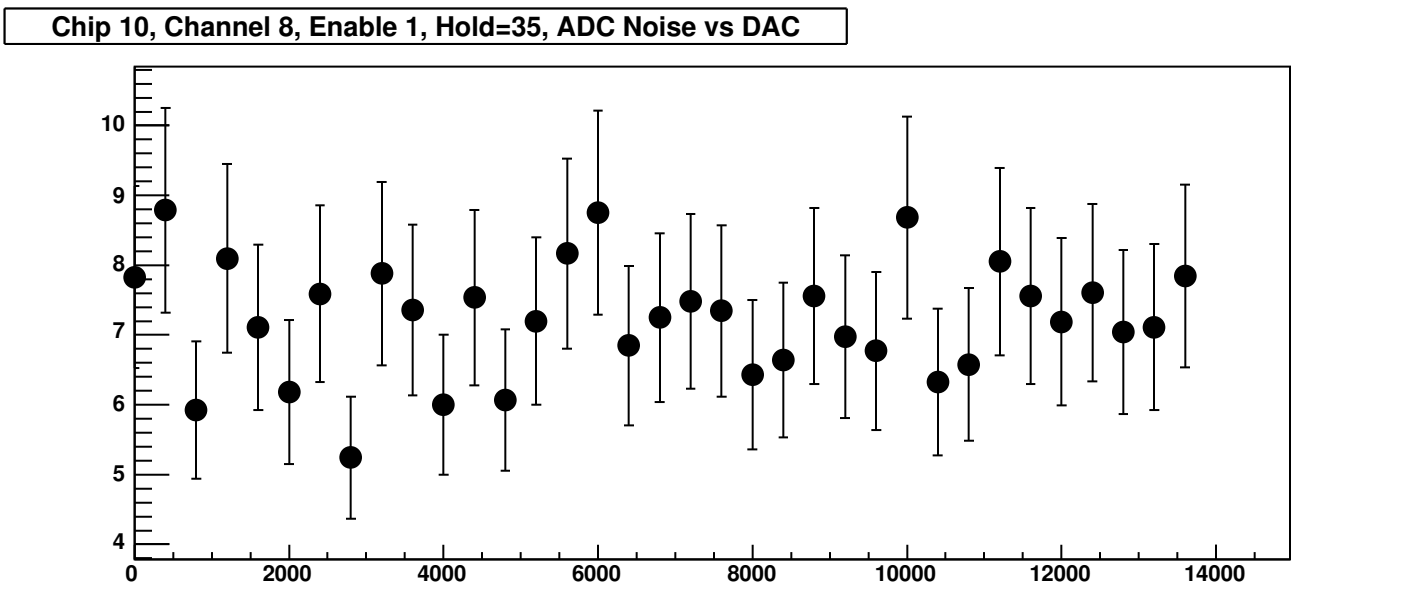

Chip 10, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

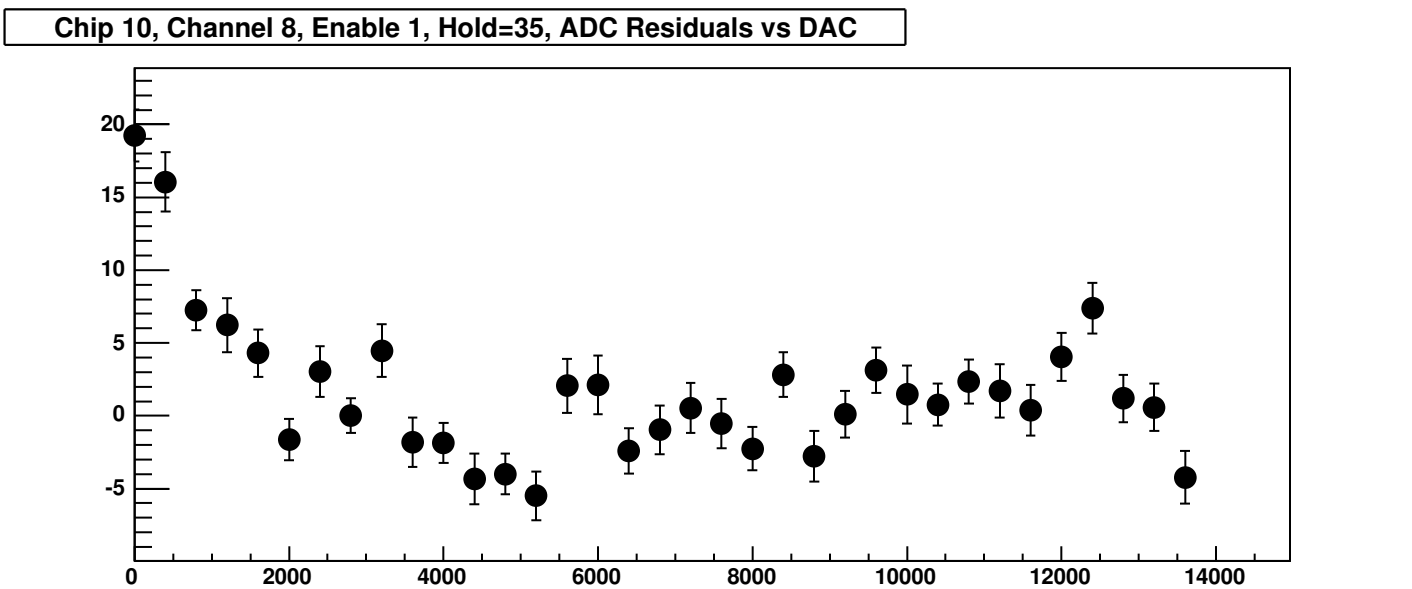

Chip 10, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.864e+05 / 23
p0 -831.9 ± 0.9718
p1 0.4396 ± 0.0002131
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

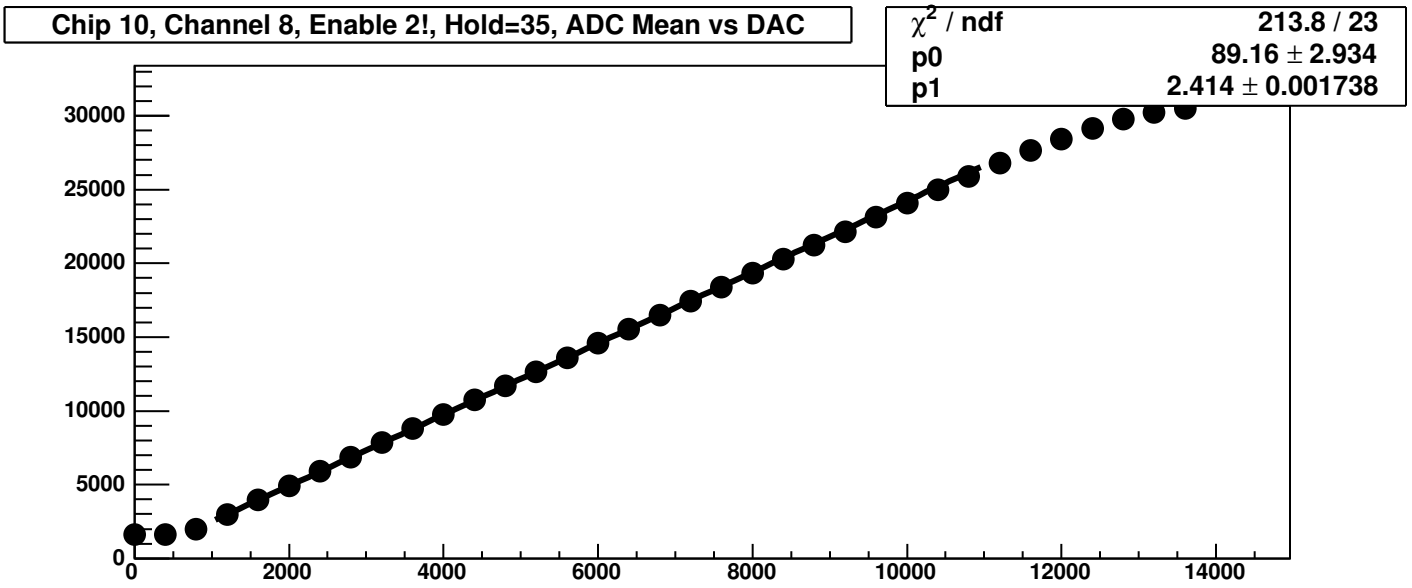

Chip 10, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

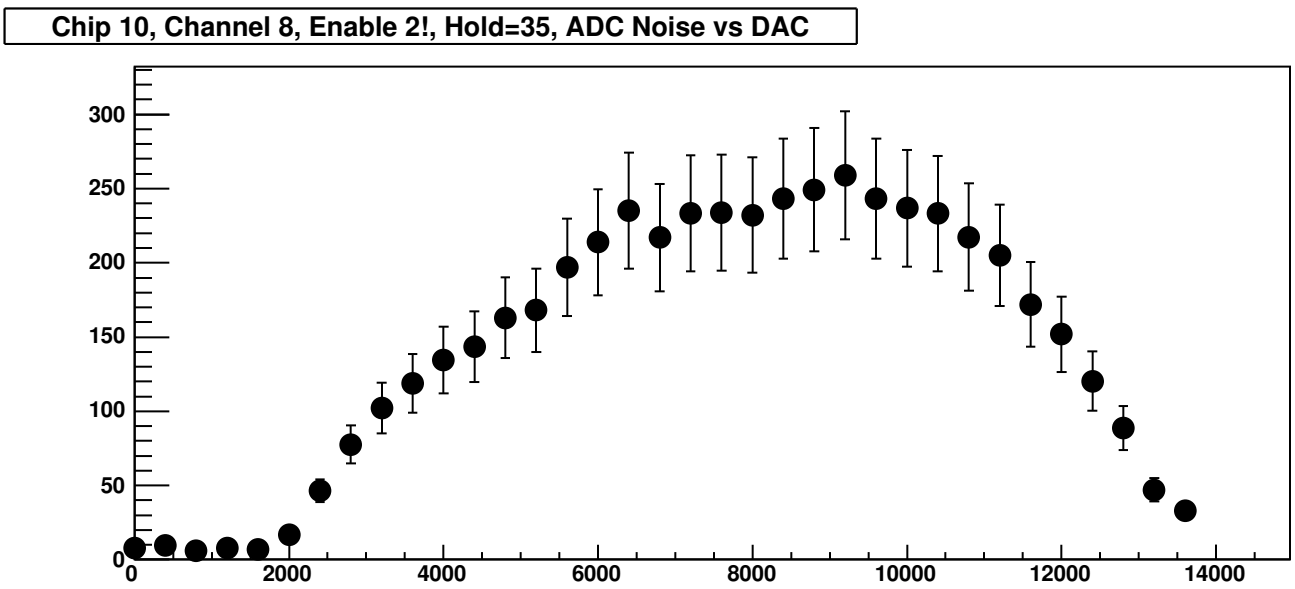

Chip 10, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

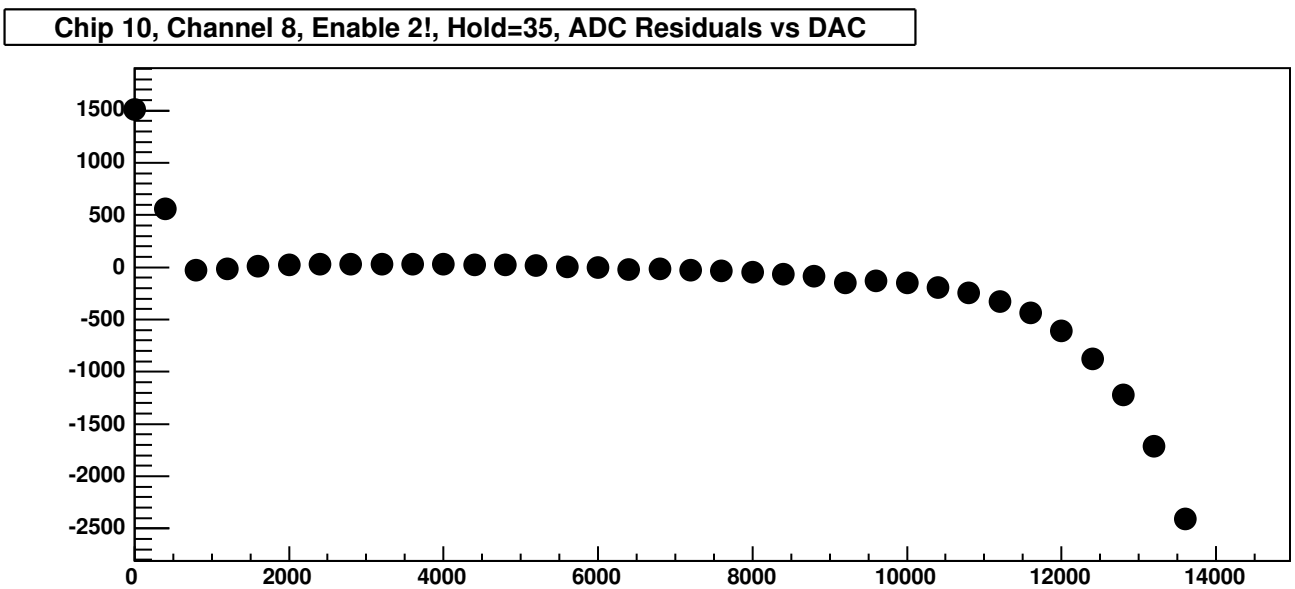

Chip 10, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 78.13 / 23
p0 15.02 ± 0.7273
p1 0.0248 ± 0.0001098
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

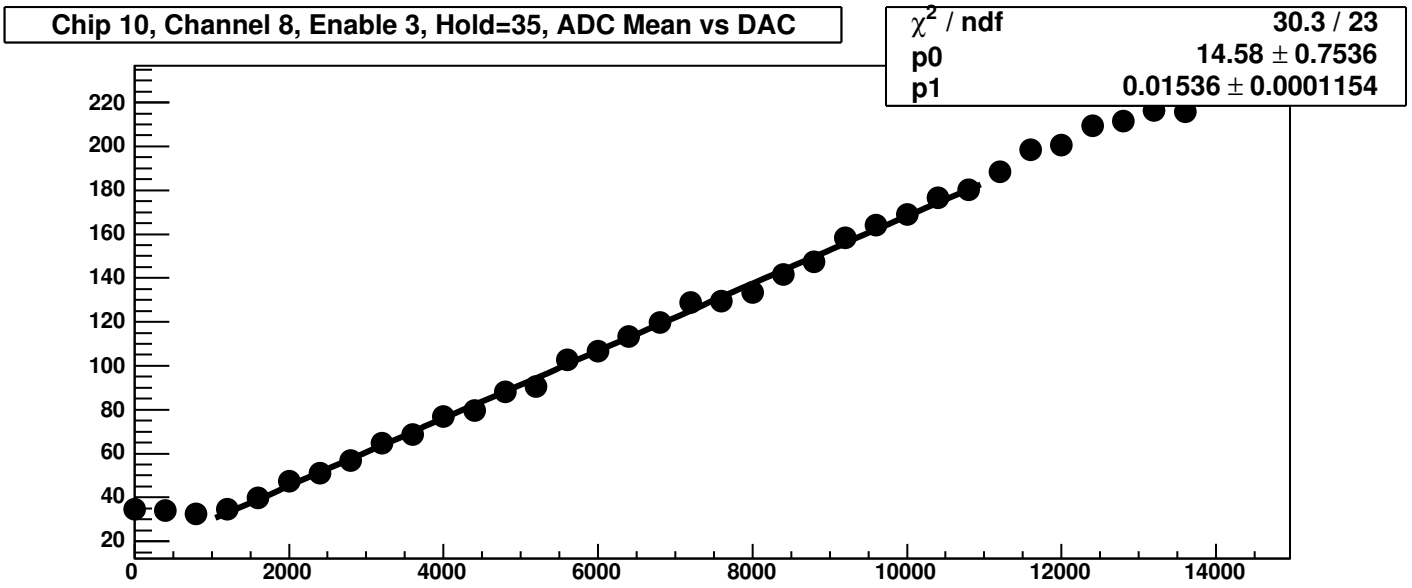

Chip 10, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

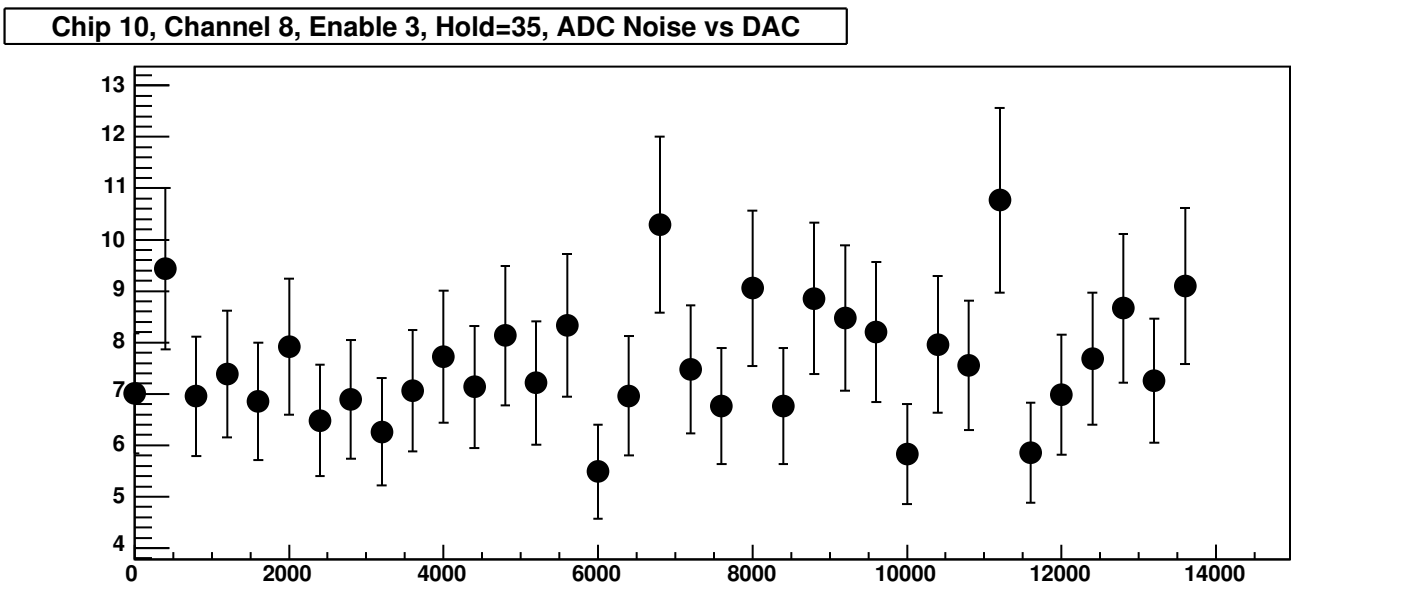

Chip 10, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

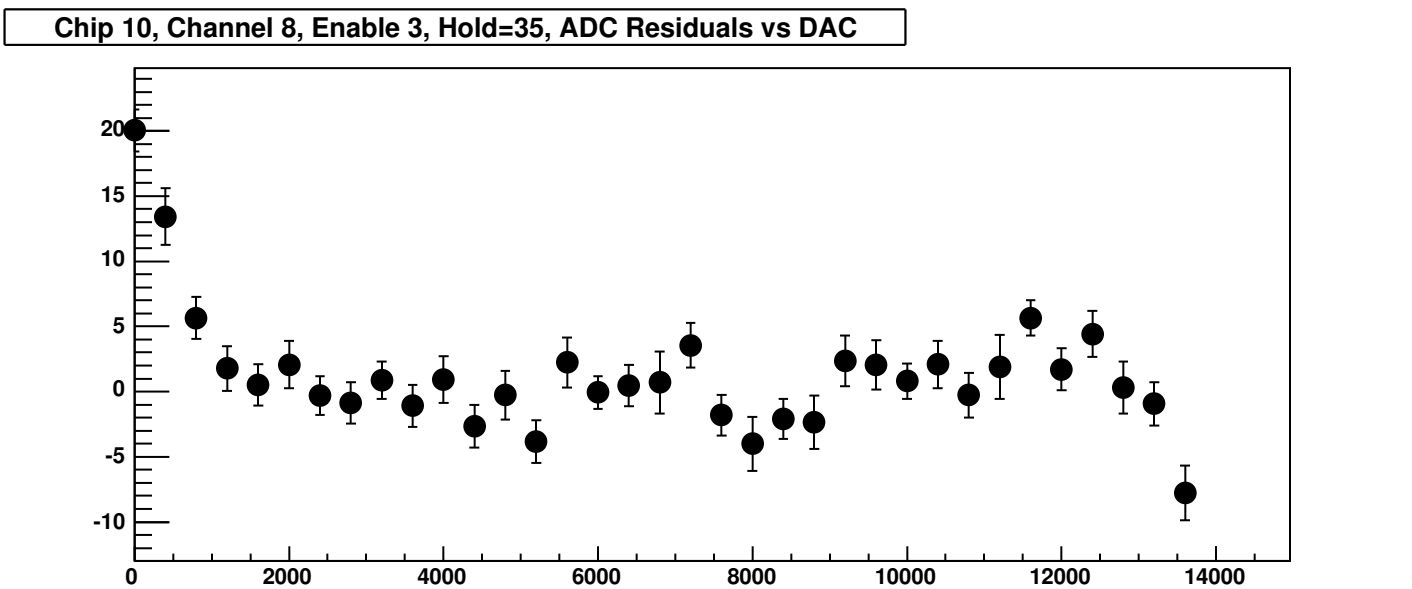

Chip 10, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 213.8 / 23
p0 89.16 ± 2.934
p1 2.414 ± 0.001738
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

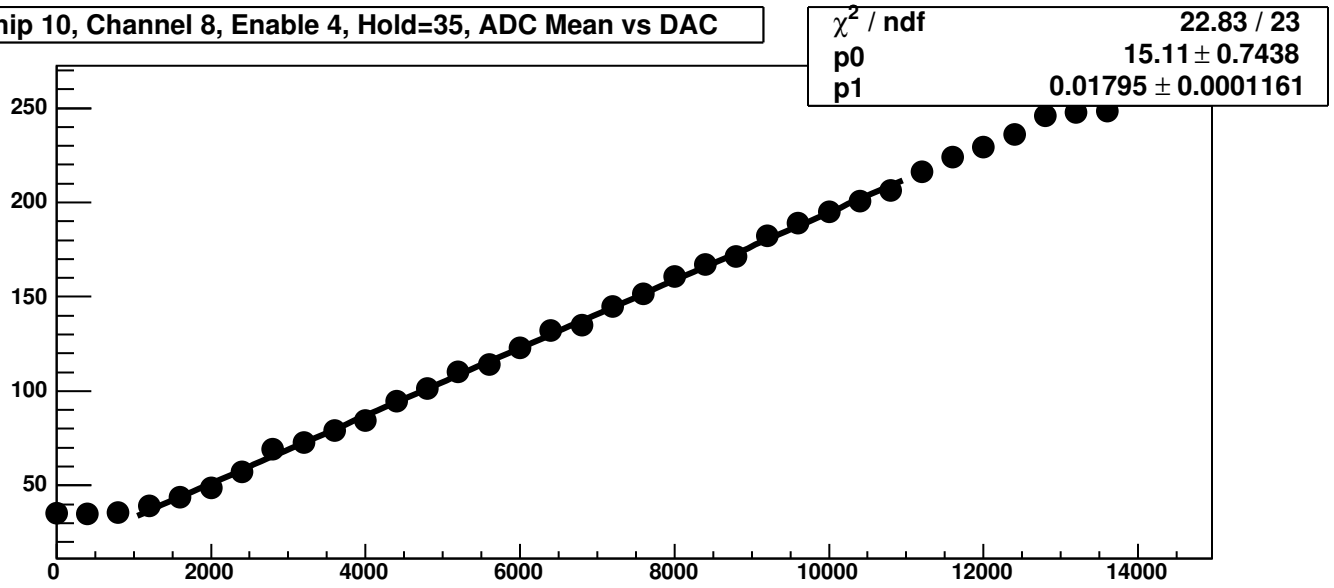

Chip 10, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 30.3 / 23
p0 14.58 ± 0.7536
p1 0.01536 ± 0.0001154
220
200
180
160
140
120
100
80
60
40
20
0
2000
4000
6000
8000
10000
12000
14000

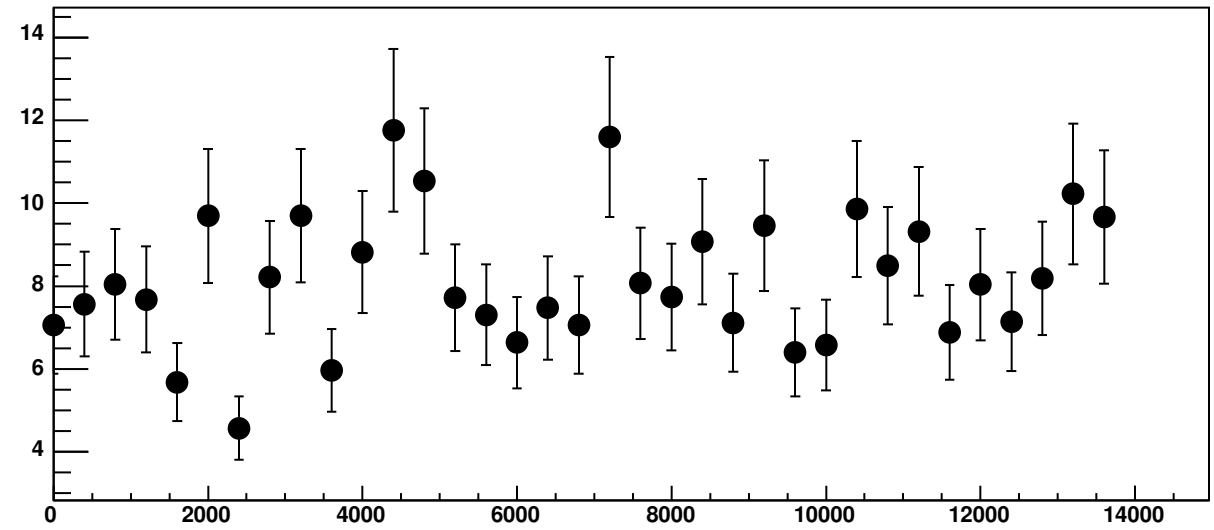

Chip 10, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

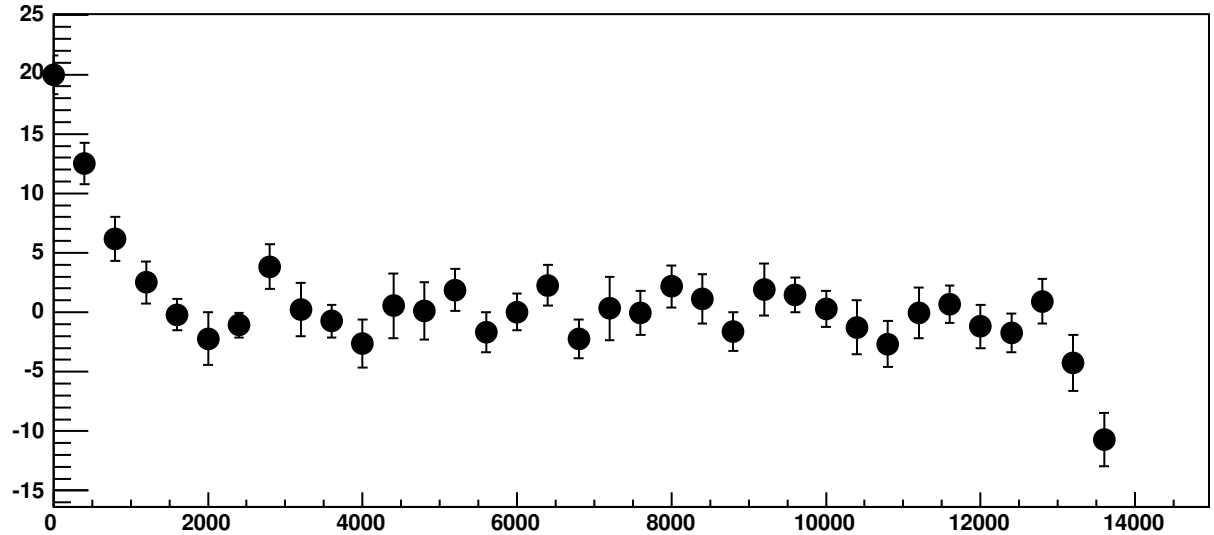
Chip 10, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

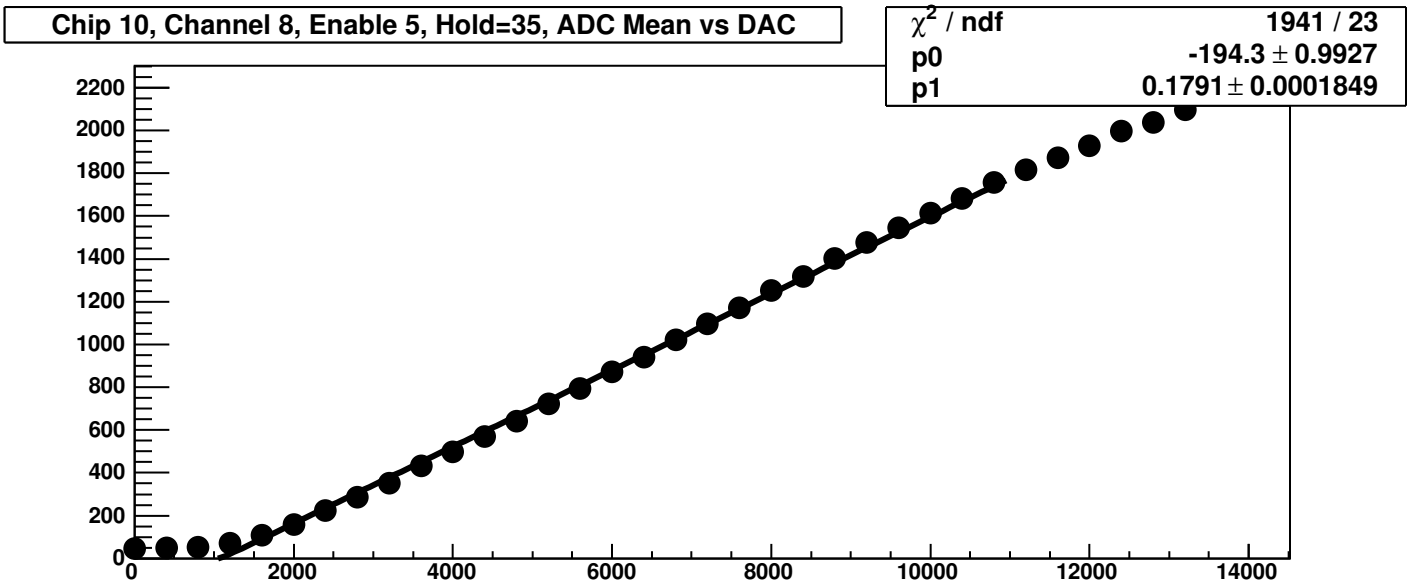


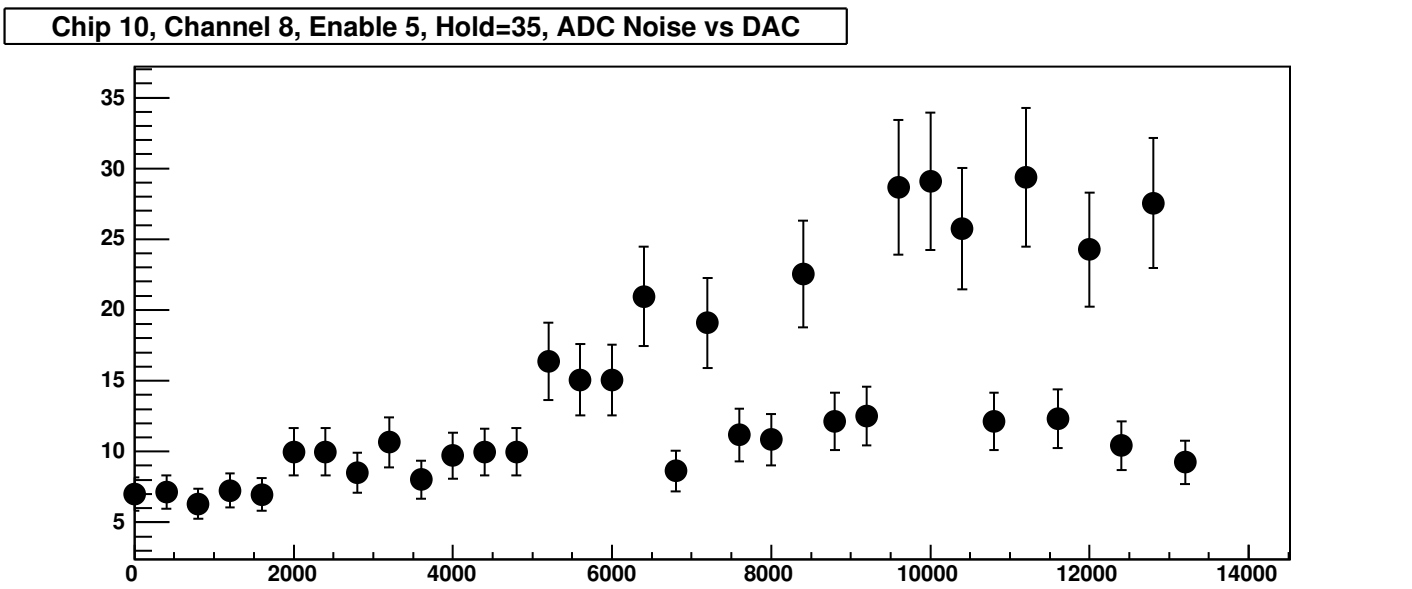
Chip 10, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC

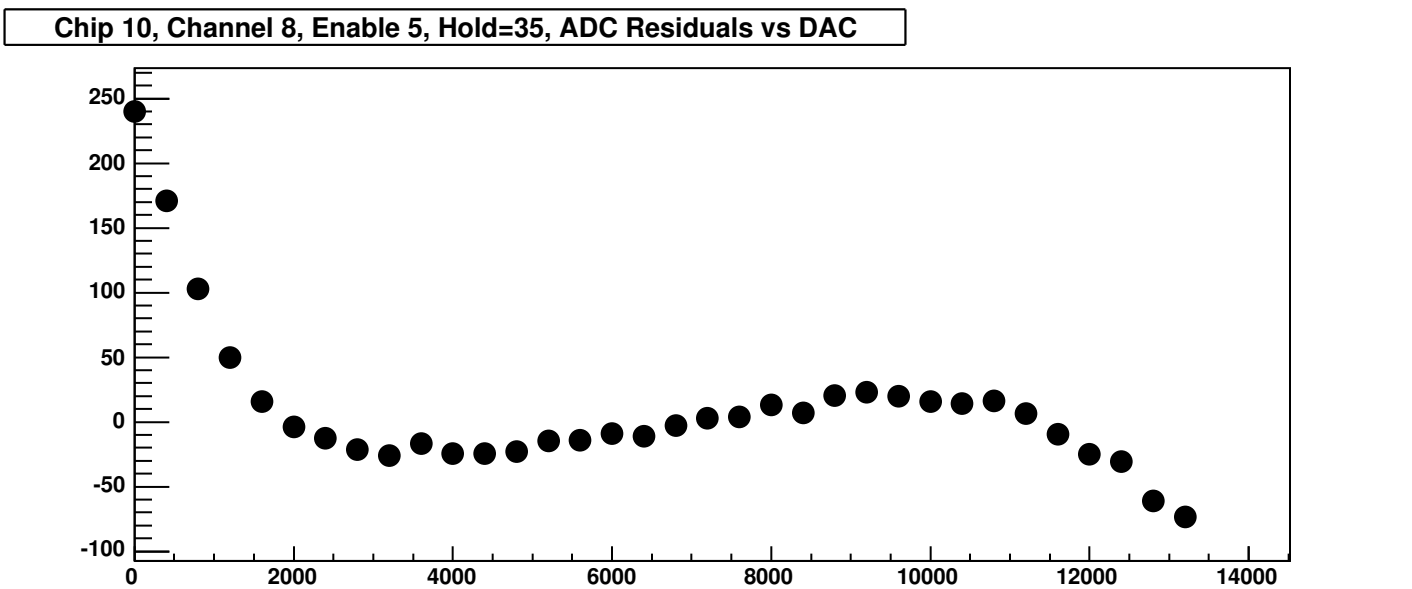


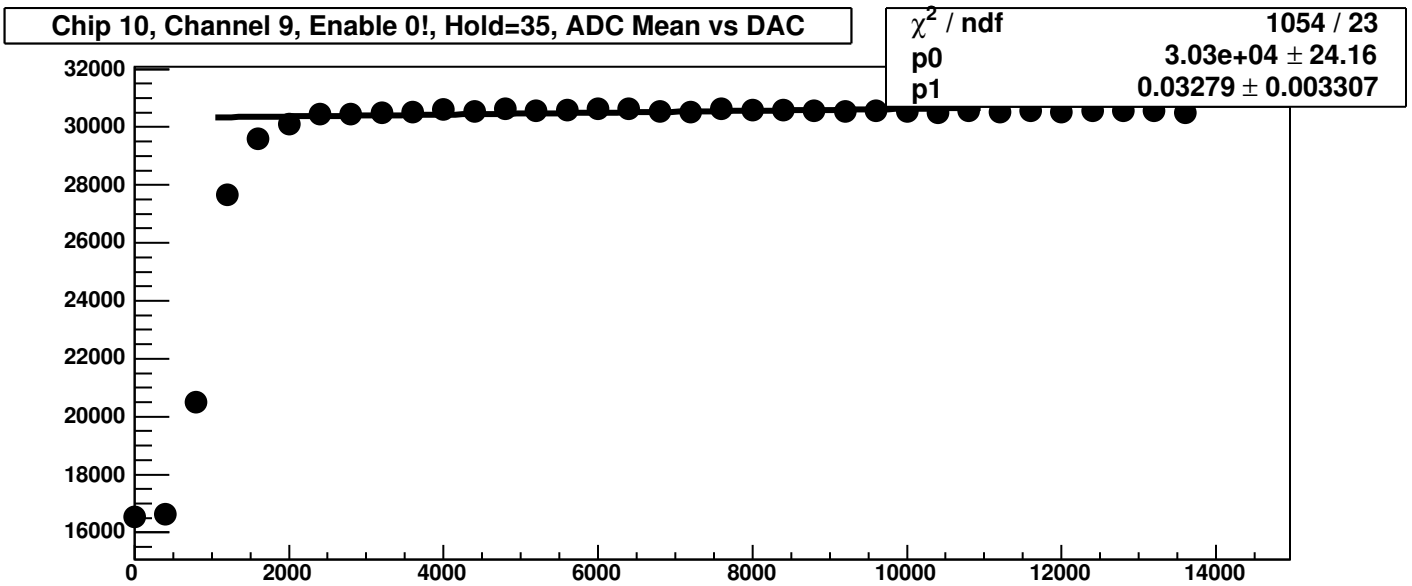
Chip 10, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

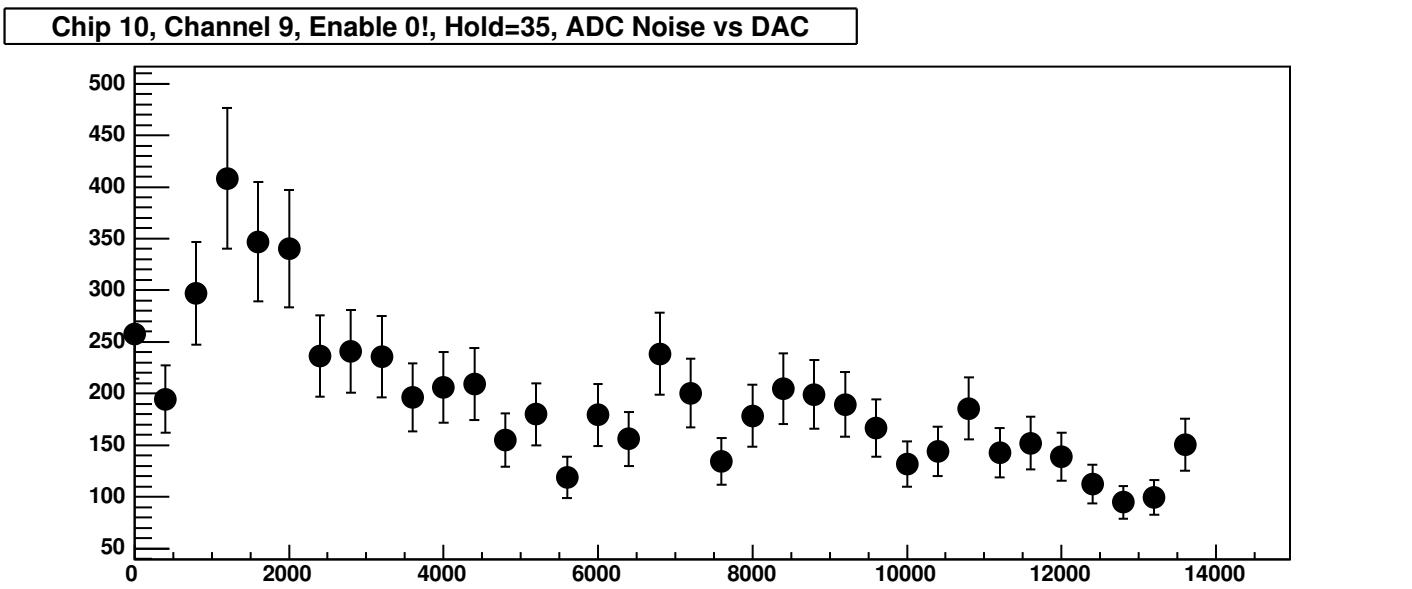


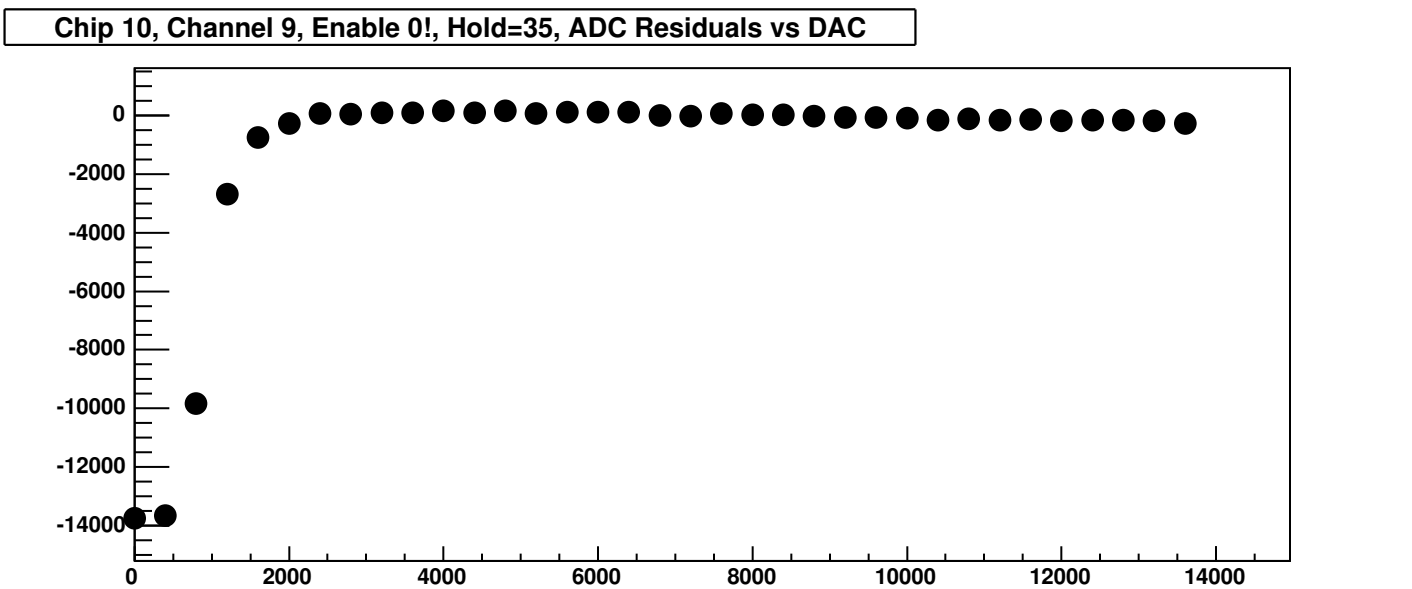

Chip 10, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1941 / 23
p0 -194.3 ± 0.9927
p1 0.1791 ± 0.0001849
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

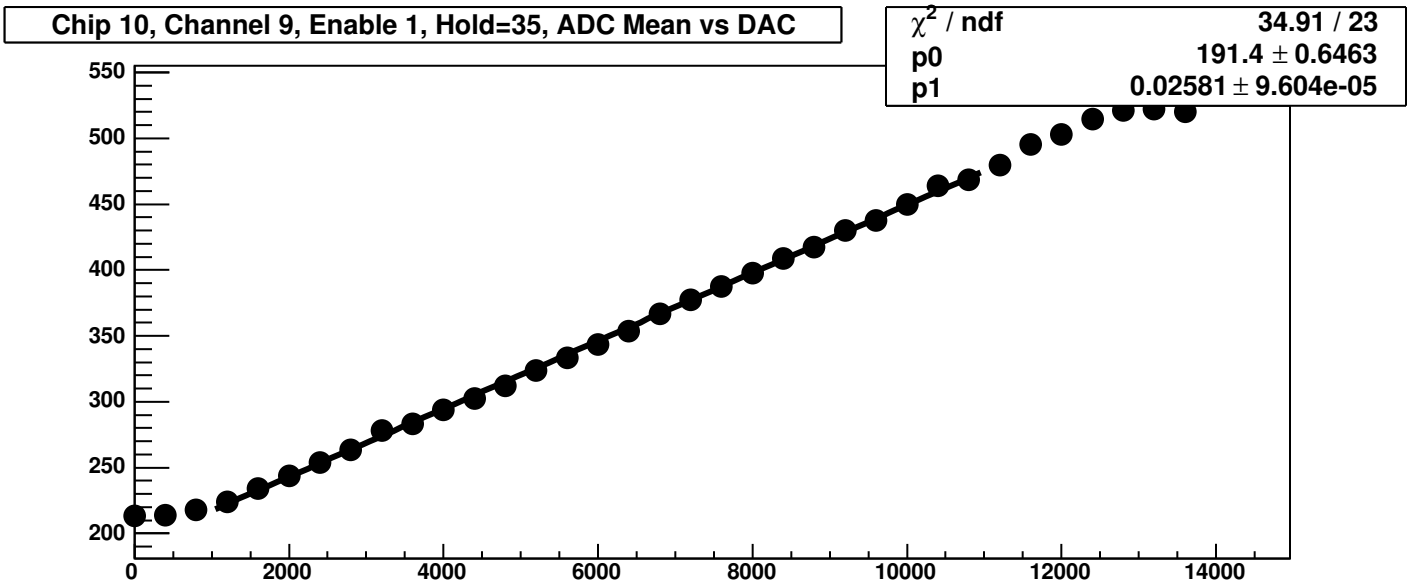

Chip 10, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

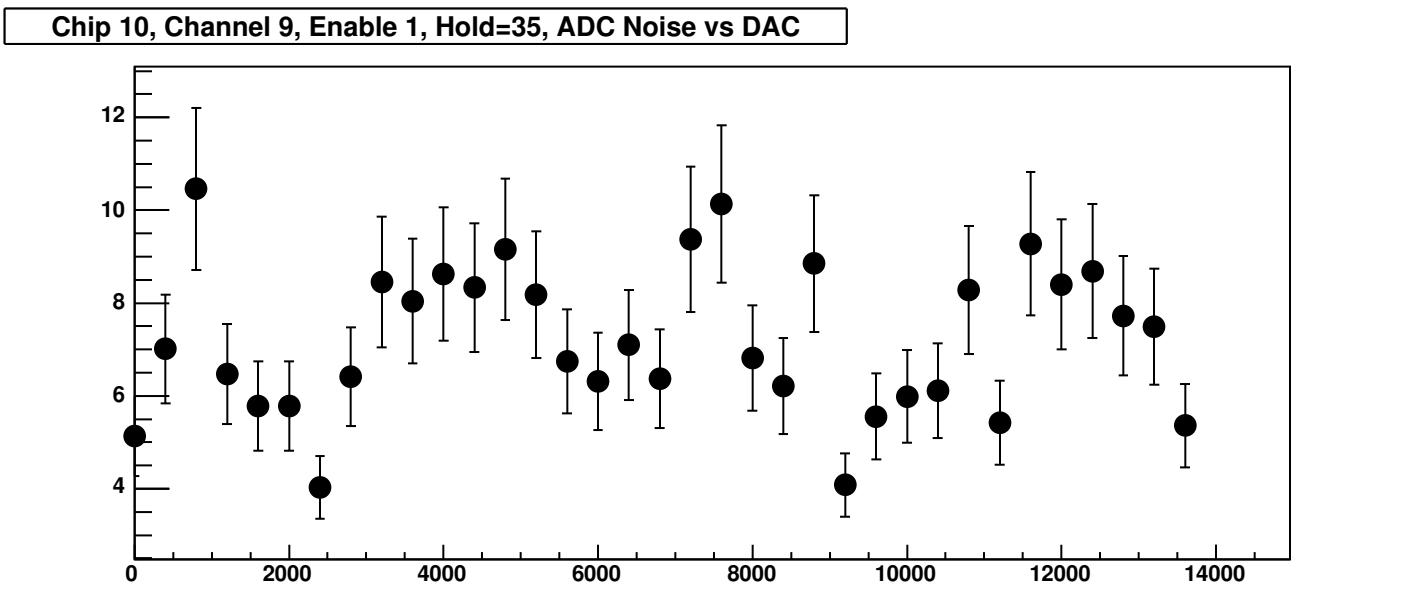

Chip 10, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

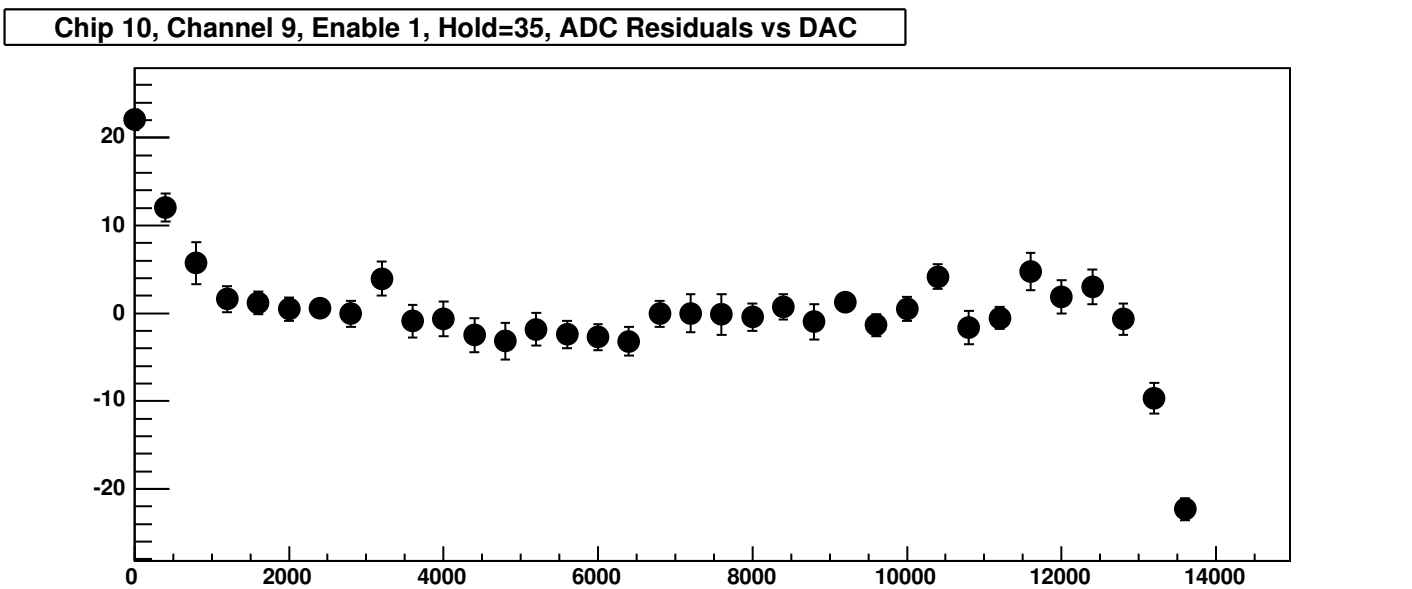

Chip 10, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1054 / 23
p0 3.03e+04 ± 24.16
p1 0.03279 ± 0.003307
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

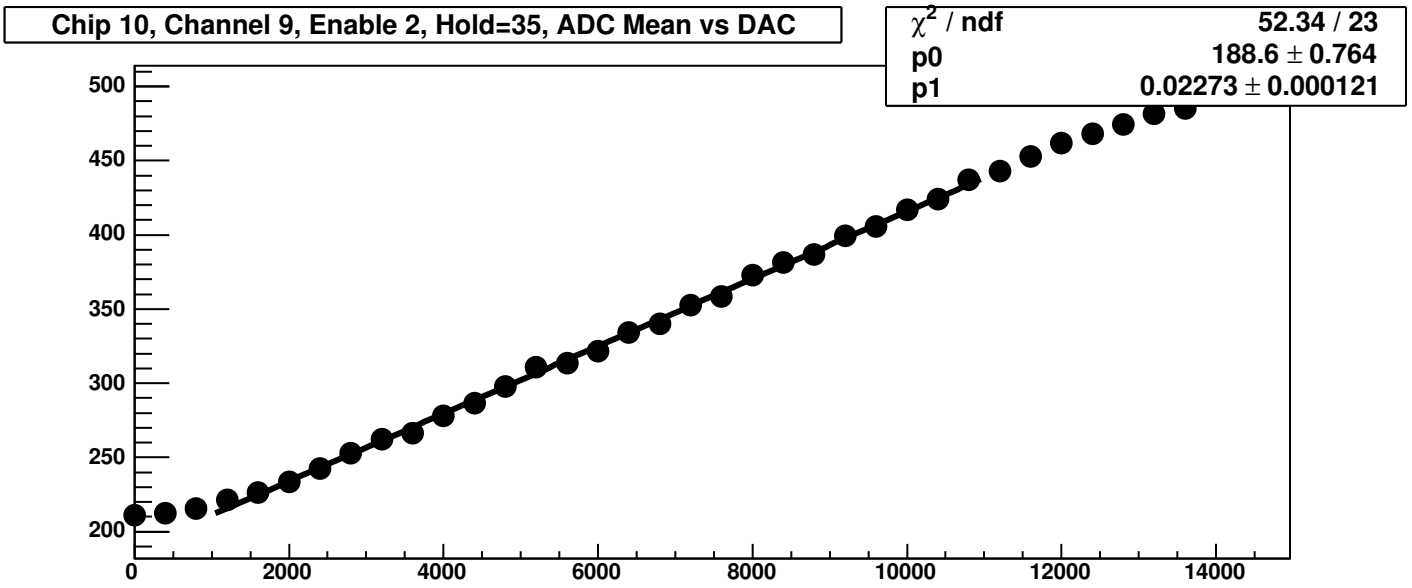

Chip 10, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
500
450
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

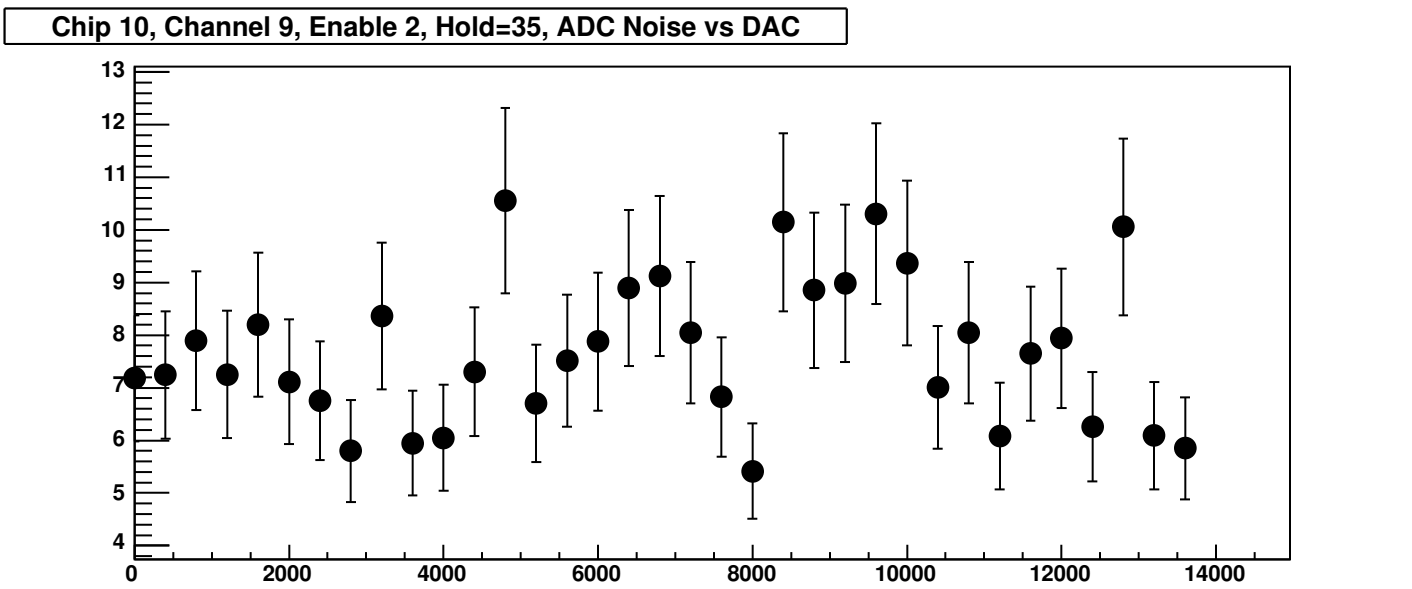

Chip 10, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

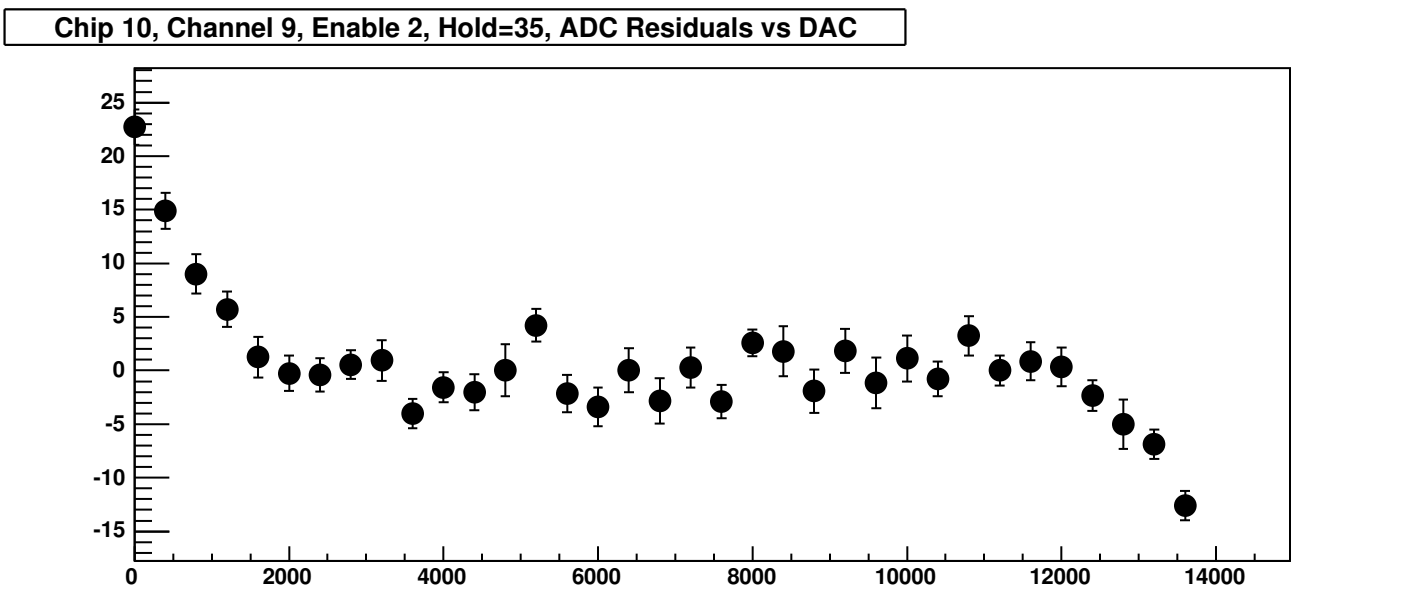

Chip 10, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 34.91 / 23
p0 191.4 ± 0.6463
p1 0.02581 ± 9.604e-05
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

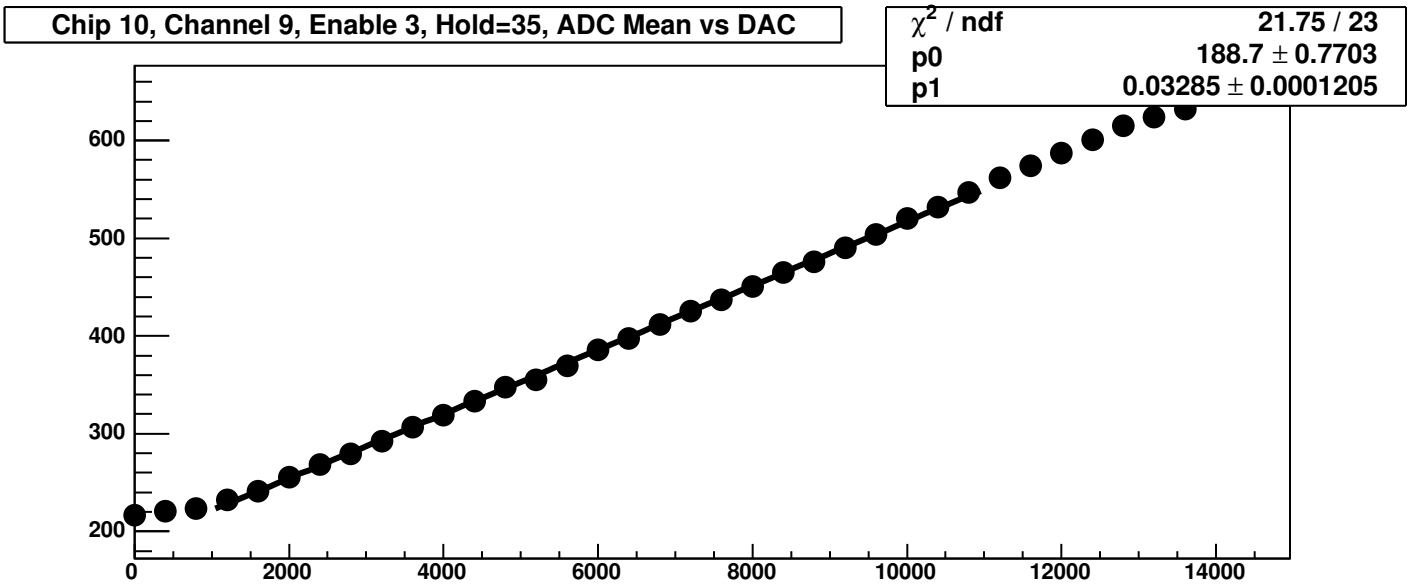

Chip 10, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

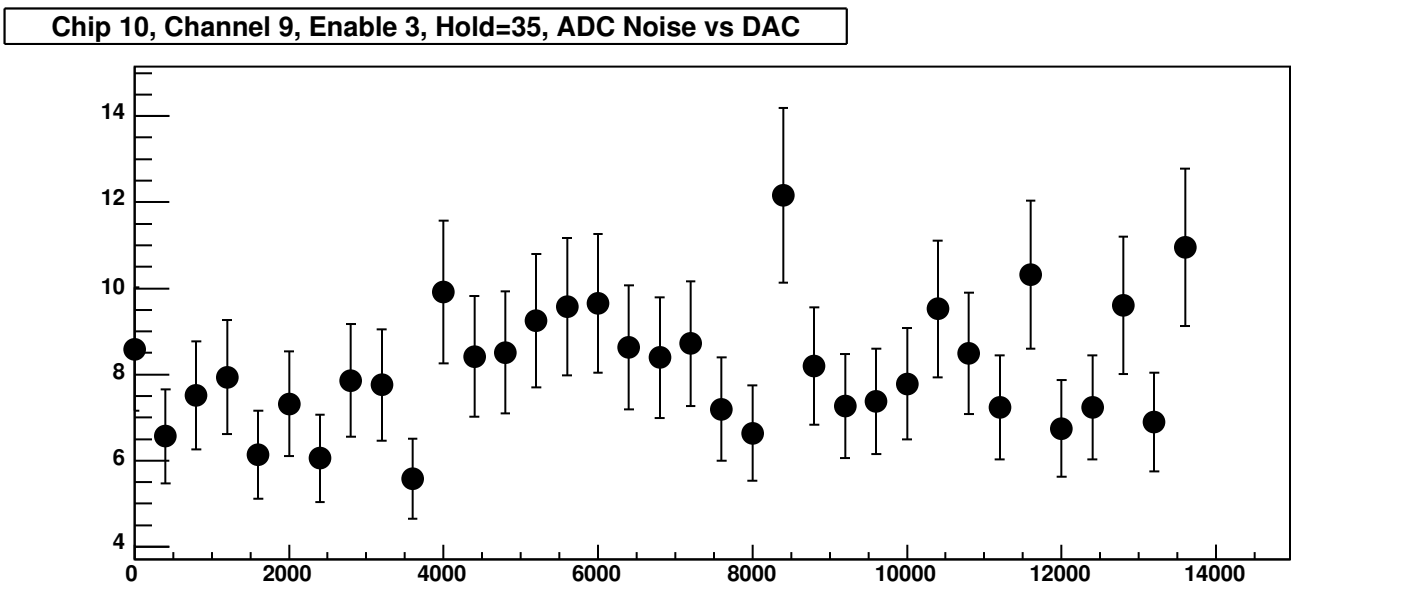

Chip 10, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

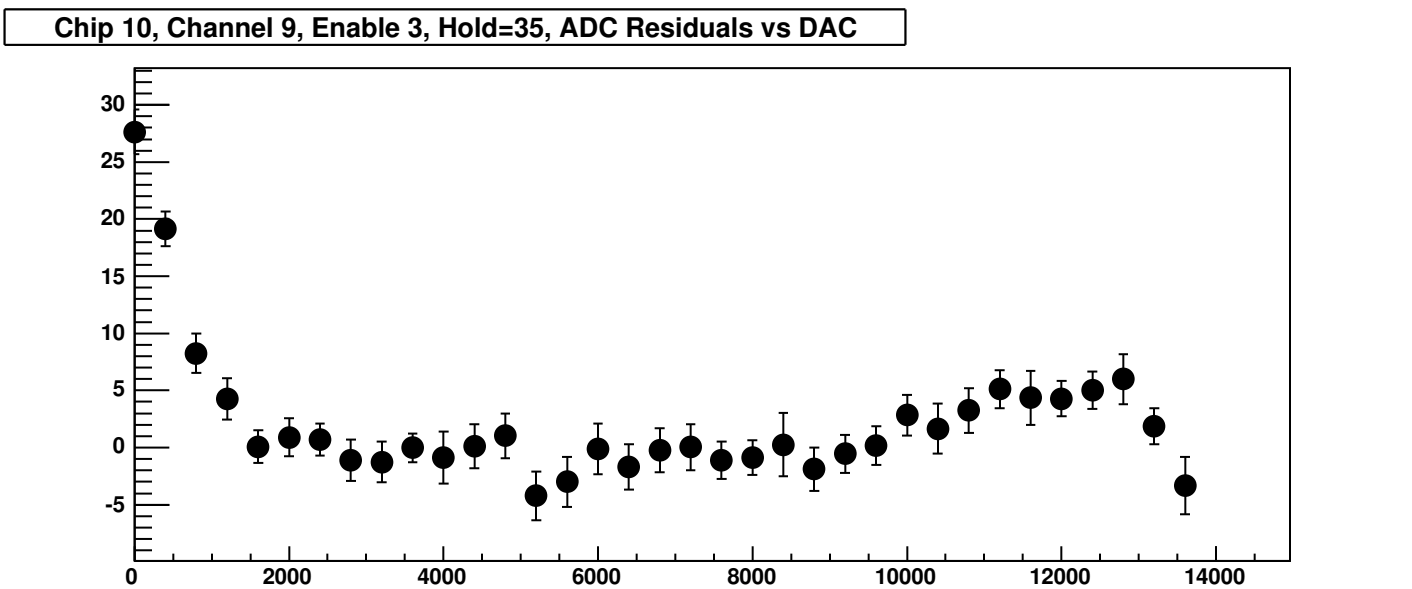

Chip 10, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 52.34 / 23
p0 188.6 ± 0.764
p1 0.02273 ± 0.000121
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

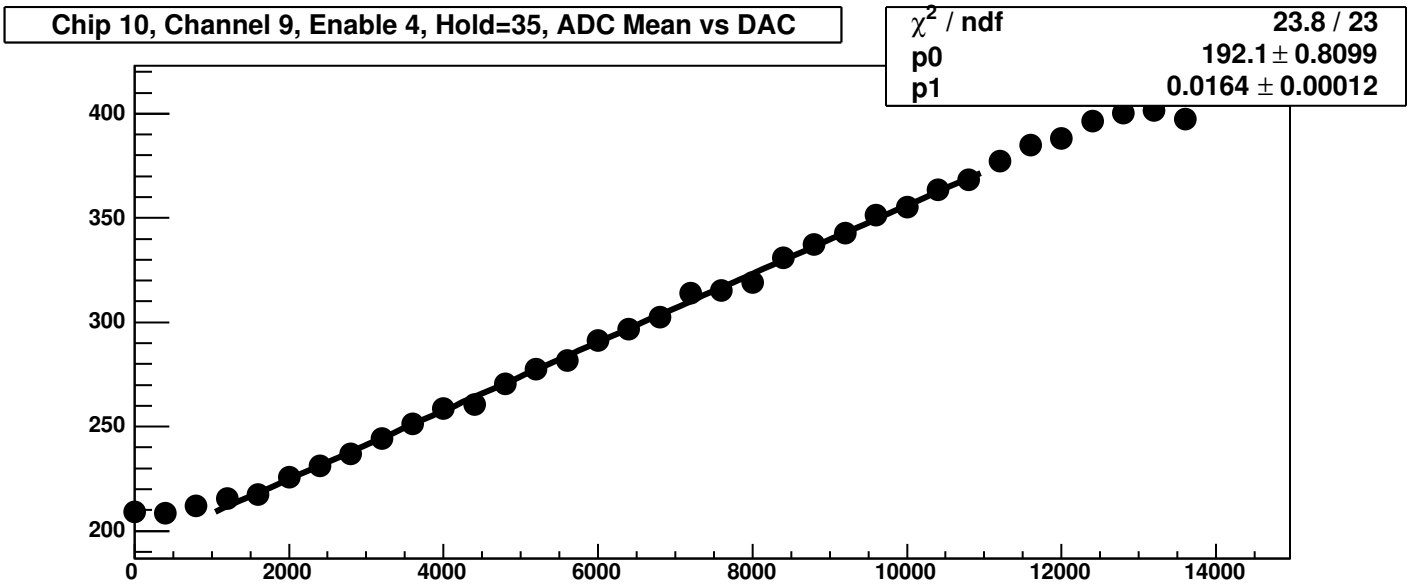

Chip 10, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

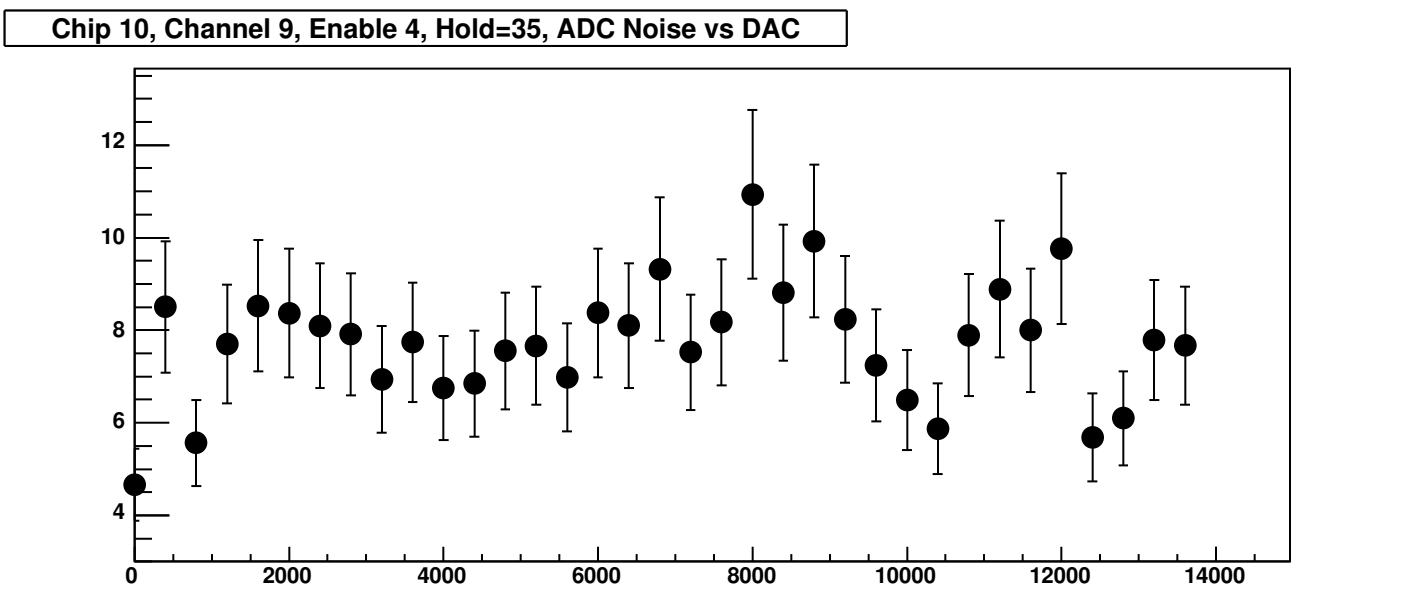

Chip 10, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

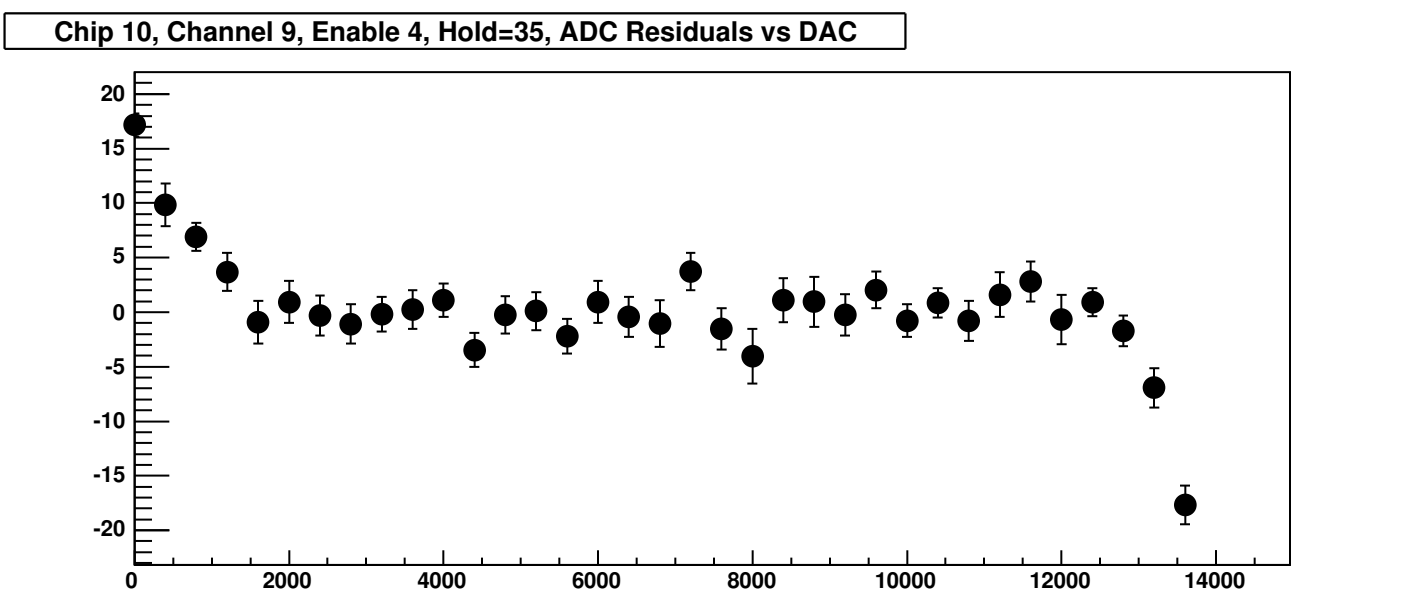

Chip 10, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.75 / 23
p0 188.7 ± 0.7703
p1 0.03285 ± 0.0001205
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000

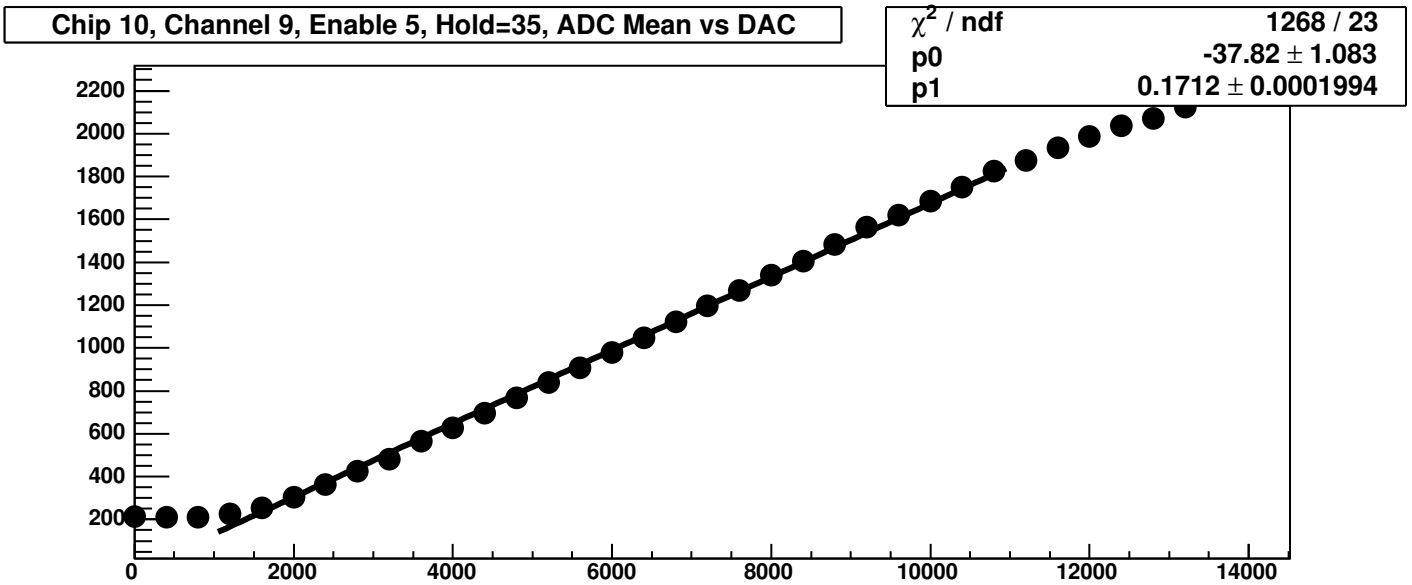

Chip 10, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

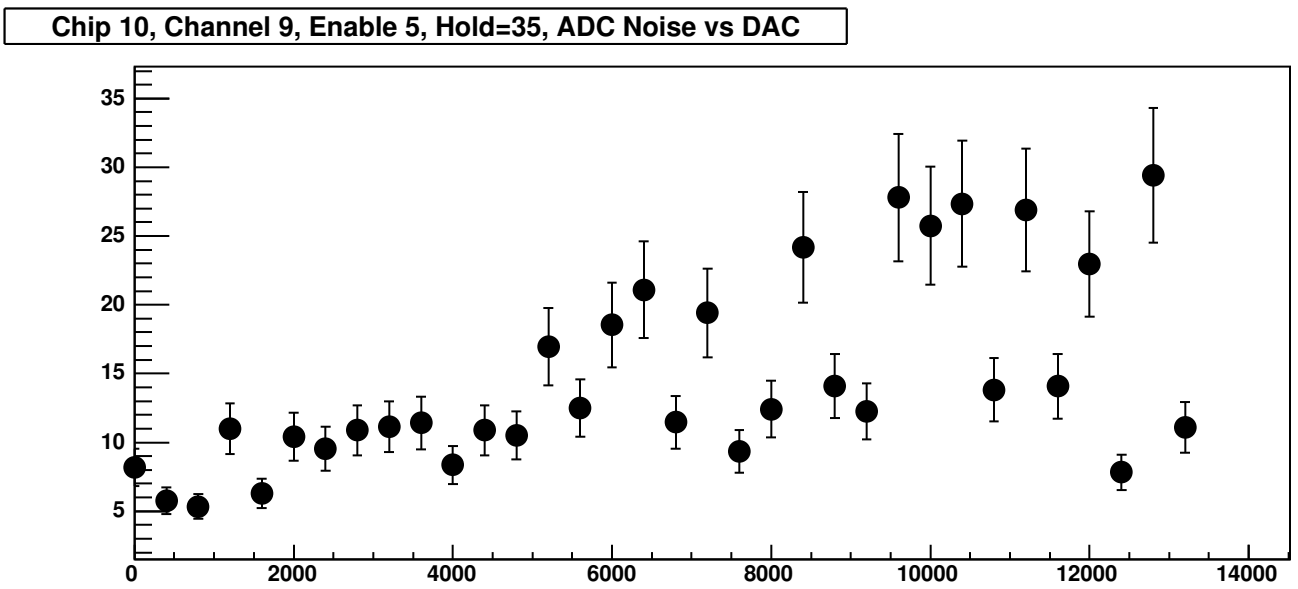

Chip 10, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

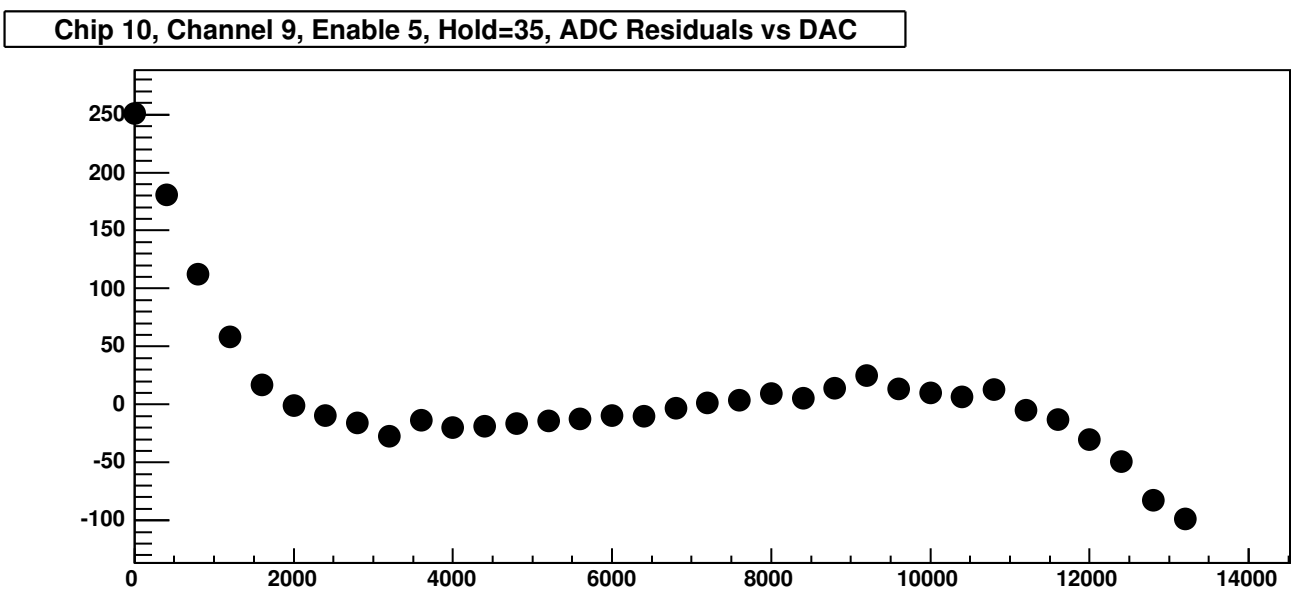

Chip 10, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 23.8 / 23
p0 192.1 ± 0.8099
p1 0.0164 ± 0.00012
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

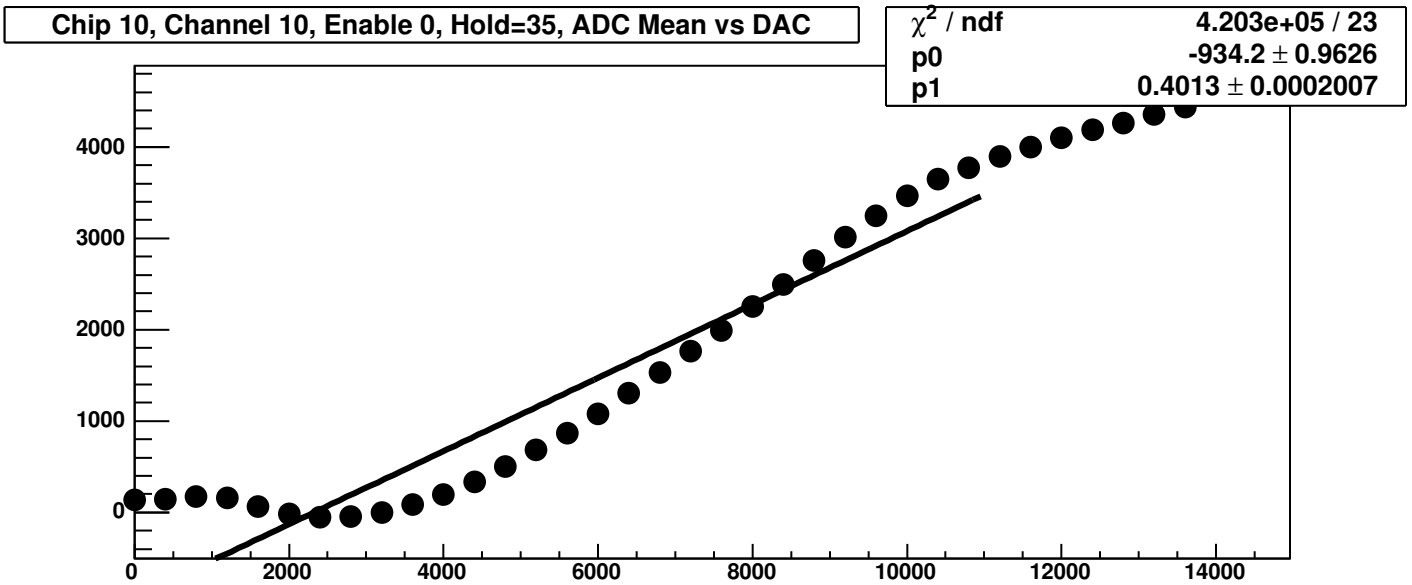

Chip 10, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

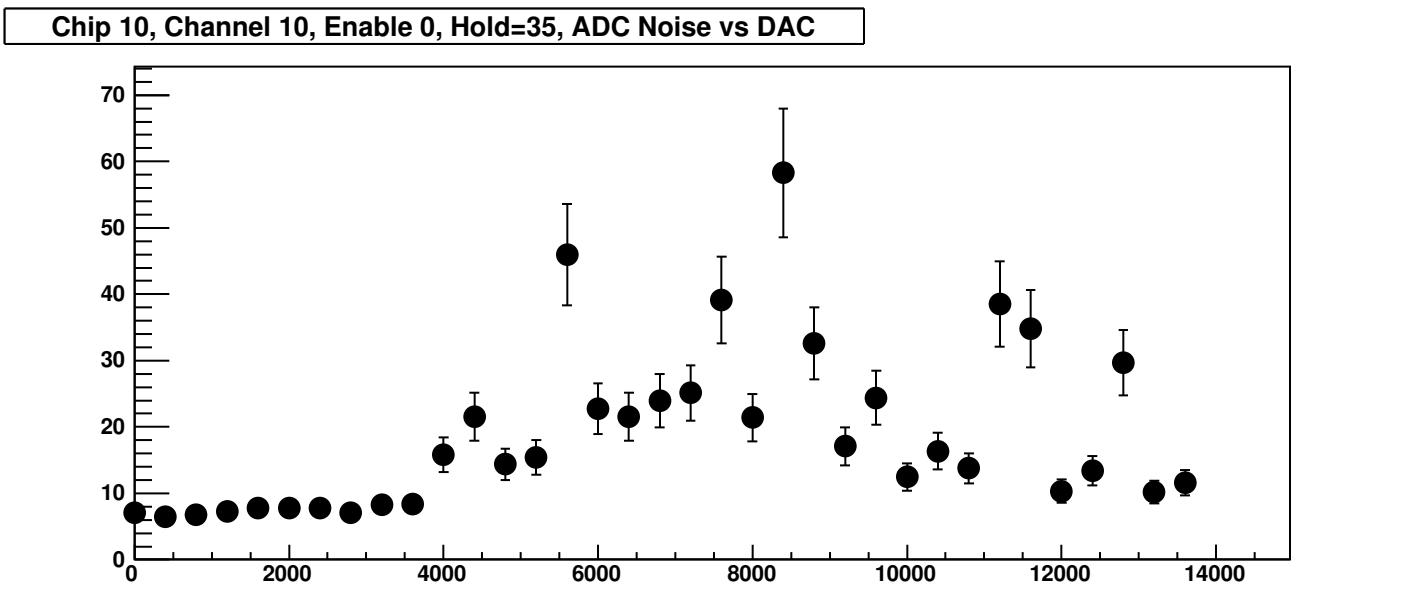

Chip 10, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

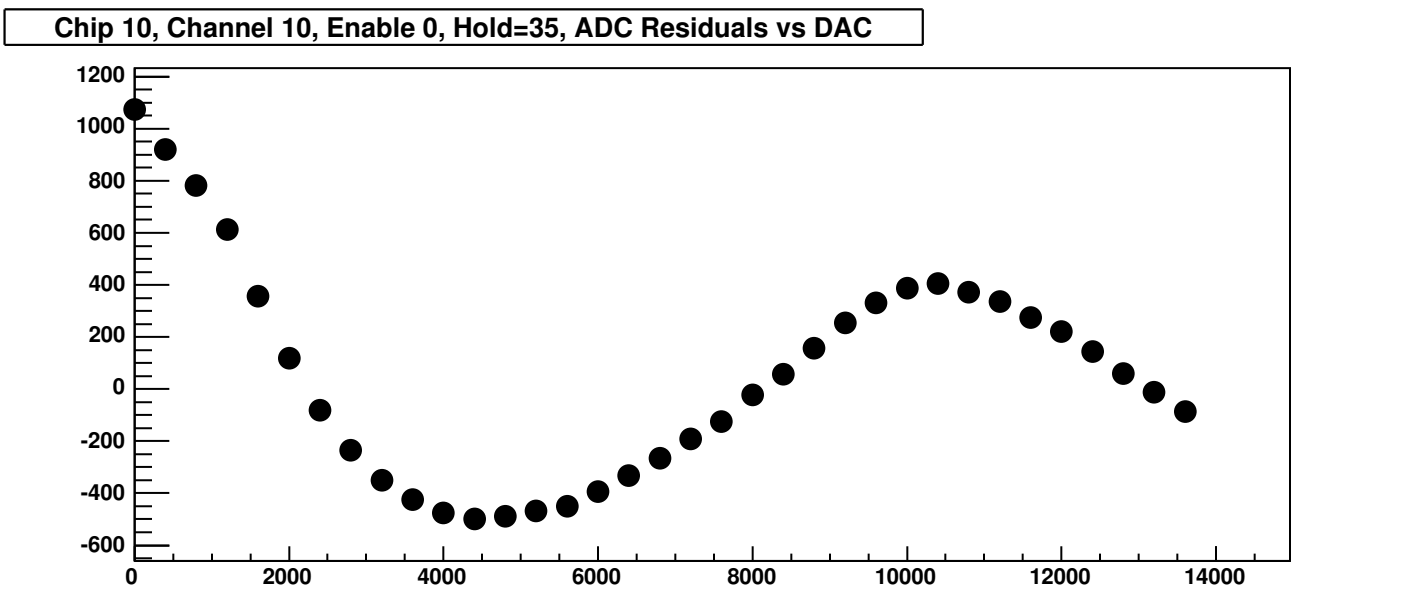

Chip 10, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1268 / 23
p0 -37.82 ± 1.083
p1 0.1712 ± 0.0001994
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

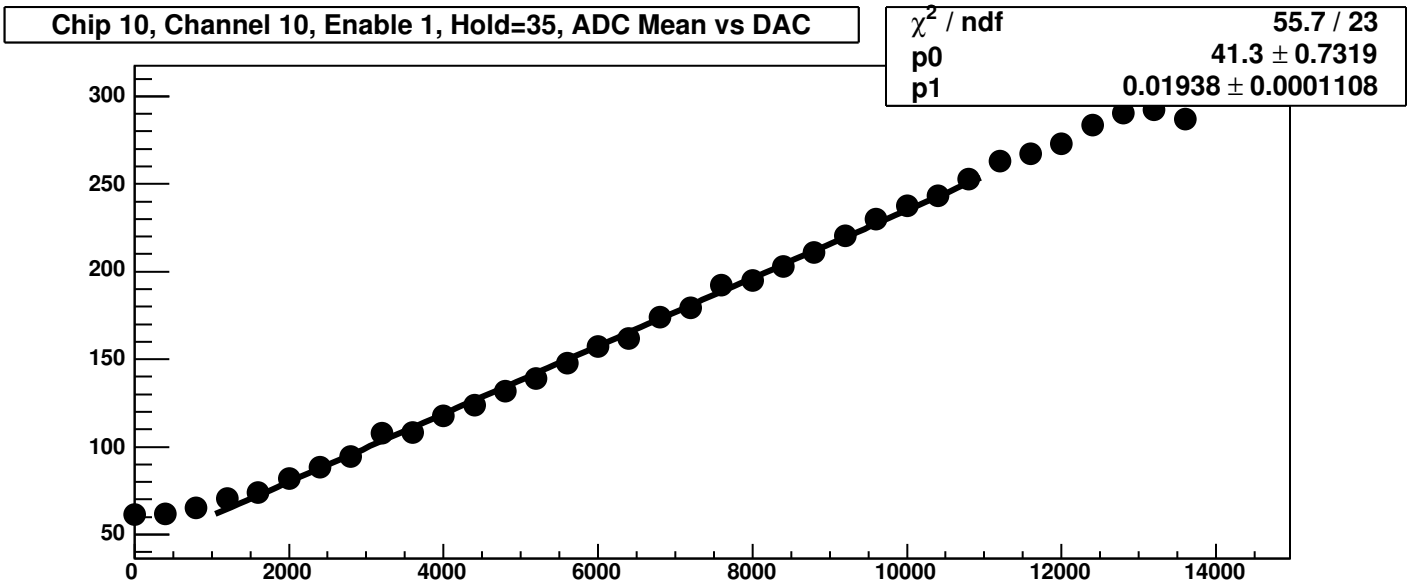

Chip 10, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

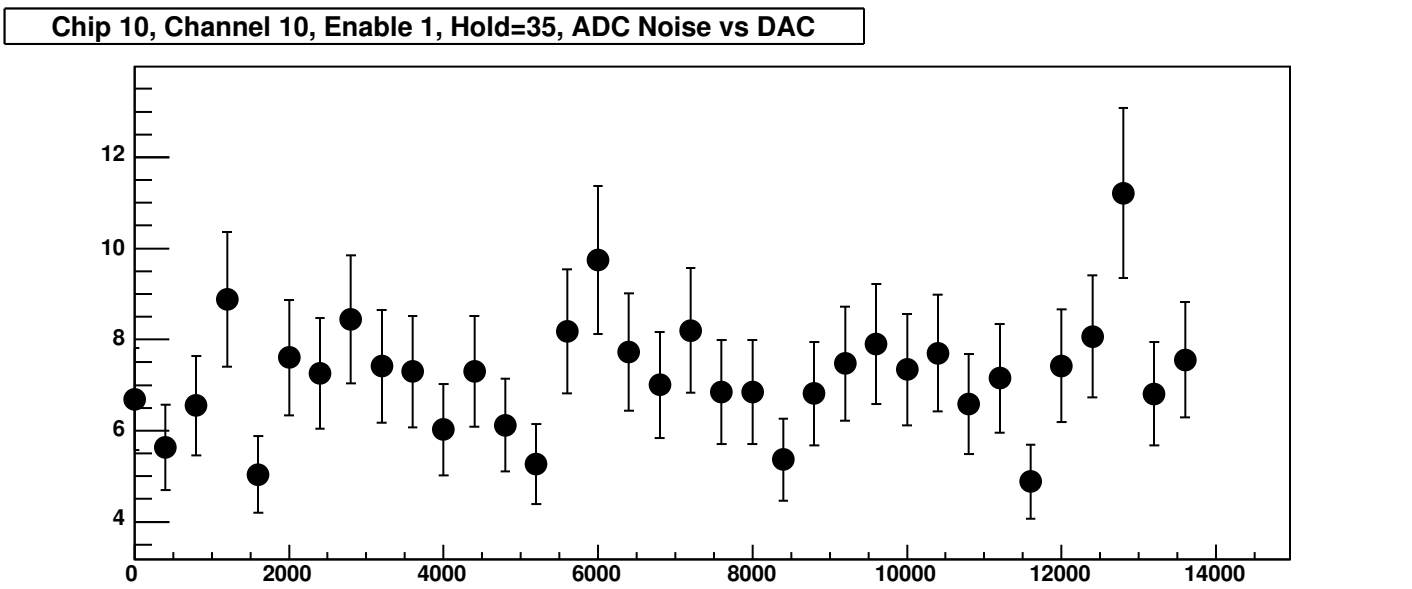

Chip 10, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

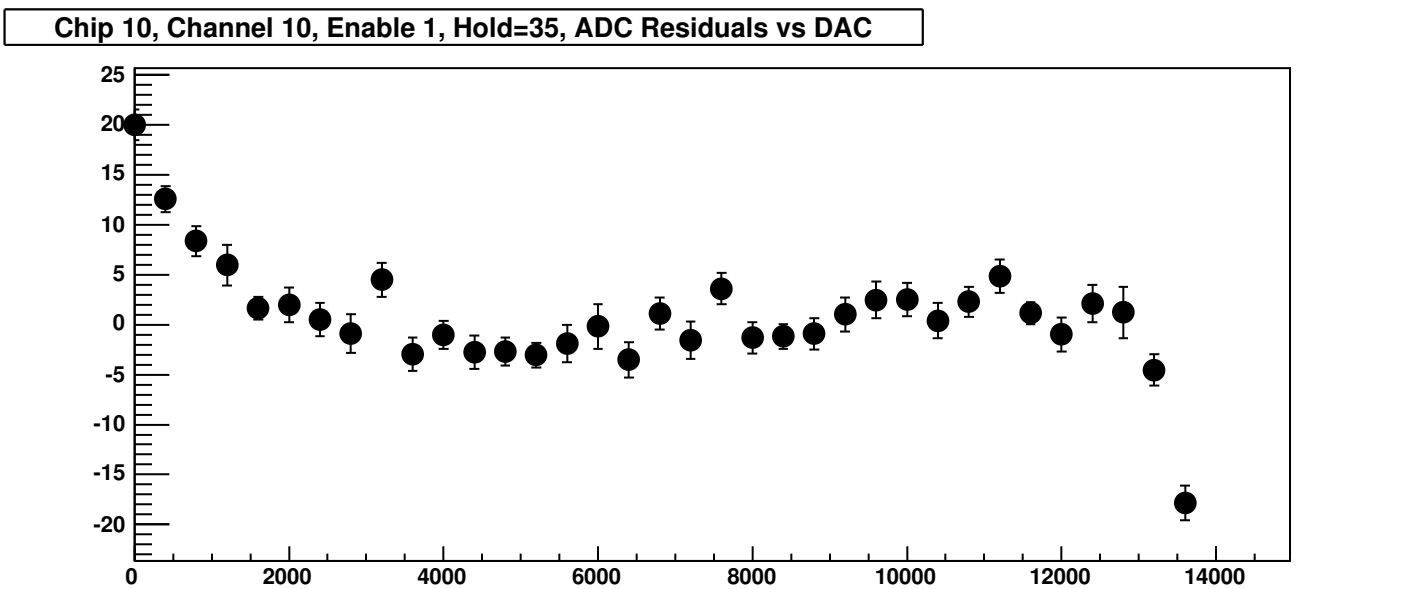

Chip 10, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.203e+05 / 23
p0 -934.2 ± 0.9626
p1 0.4013 ± 0.0002007
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

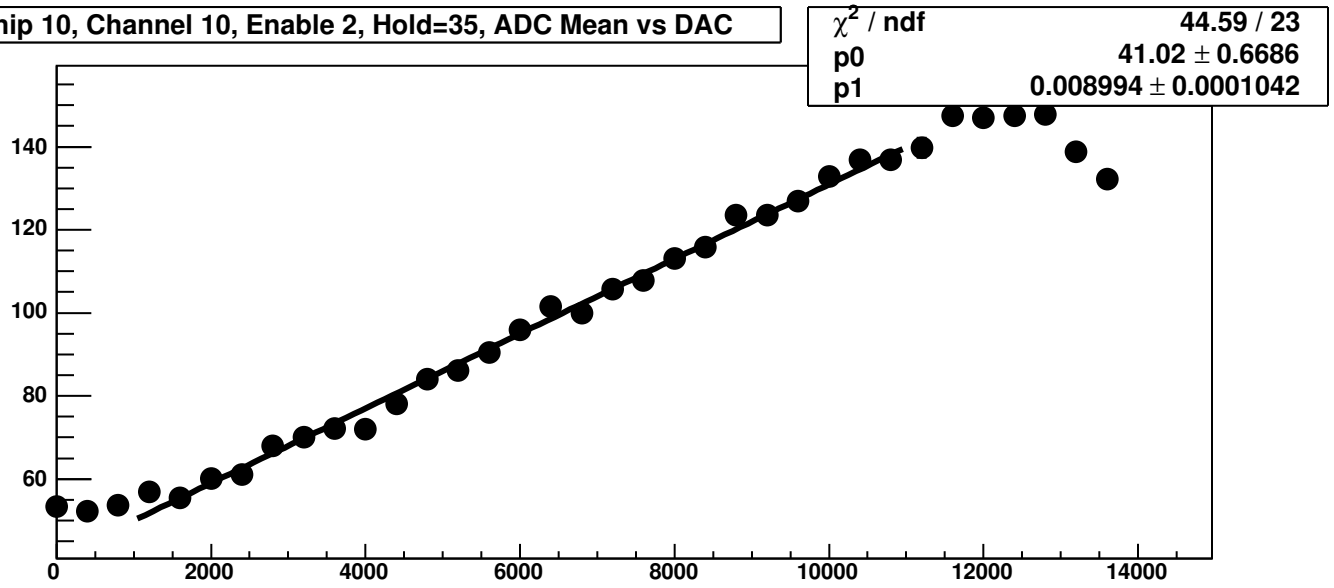

Chip 10, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 55.7 / 23
p0 41.3 ± 0.7319
p1 0.01938 ± 0.0001108
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

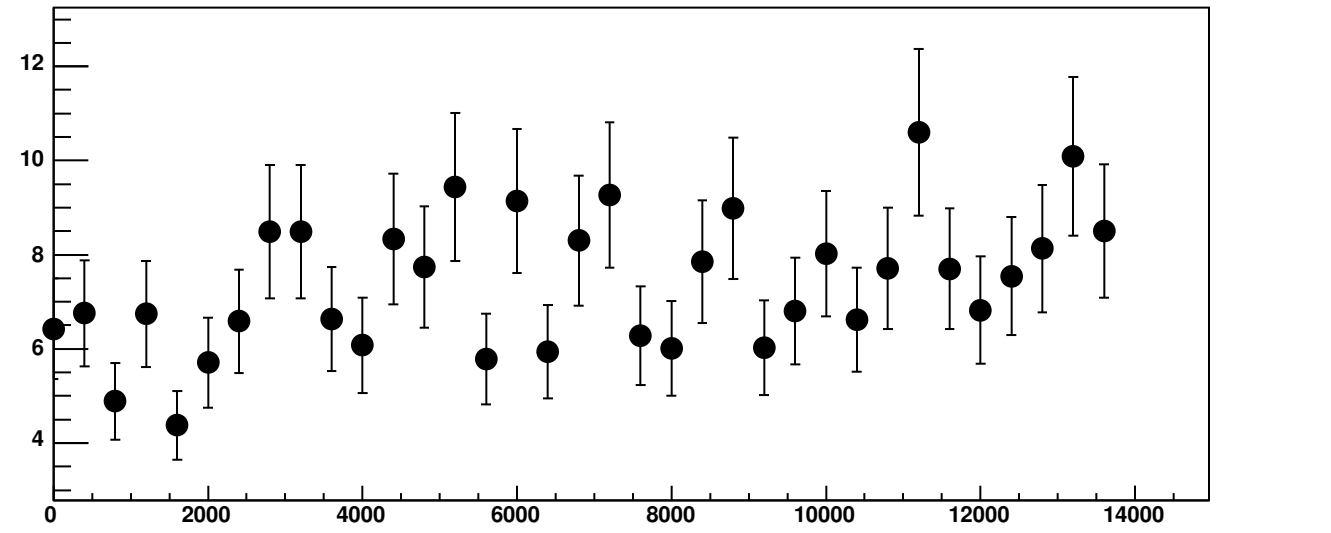

Chip 10, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

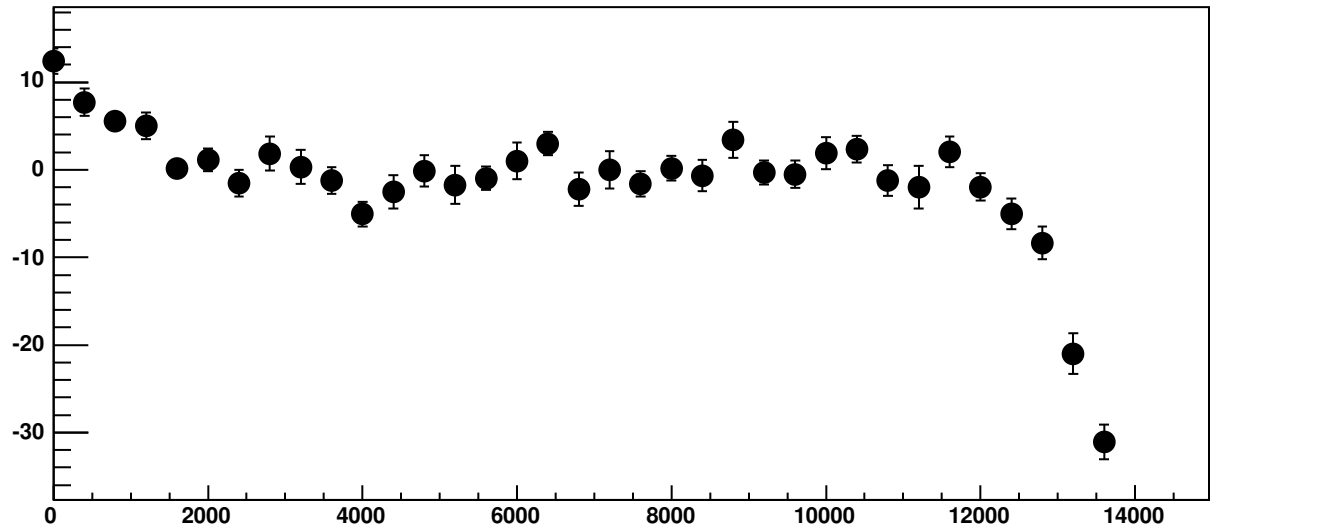
Chip 10, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

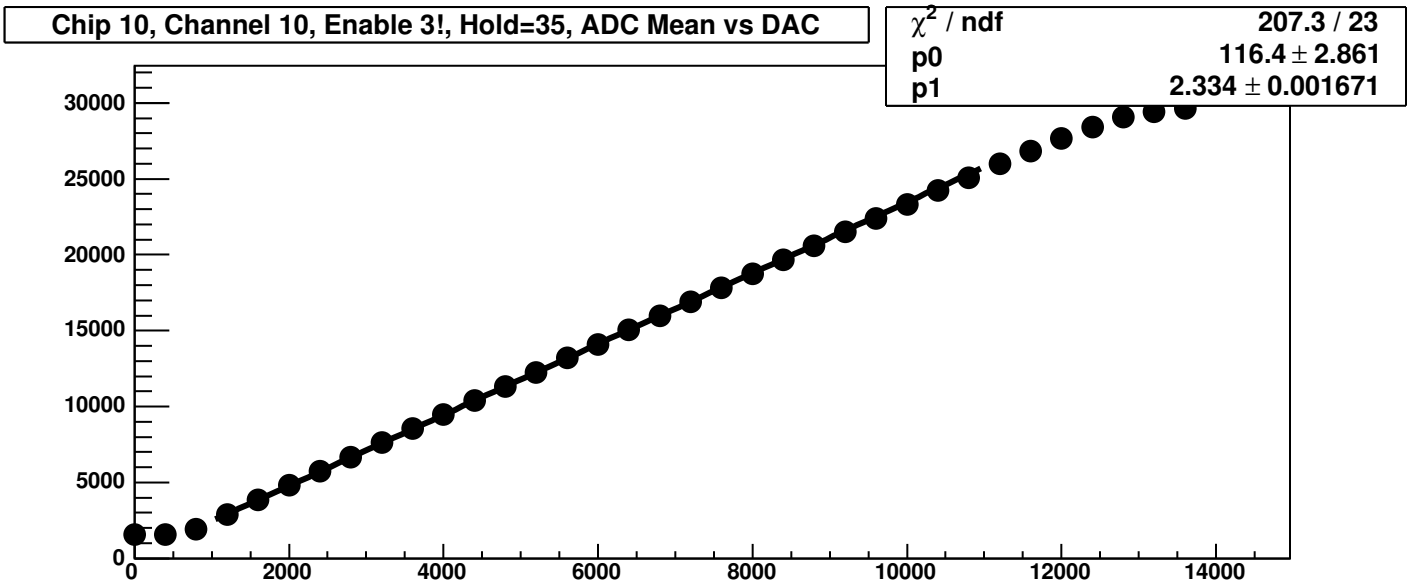


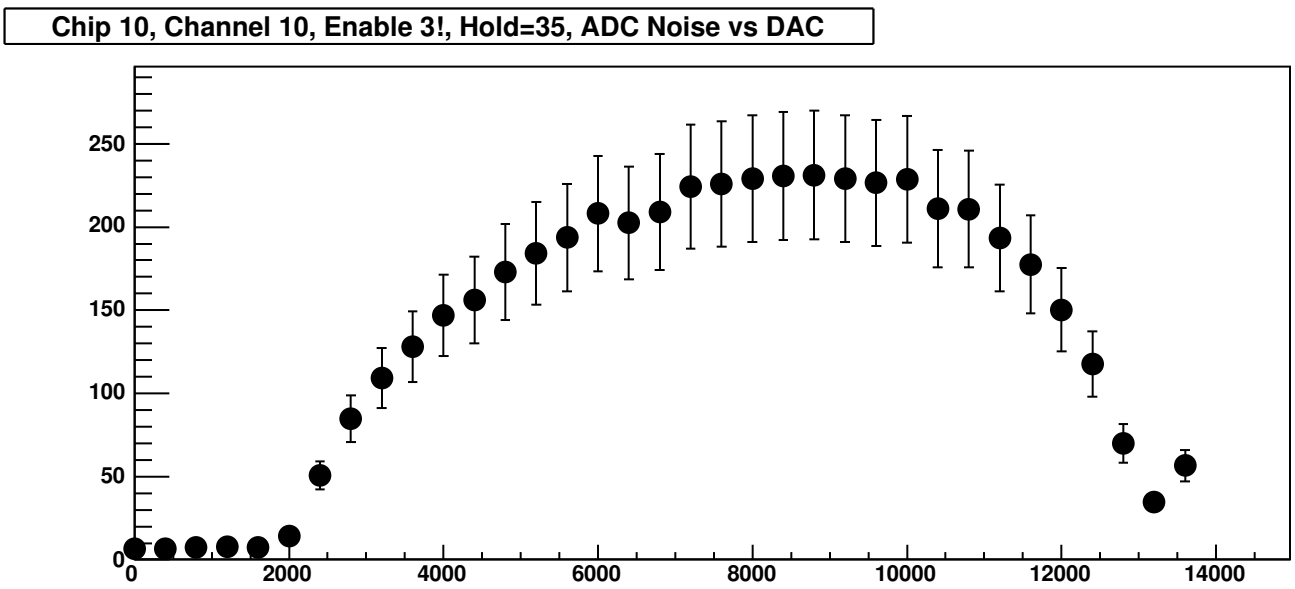
Chip 10, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

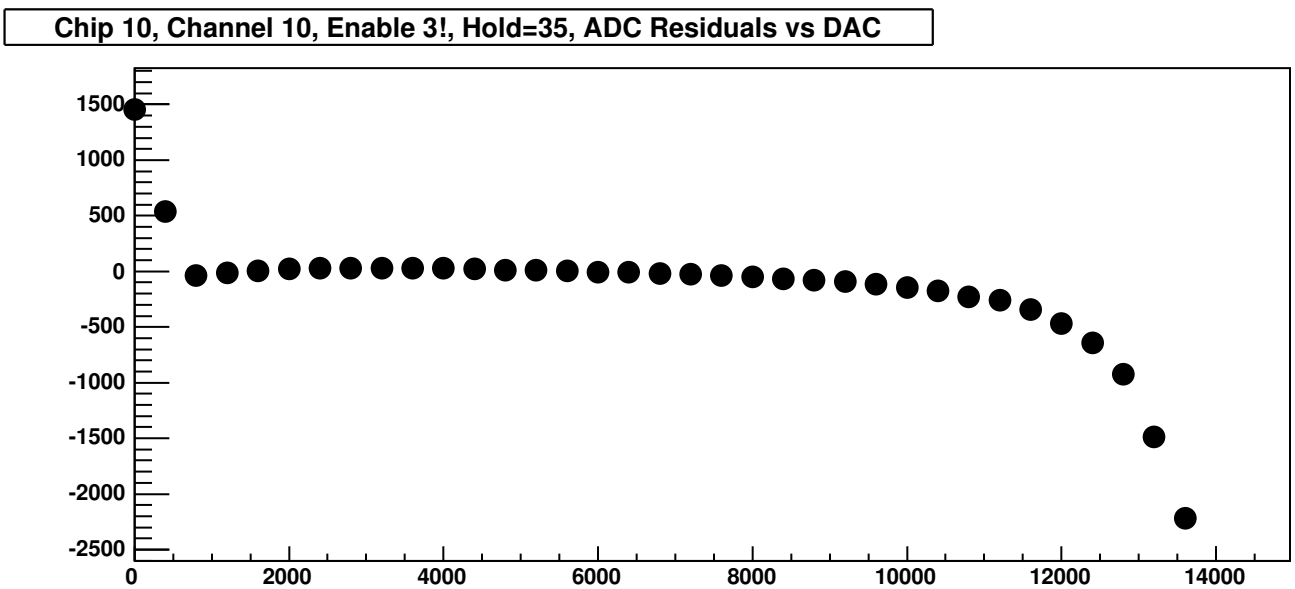


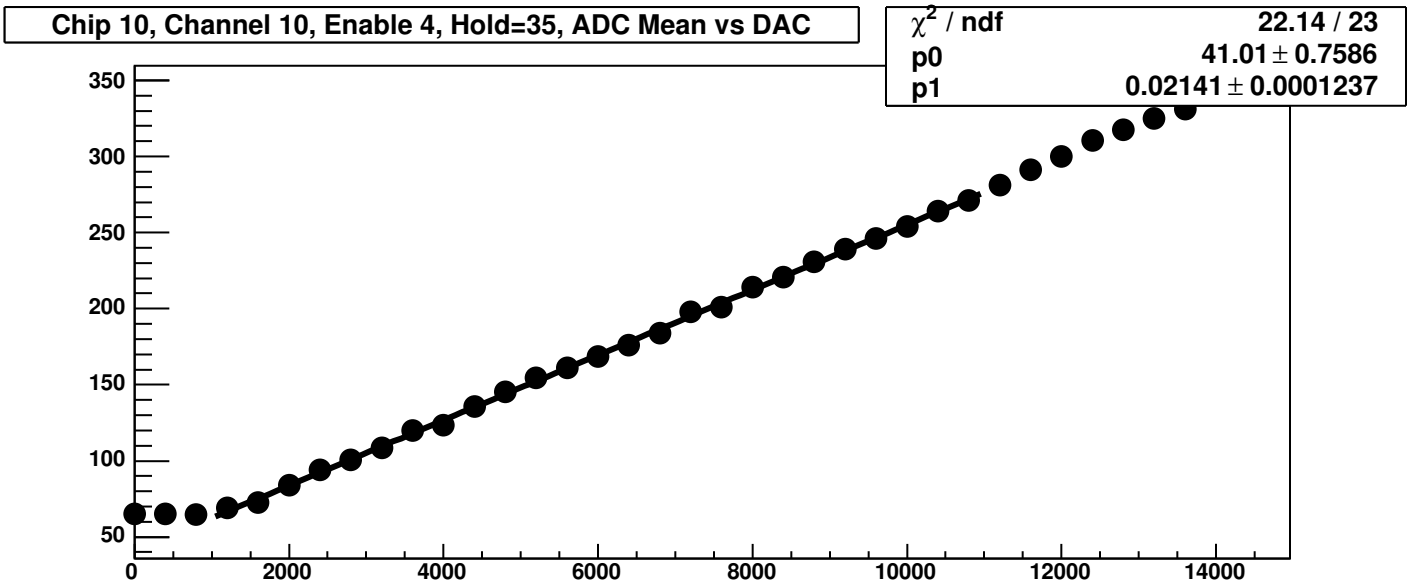
Chip 10, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

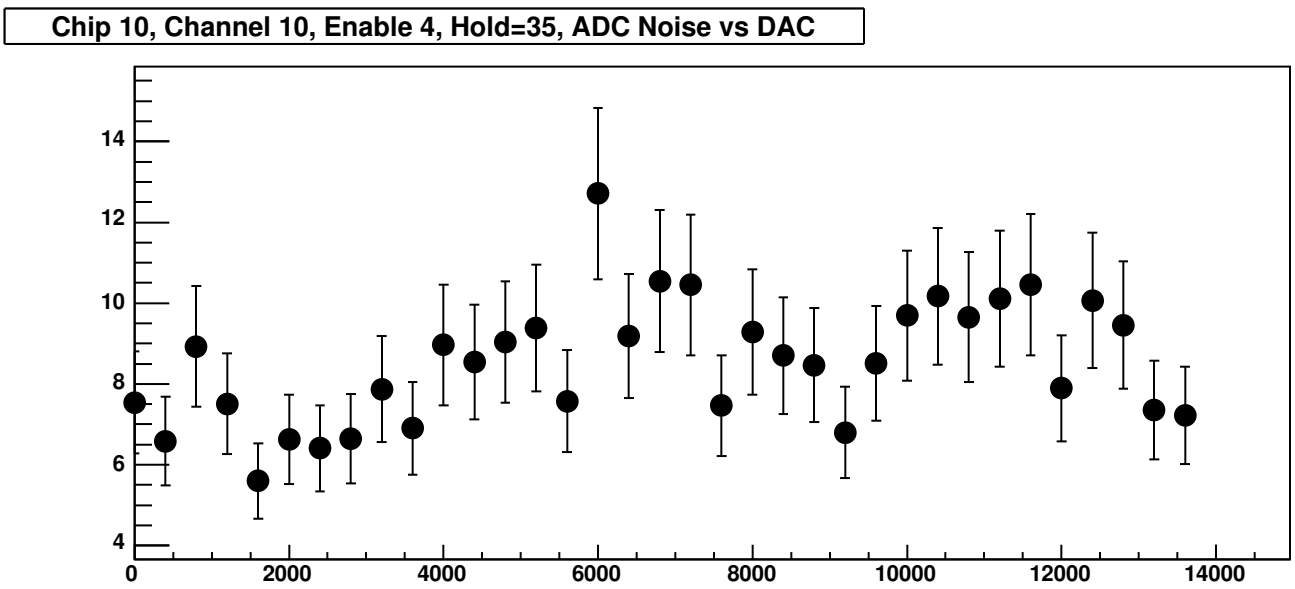


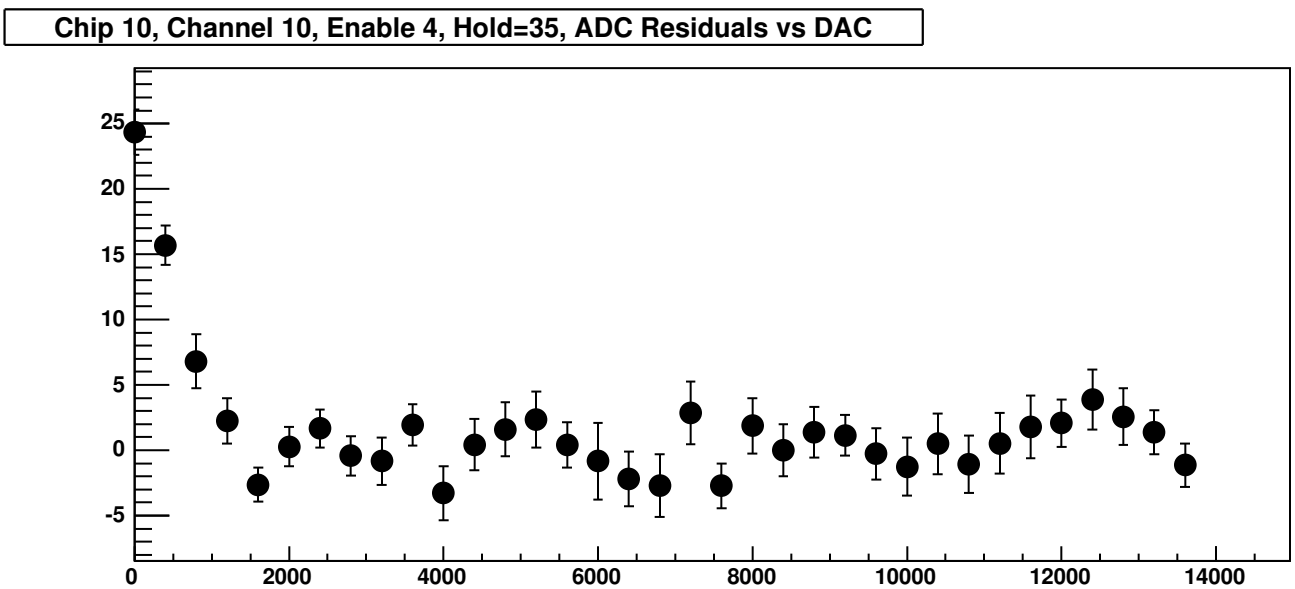

Chip 10, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 207.3 / 23
p0 116.4 ± 2.861
p1 2.334 ± 0.001671
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

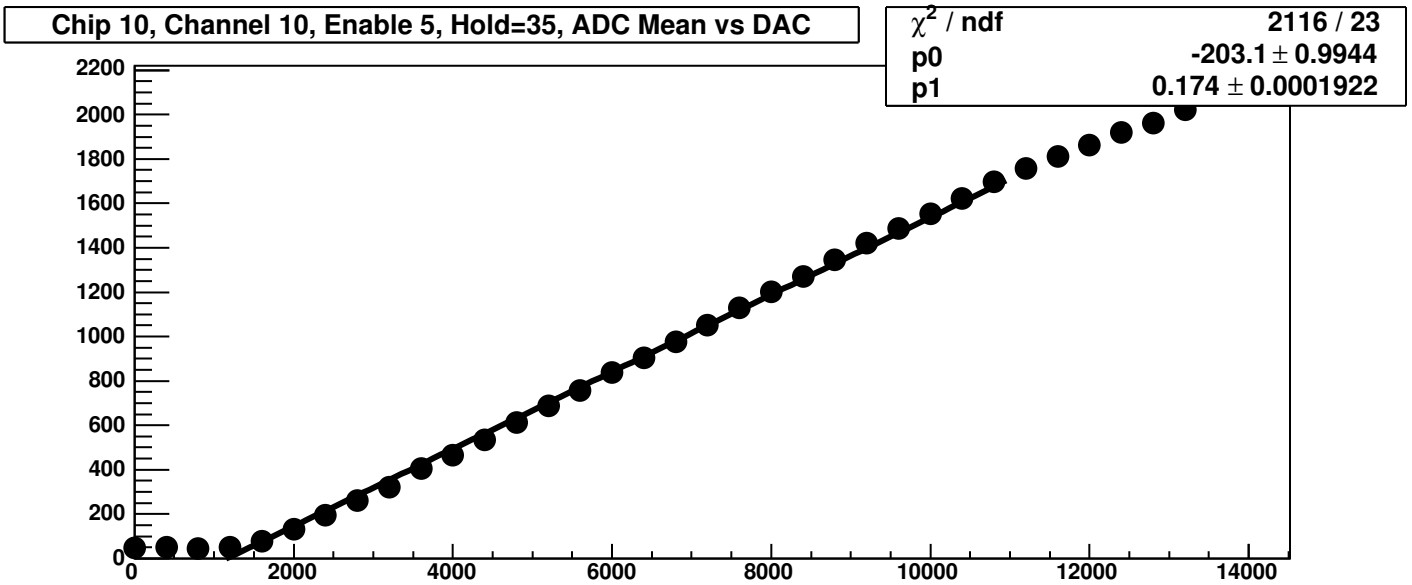

Chip 10, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

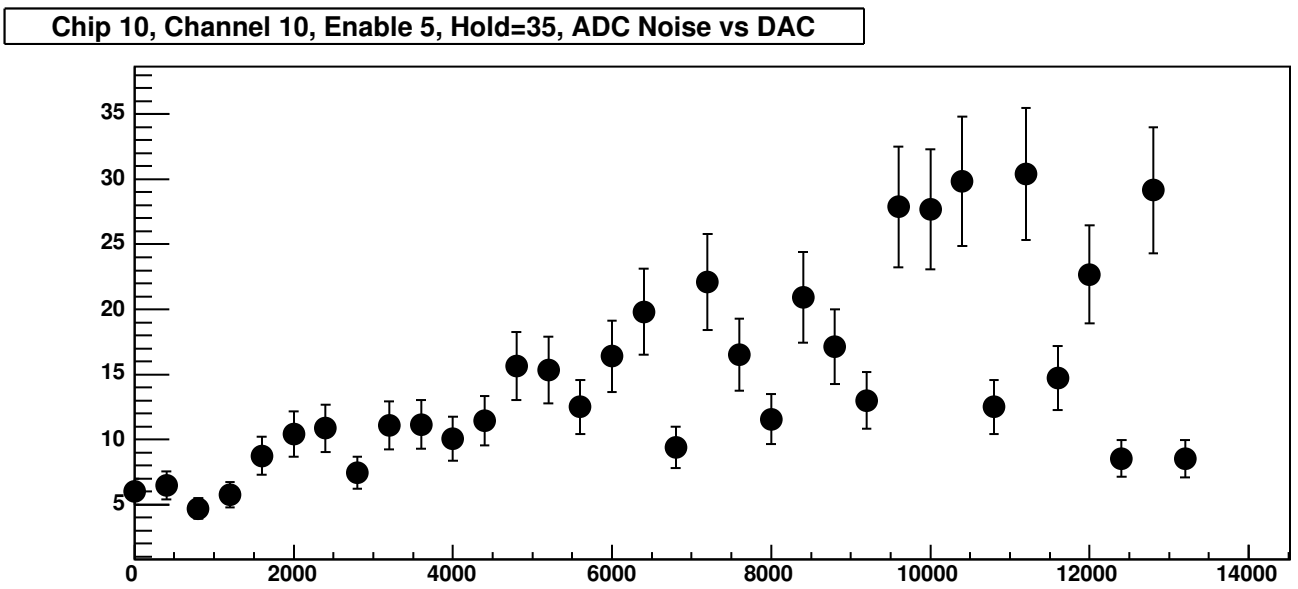

Chip 10, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

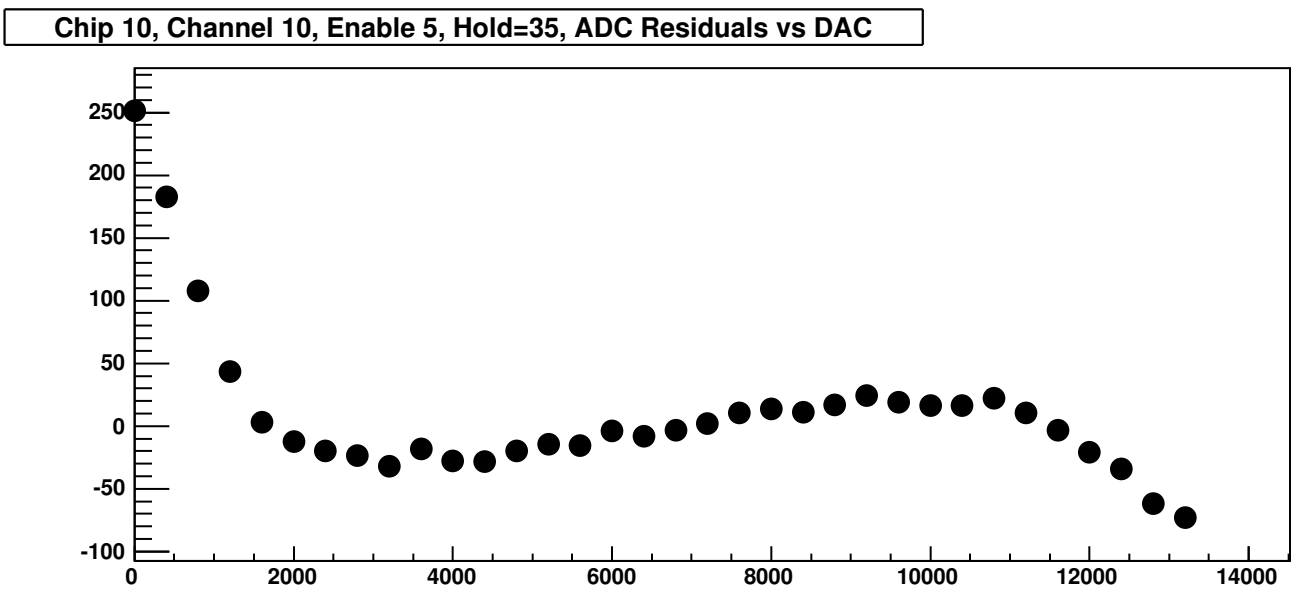

Chip 10, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 22.14 / 23
p0 41.01 ± 0.7586
p1 0.02141 ± 0.0001237
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

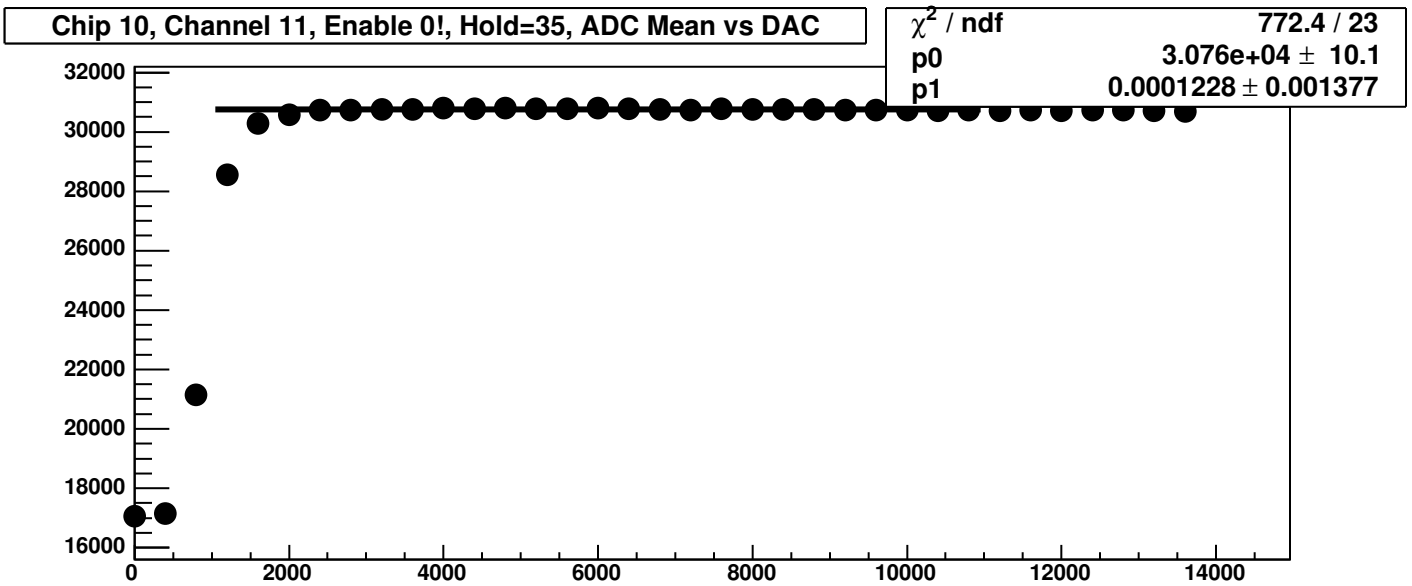

Chip 10, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

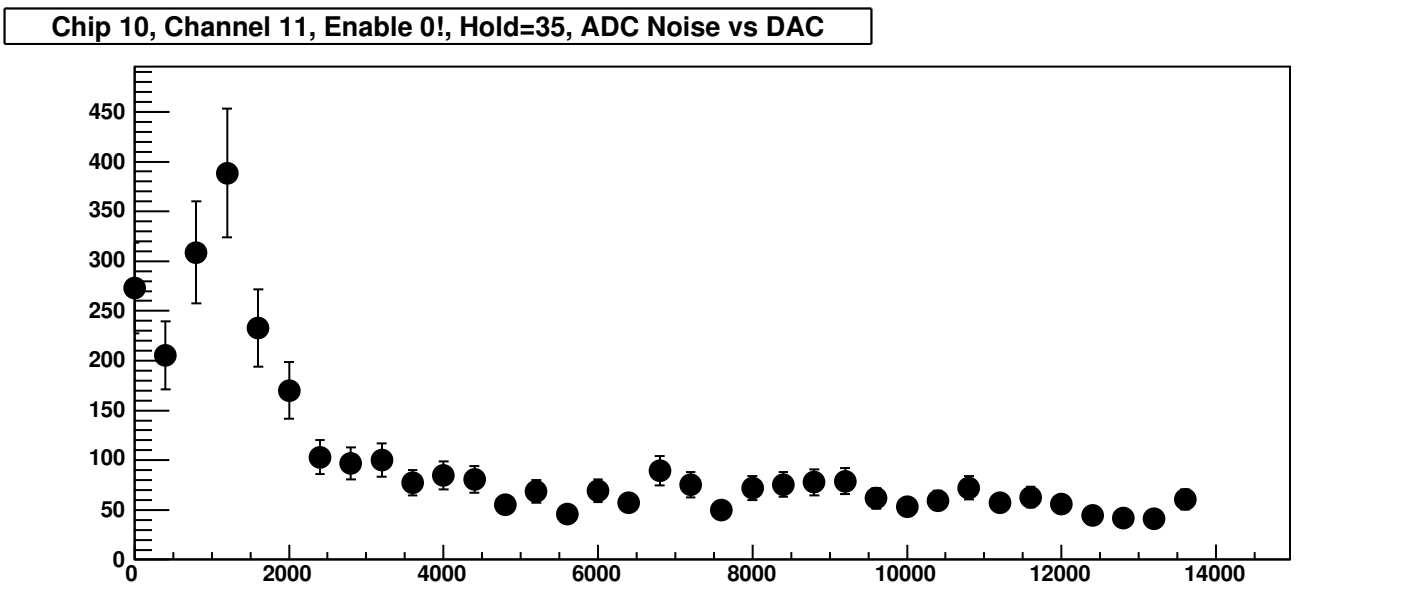

Chip 10, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

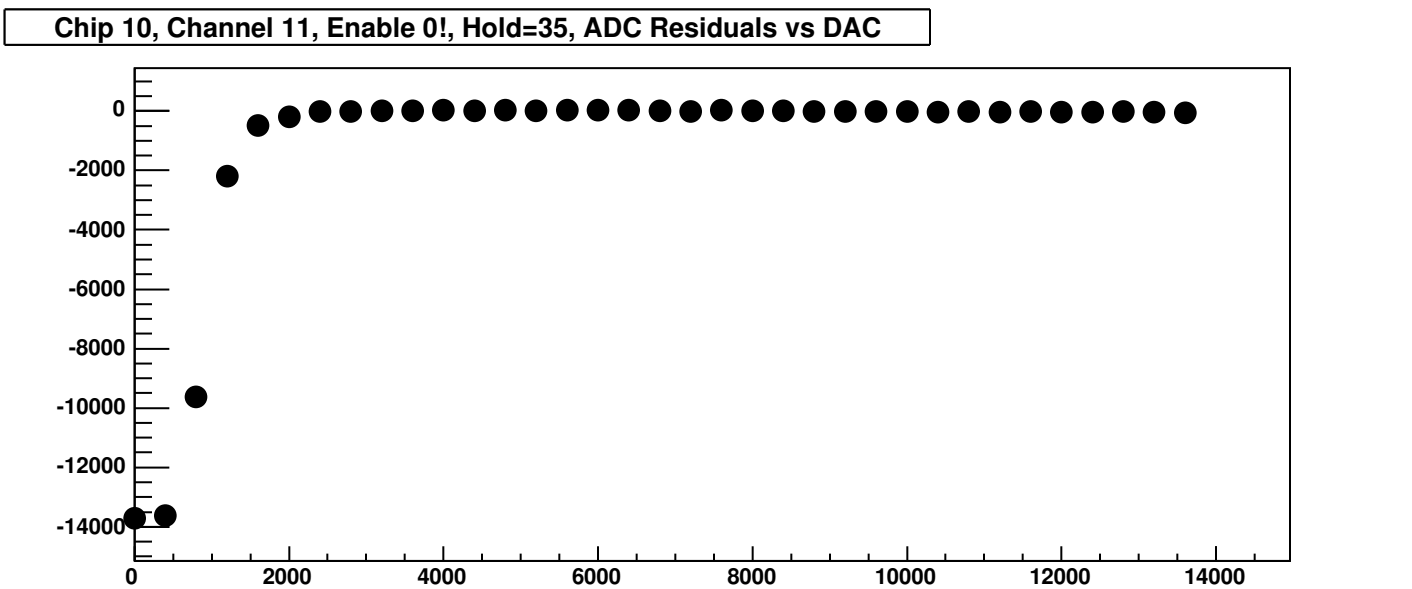

Chip 10, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2116 / 23
p0 -203.1 ± 0.9944
p1 0.174 ± 0.0001922
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

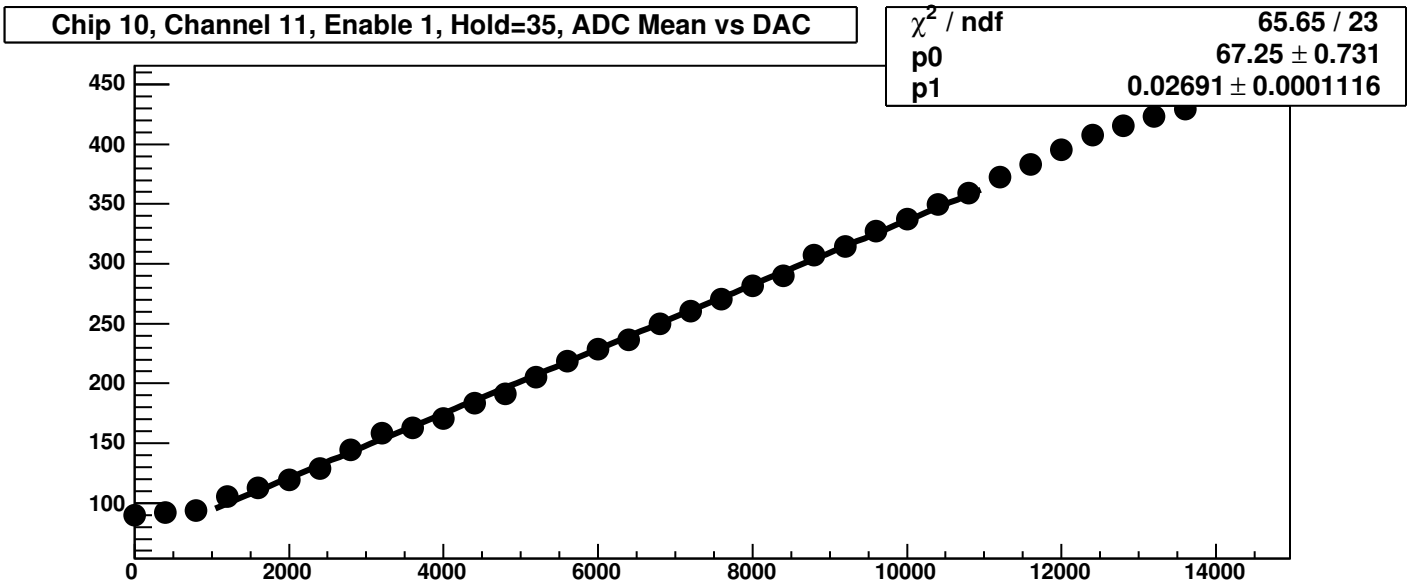

Chip 10, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

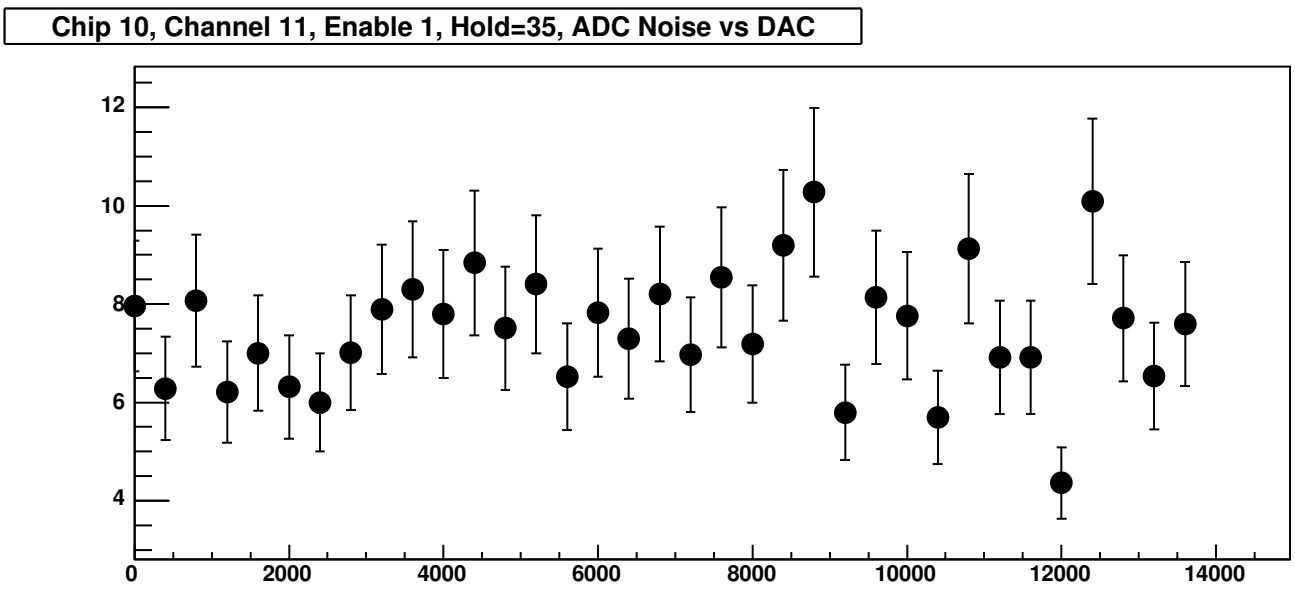

Chip 10, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

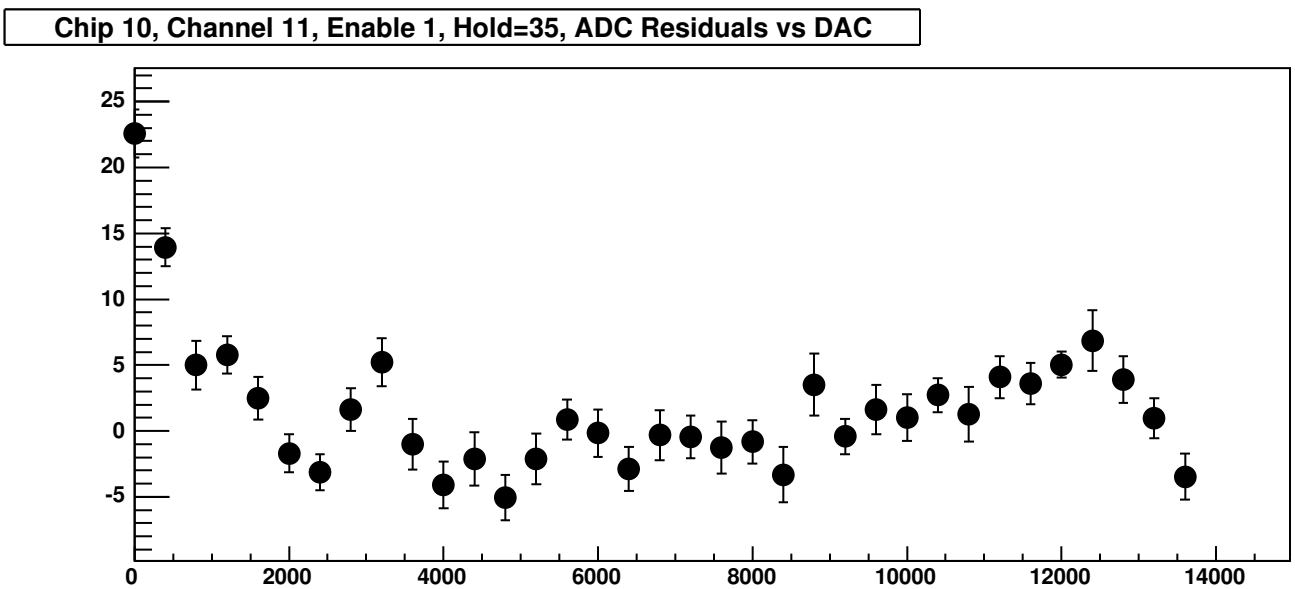

Chip 10, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 772.4 / 23
p0 3.076e+04 ± 10.1
p1 0.0001228 ± 0.001377
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

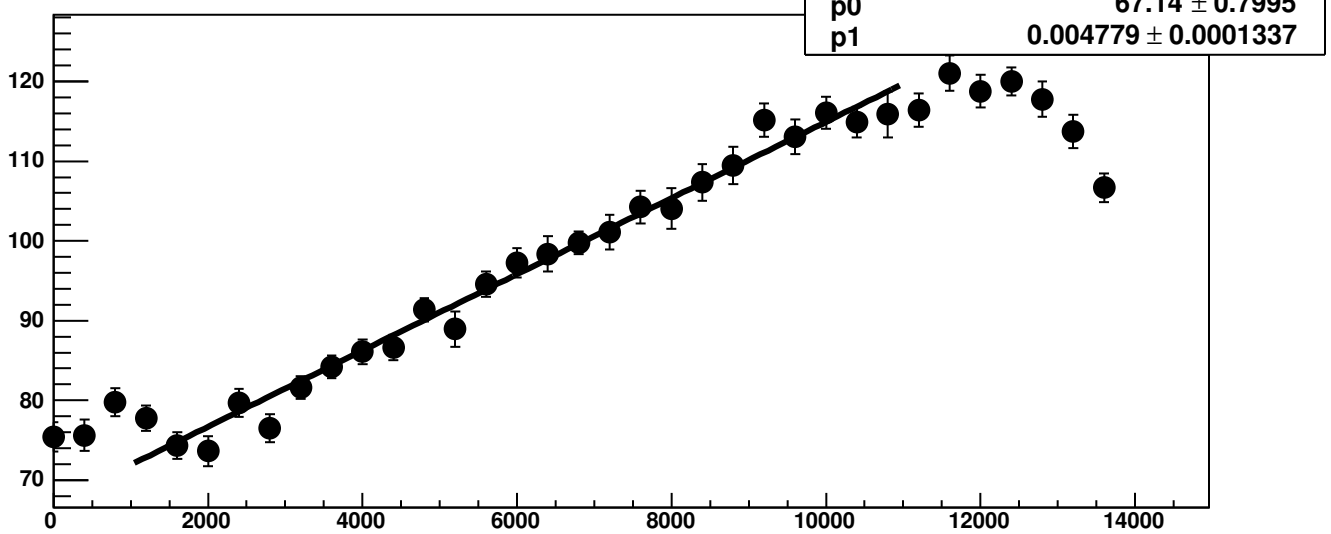

Chip 10, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 65.65 / 23
p0 67.25 ± 0.731
p1 0.02691 ± 0.0001116
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

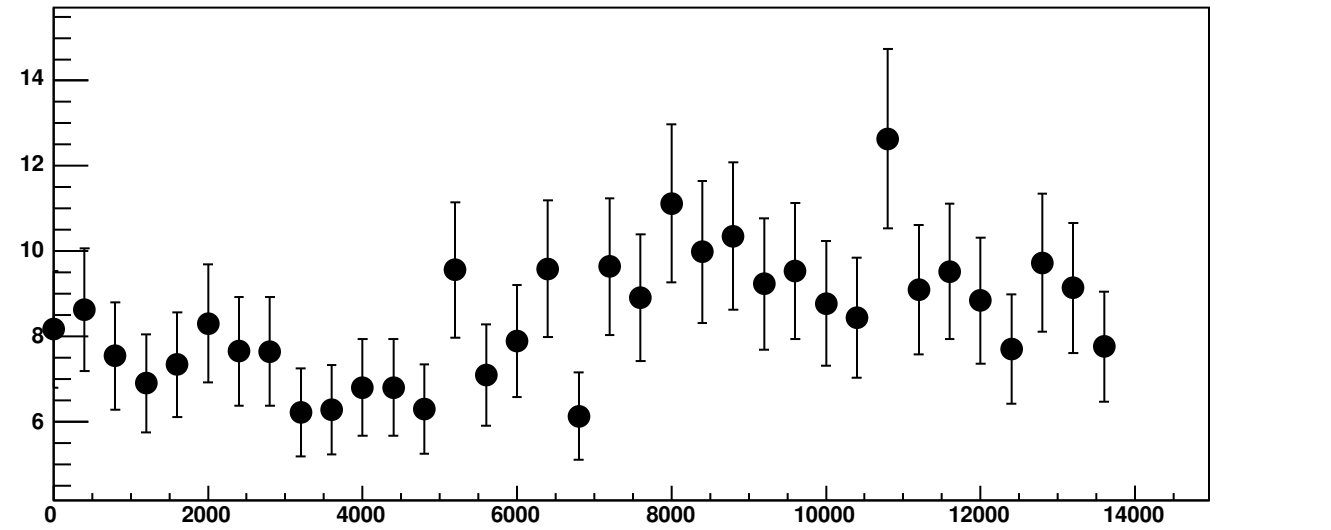

Chip 10, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

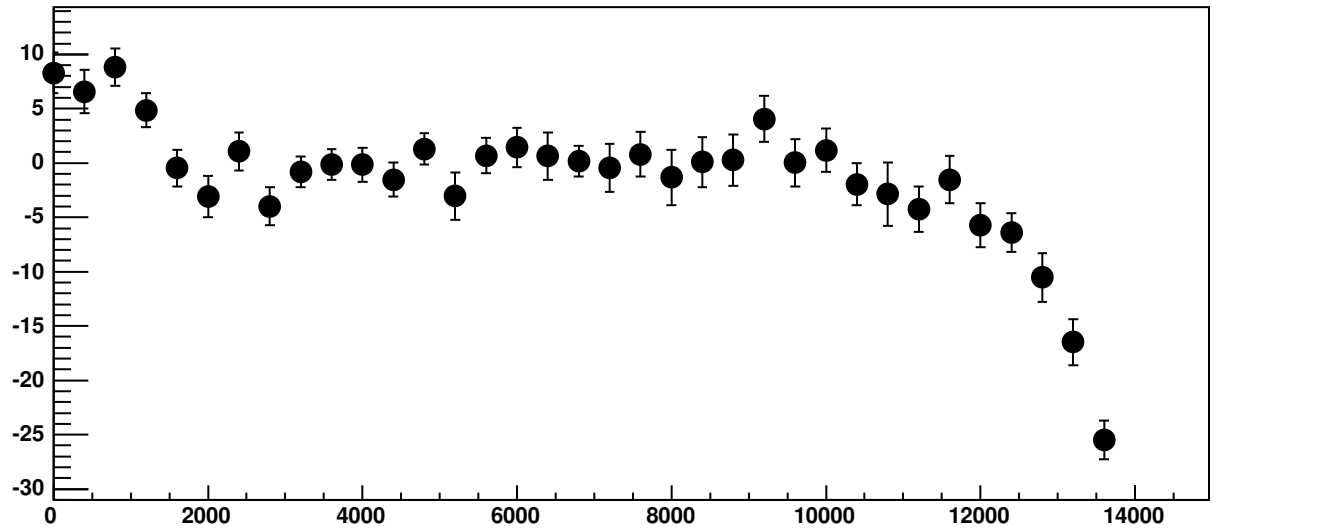
Chip 10, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

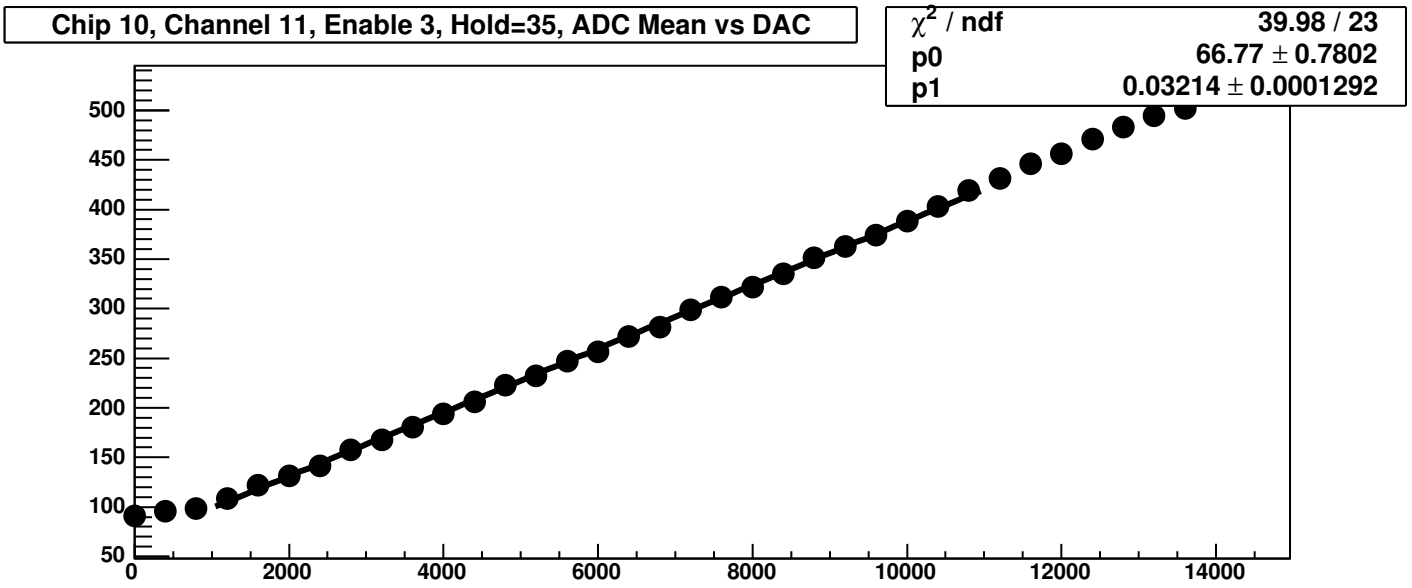


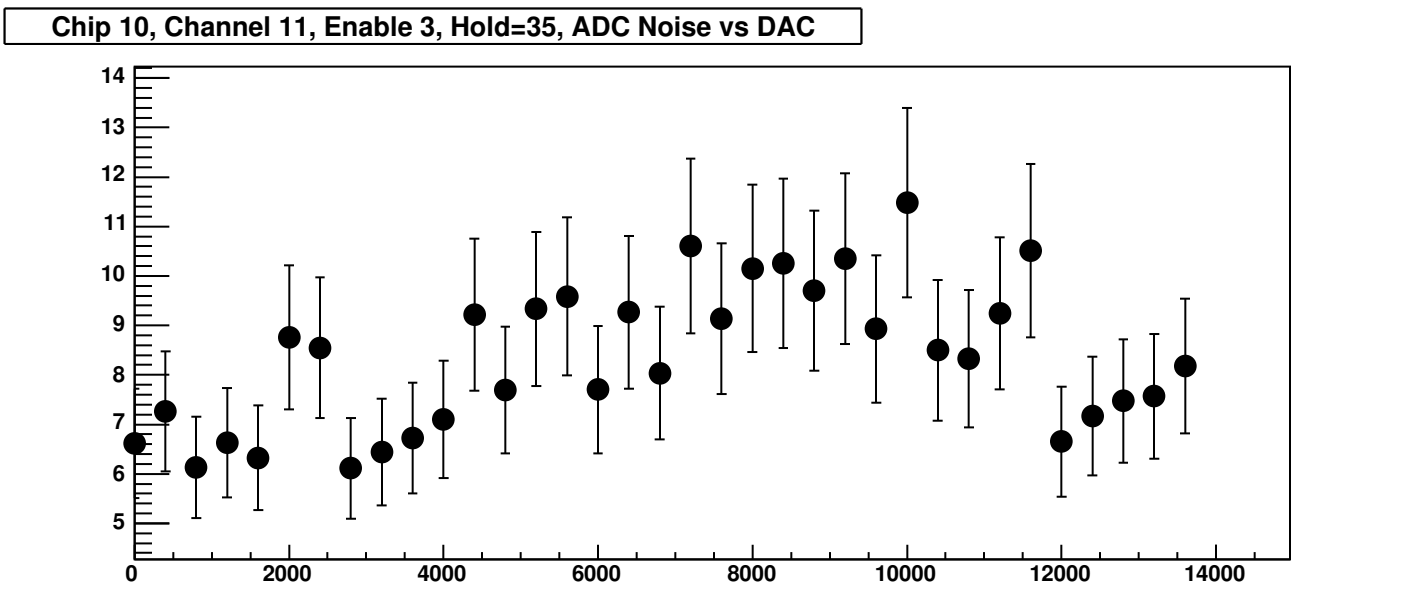
Chip 10, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

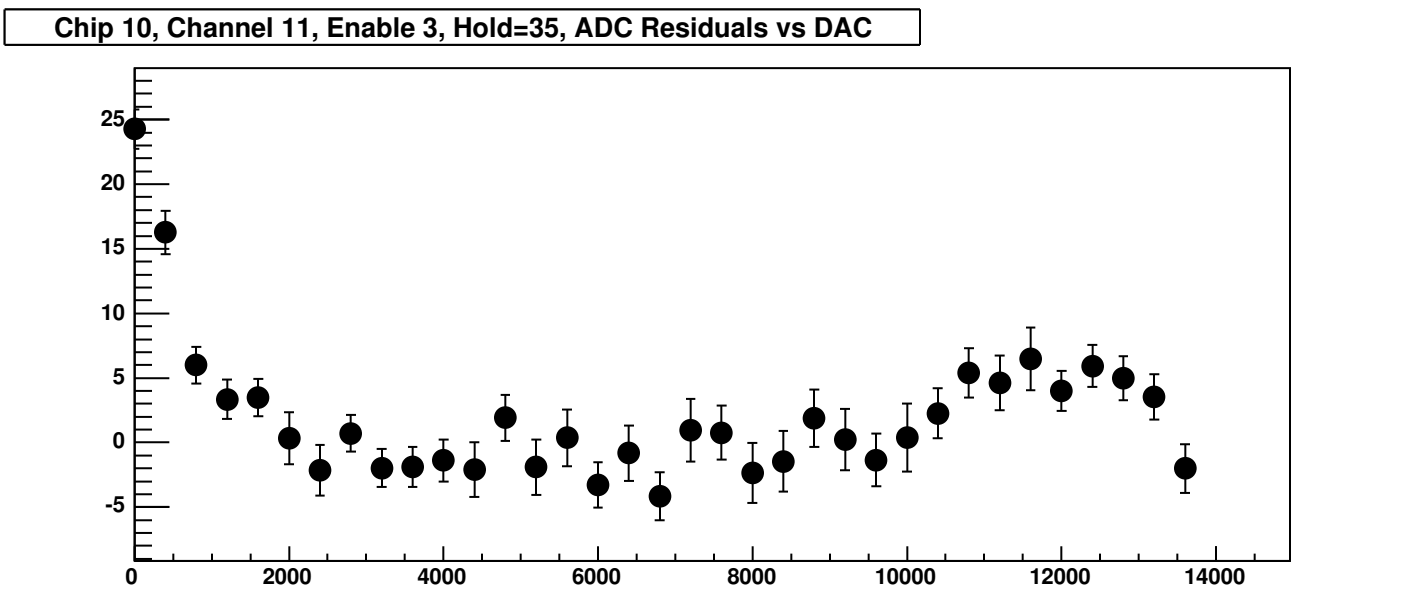


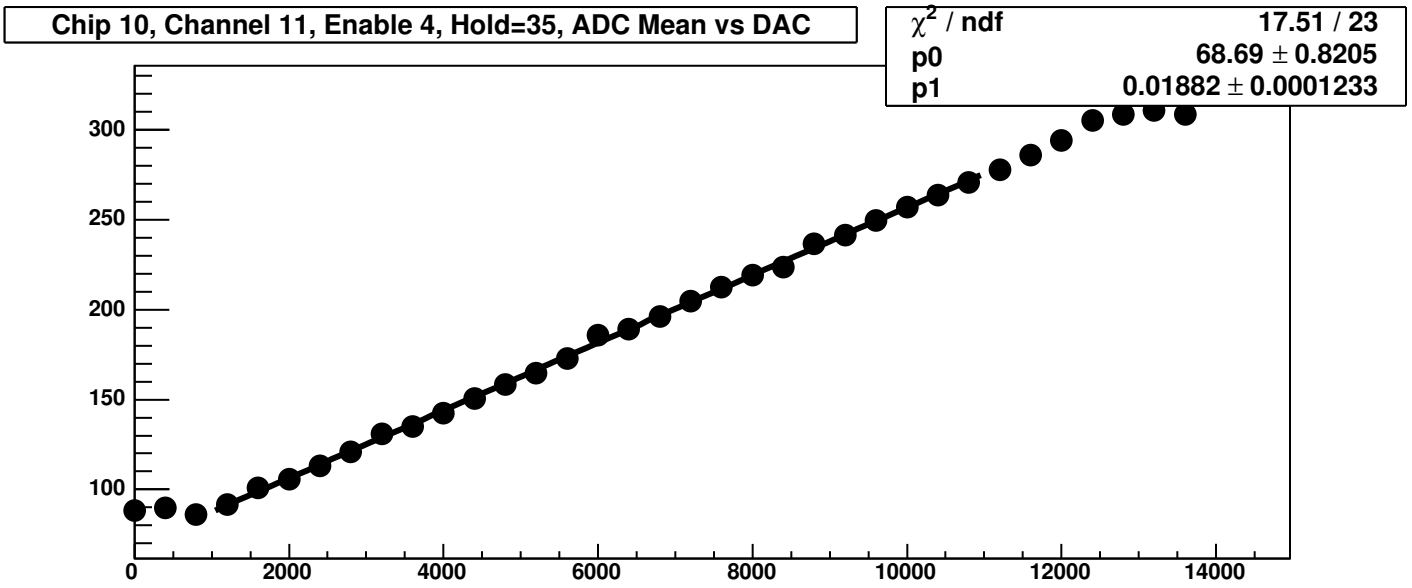
Chip 10, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

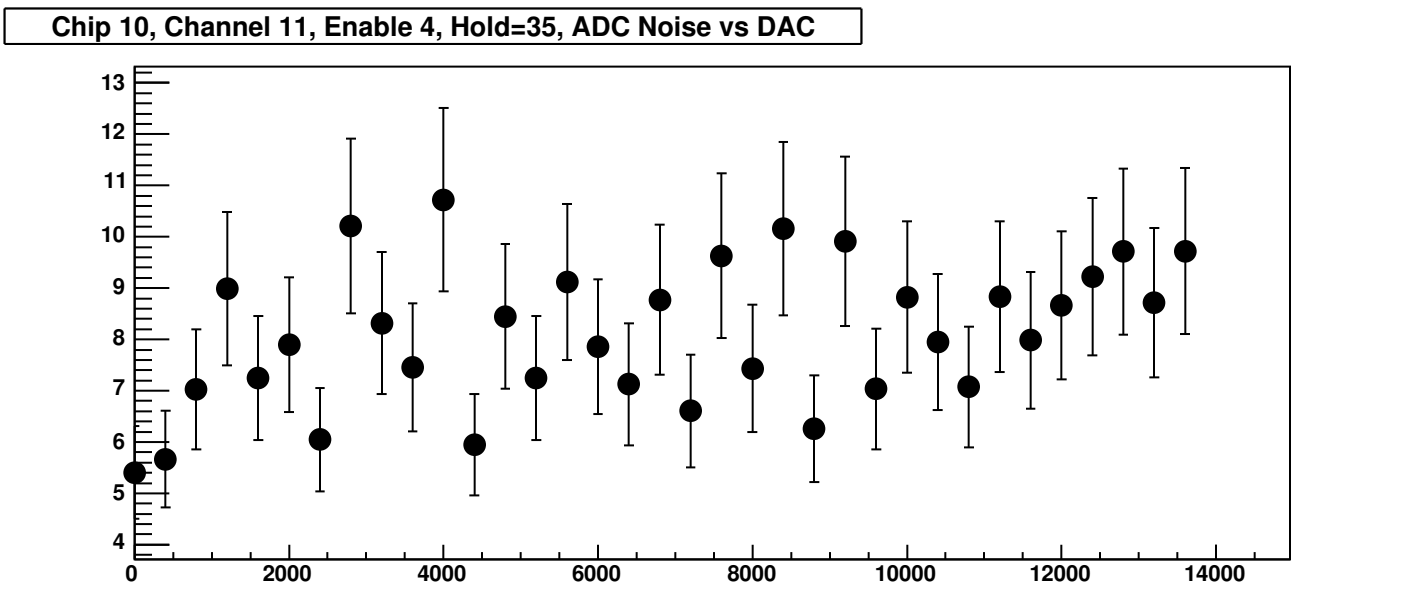


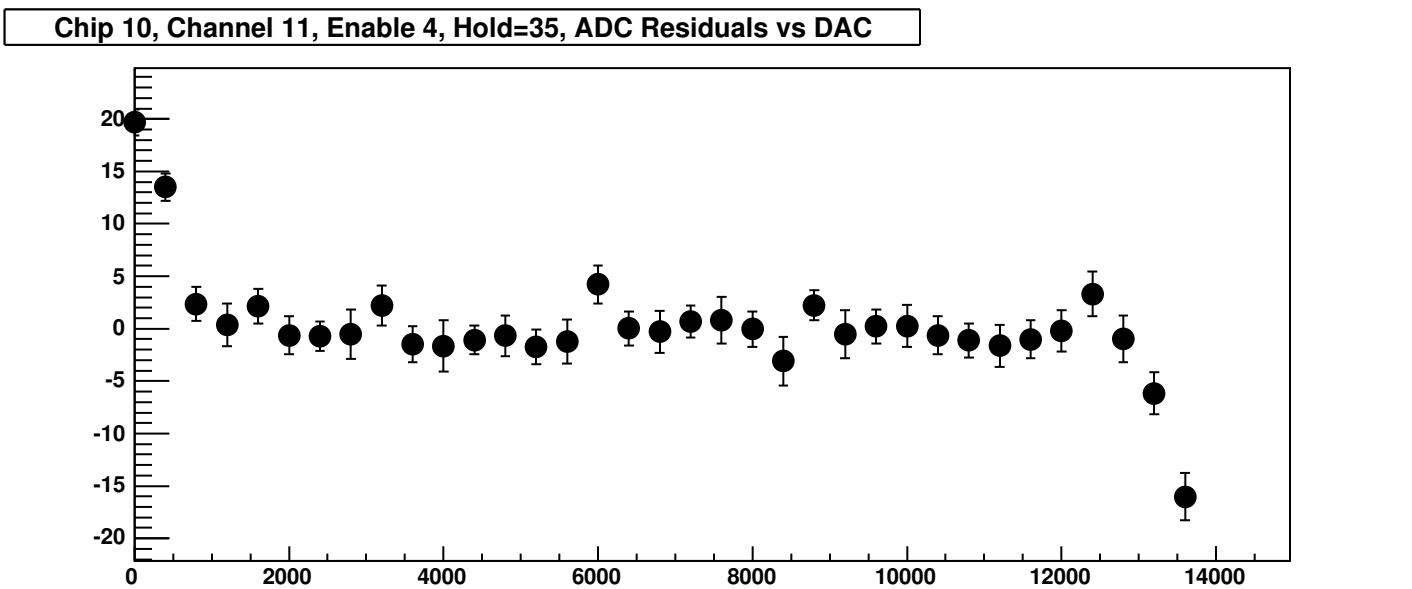

Chip 10, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 39.98 / 23
p0 66.77 ± 0.7802
p1 0.03214 ± 0.0001292
500
450
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

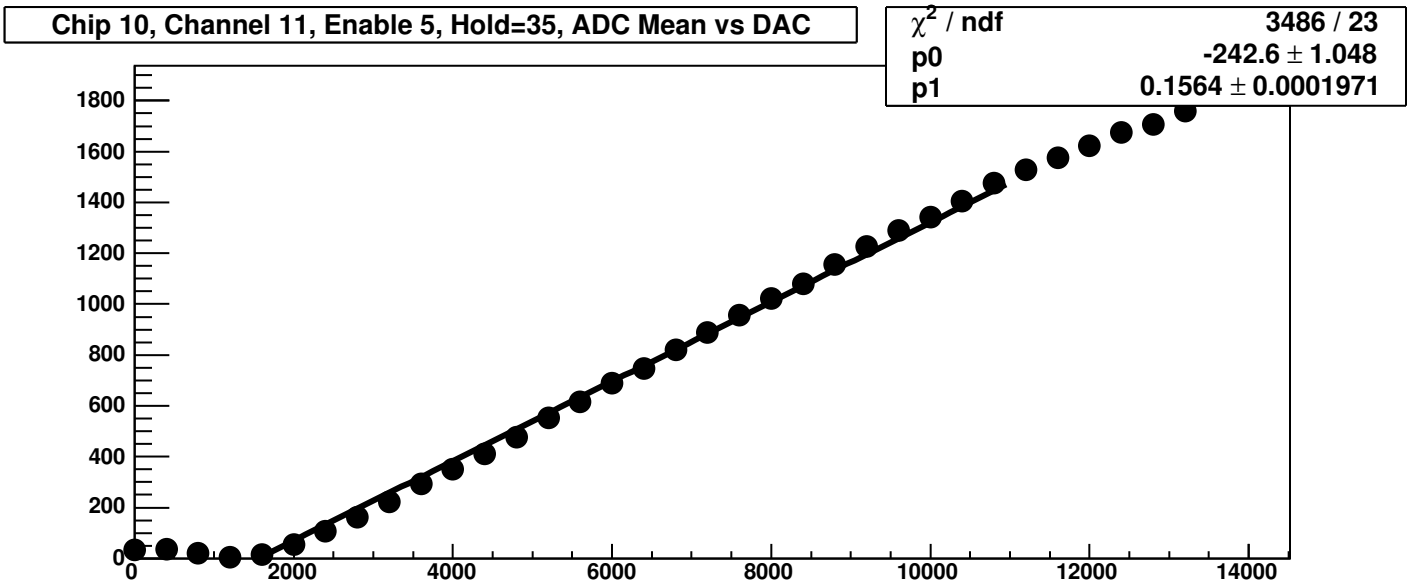

Chip 10, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC
14
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

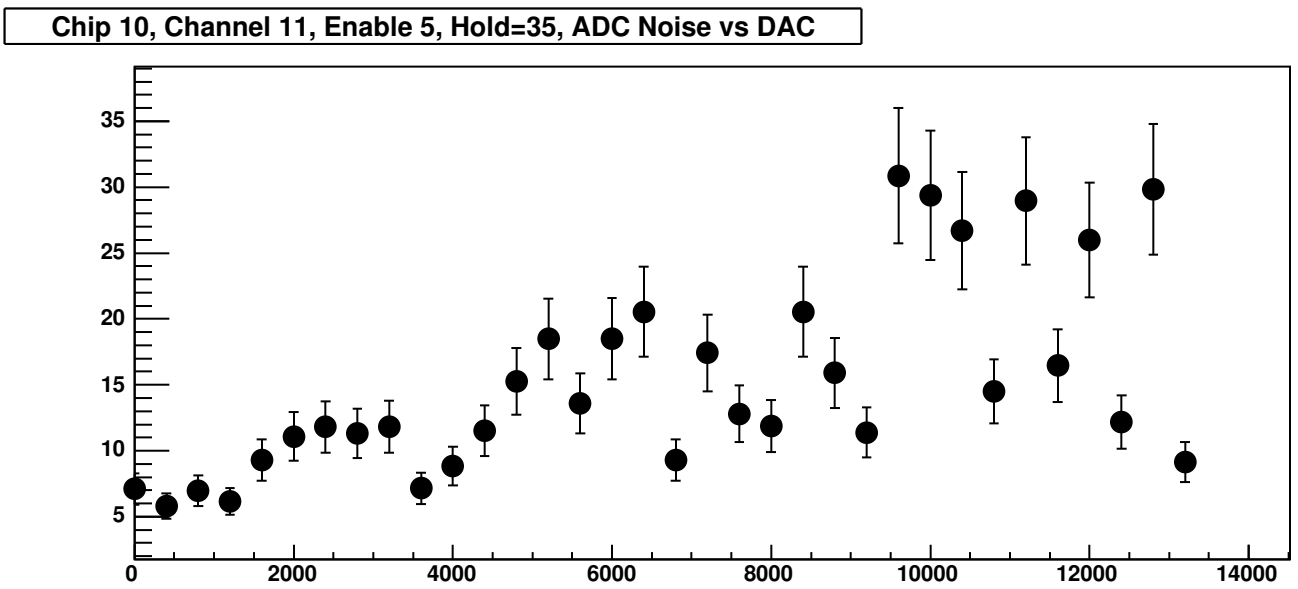

Chip 10, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

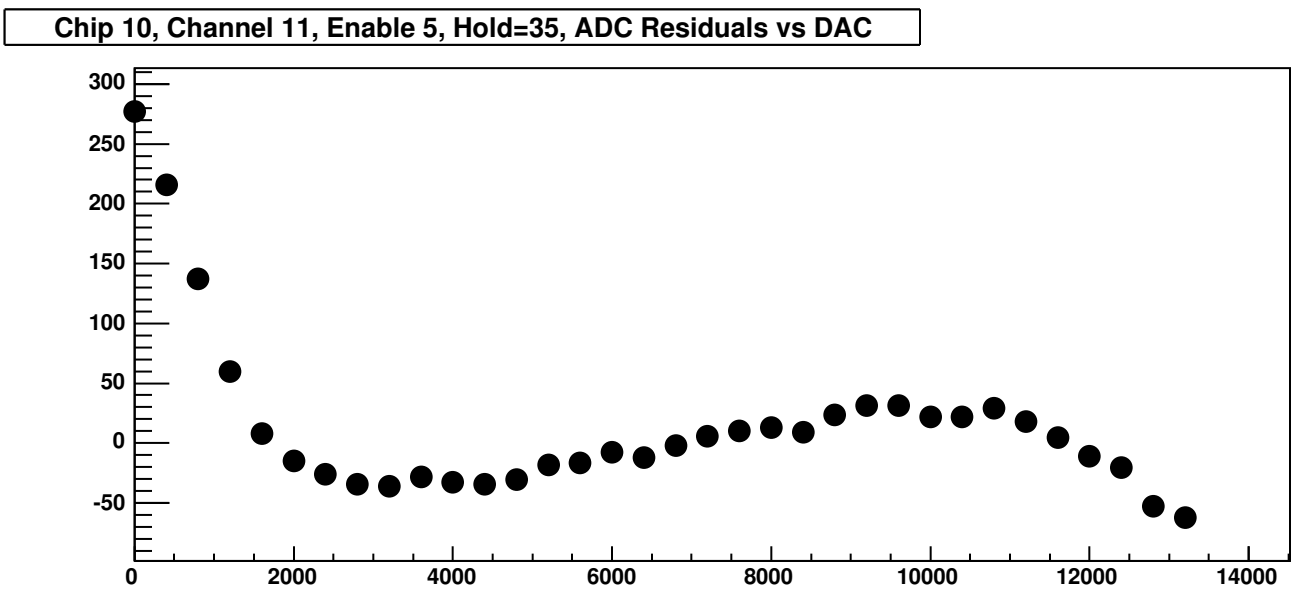

Chip 10, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 17.51 / 23
p0 68.69 ± 0.8205
p1 0.01882 ± 0.0001233
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

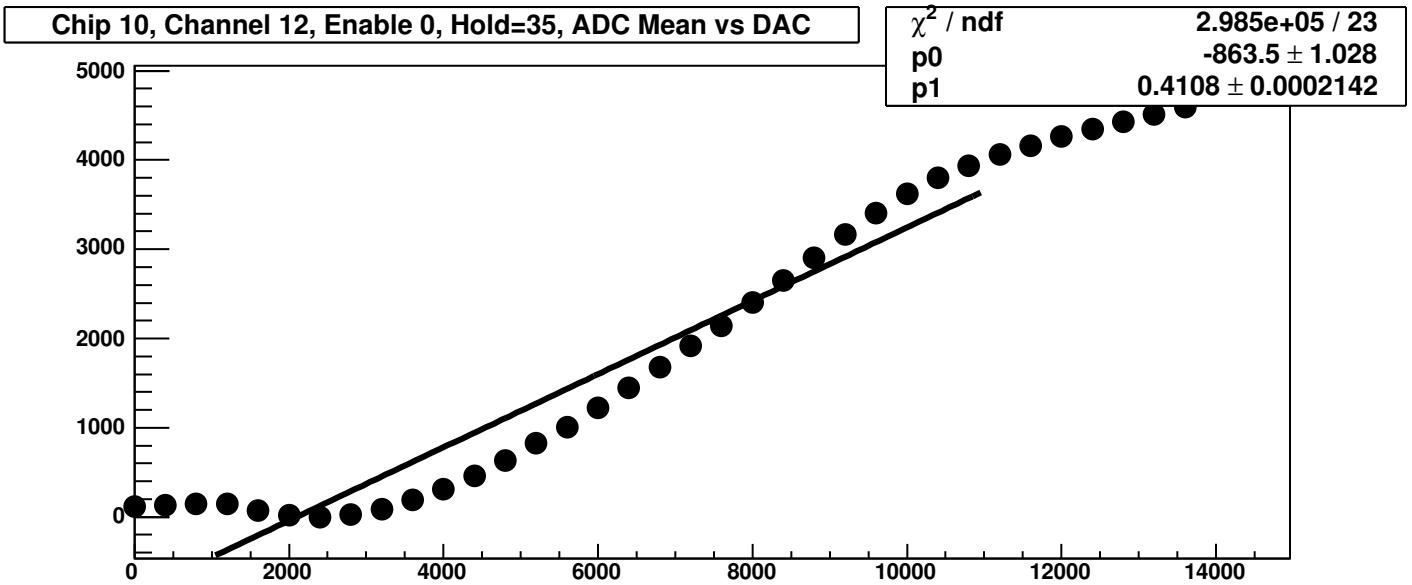

Chip 10, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

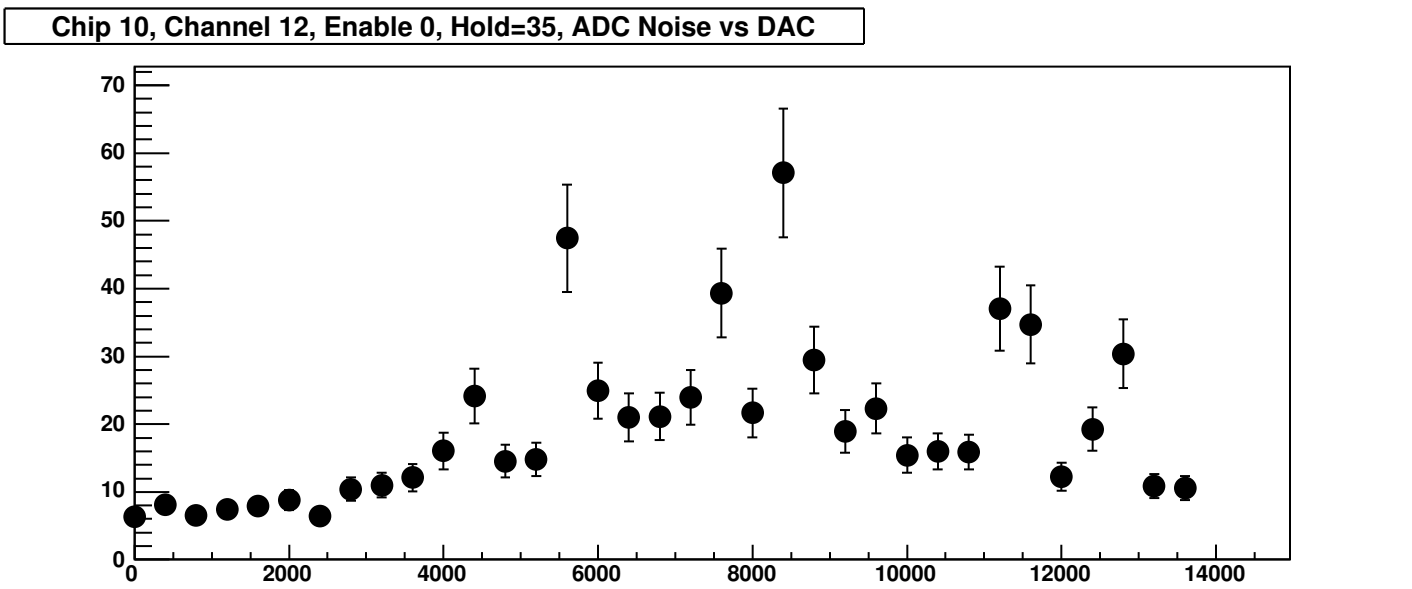

Chip 10, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

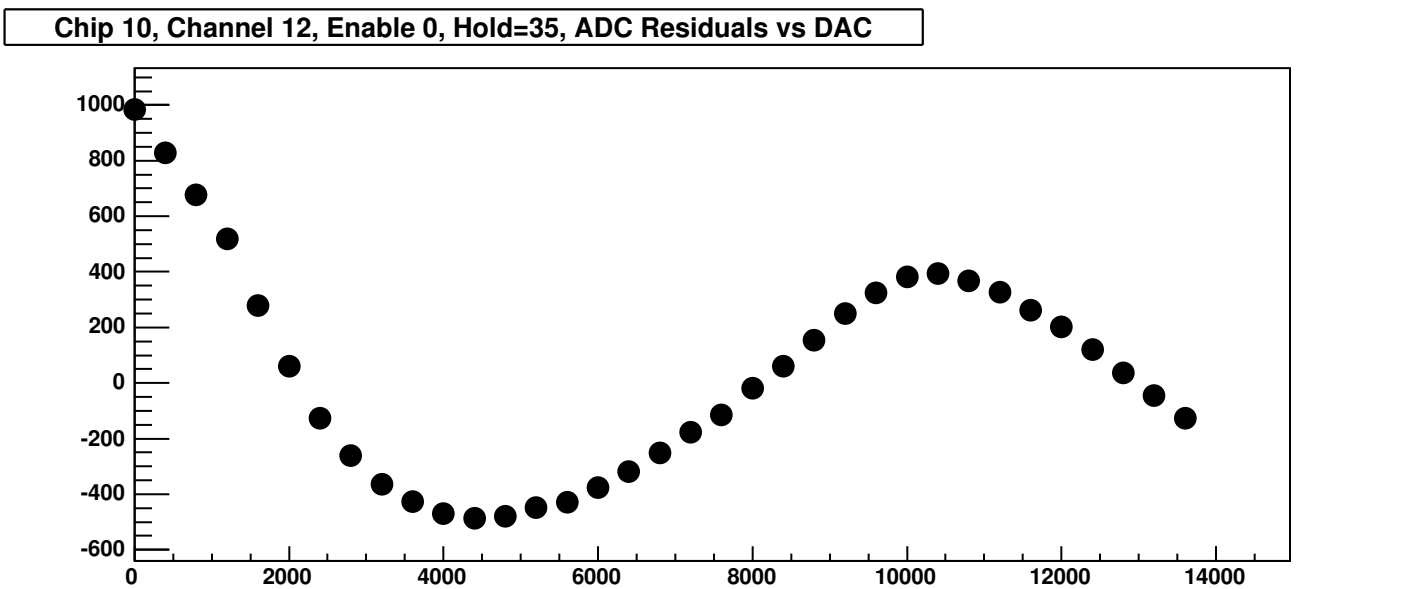

Chip 10, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3486 / 23
p0 -242.6 ± 1.048
p1 0.1564 ± 0.0001971
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

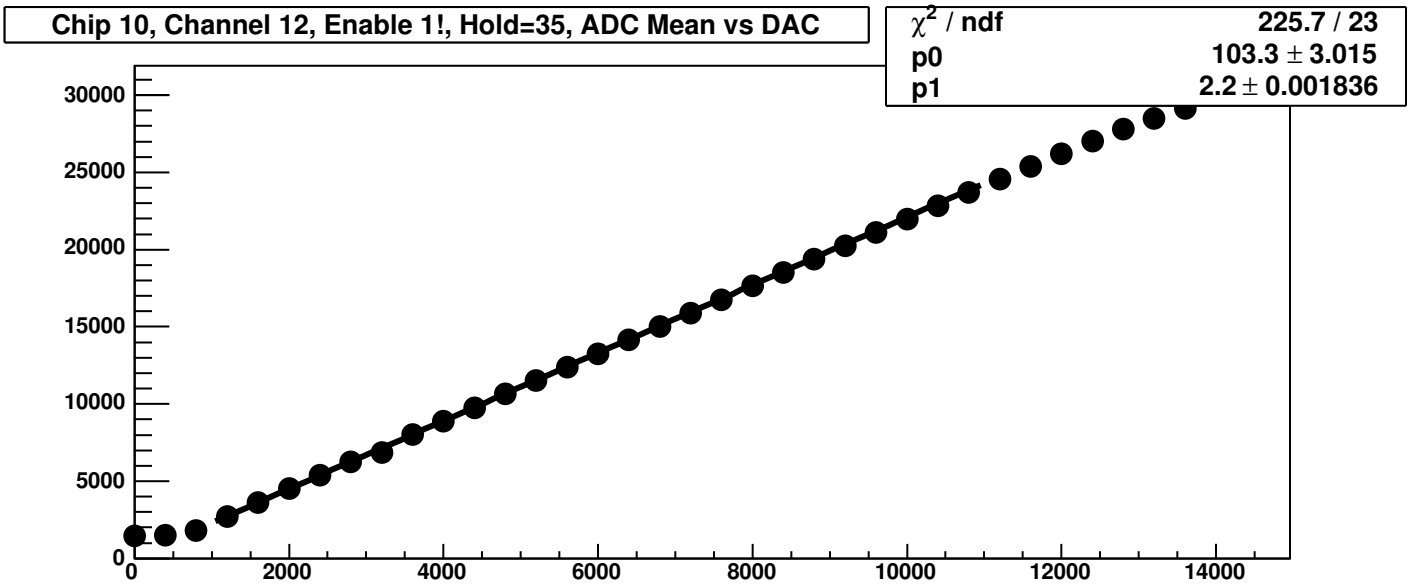

Chip 10, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

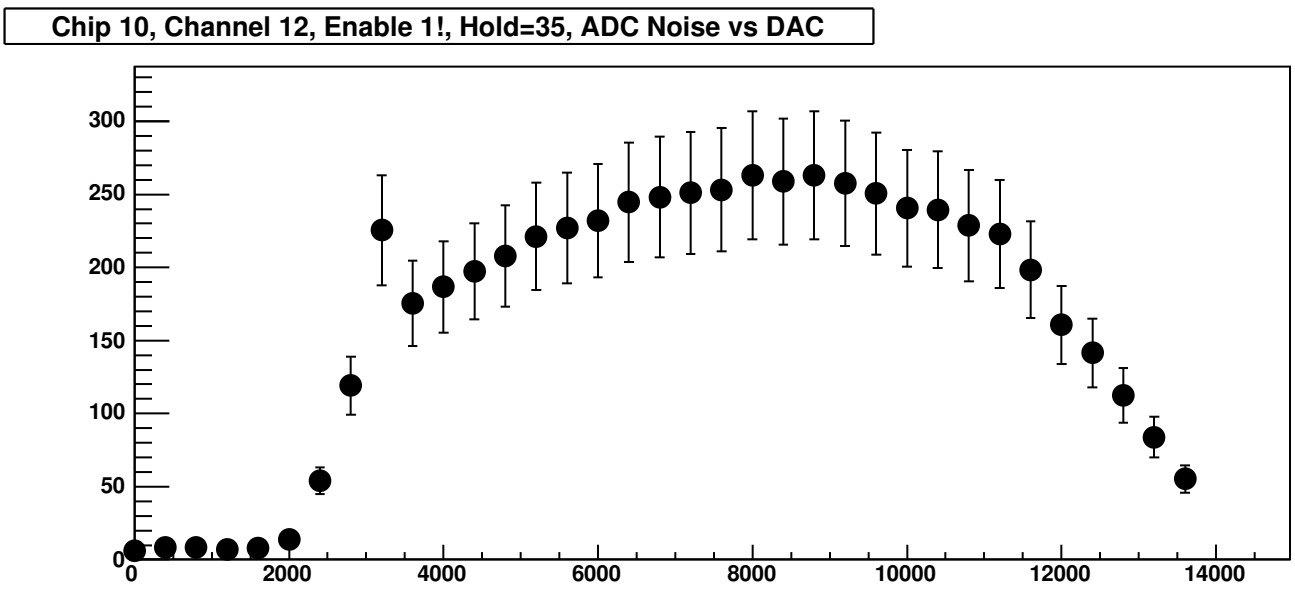

Chip 10, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

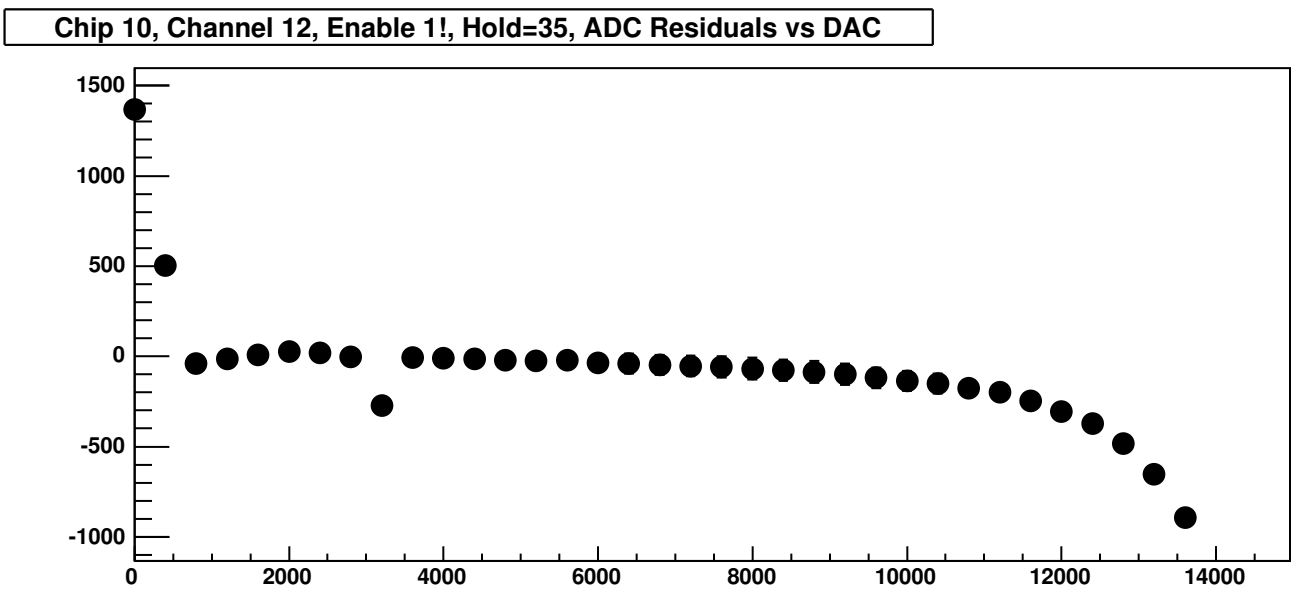

Chip 10, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.985e+05 / 23
p0 -863.5 ± 1.028
p1 0.4108 ± 0.0002142
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

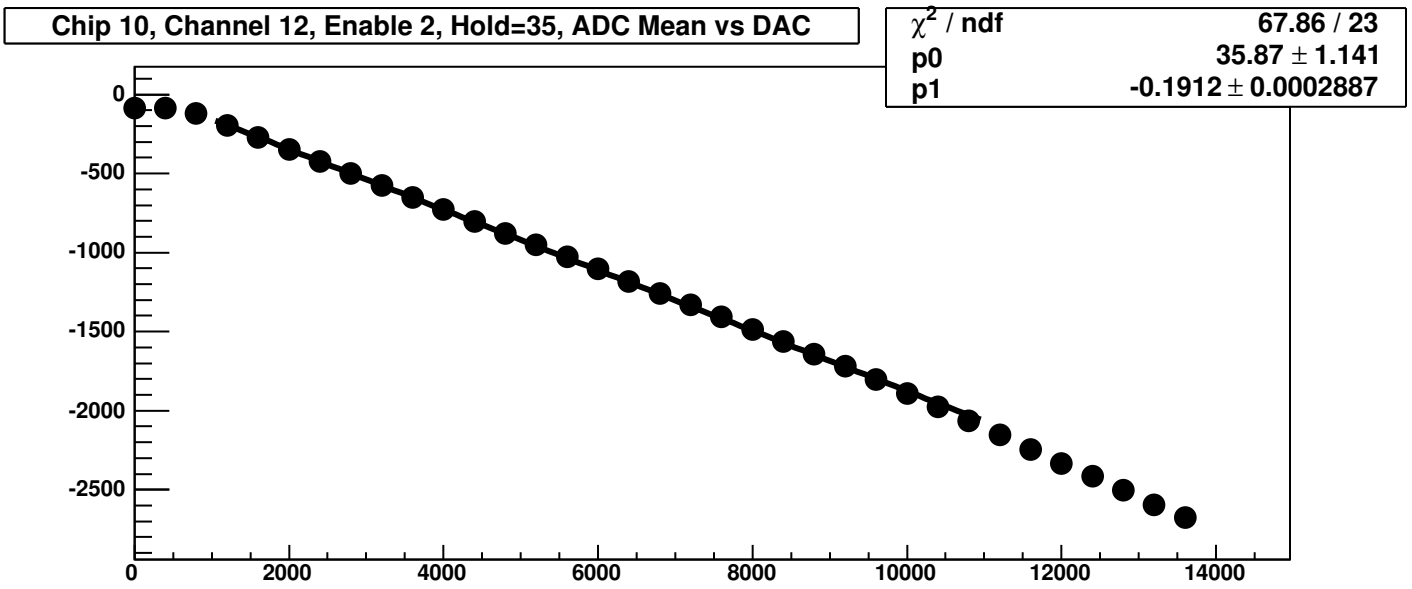

Chip 10, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

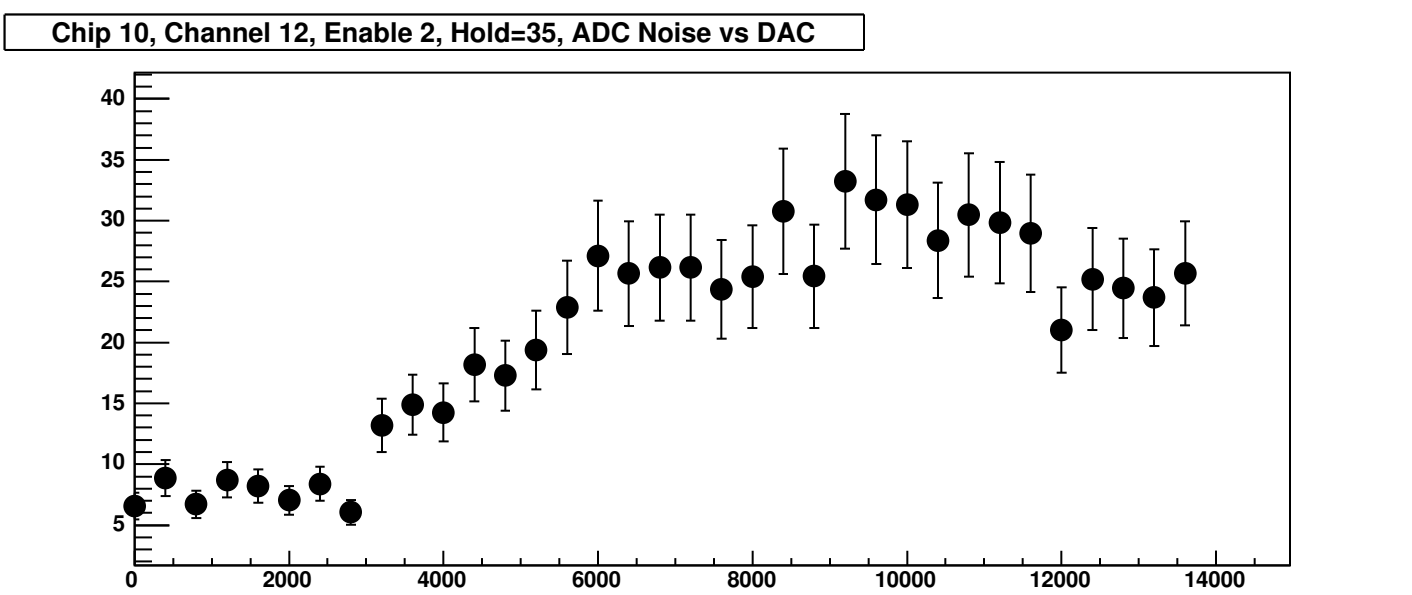

Chip 10, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

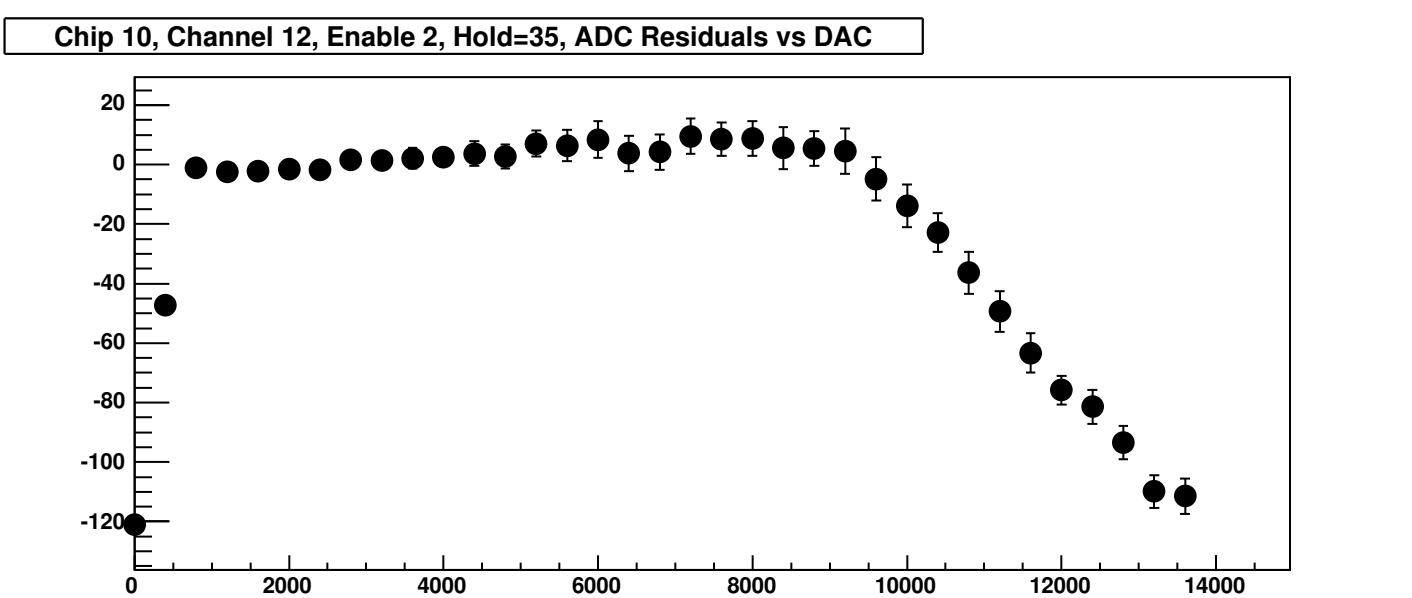

Chip 10, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 225.7 / 23
p0 103.3 ± 3.015
p1 2.2 ± 0.001836
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

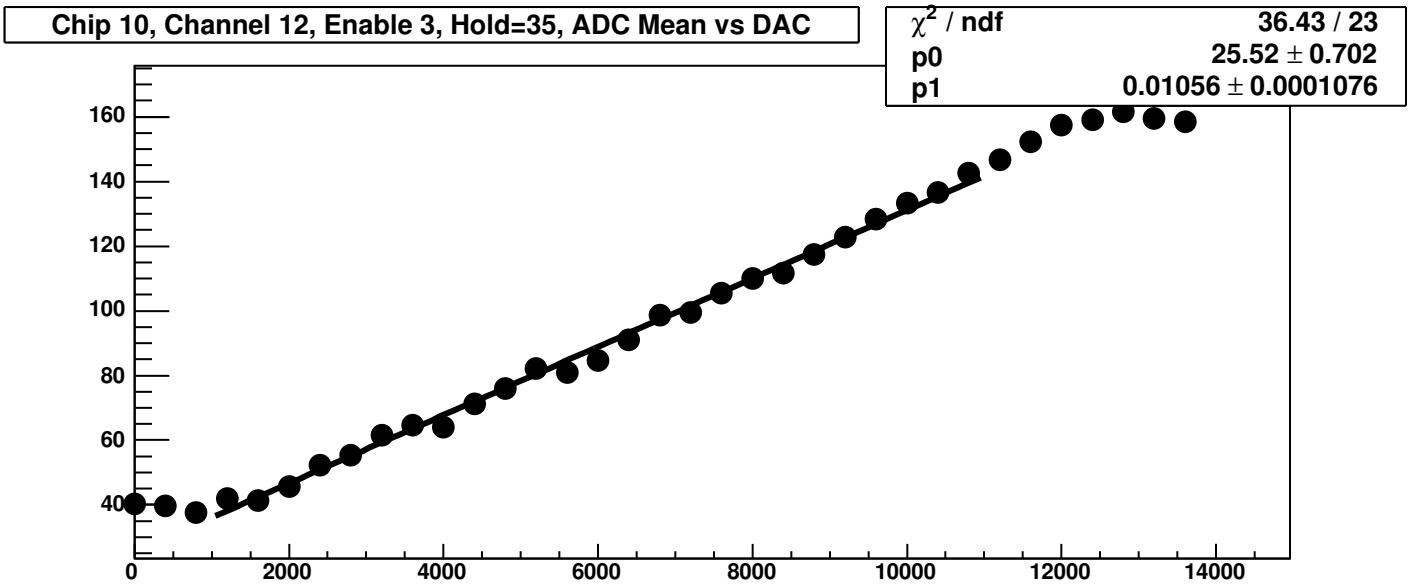

Chip 10, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

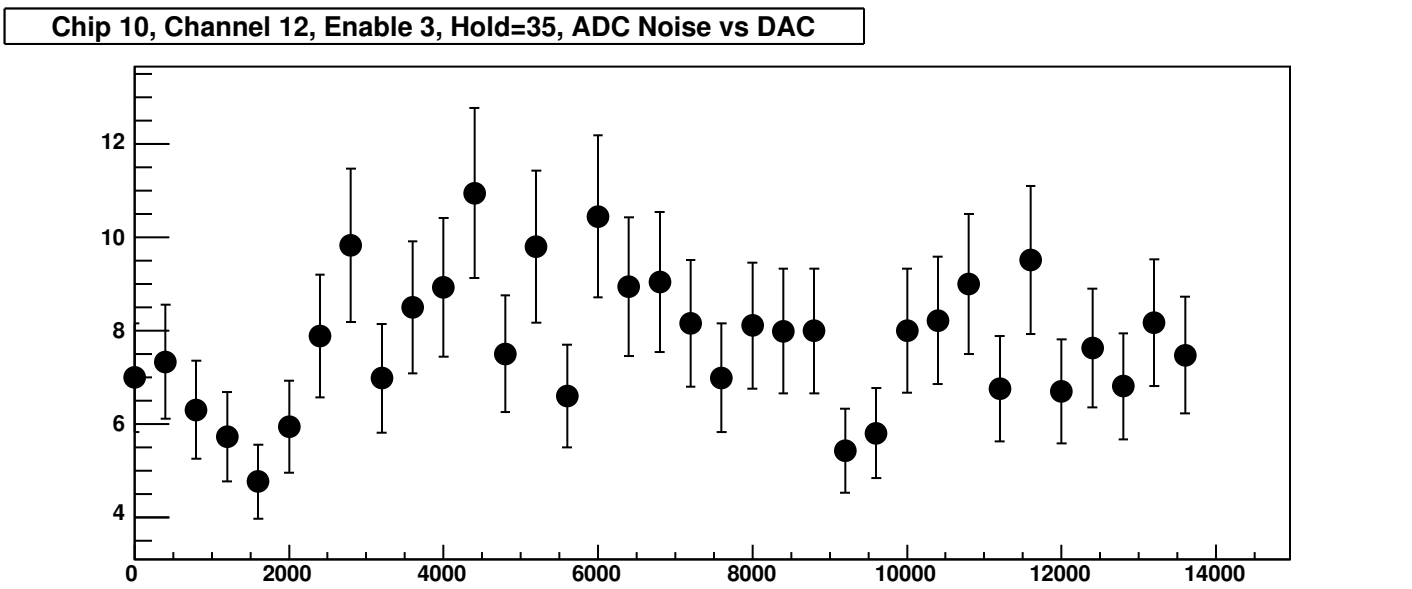

Chip 10, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

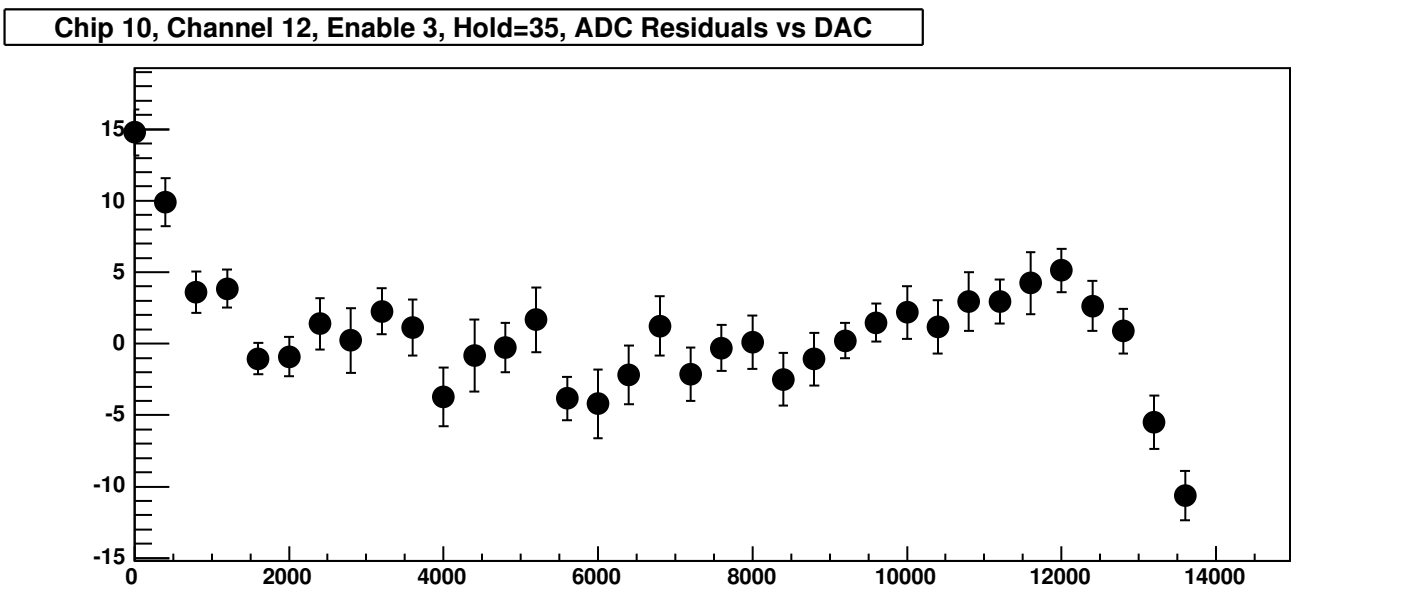

Chip 10, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 67.86 / 23
p0 35.87 ± 1.141
p1 -0.1912 ± 0.0002887
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

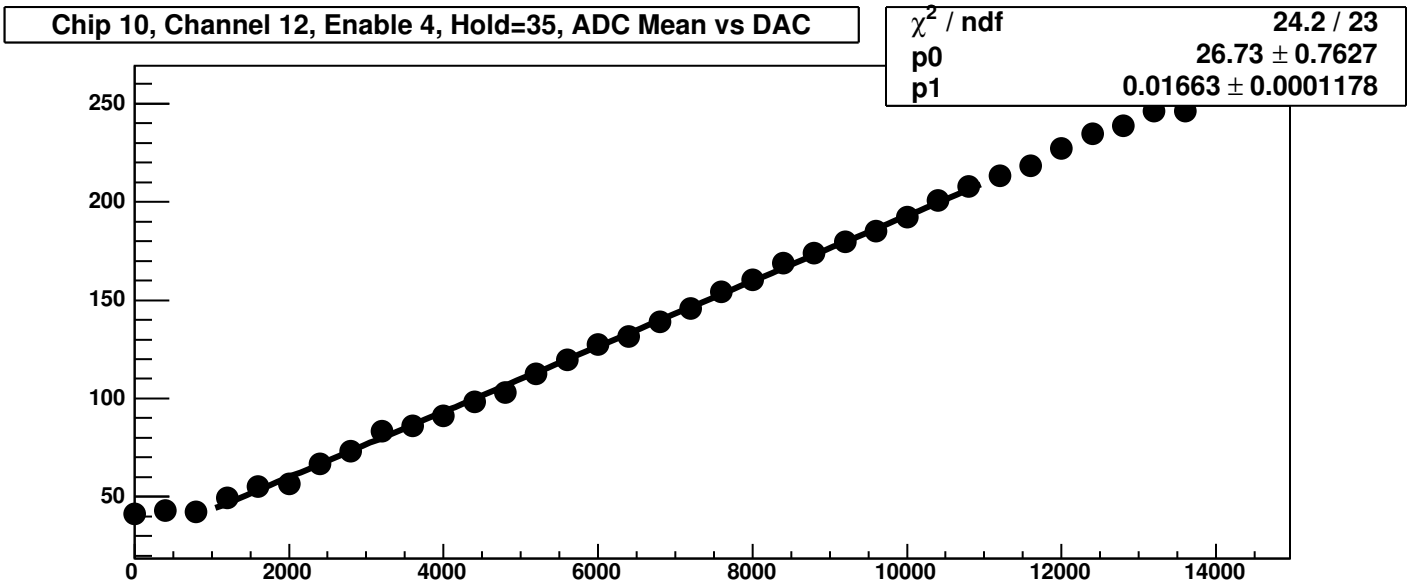

Chip 10, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

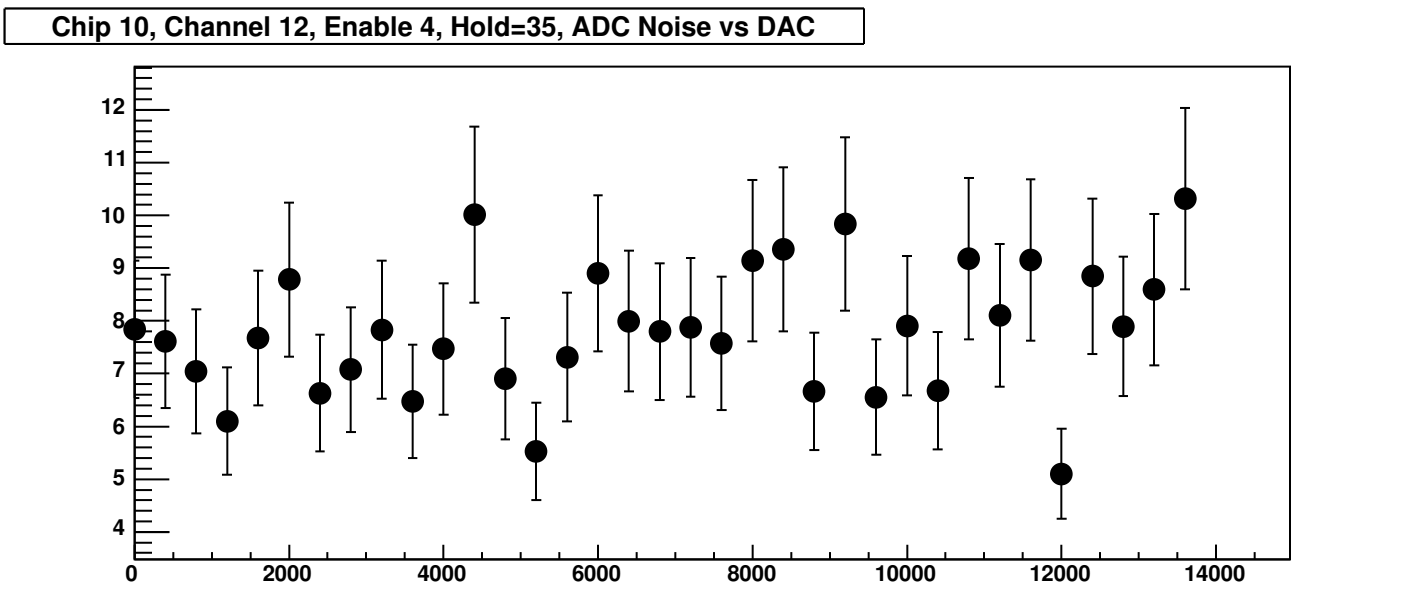

Chip 10, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
0
2000
4000
6000
8000
10000
12000
14000

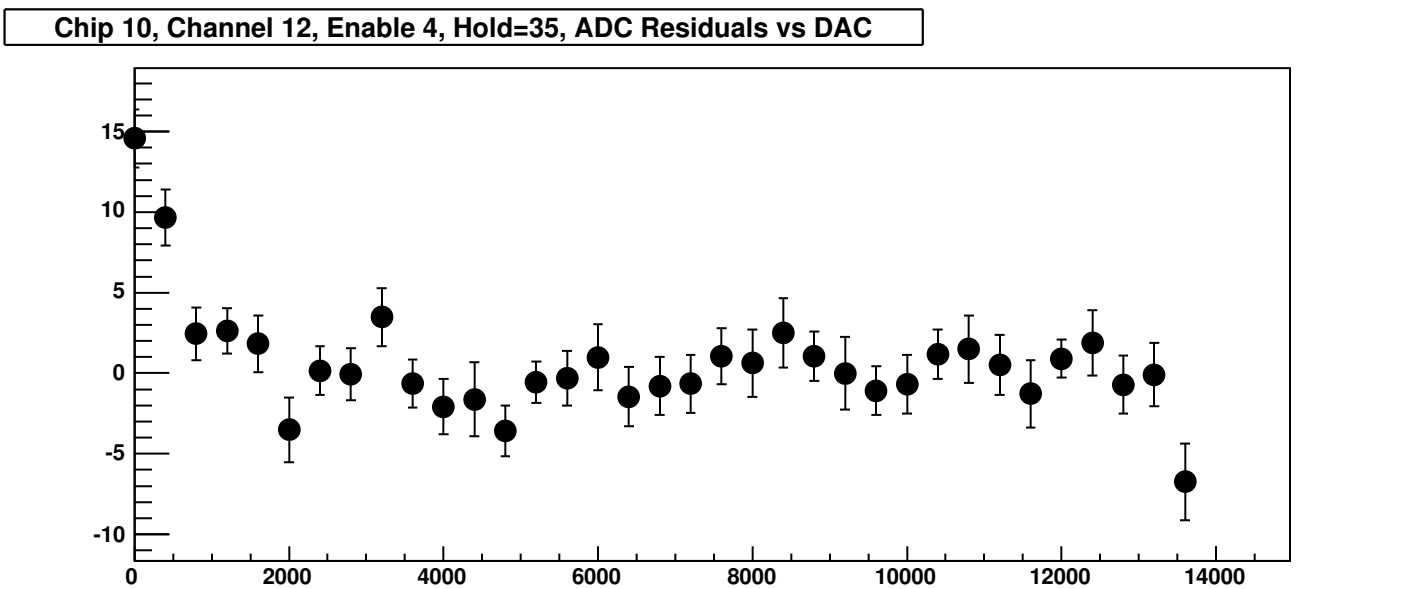

Chip 10, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.43 / 23
p0 25.52 ± 0.702
p1 0.01056 ± 0.0001076
160
140
120
100
80
60
40
0
2000
4000
6000
8000
10000
12000
14000

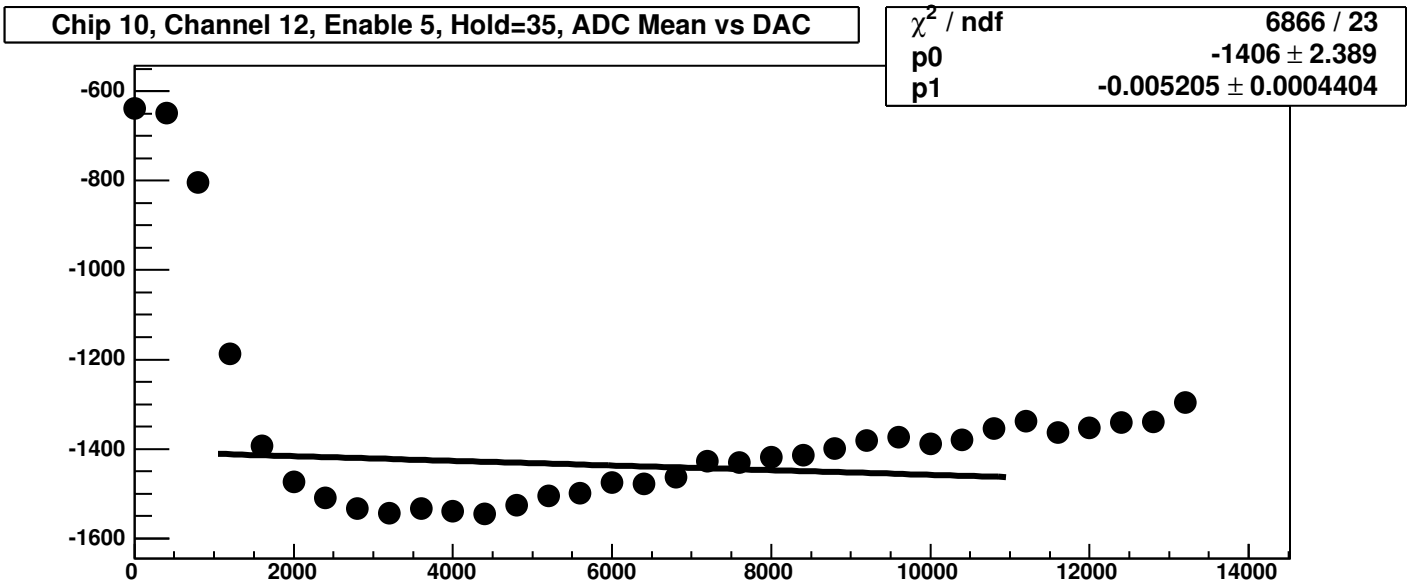

Chip 10, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

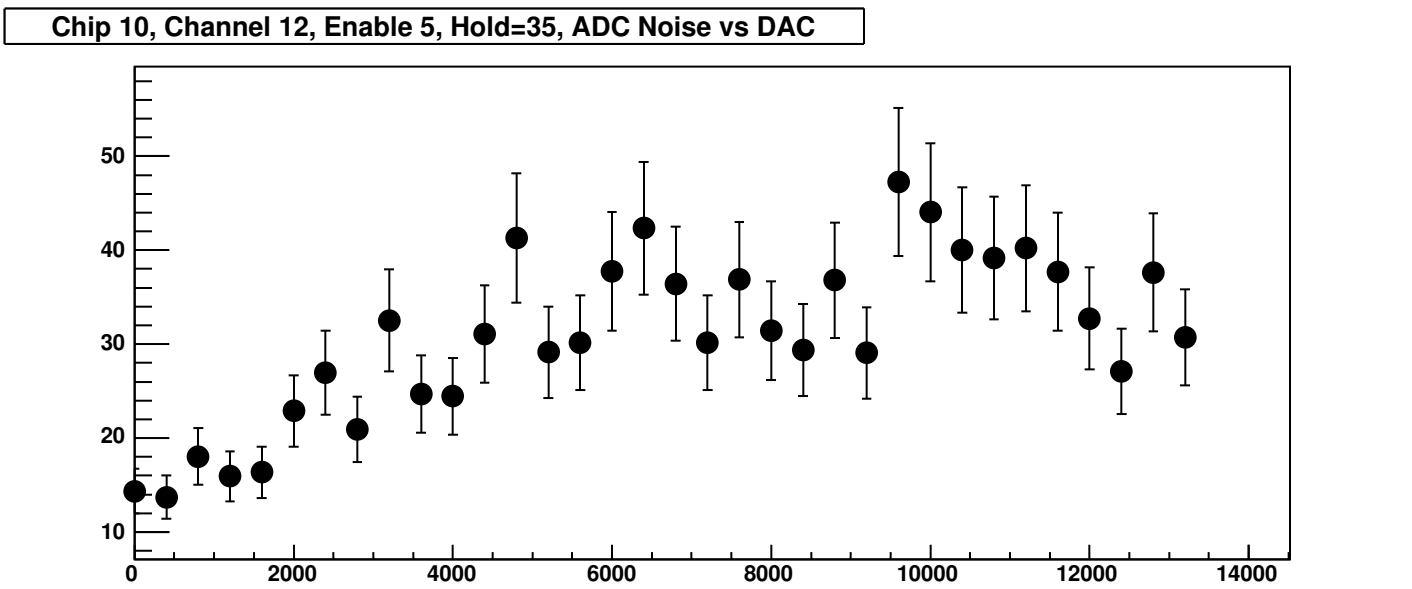

Chip 10, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

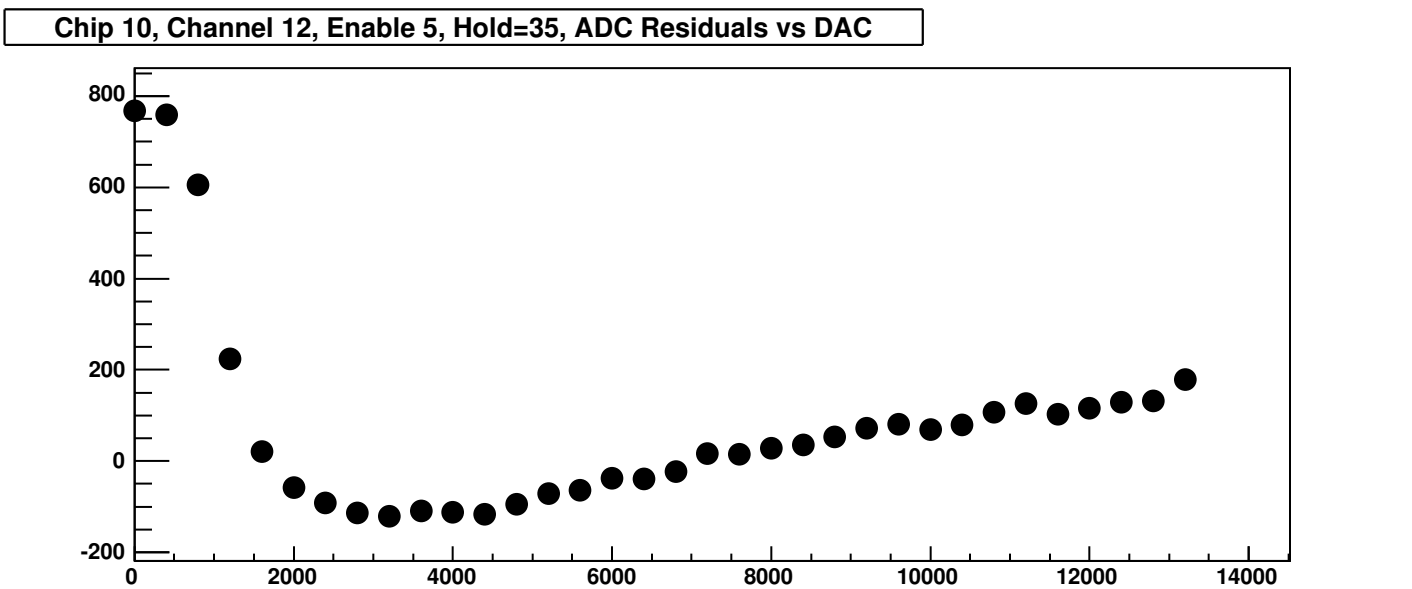

Chip 10, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.2 / 23
p0 26.73 ± 0.7627
p1 0.01663 ± 0.0001178
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

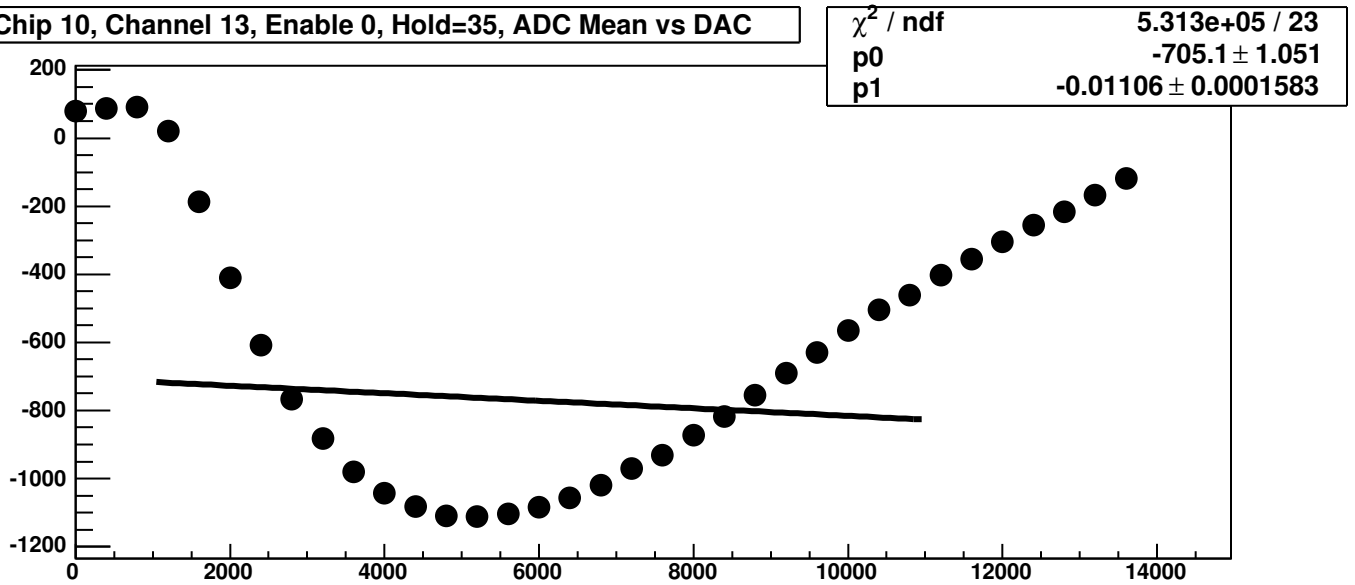

Chip 10, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 6866 / 23
p0 -1406 ± 2.389
p1 -0.005205 ± 0.0004404
-600
-800
-1000
-1200
-1400
-1600
0
2000
4000
6000
8000
10000
12000
14000

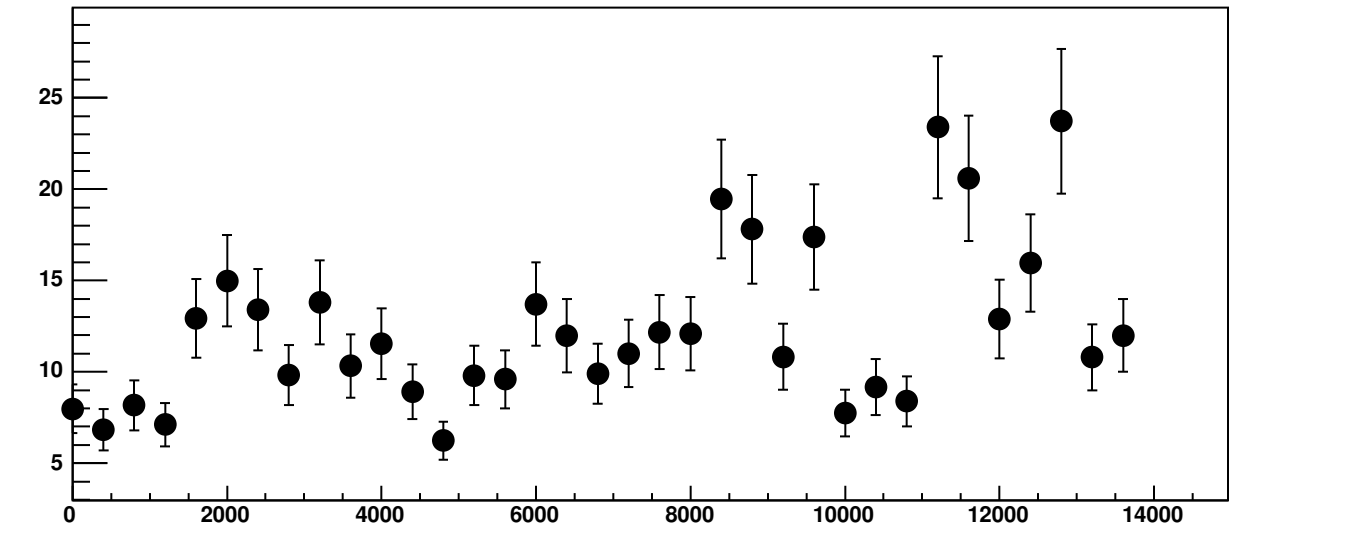

Chip 10, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

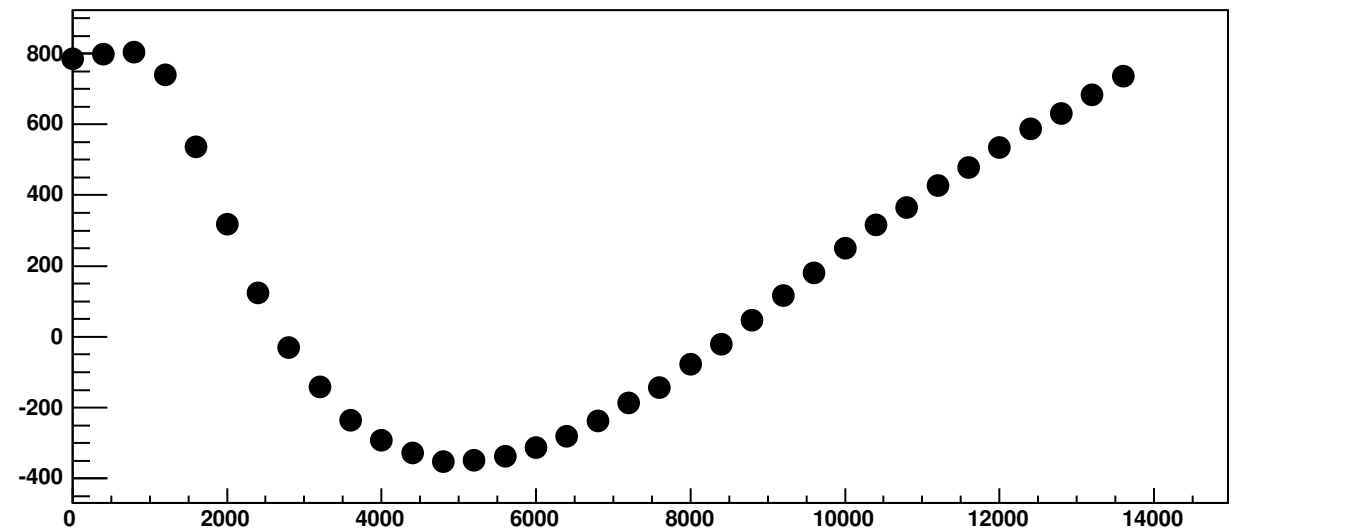
Chip 10, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

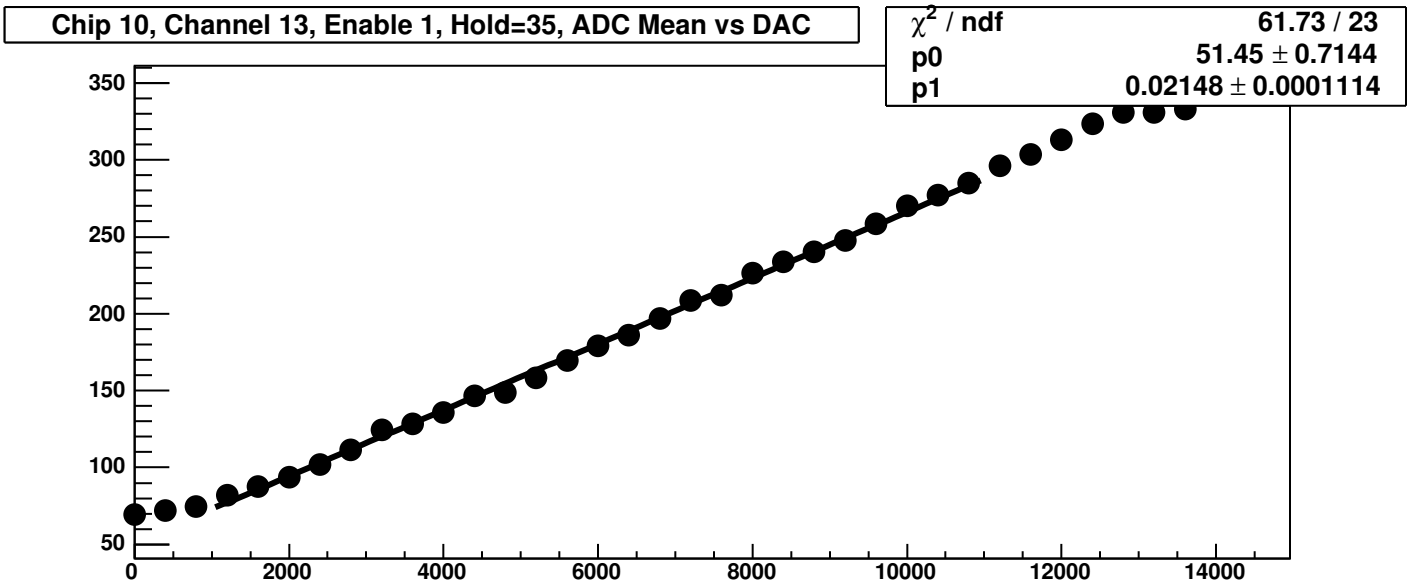


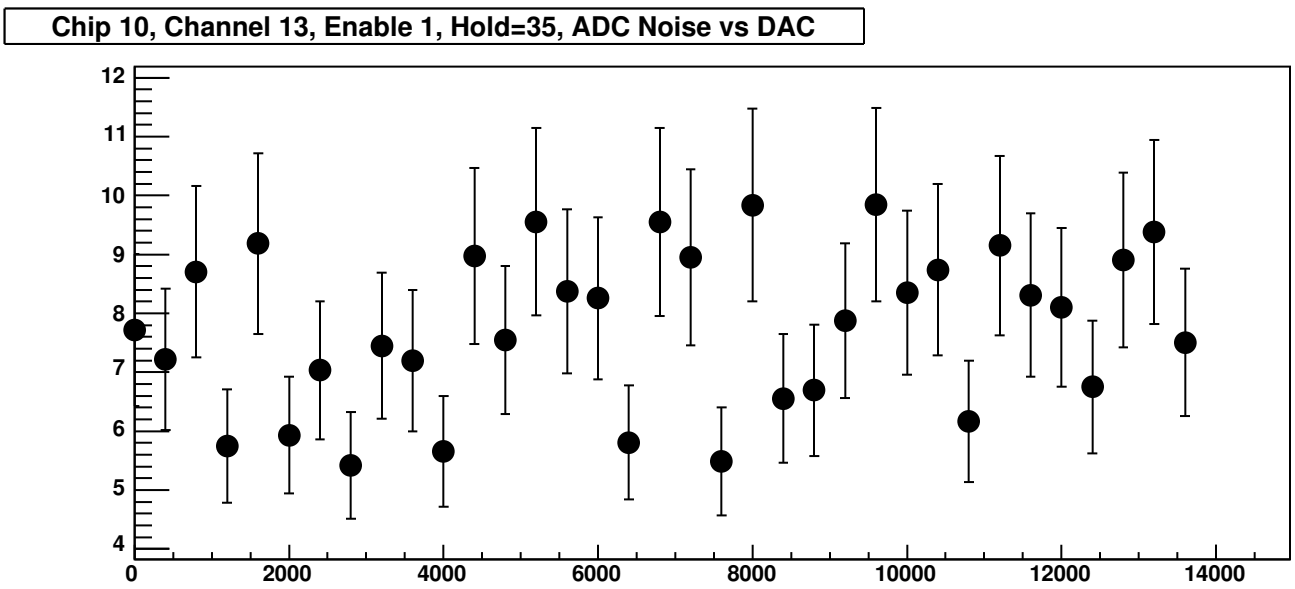
Chip 10, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

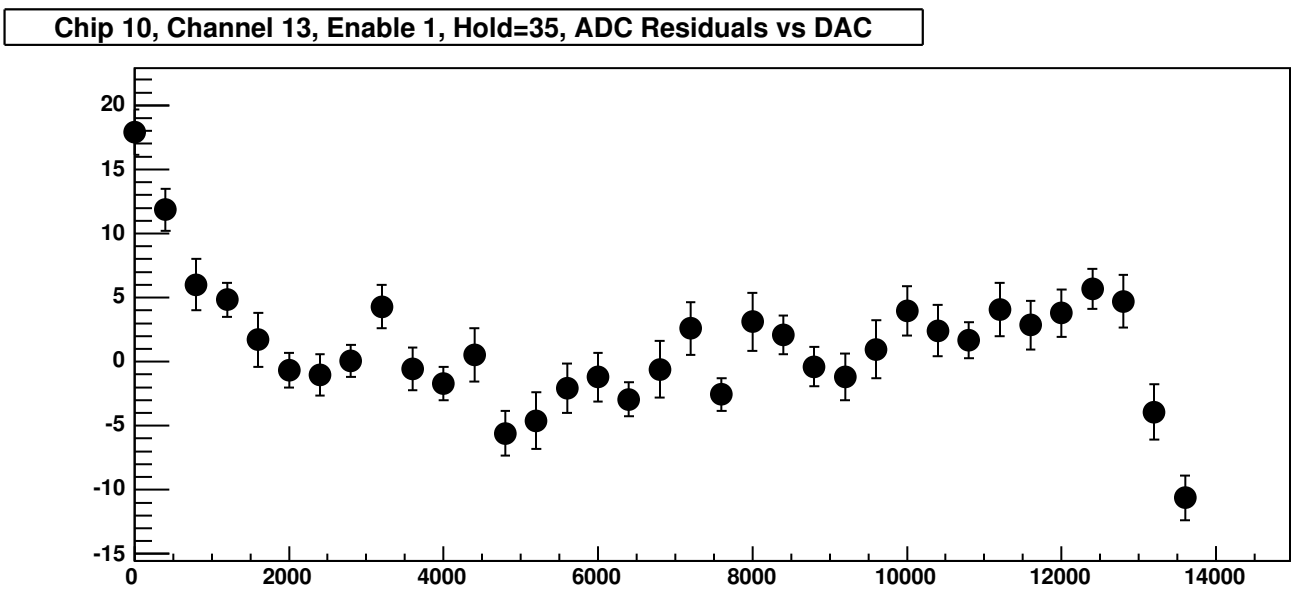


Chip 10, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



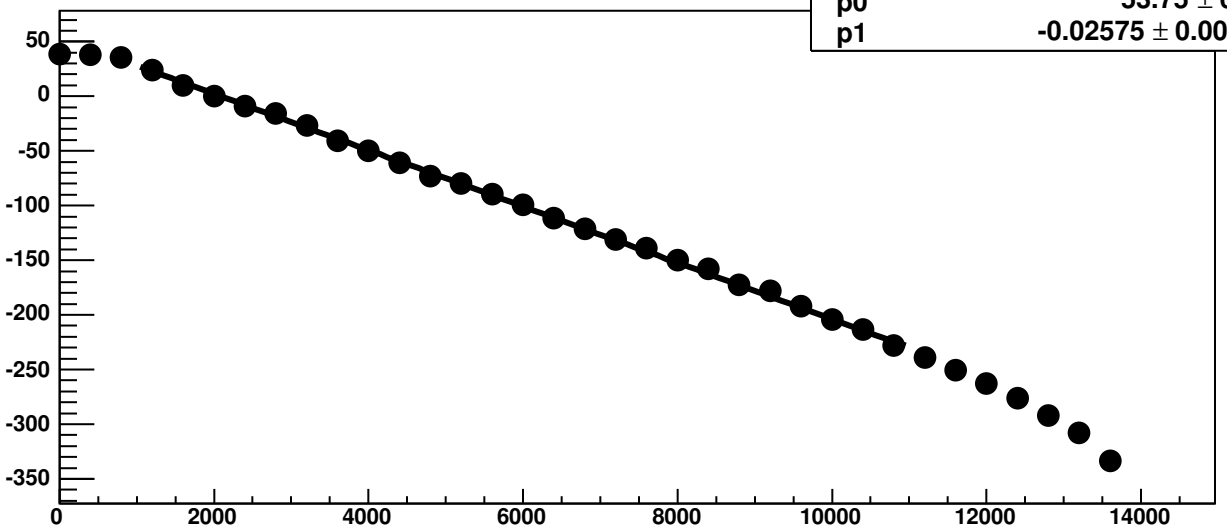

Chip 10, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 61.73 / 23
p0 51.45 ± 0.7144
p1 0.02148 ± 0.0001114
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

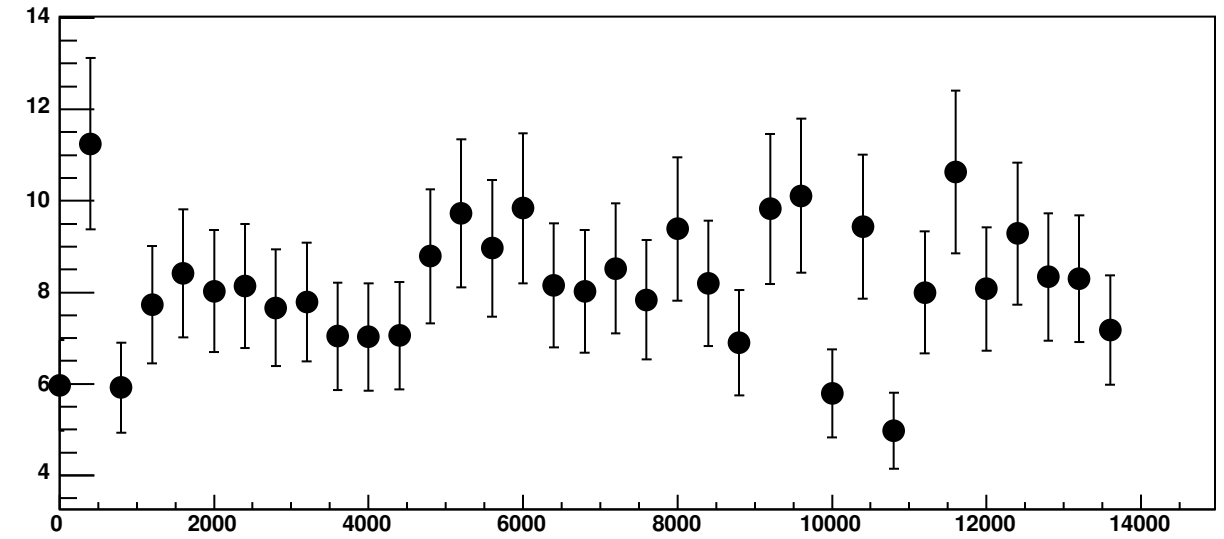

Chip 10, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

Chip 10, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

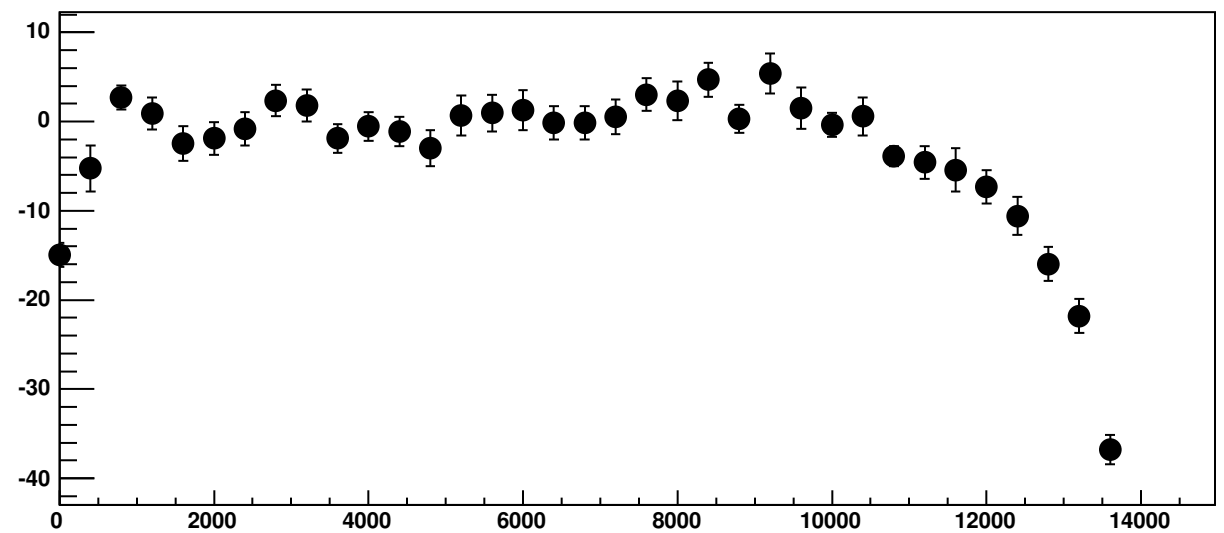
χ^2 / ndf	38.79 / 23
p0	53.75 $\pm$ 0.8097
p1	-0.02575 $\pm$ 0.0001164



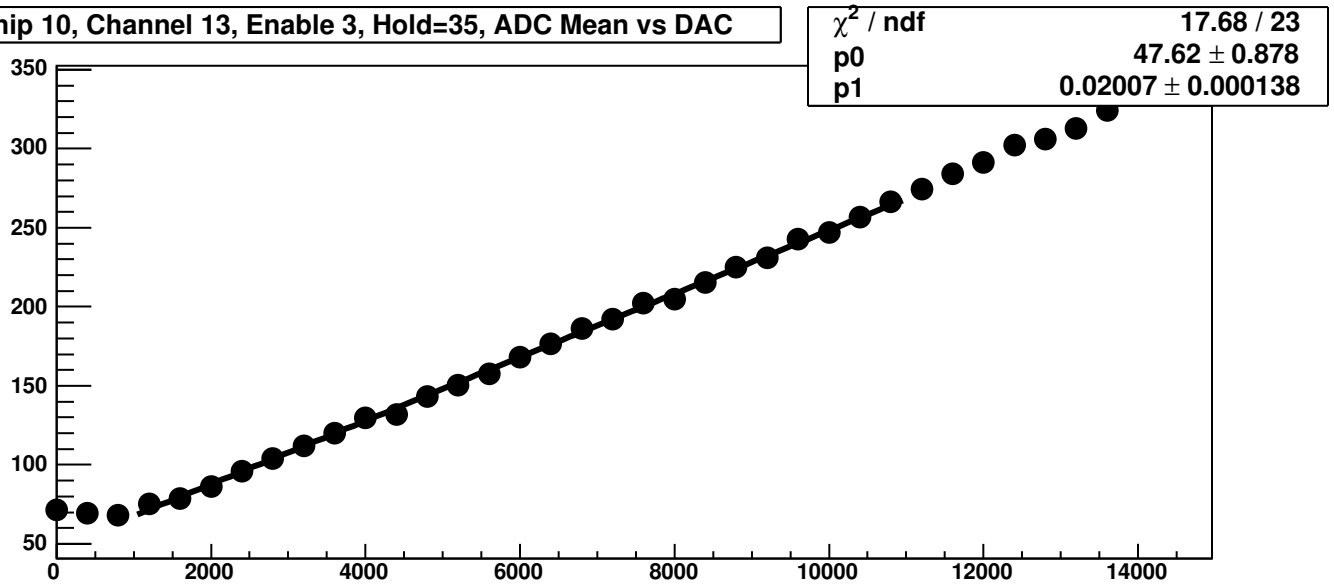
Chip 10, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC



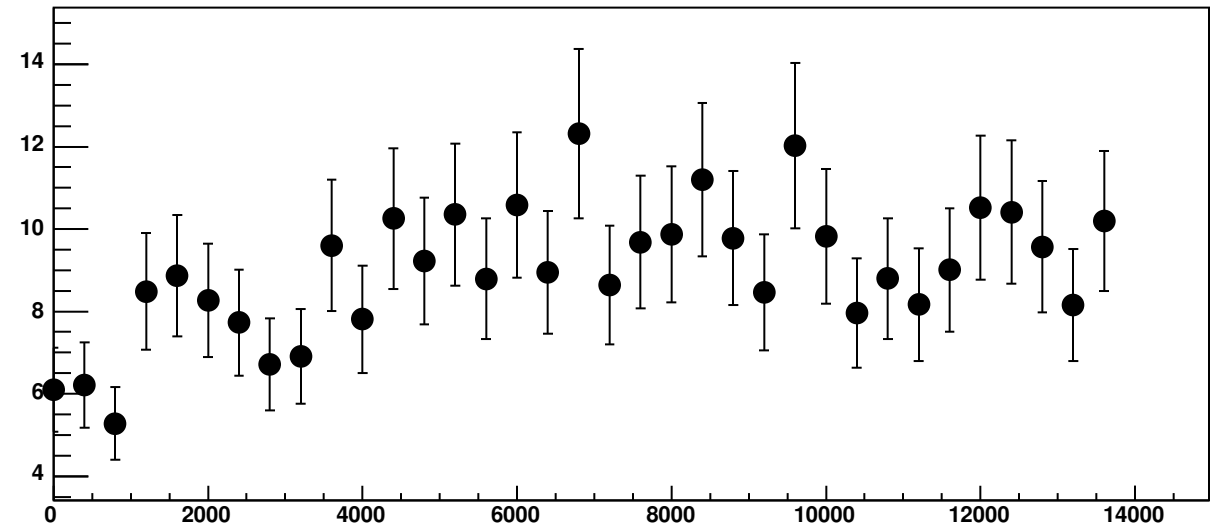
Chip 10, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC



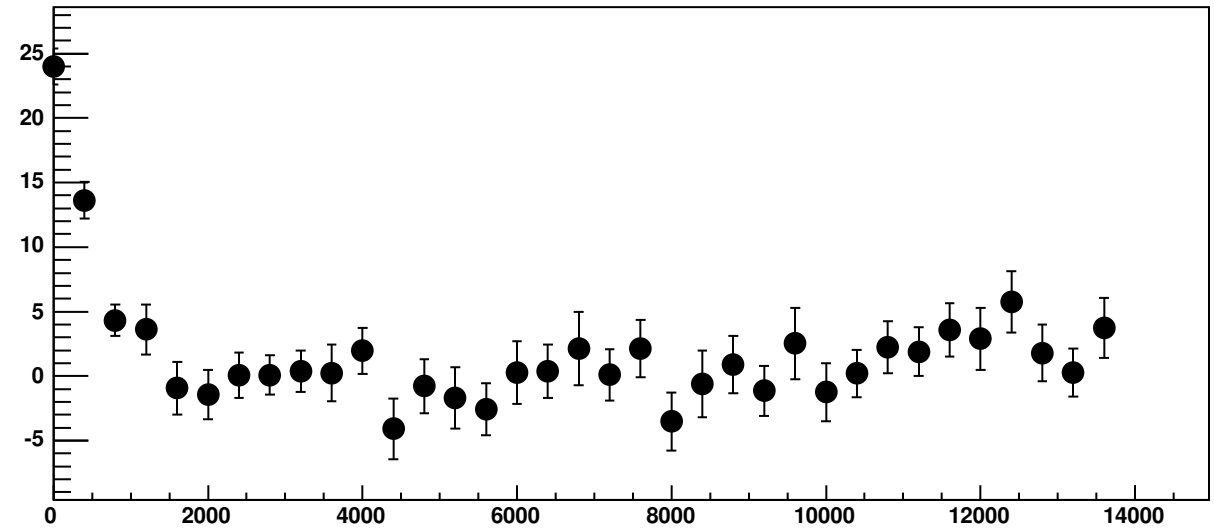
Chip 10, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

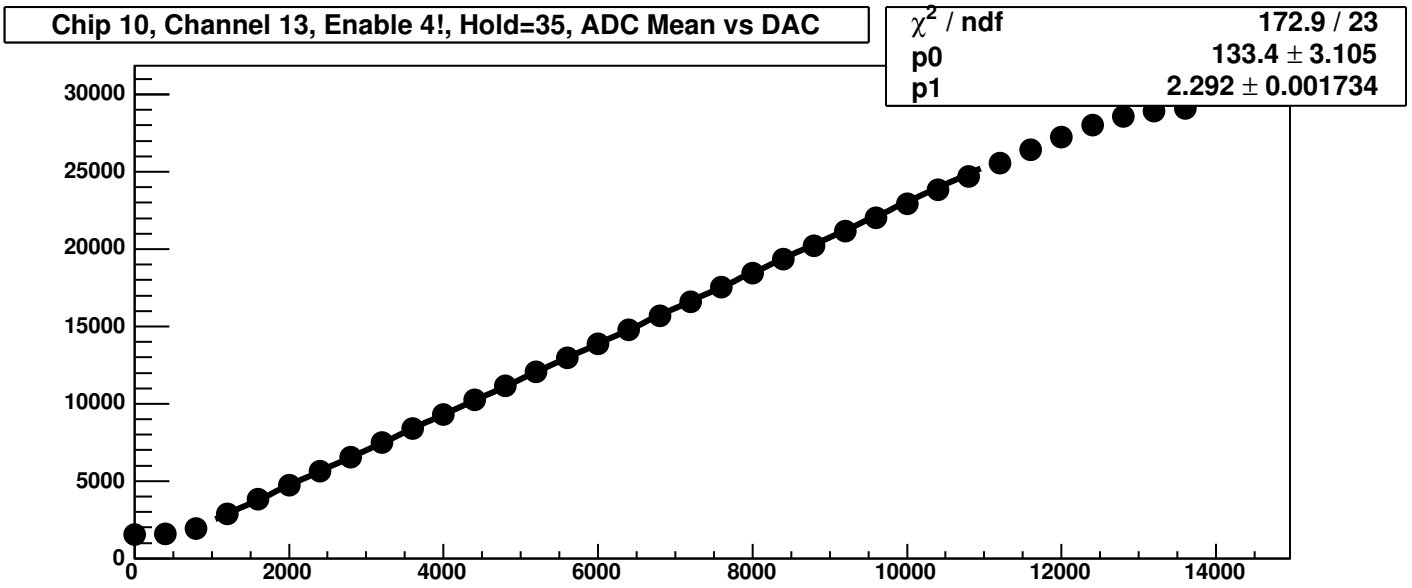


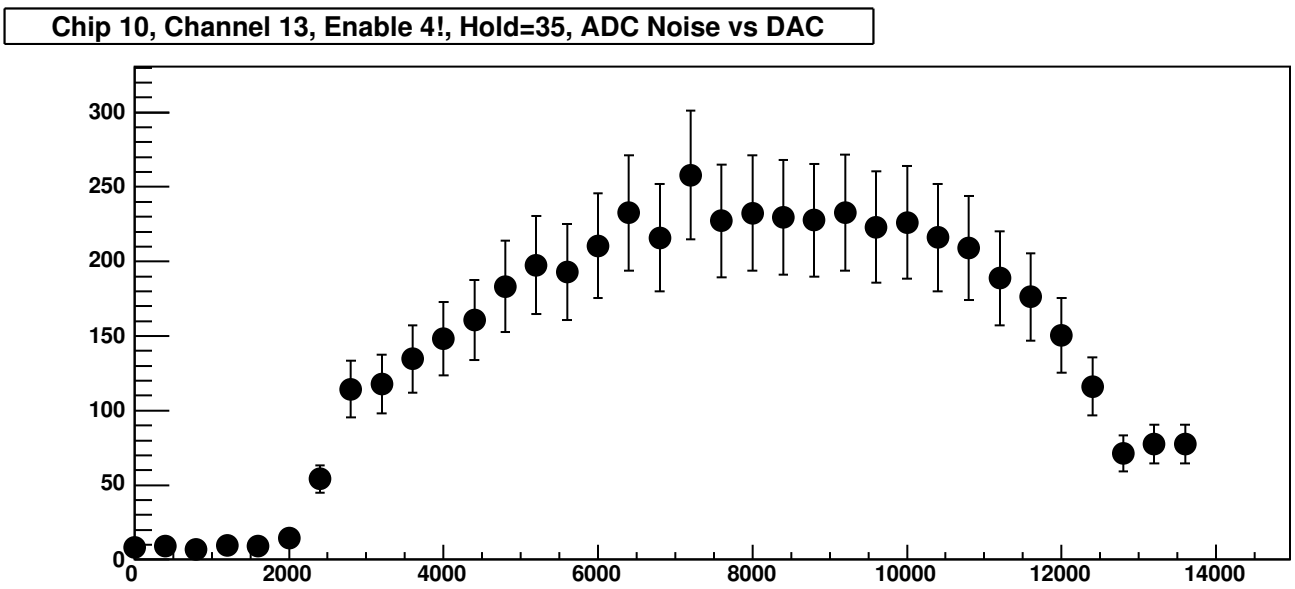
Chip 10, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

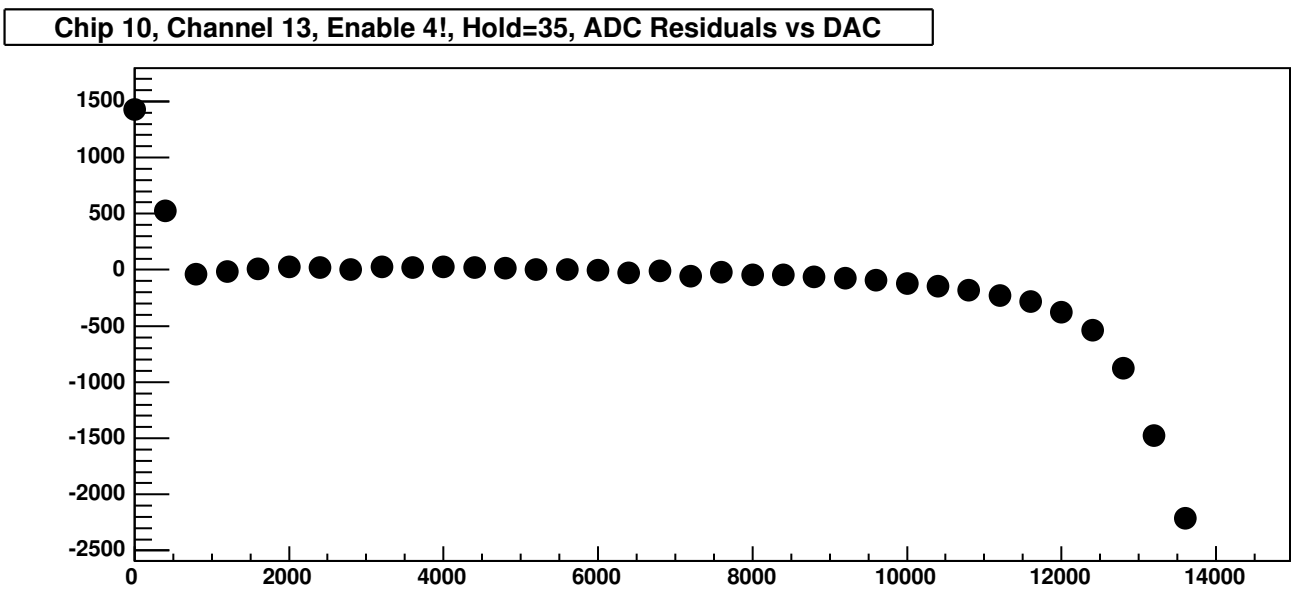


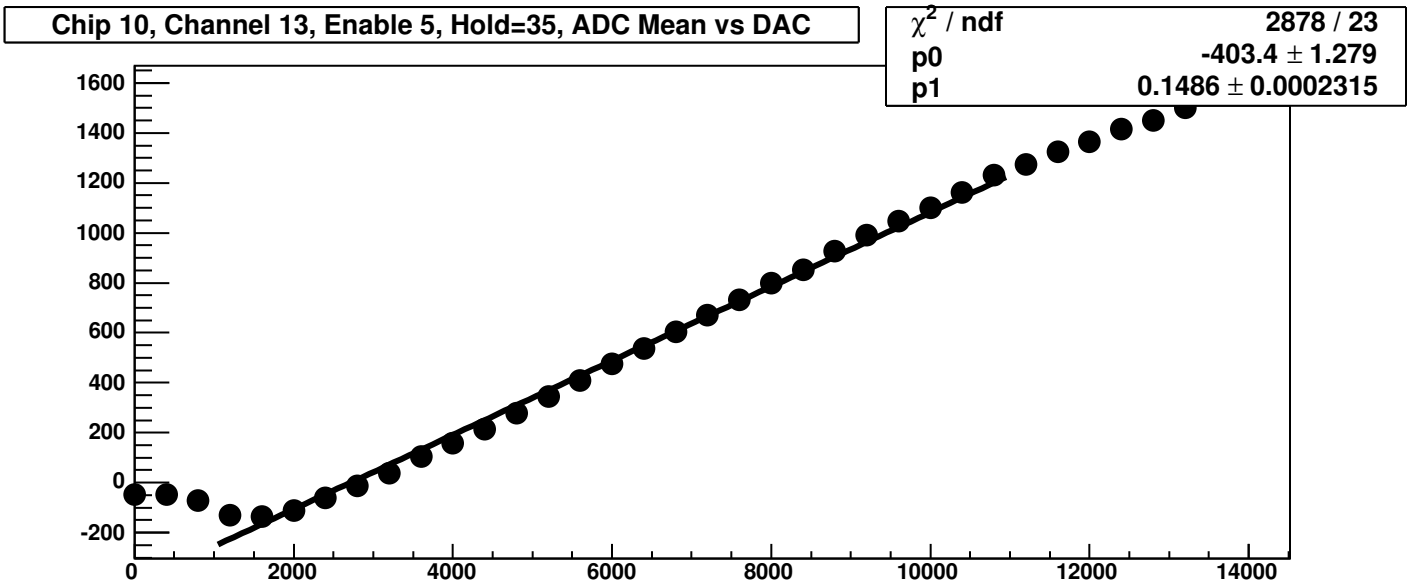
Chip 10, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

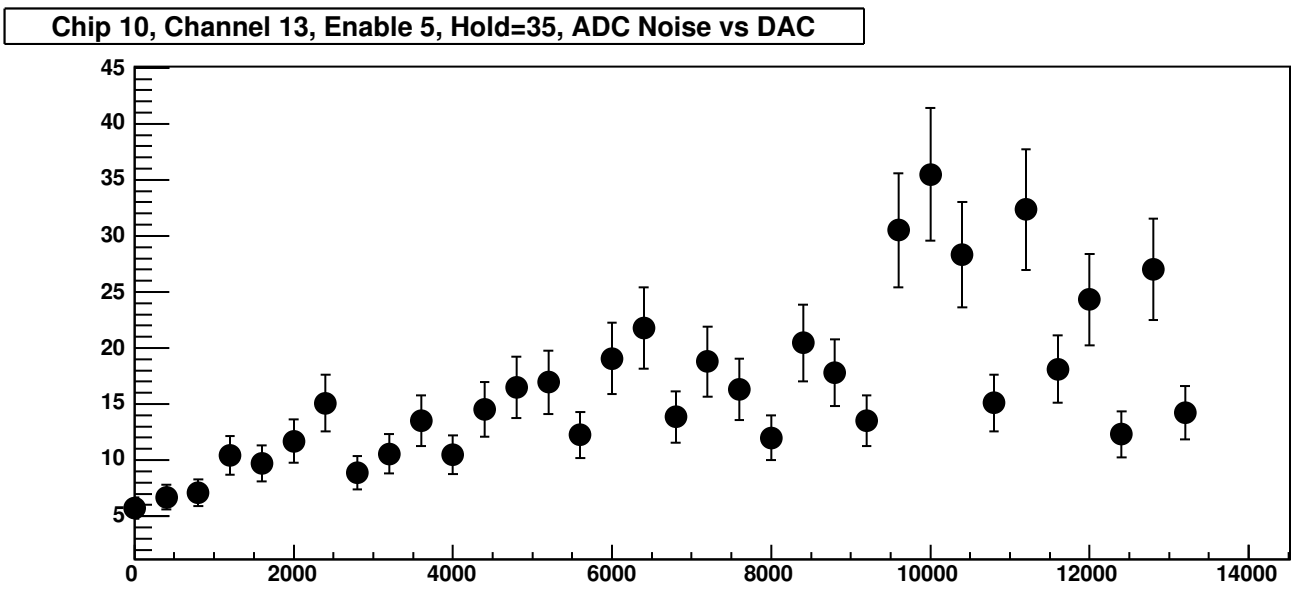


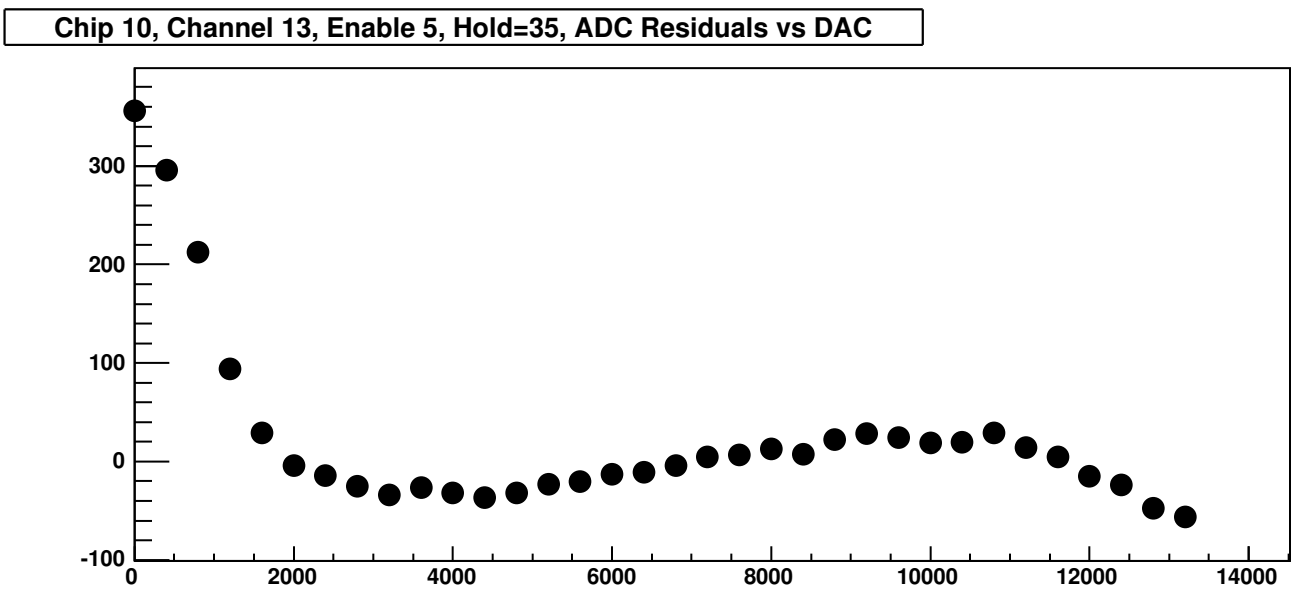

Chip 10, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 172.9 / 23
p0 133.4 ± 3.105
p1 2.292 ± 0.001734
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

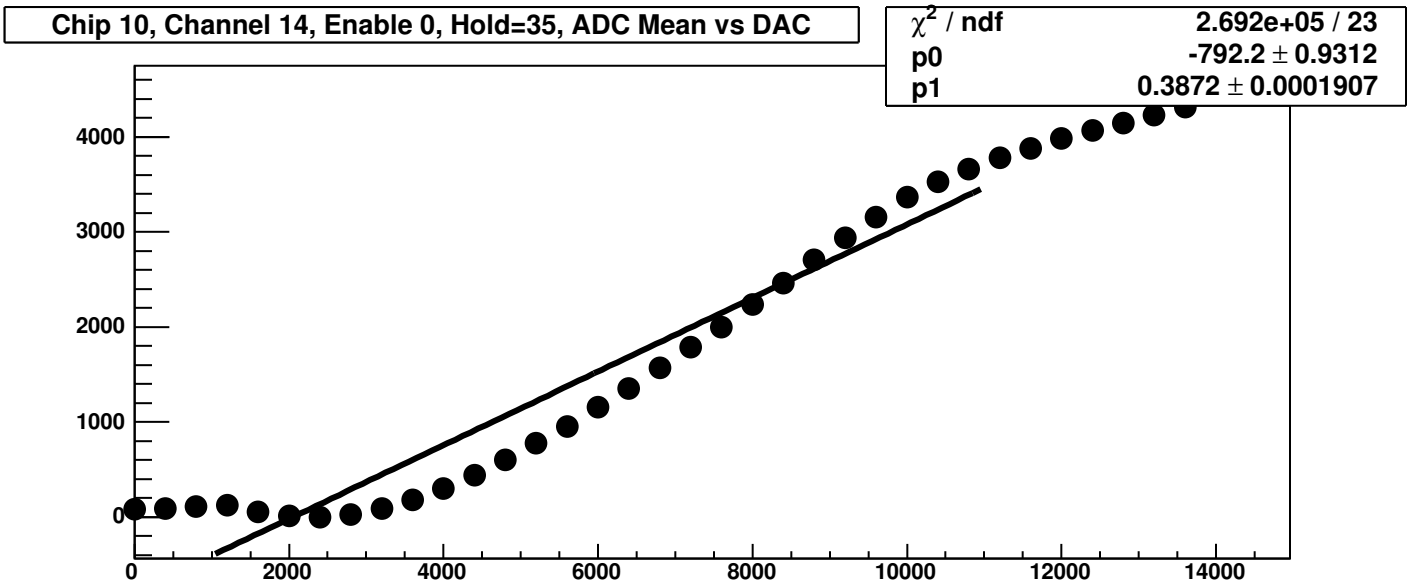

Chip 10, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

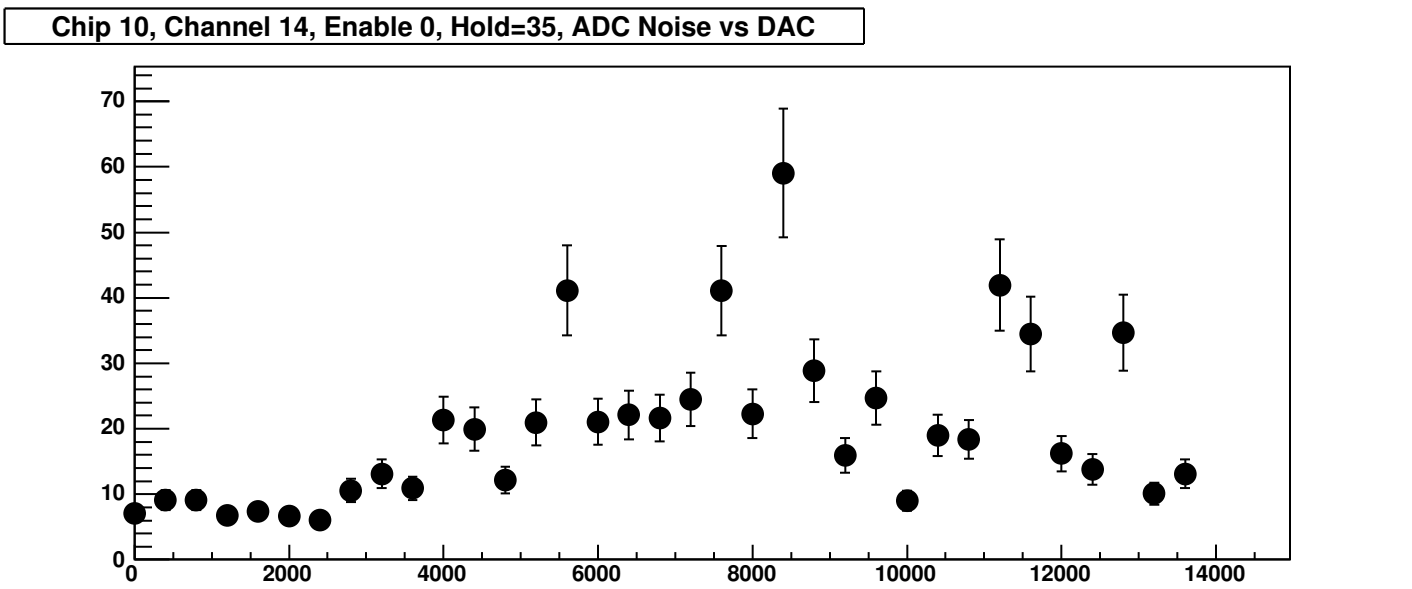

Chip 10, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

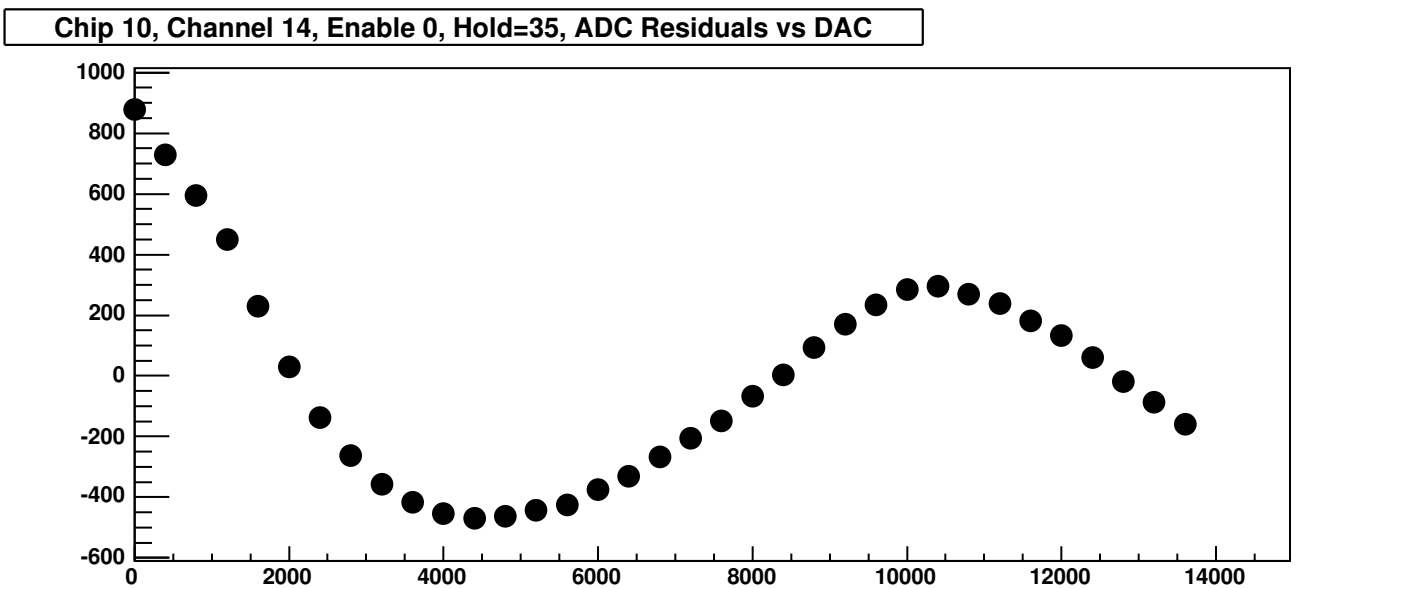

Chip 10, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2878 / 23
p0 -403.4 ± 1.279
p1 0.1486 ± 0.0002315
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

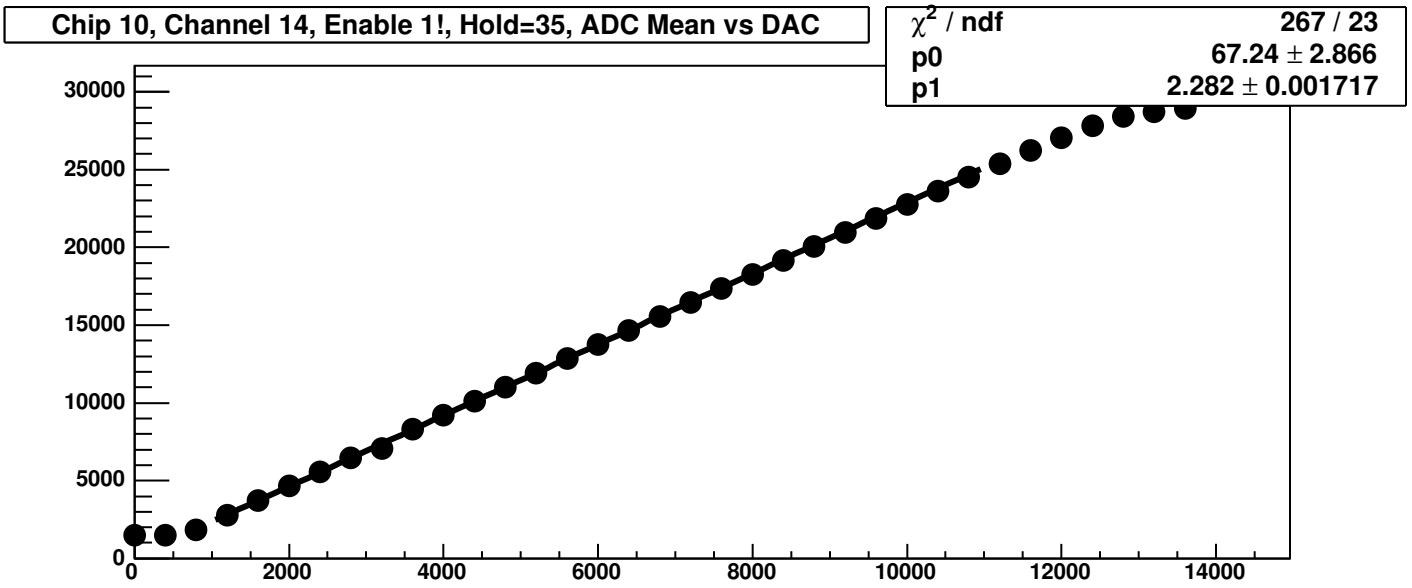

Chip 10, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

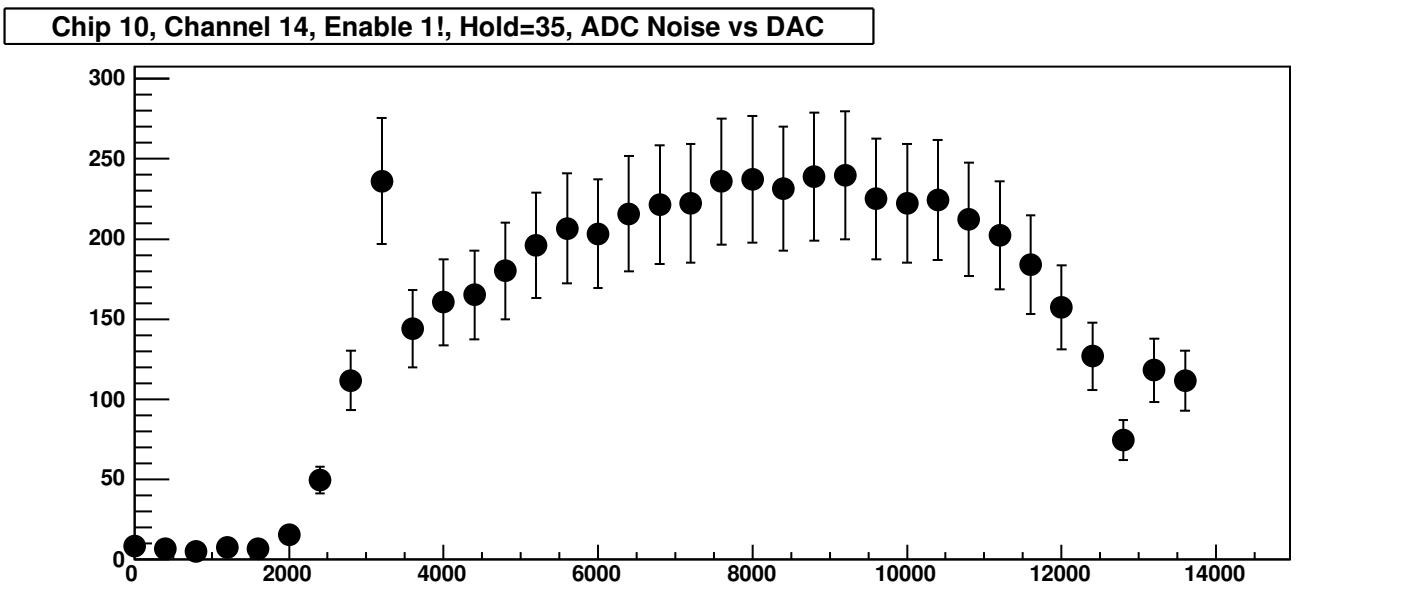

Chip 10, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

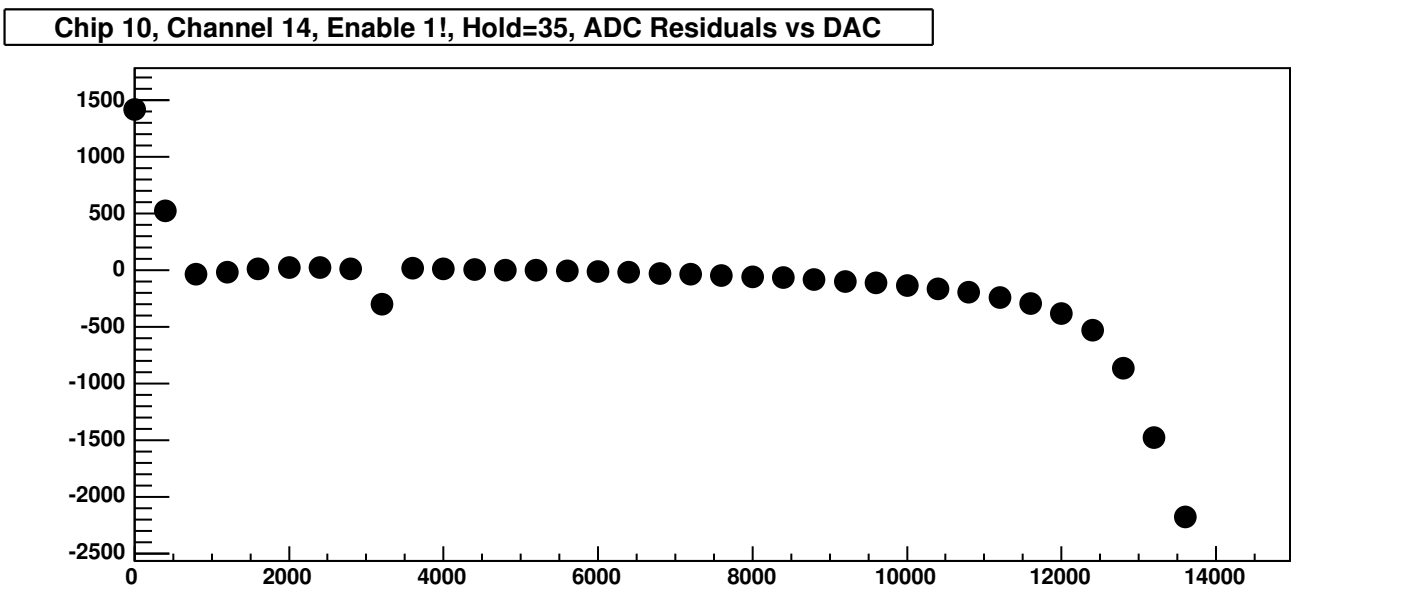

Chip 10, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.692e+05 / 23
p0 -792.2 ± 0.9312
p1 0.3872 ± 0.0001907
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

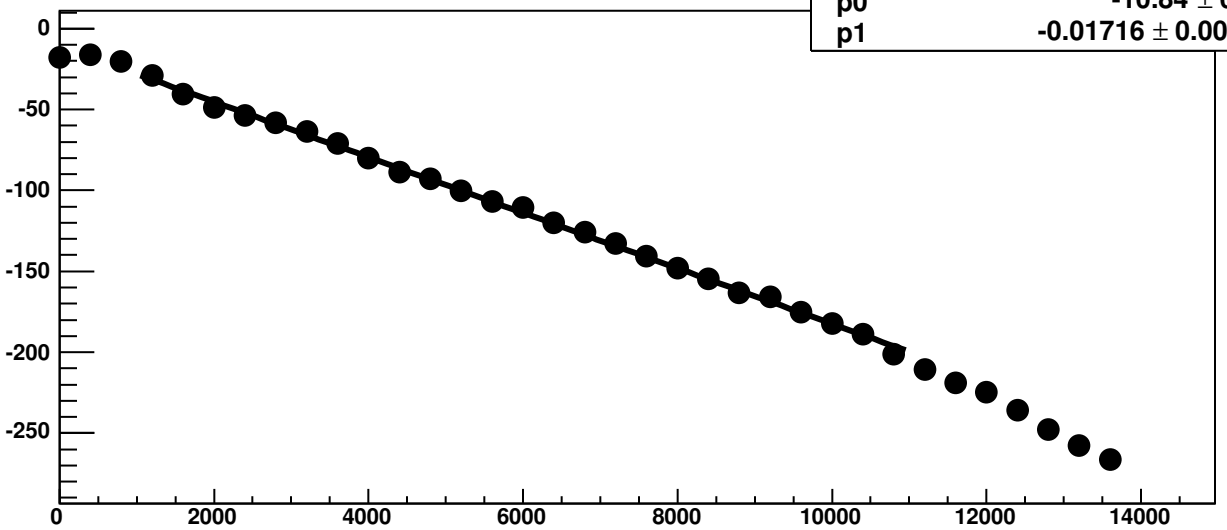

Chip 10, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 267 / 23
p0 67.24 ± 2.866
p1 2.282 ± 0.001717
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

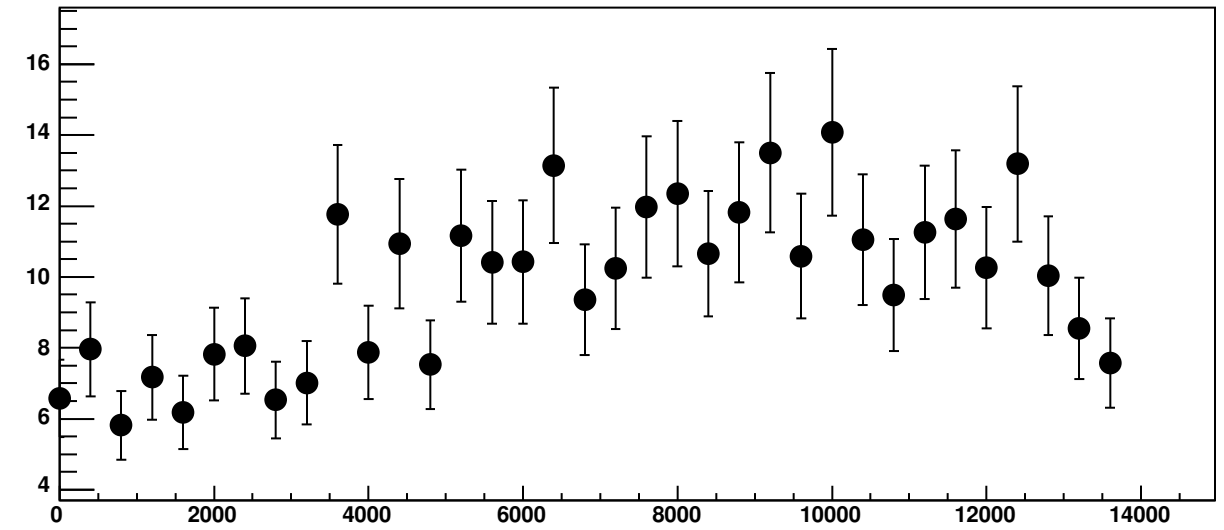

Chip 10, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

Chip 10, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

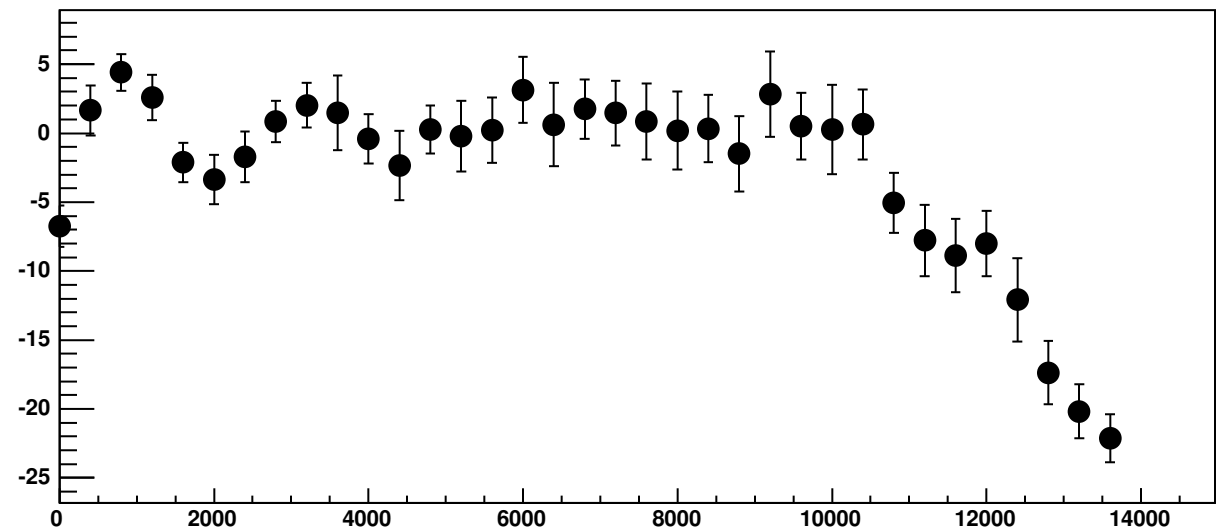
χ^2 / ndf	21.84 / 23
p0	-10.84 ± 0.8347
p1	-0.01716 ± 0.0001454

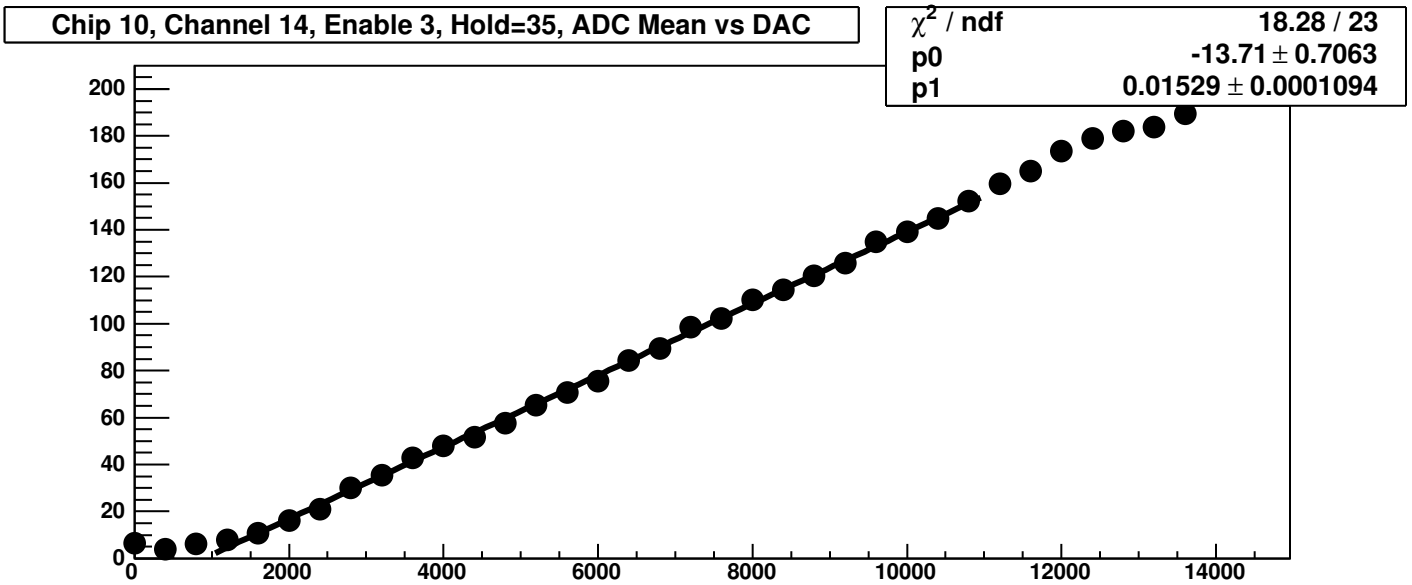


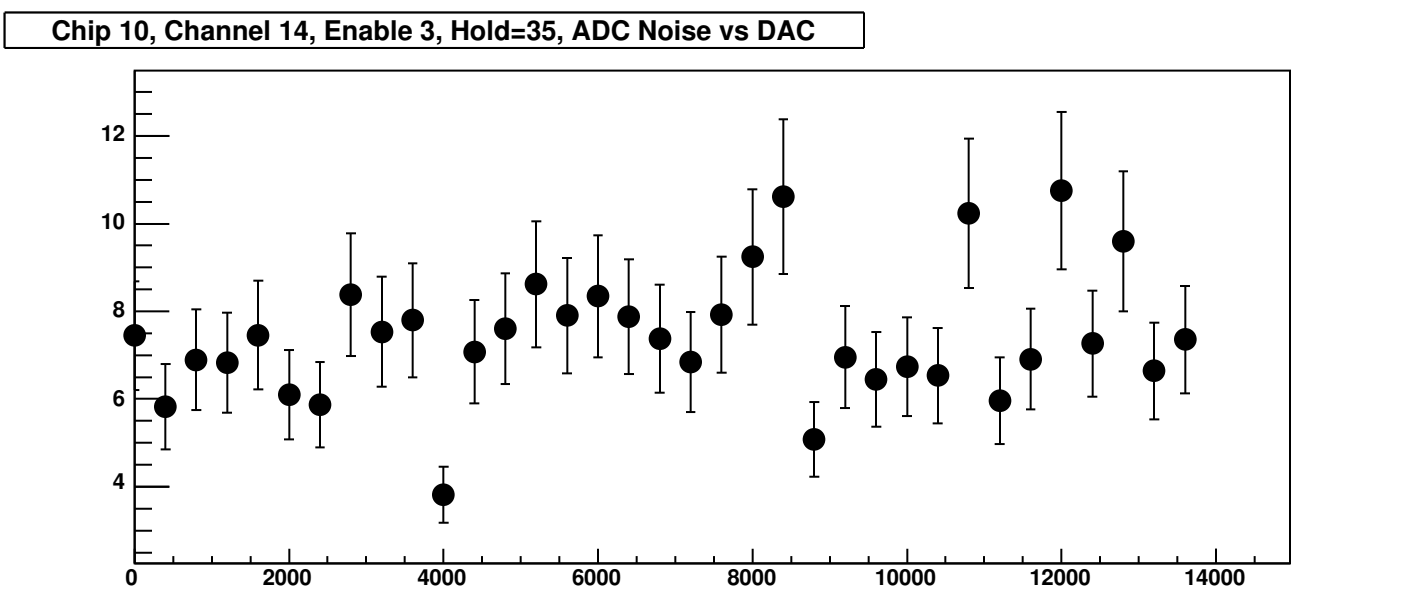
Chip 10, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC

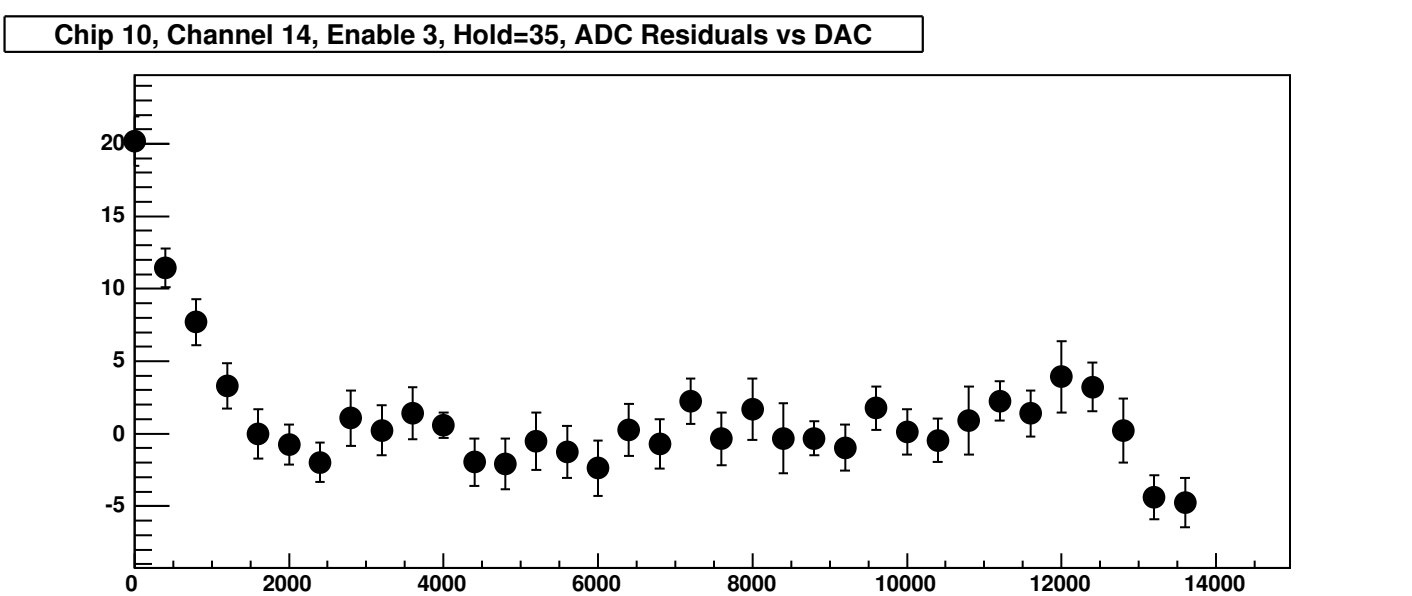


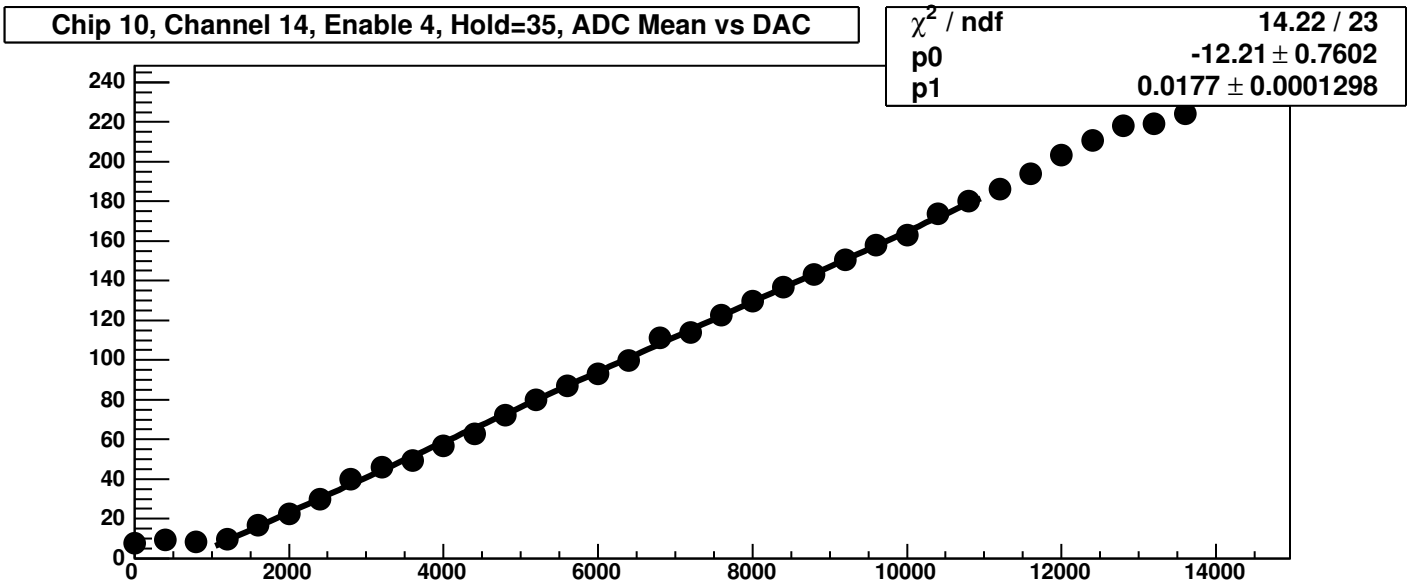
Chip 10, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

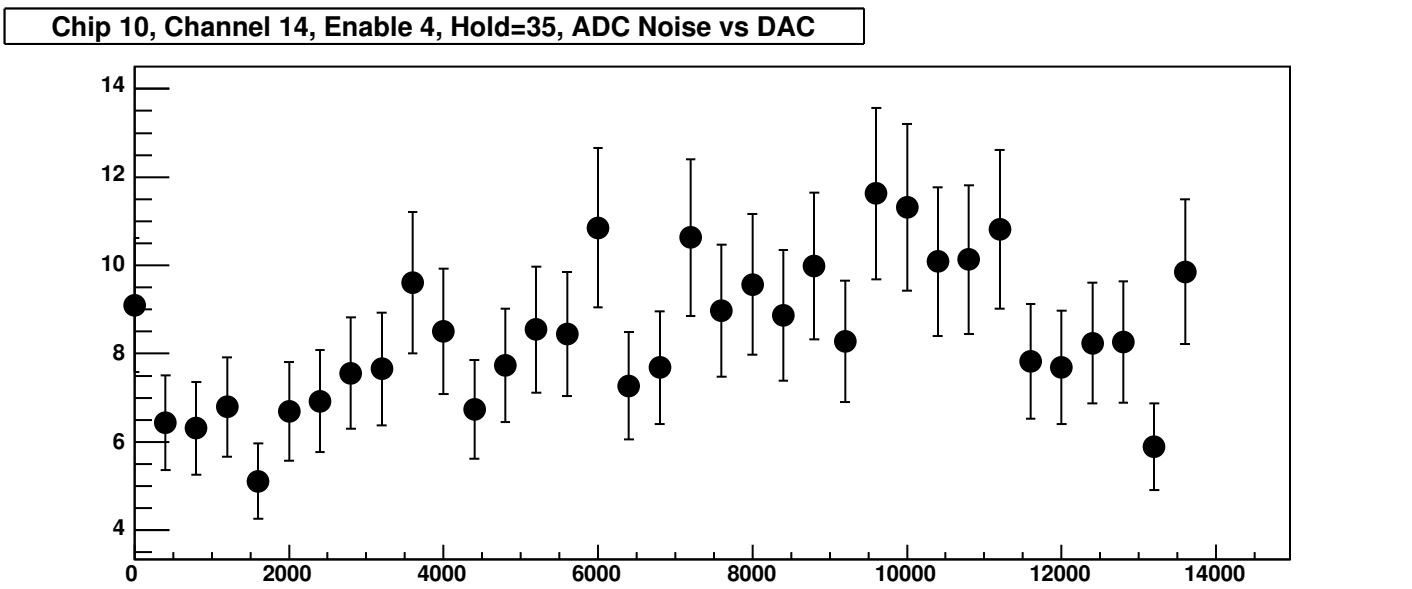


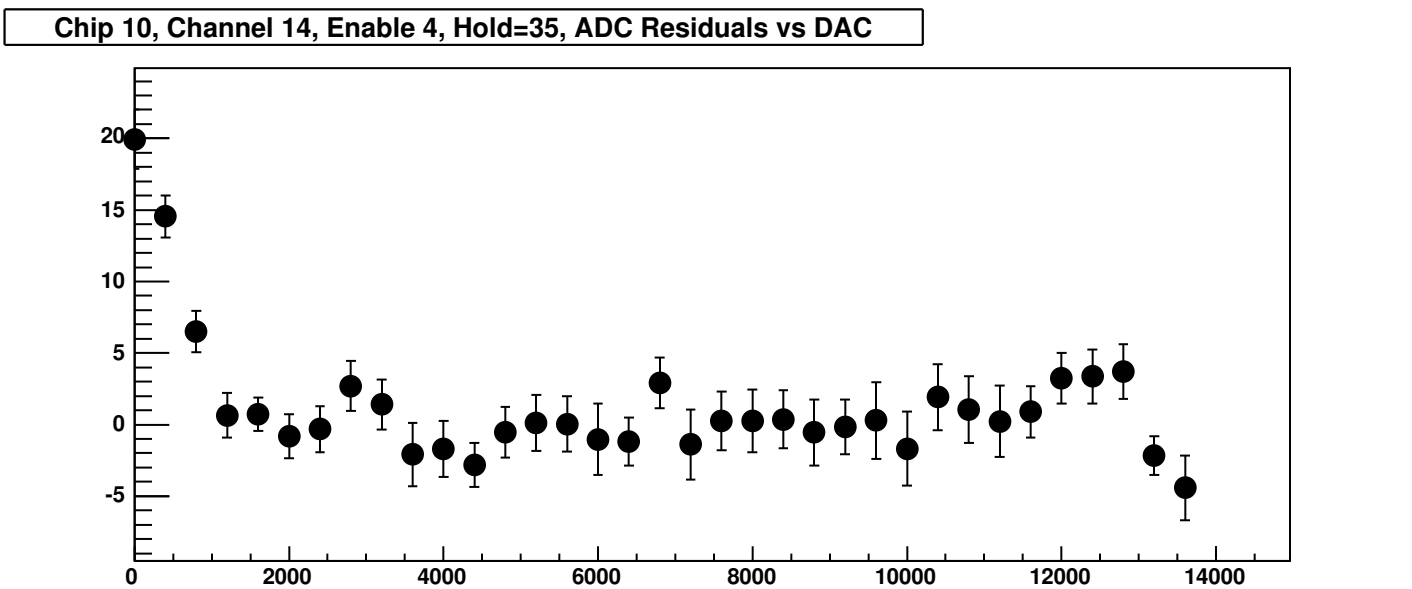

Chip 10, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 18.28 / 23
p0 -13.71 ± 0.7063
p1 0.01529 ± 0.0001094
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

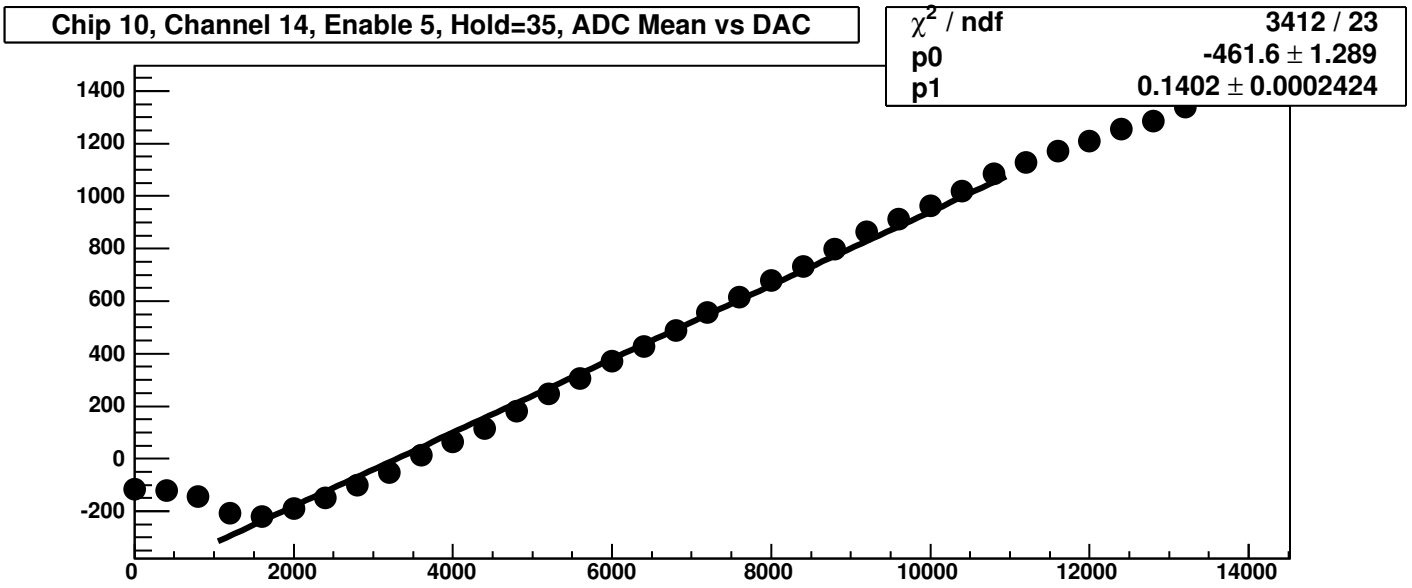

Chip 10, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

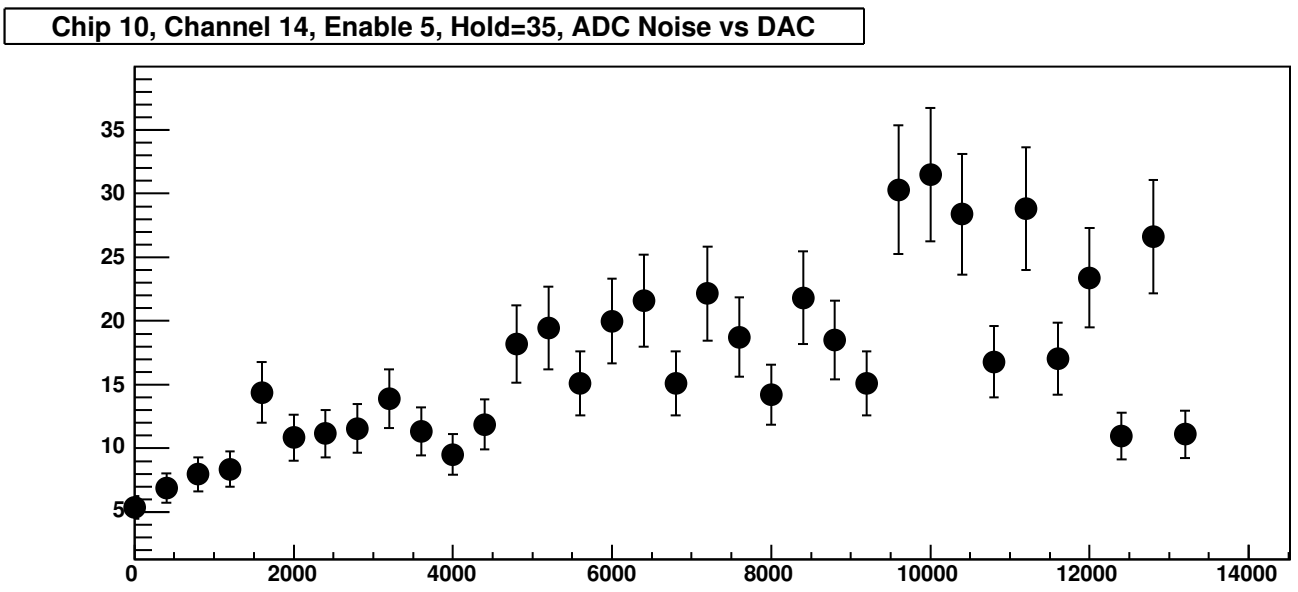

Chip 10, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

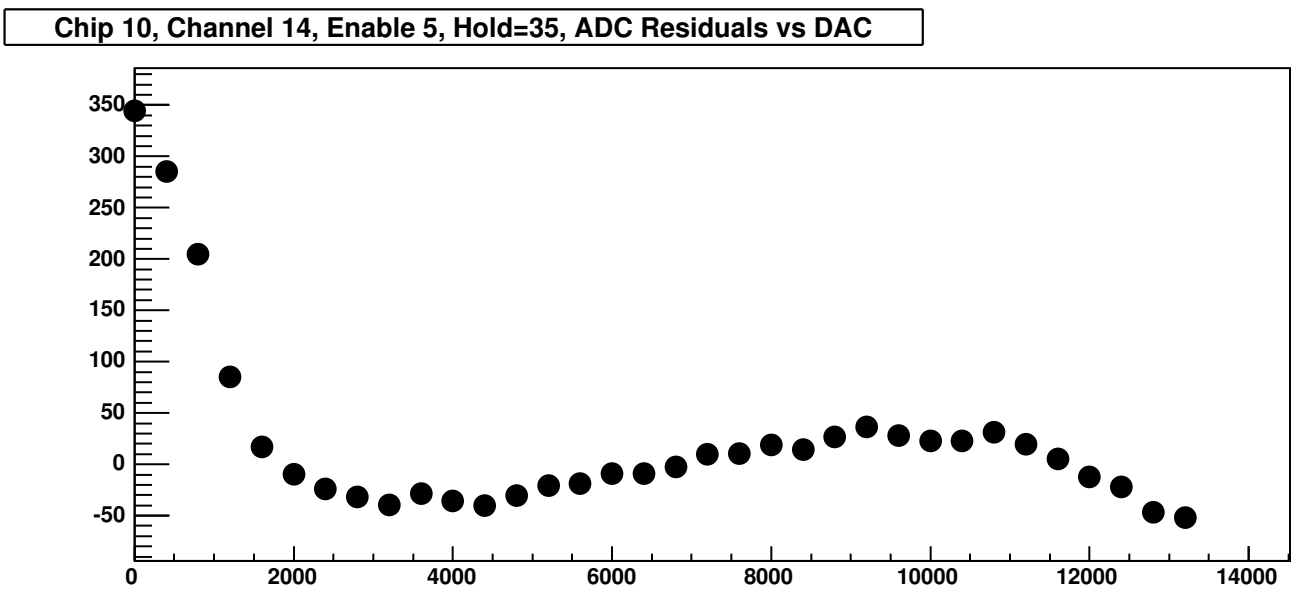

Chip 10, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 14.22 / 23
p0 -12.21 ± 0.7602
p1 0.0177 ± 0.0001298
240
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

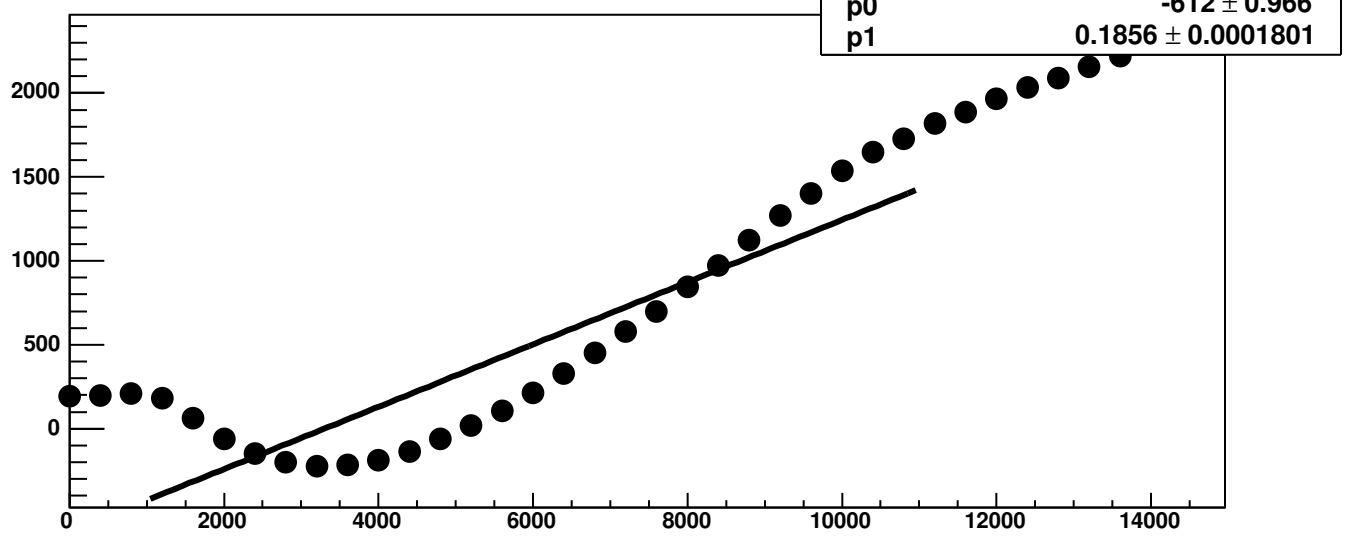

Chip 10, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3412 / 23
p0 -461.6 ± 1.289
p1 0.1402 ± 0.0002424
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

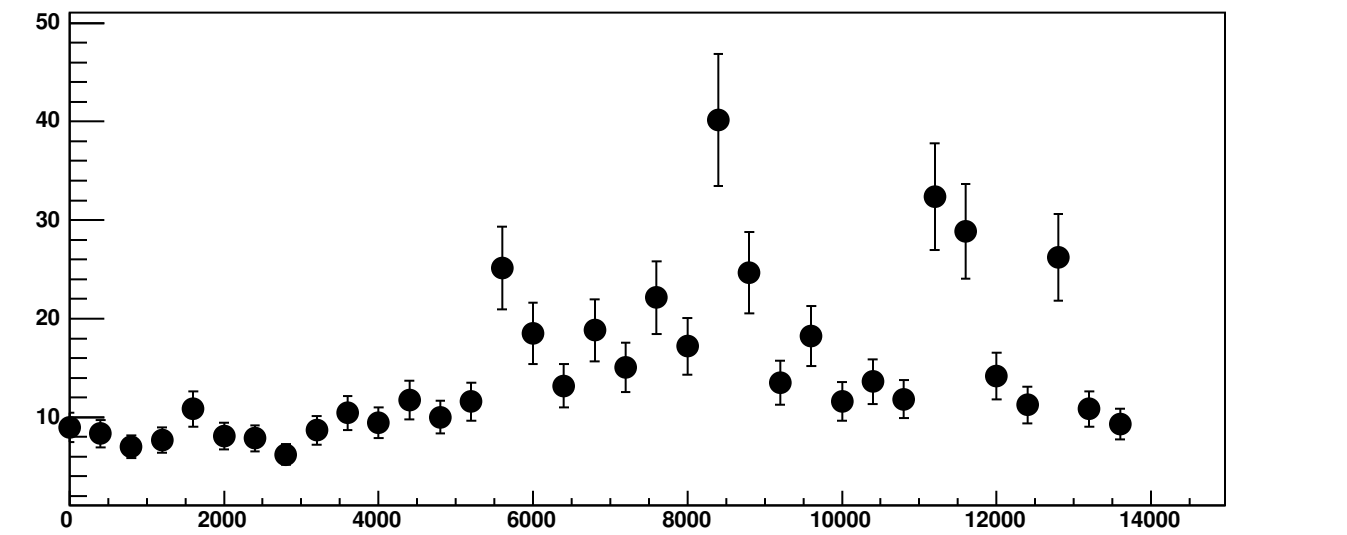

Chip 10, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

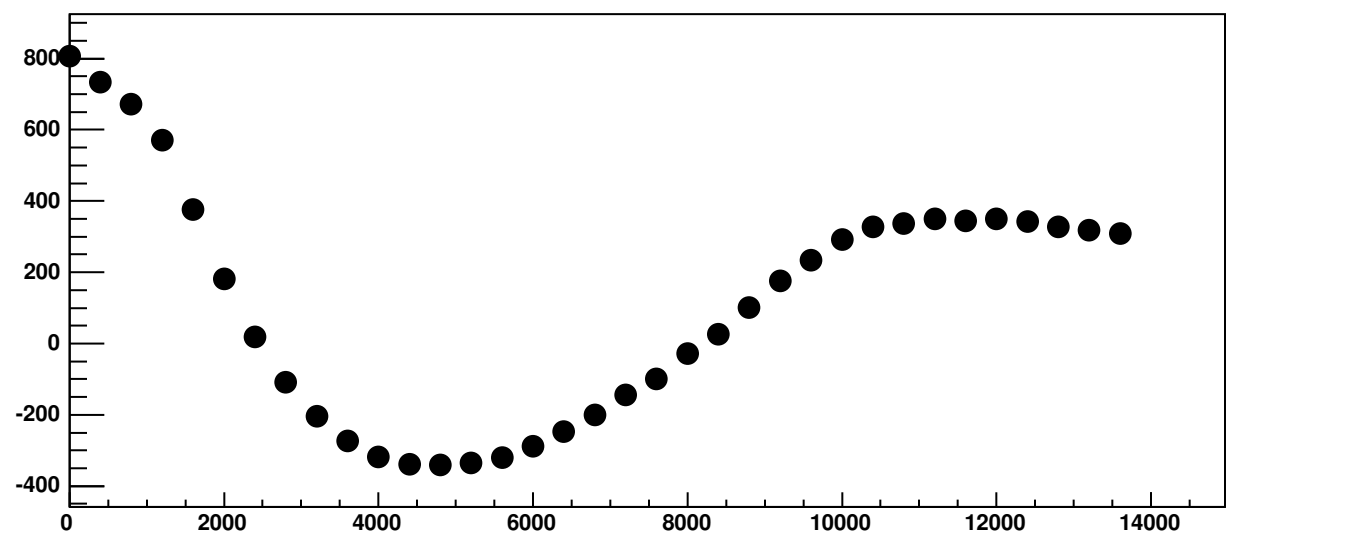
Chip 10, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

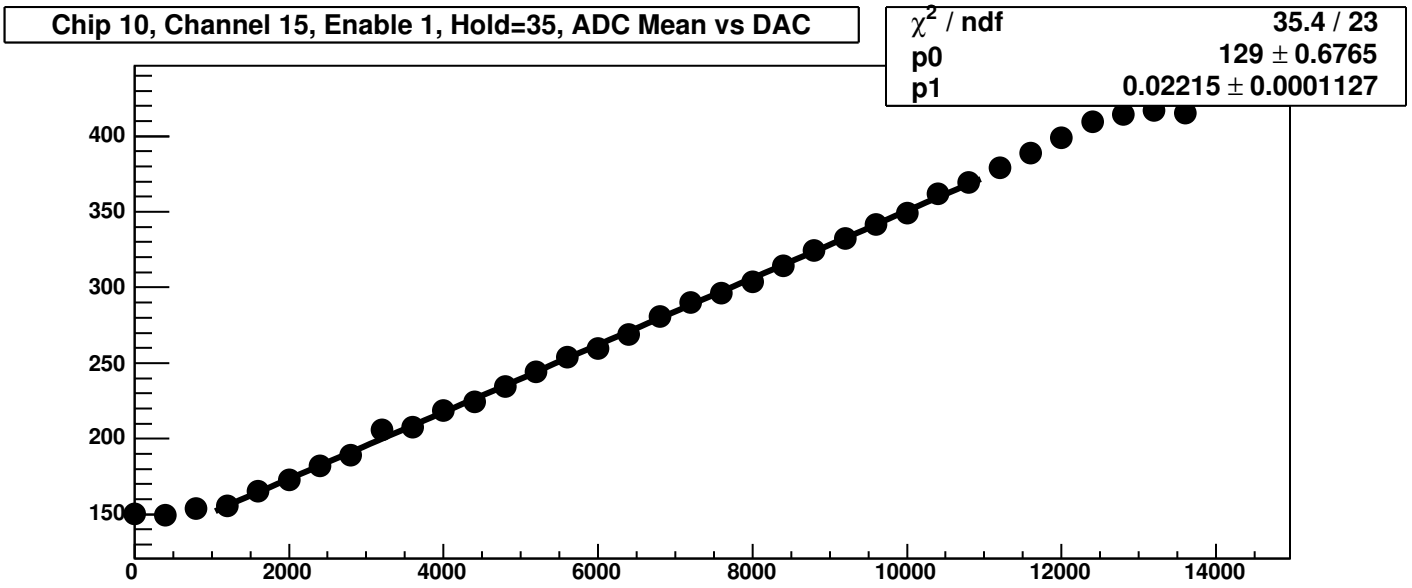


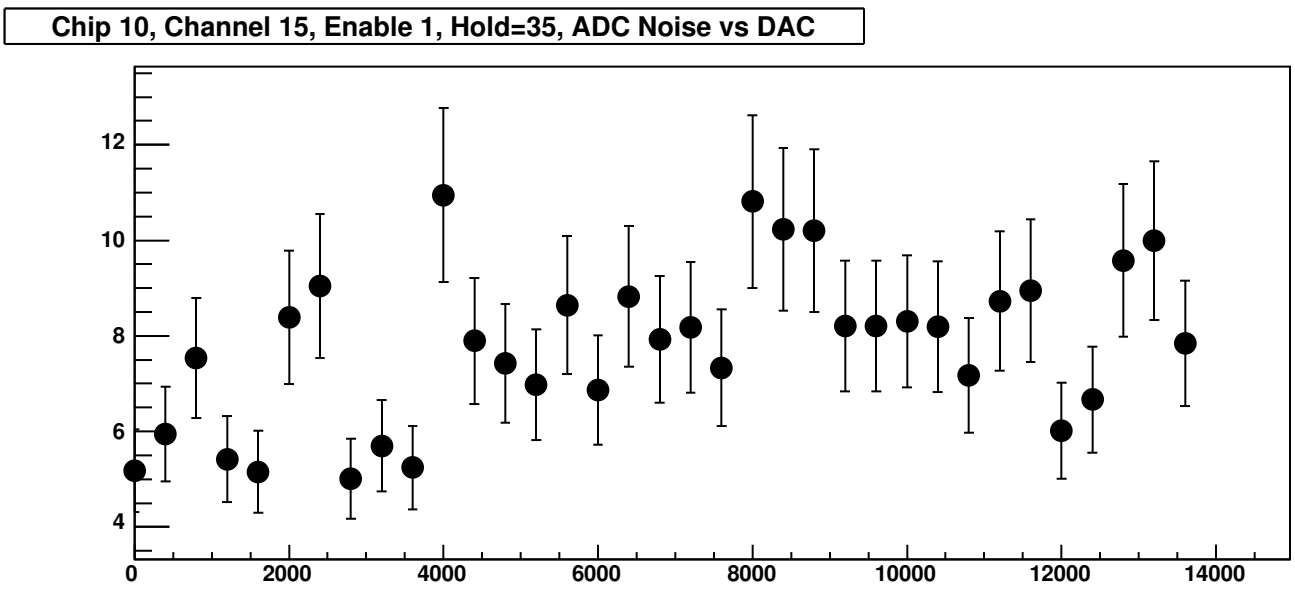
Chip 10, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC

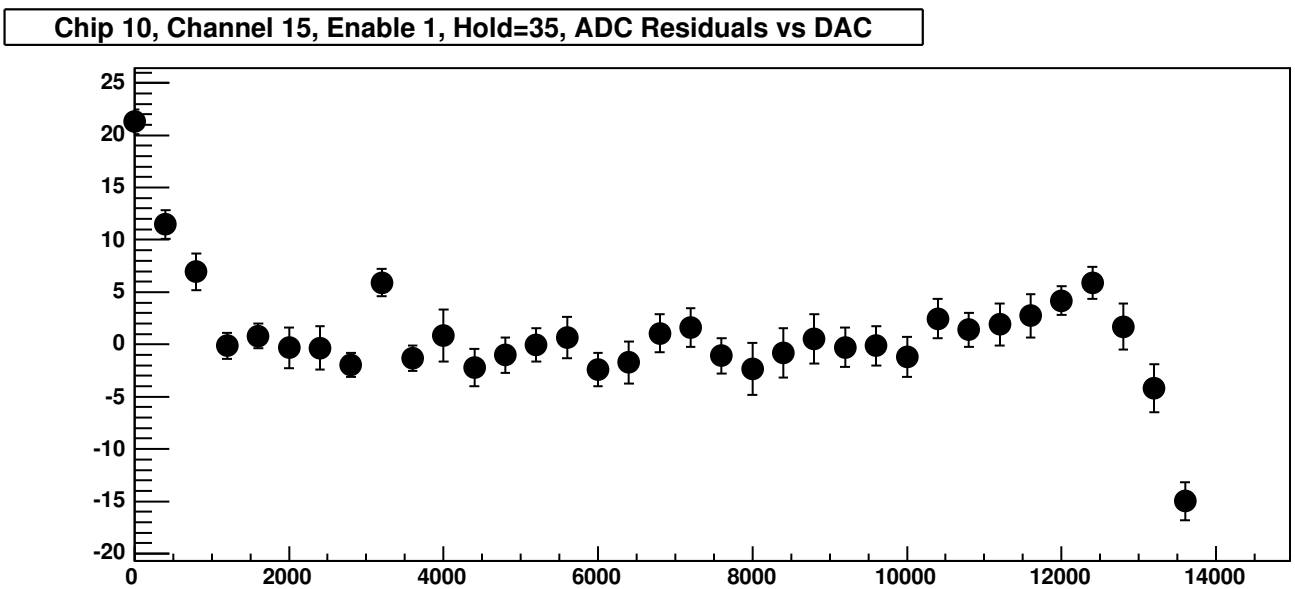


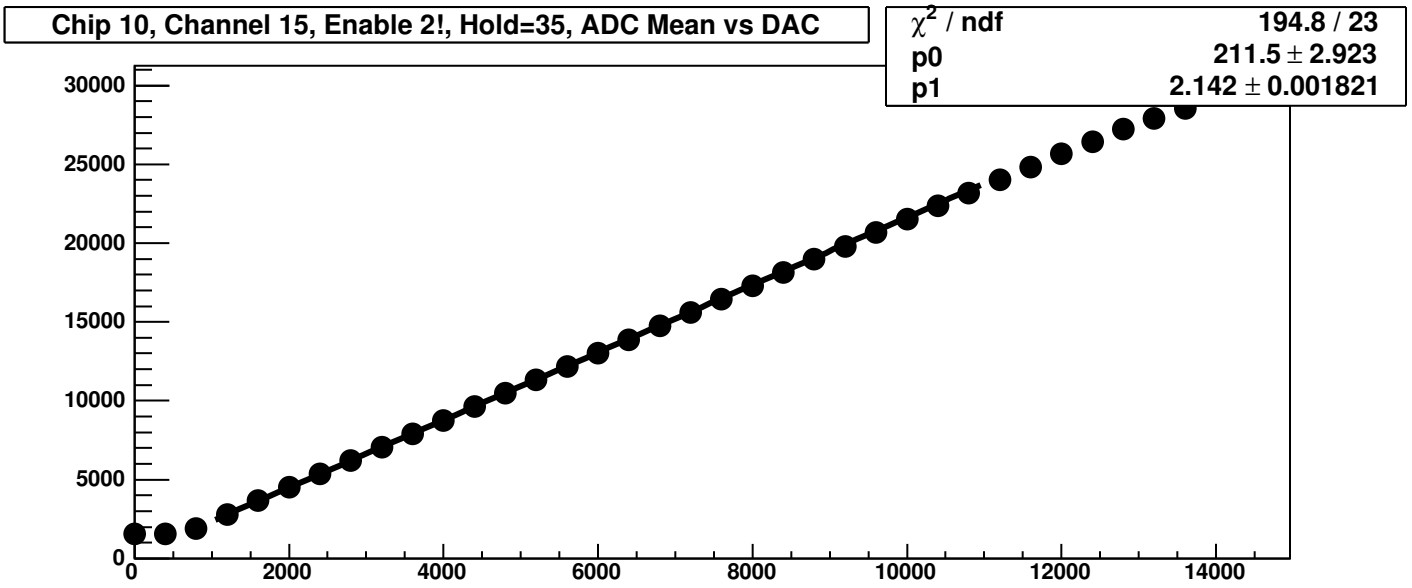
Chip 10, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

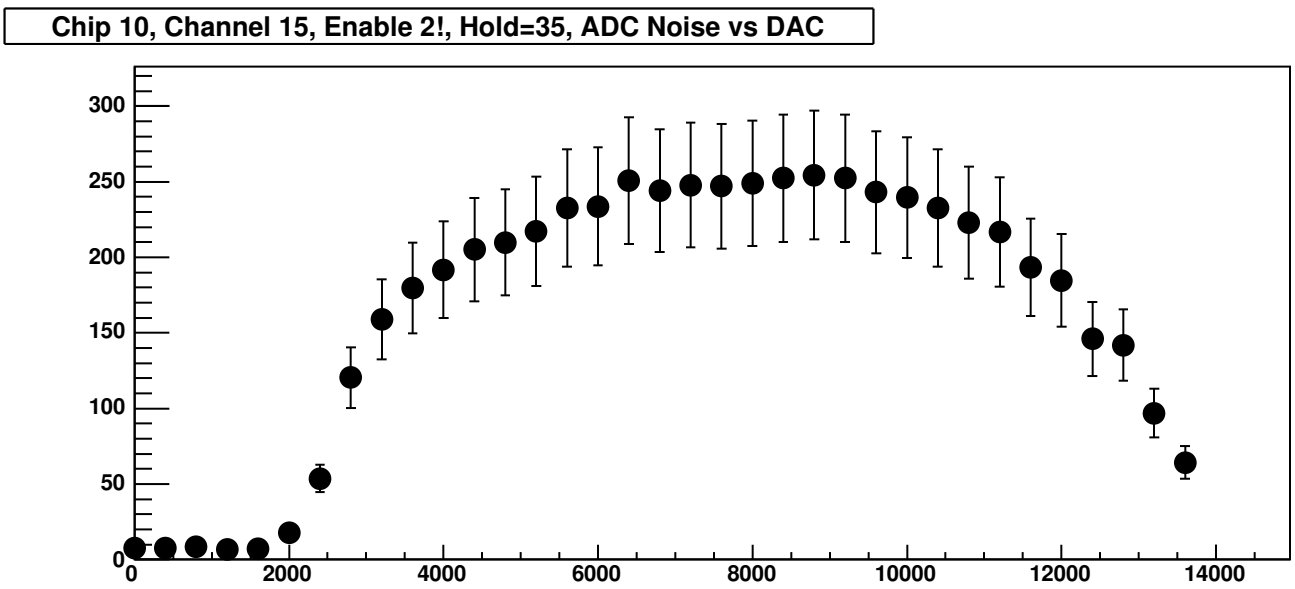


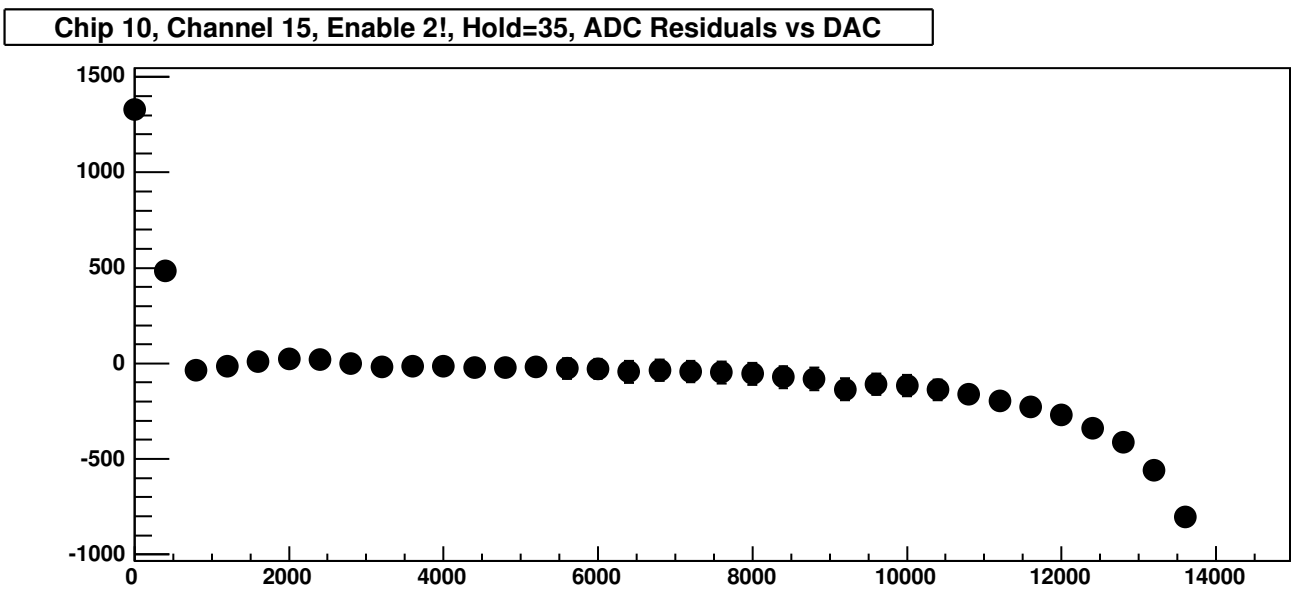

Chip 10, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 35.4 / 23
p0 129 ± 0.6765
p1 0.02215 ± 0.0001127
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

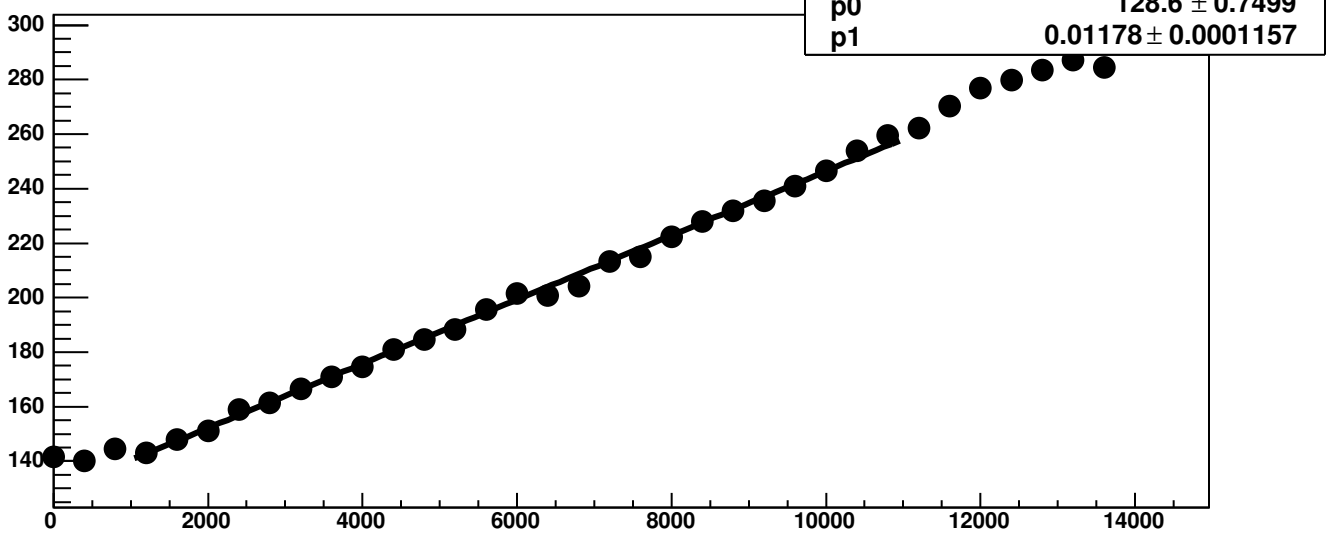

Chip 10, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 194.8 / 23
p0 211.5 ± 2.923
p1 2.142 ± 0.001821
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

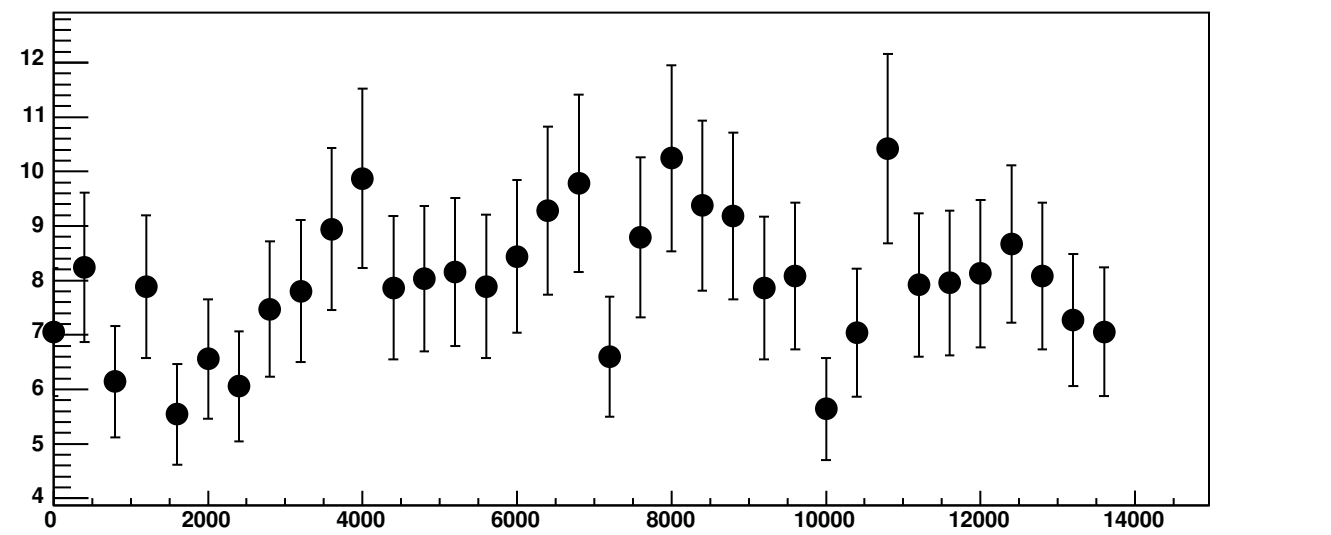

Chip 10, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

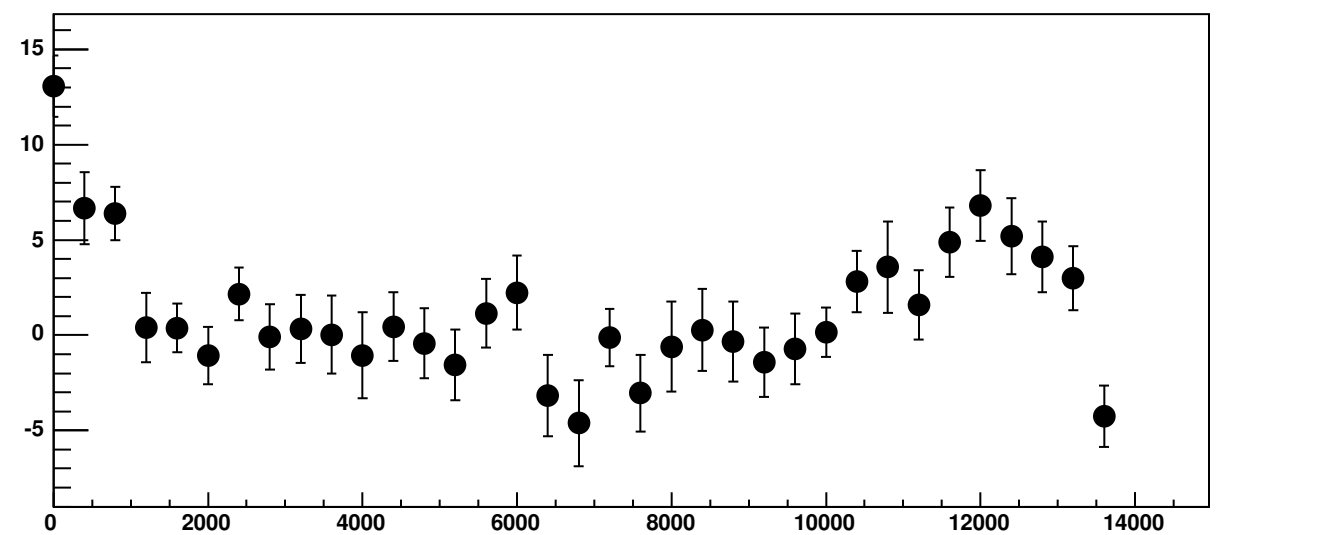
Chip 10, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC



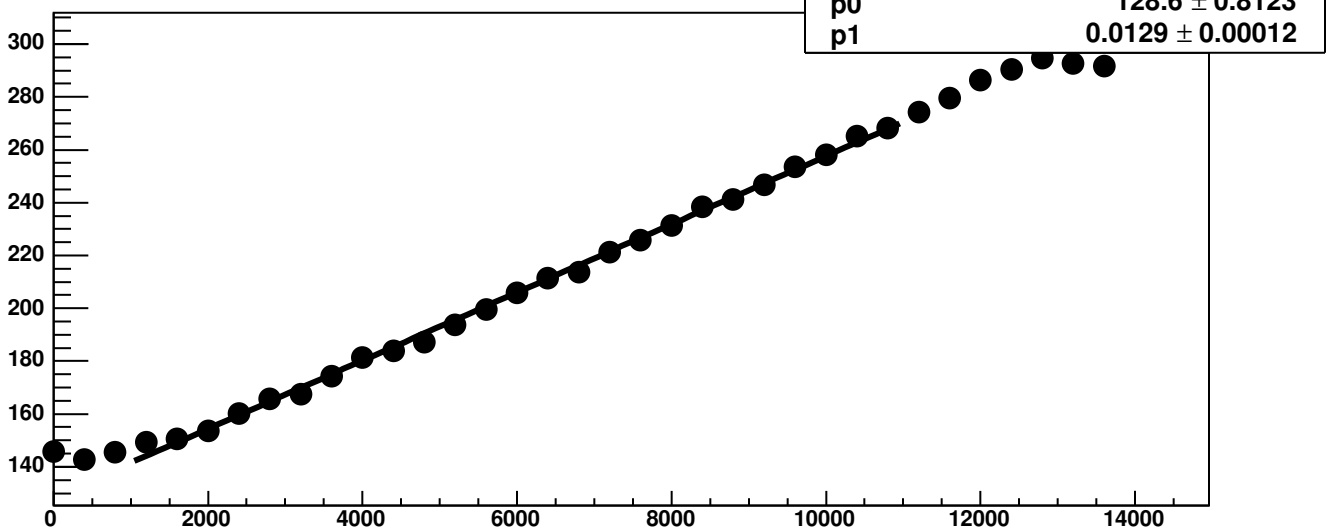
Chip 10, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC



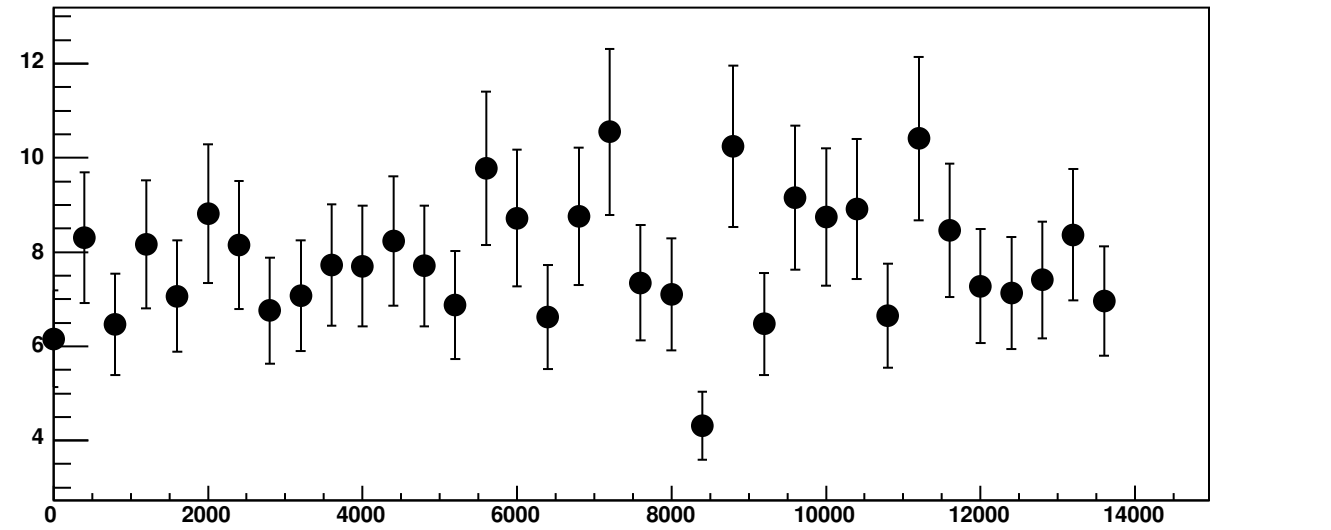
Chip 10, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC



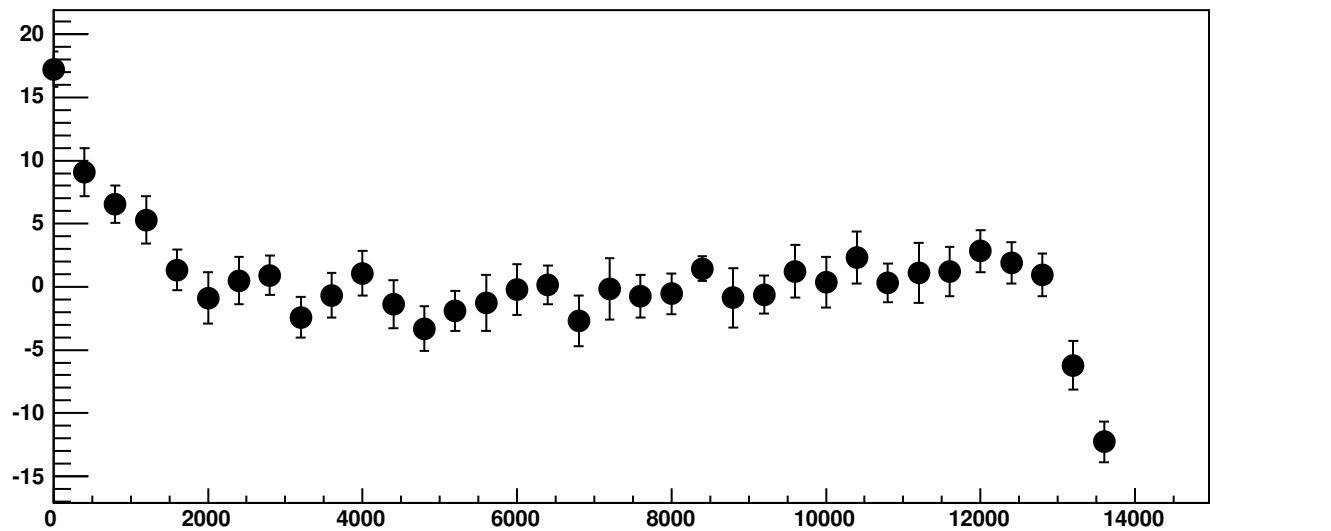
Chip 10, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

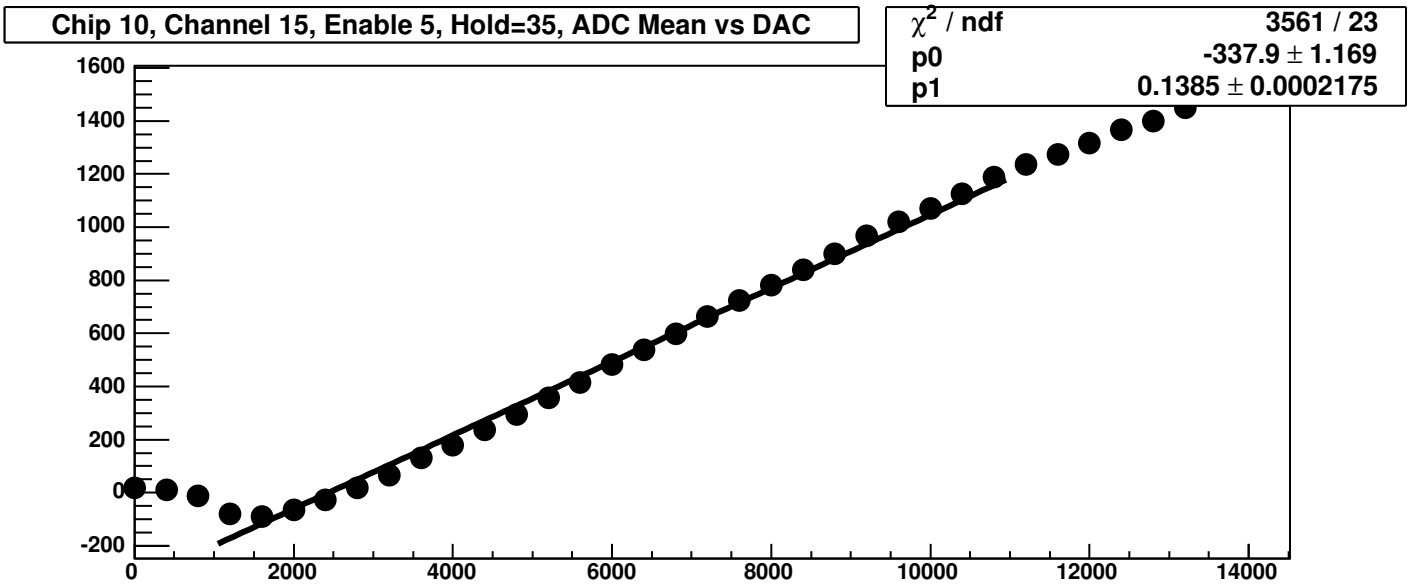


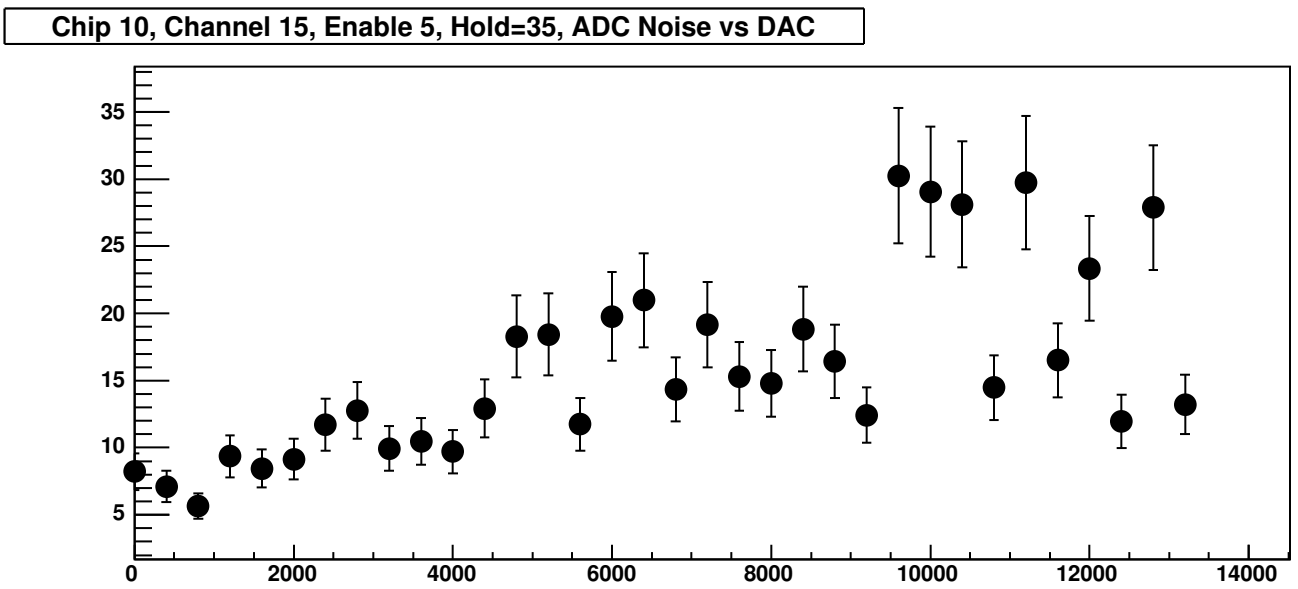
Chip 10, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC

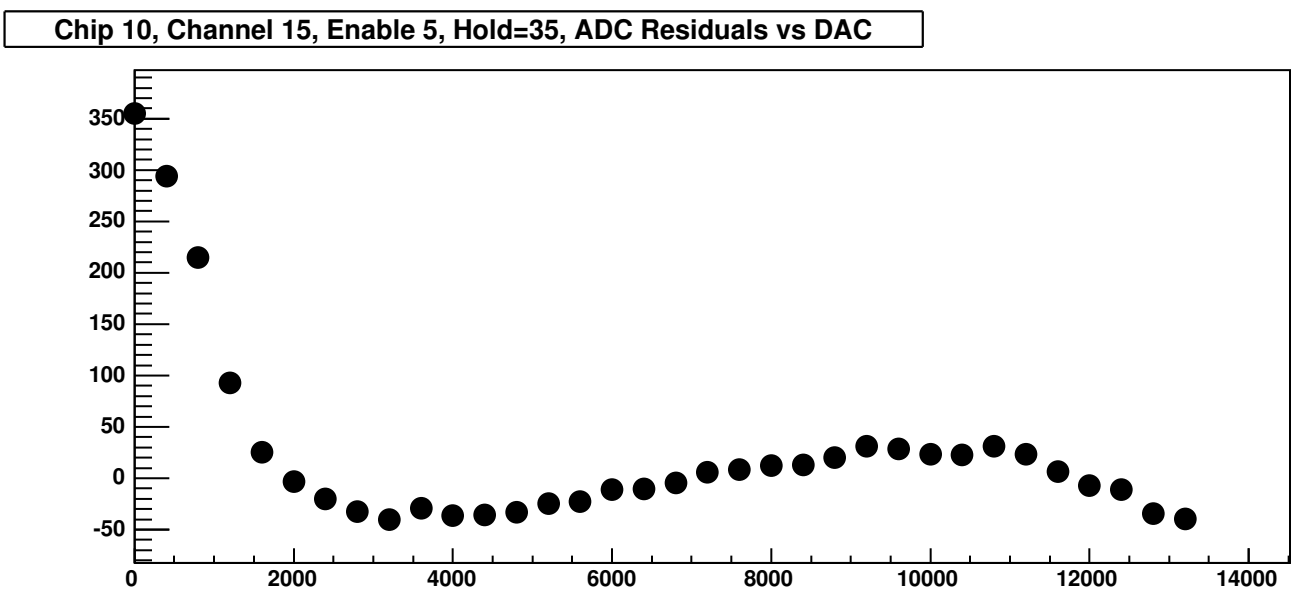


Chip 10, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

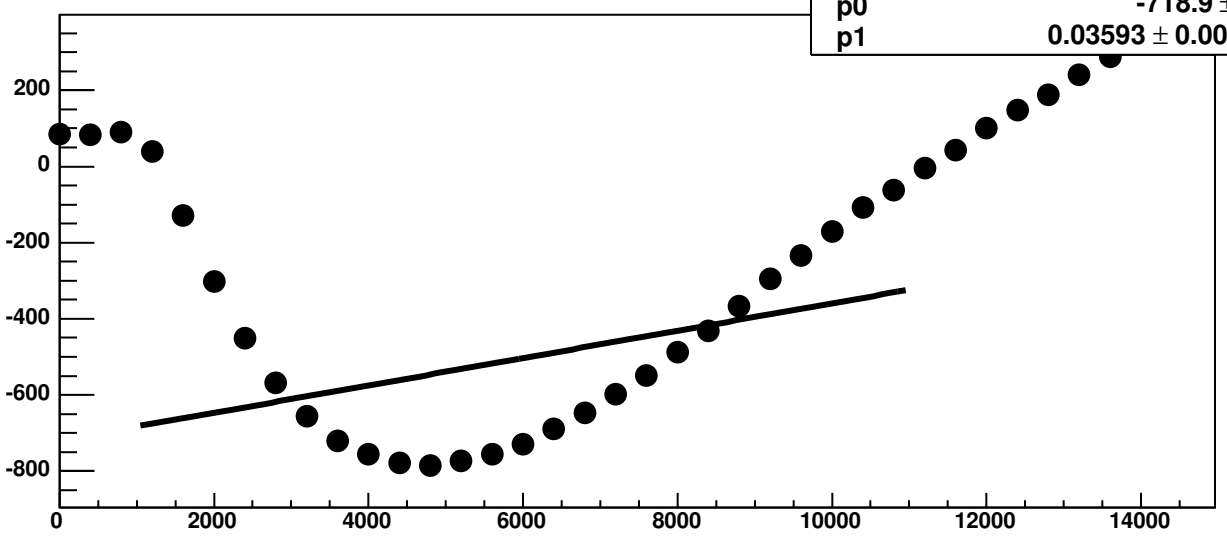



Chip 10, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3561 / 23
p0 -337.9 ± 1.169
p1 0.1385 ± 0.0002175
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

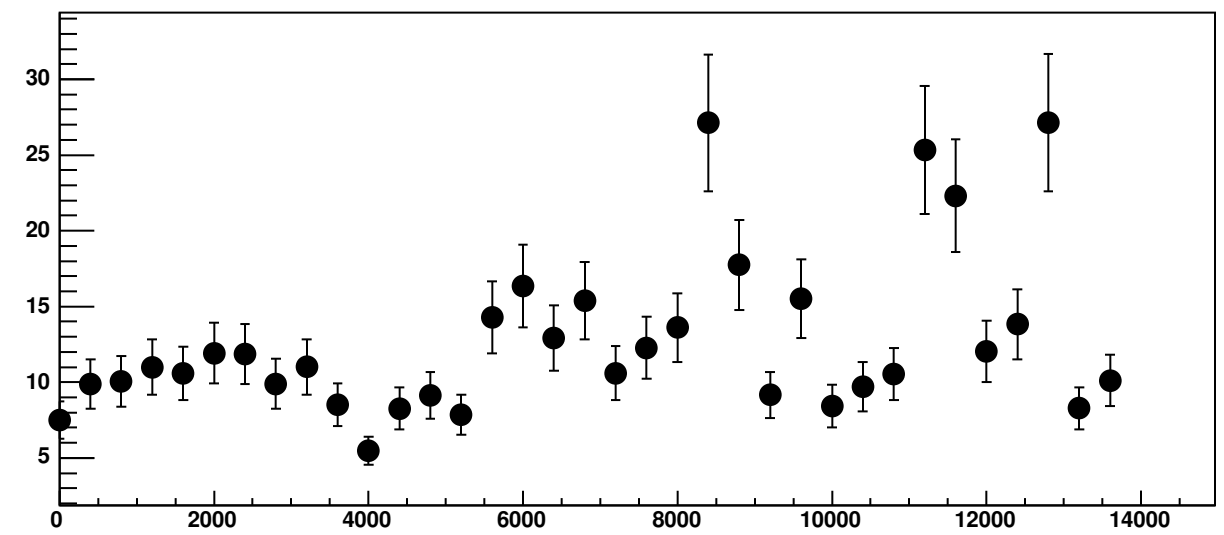

Chip 10, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

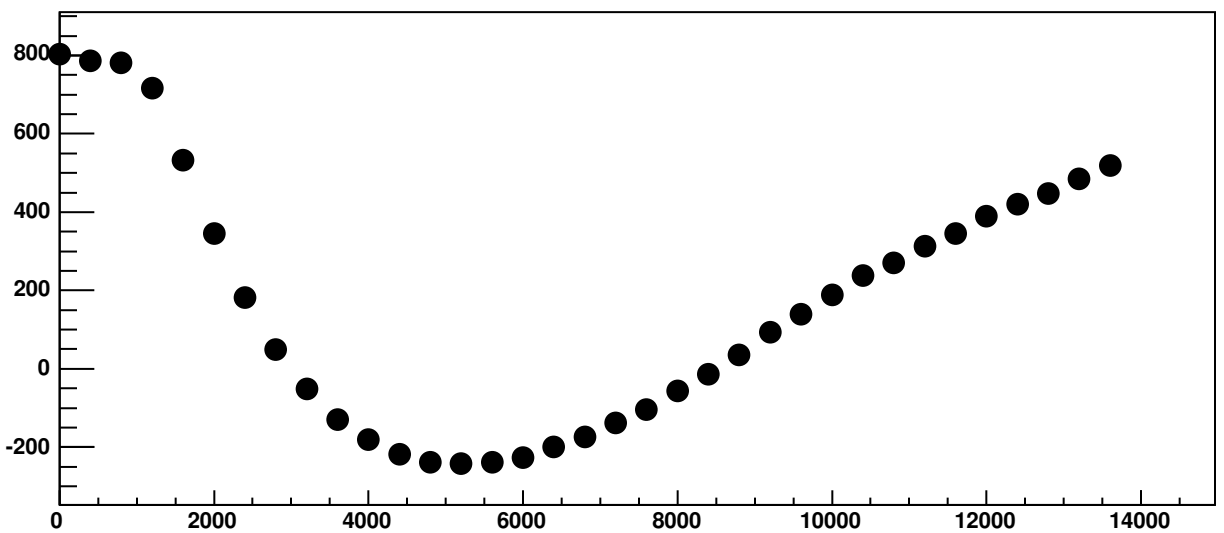
Chip 10, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

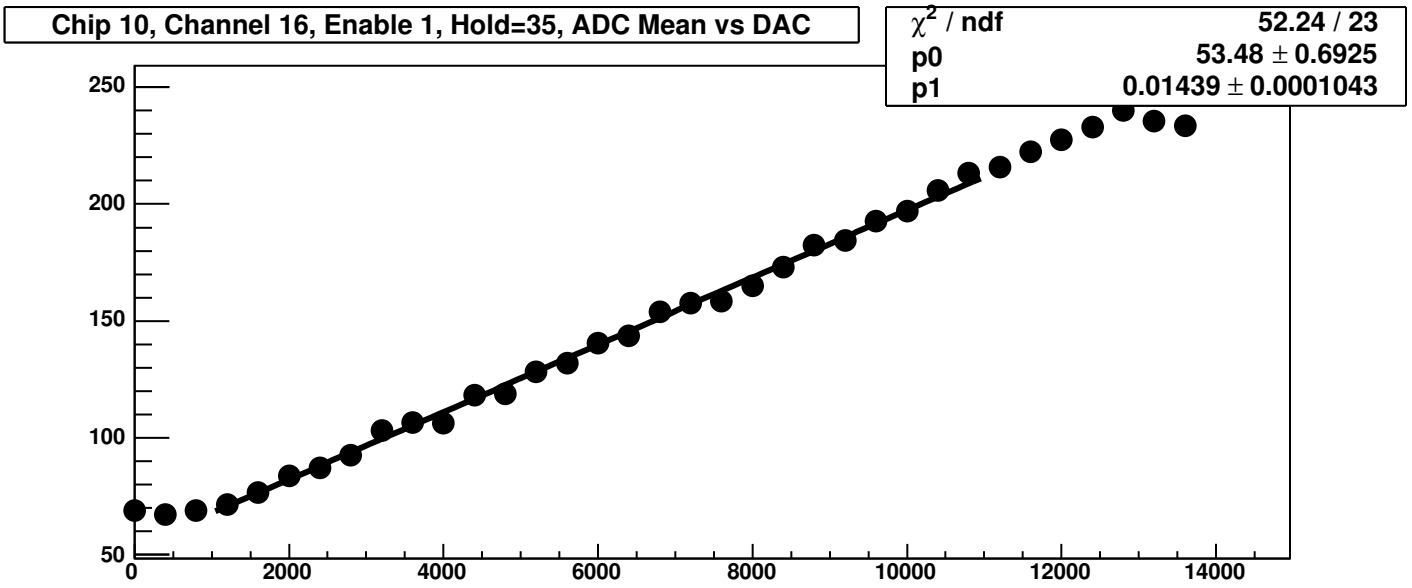


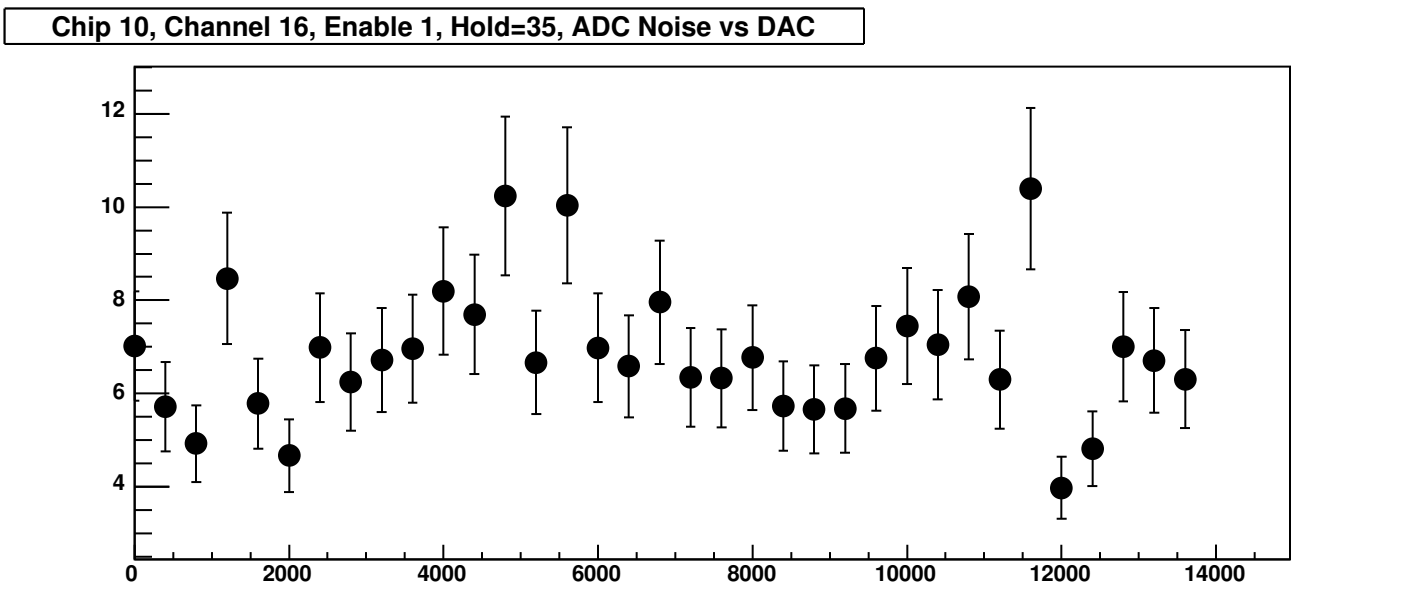
Chip 10, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC

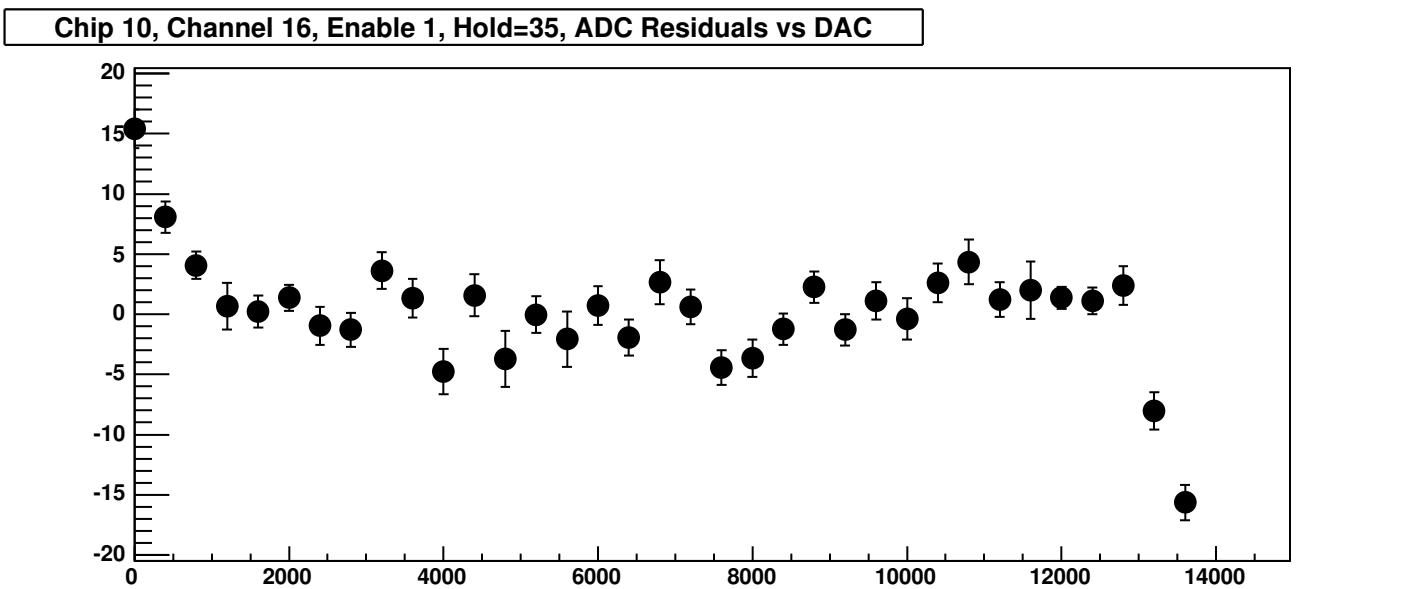


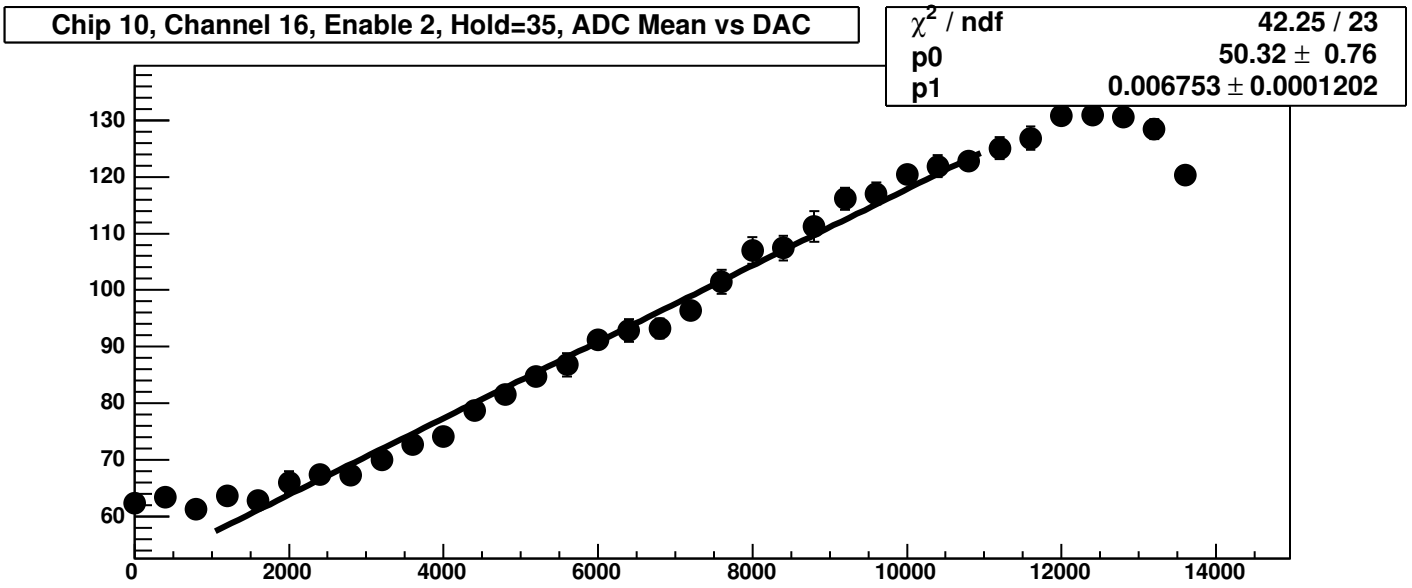
Chip 10, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

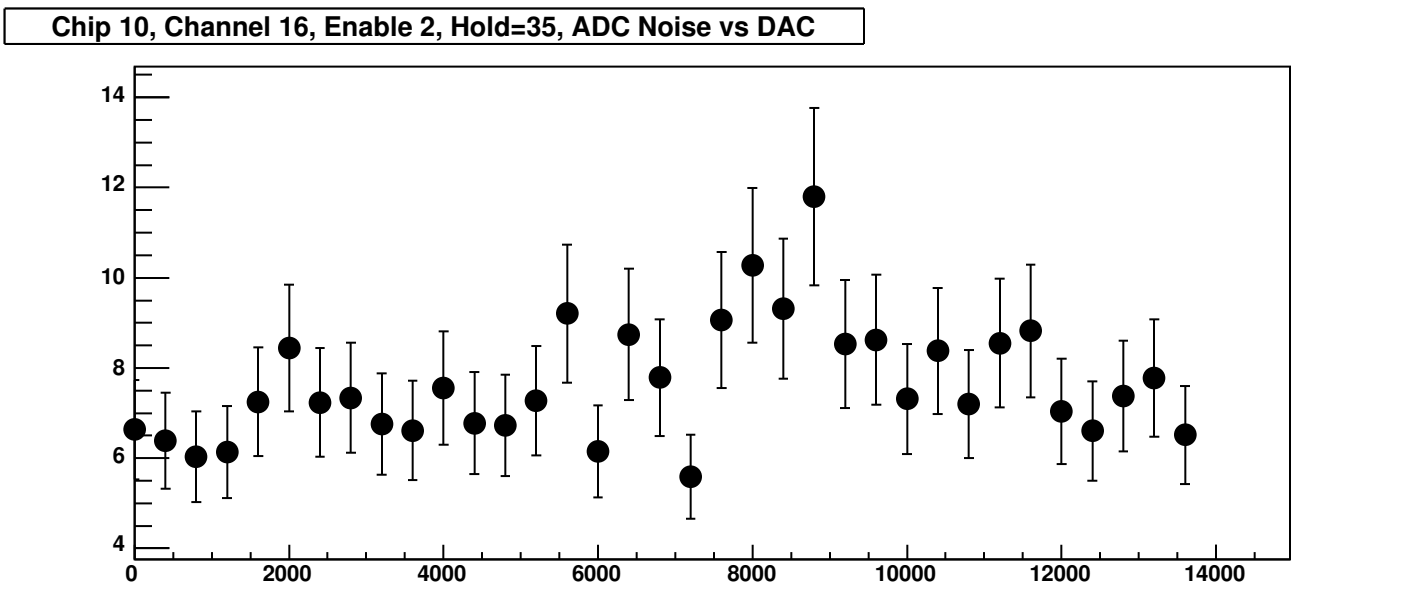


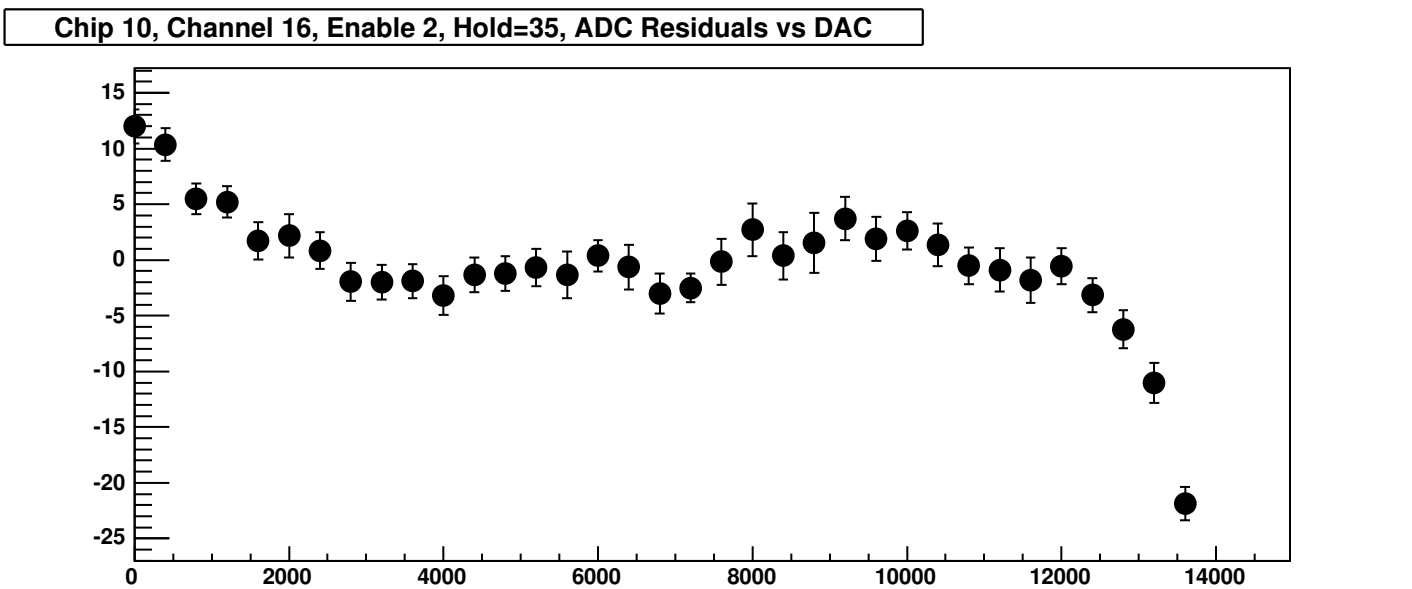

Chip 10, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 52.24 / 23
p0 53.48 ± 0.6925
p1 0.01439 ± 0.0001043
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

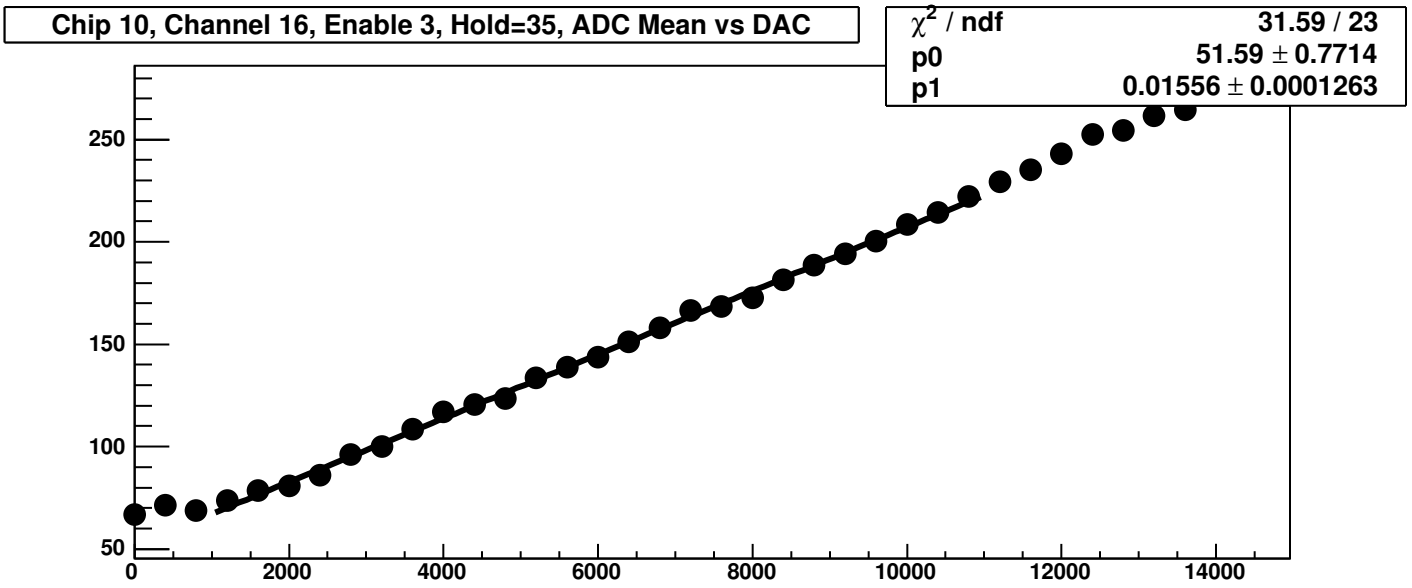

Chip 10, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

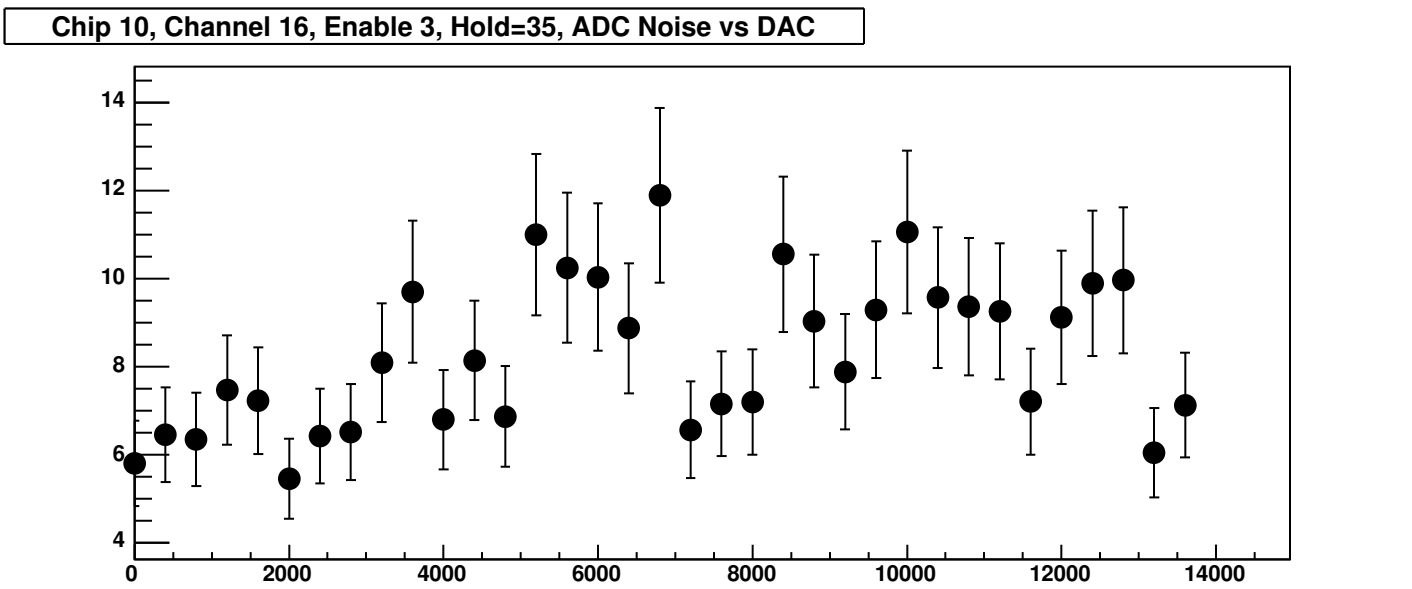

Chip 10, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

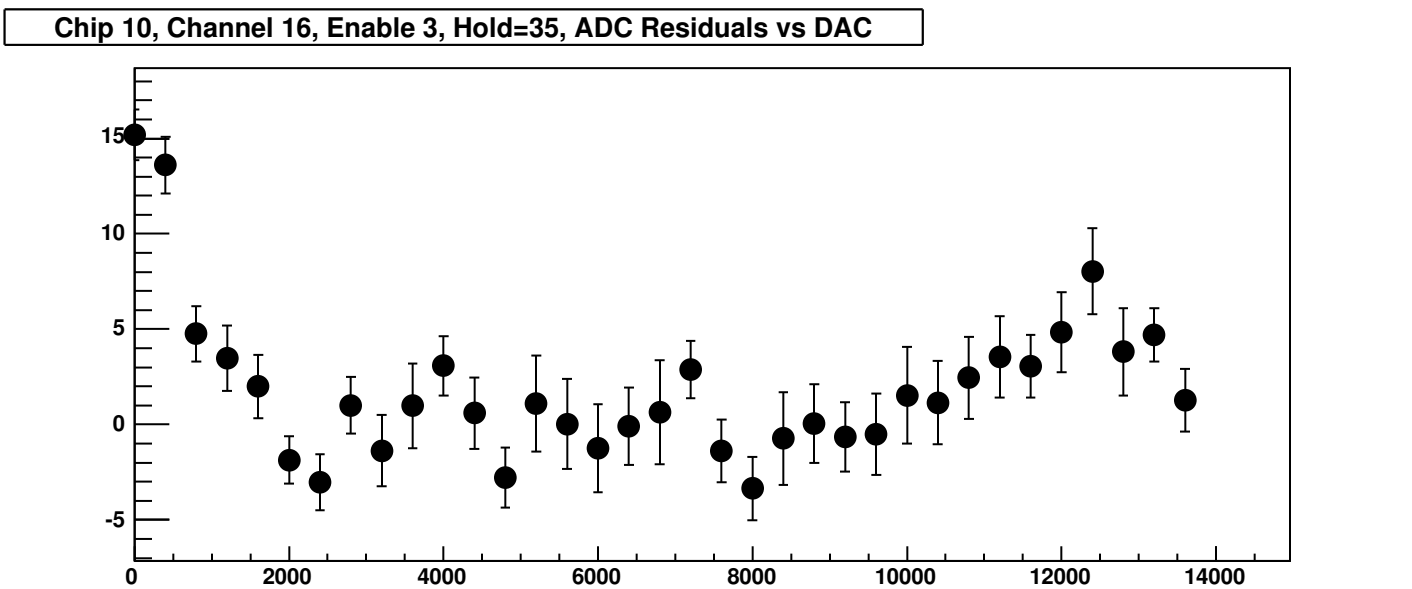

Chip 10, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 42.25 / 23
p0 50.32 ± 0.76
p1 0.006753 ± 0.0001202
130
120
110
100
90
80
70
60
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

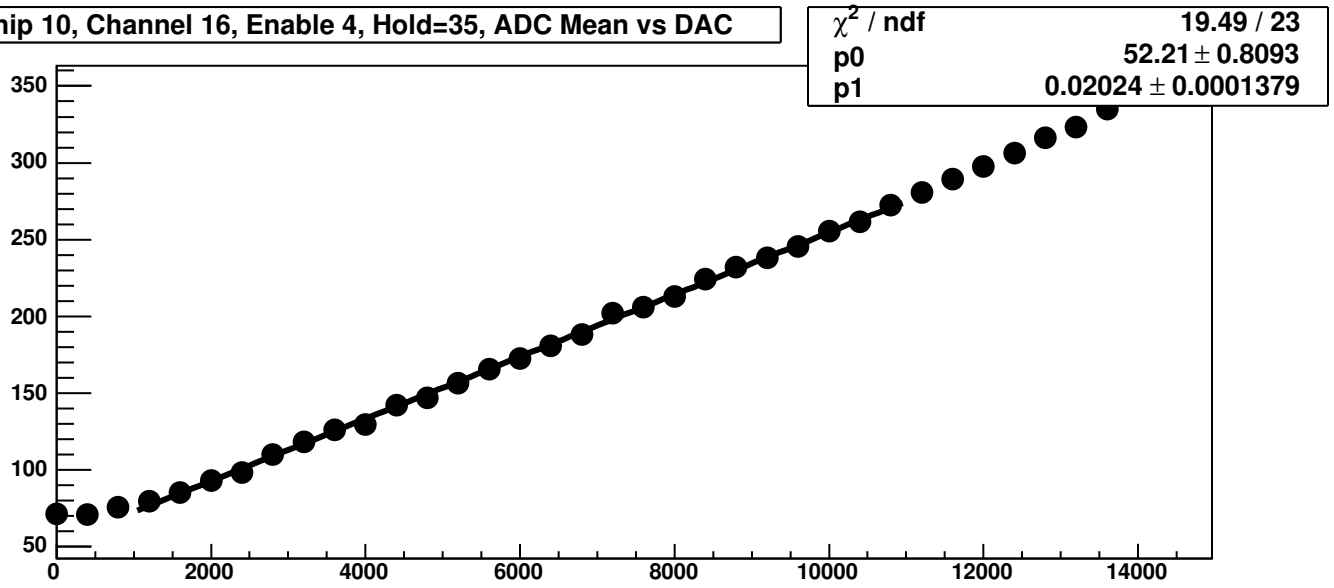

Chip 10, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.59 / 23
p0 51.59 ± 0.7714
p1 0.01556 ± 0.0001263
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

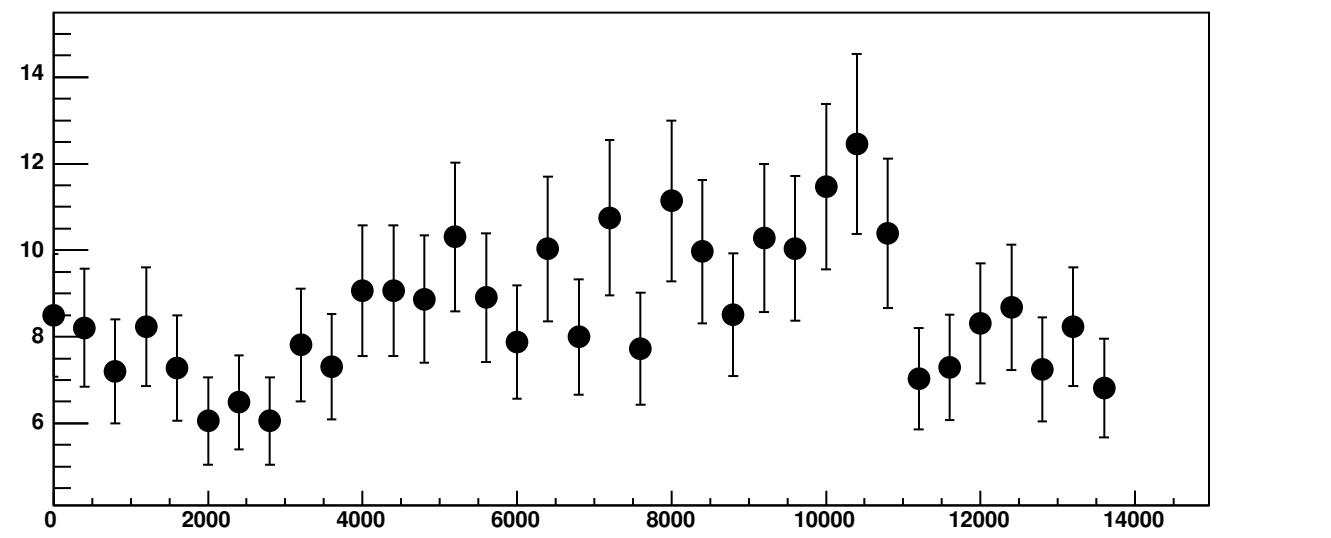

Chip 10, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

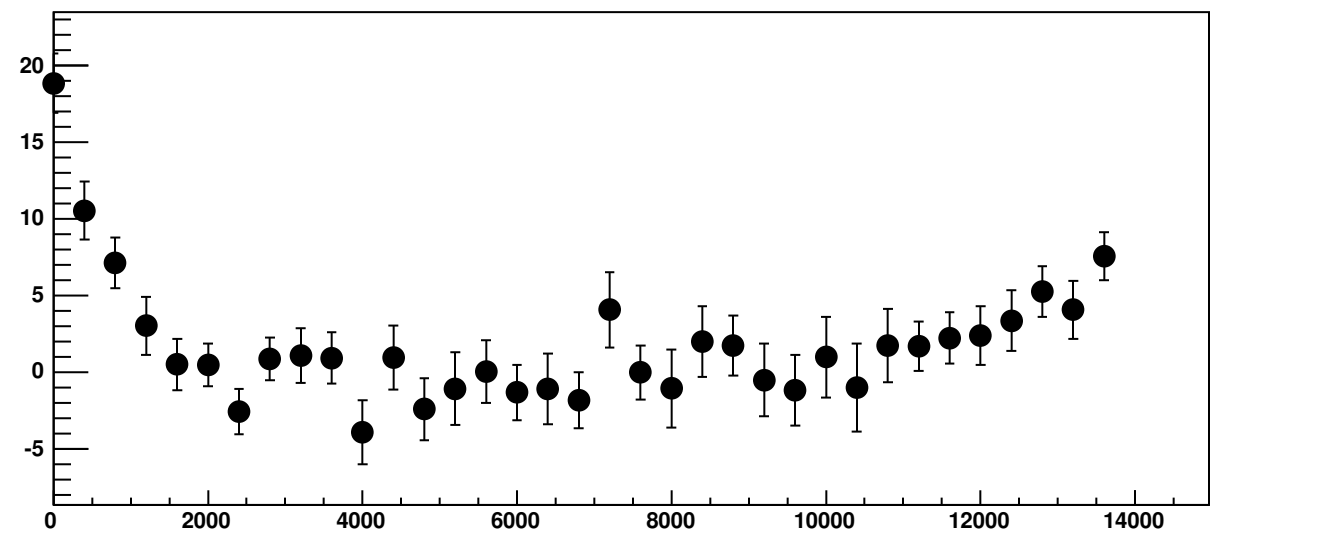
Chip 10, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

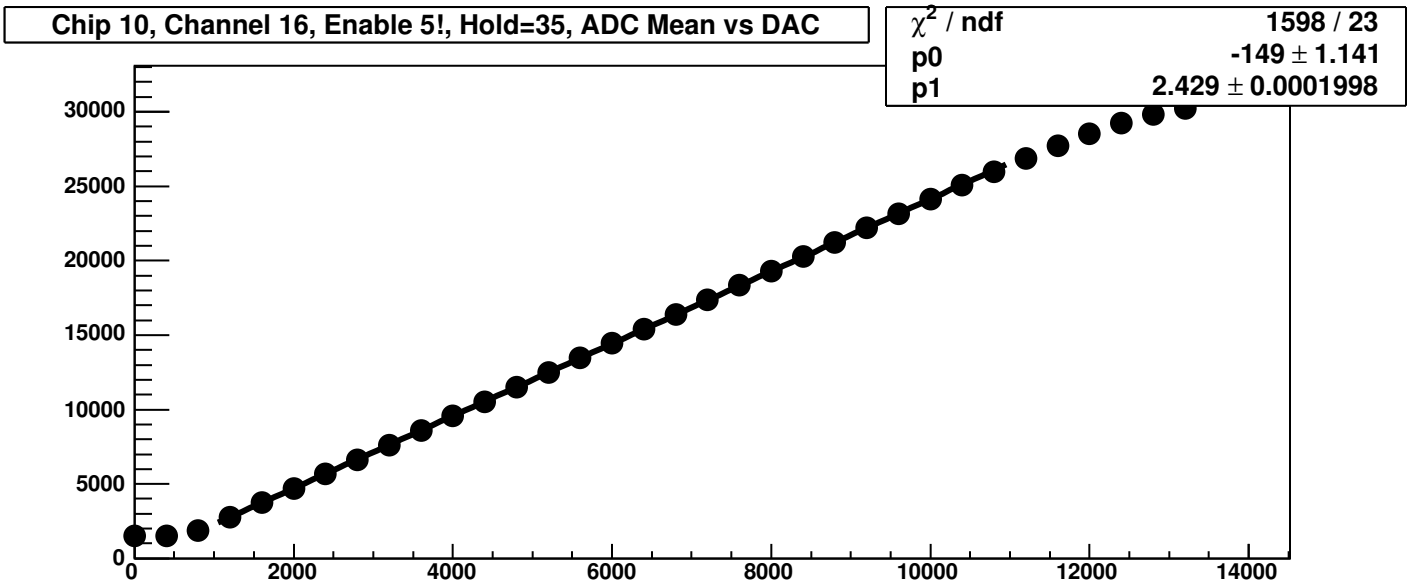


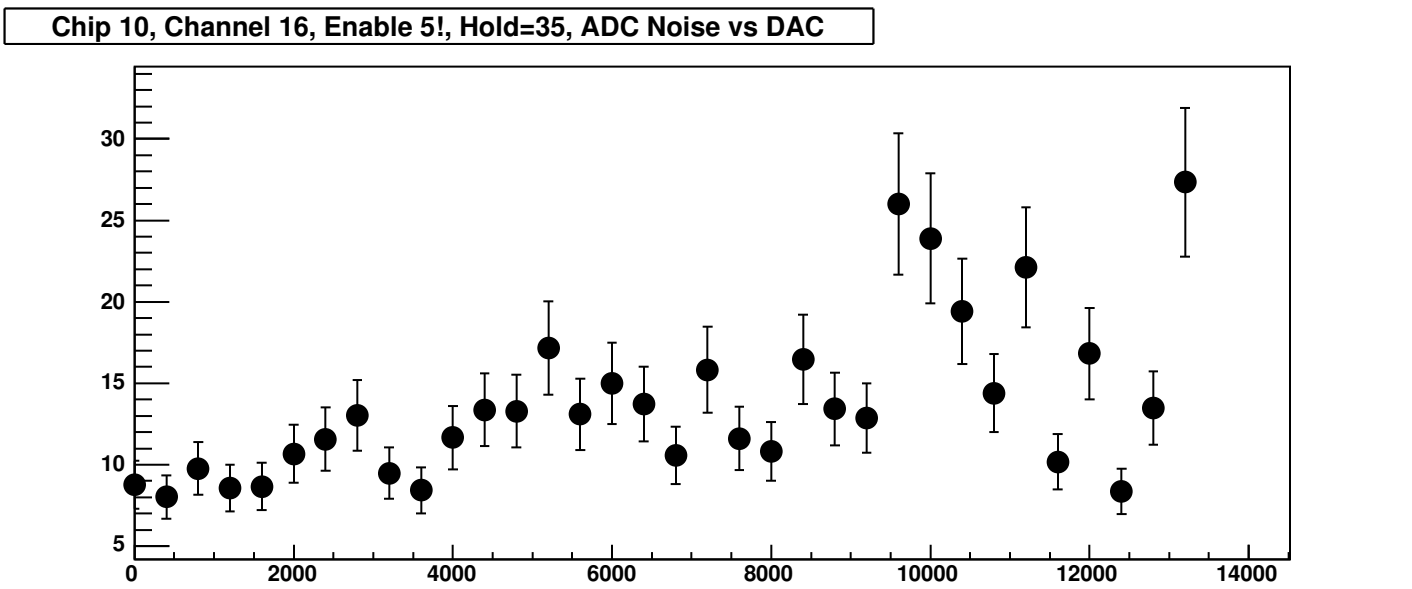
Chip 10, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC

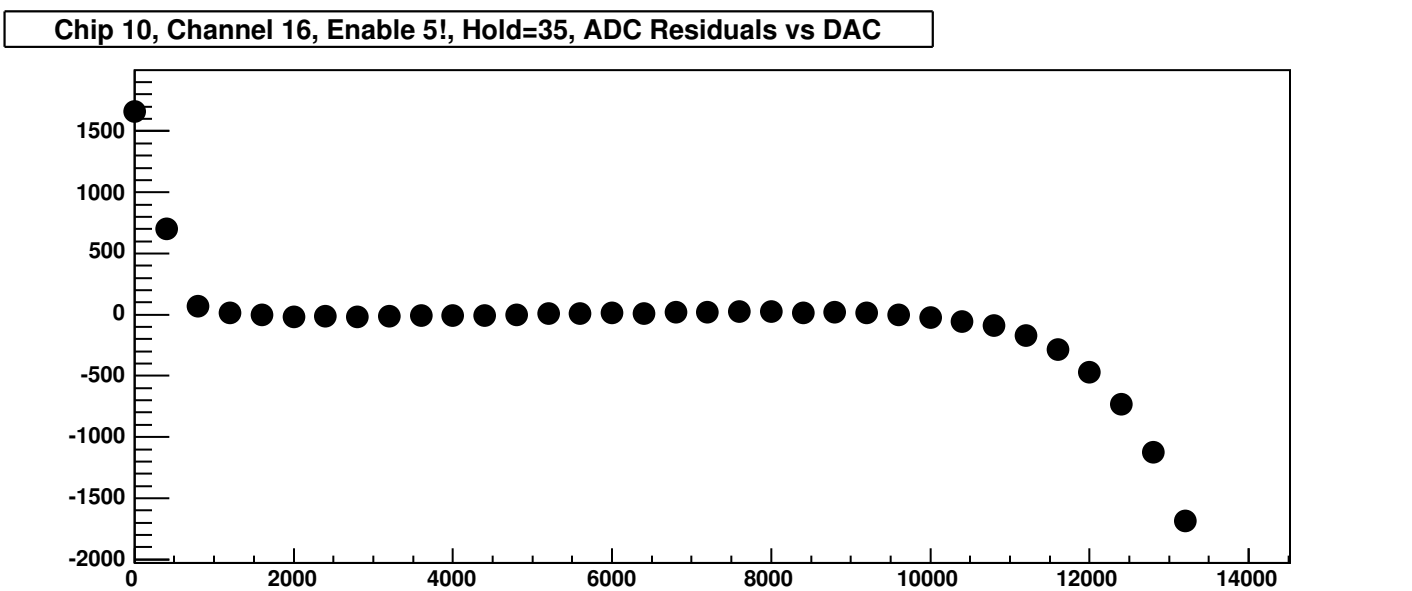


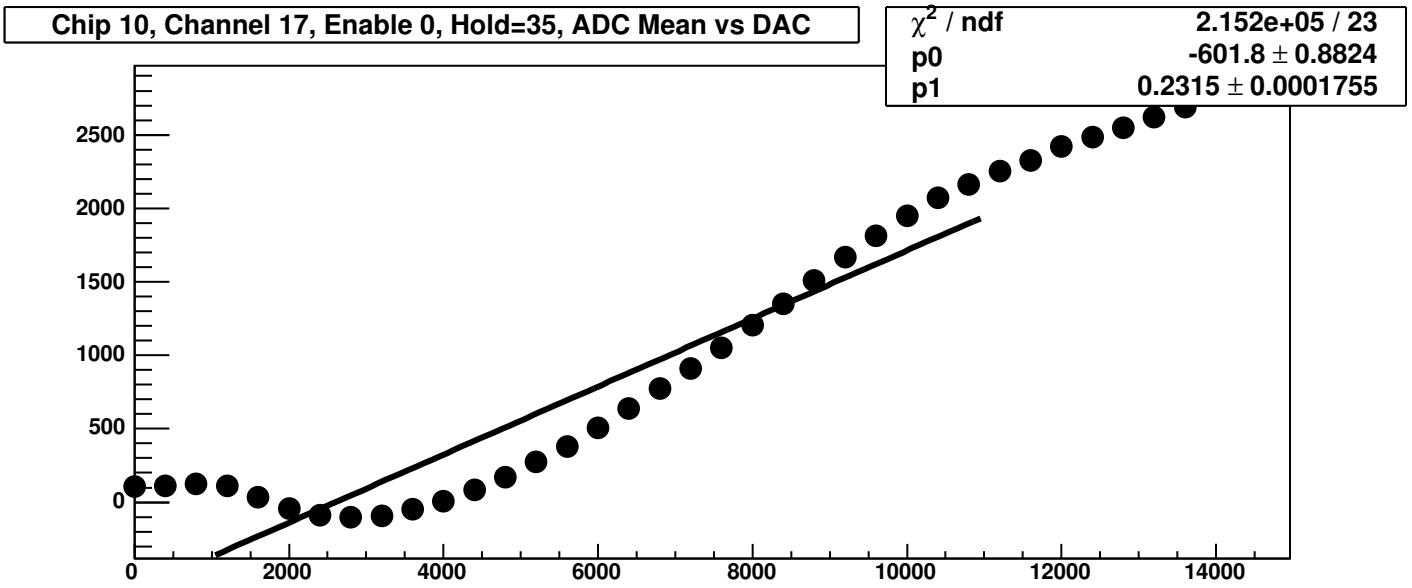
Chip 10, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

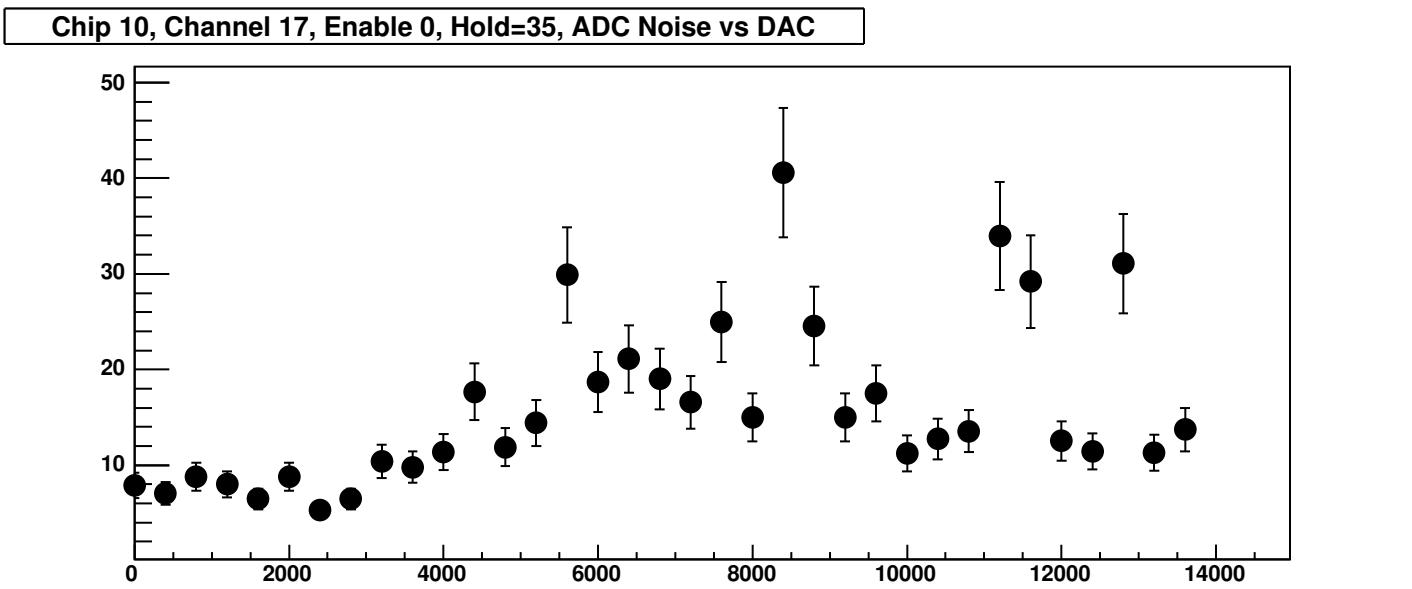


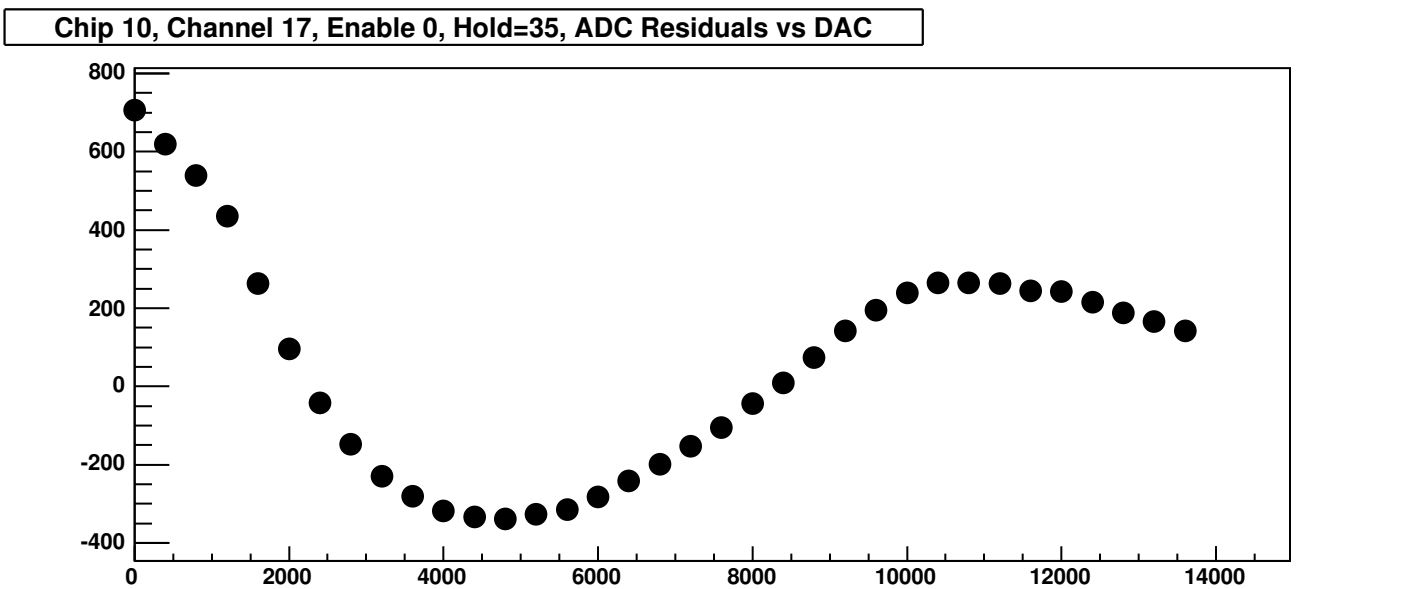

Chip 10, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1598 / 23
p0 -149 ± 1.141
p1 2.429 ± 0.0001998
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

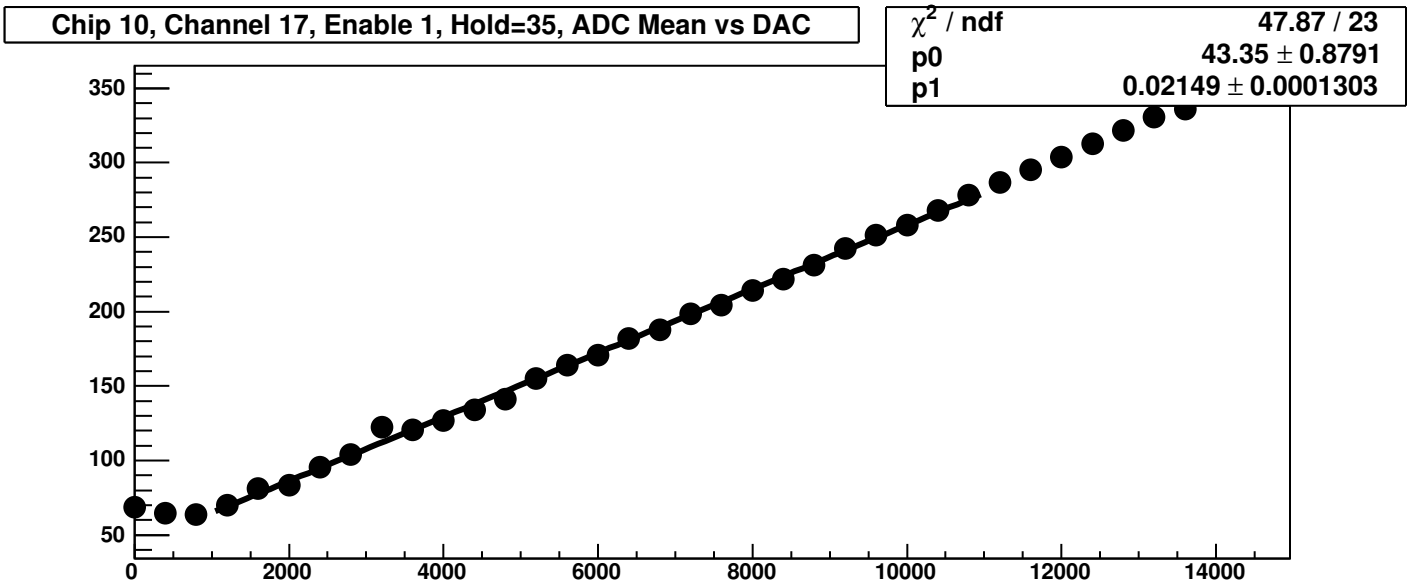

Chip 10, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

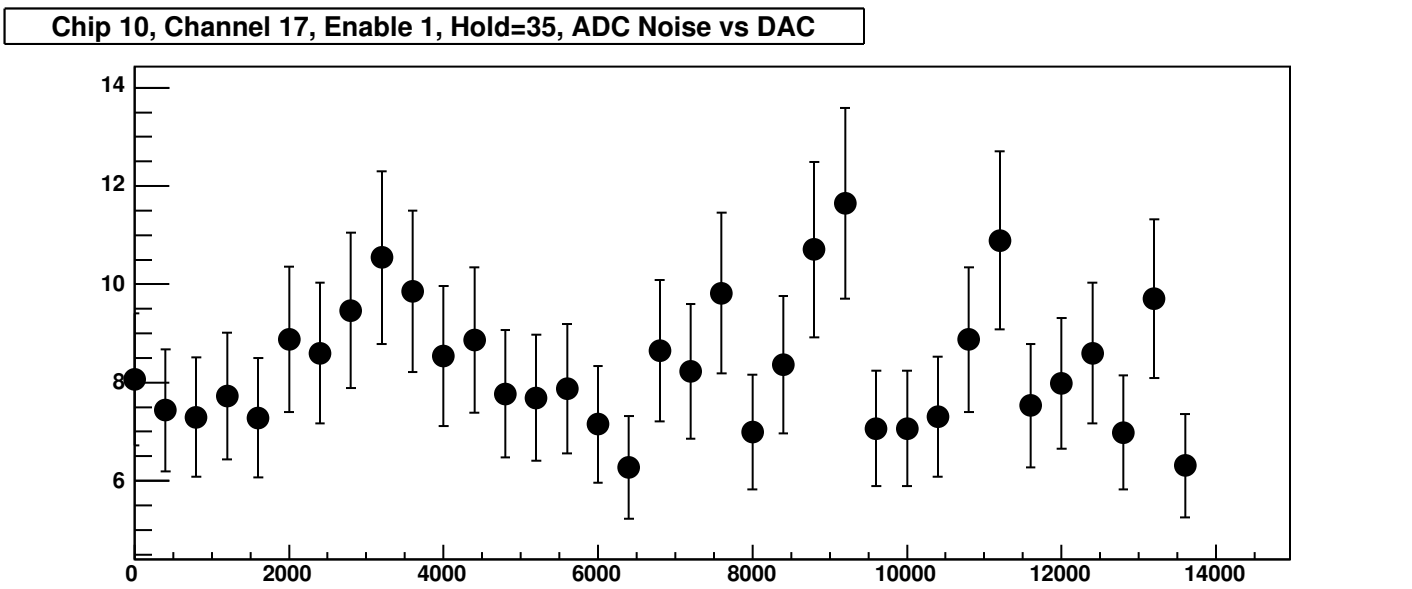

Chip 10, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

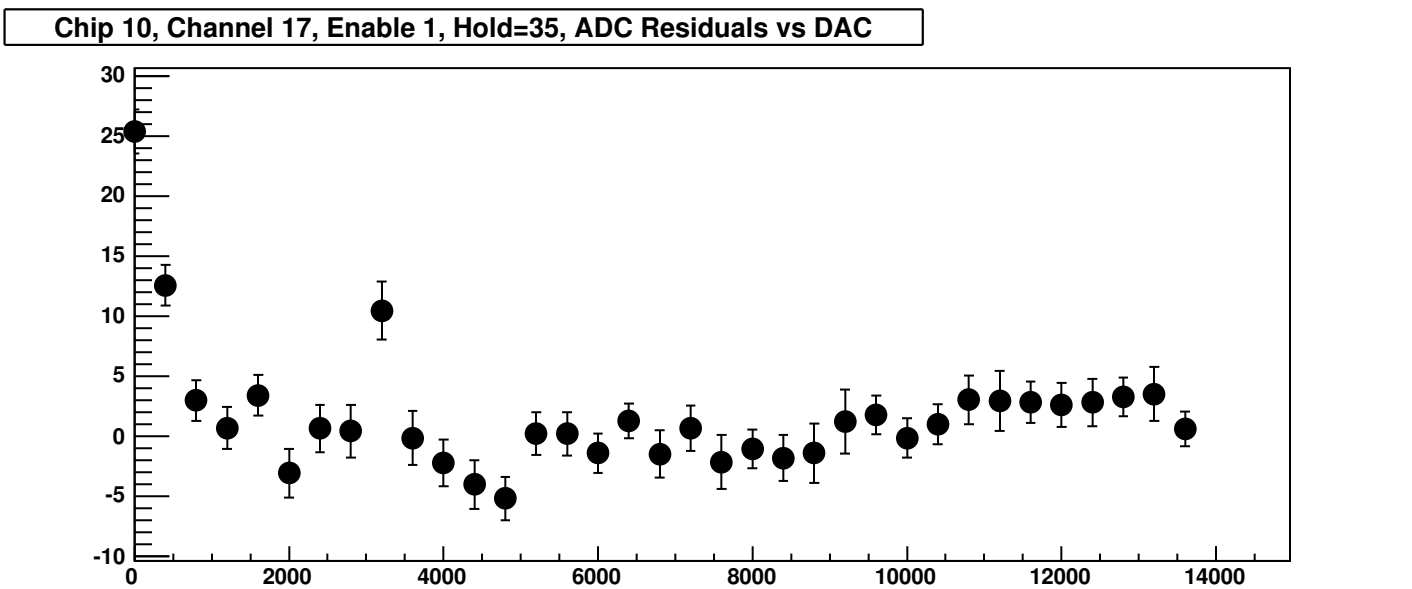

Chip 10, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.152e+05 / 23
p0 -601.8 ± 0.8824
p1 0.2315 ± 0.0001755
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

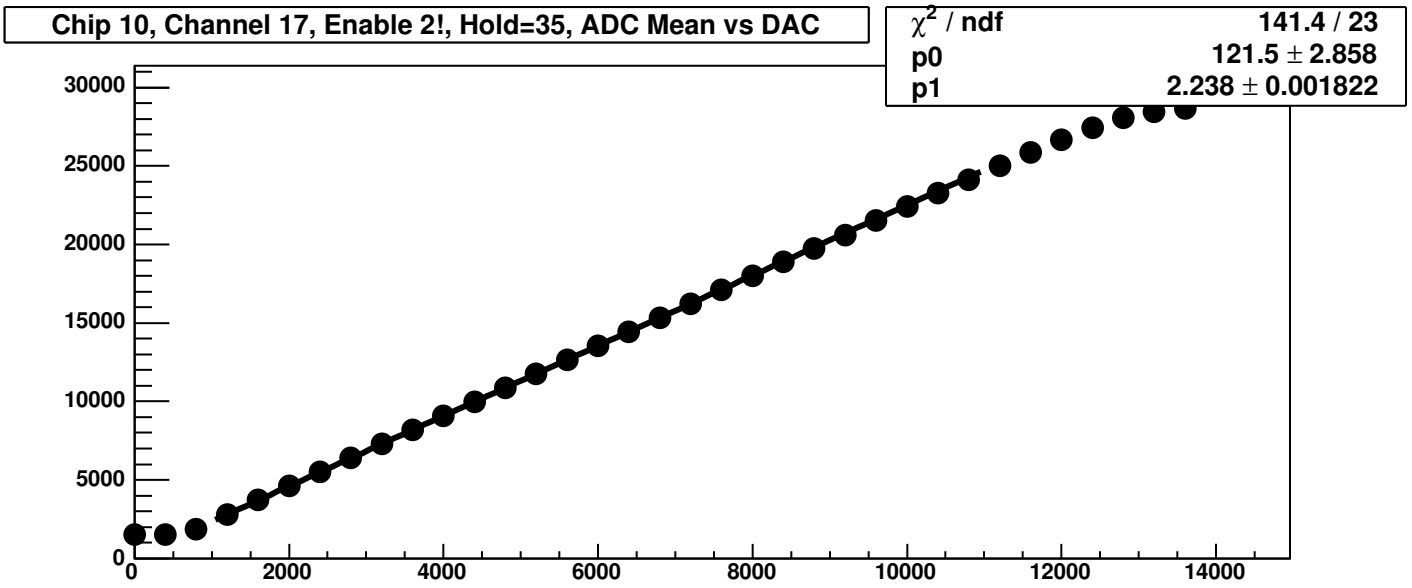

Chip 10, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

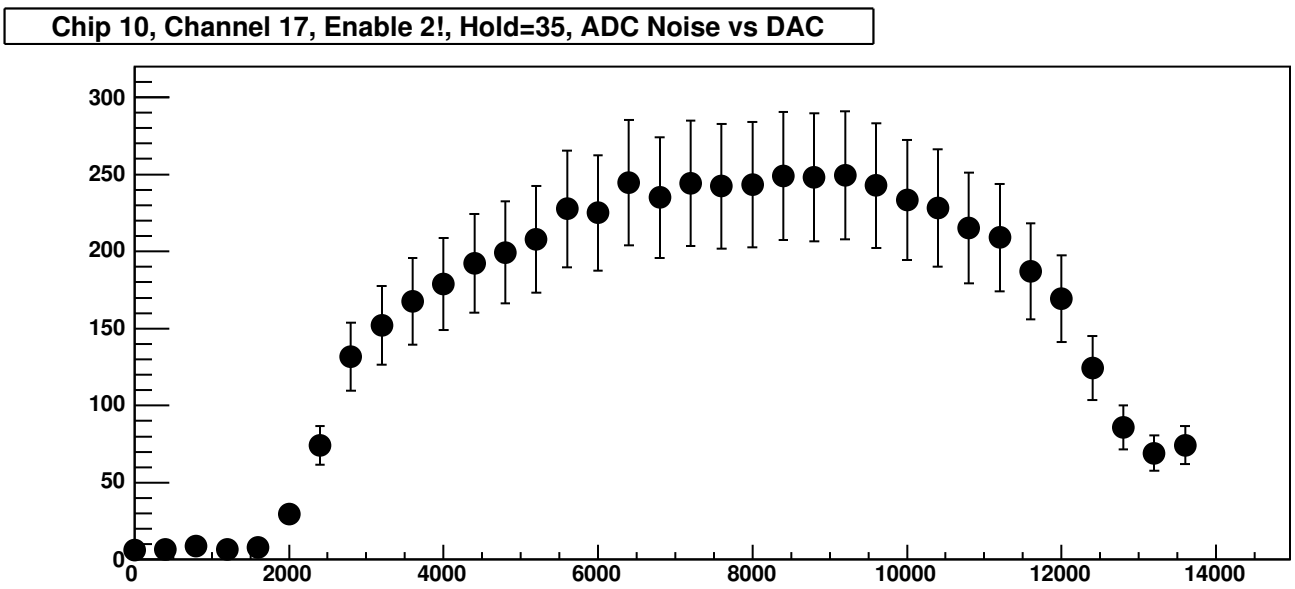

Chip 10, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

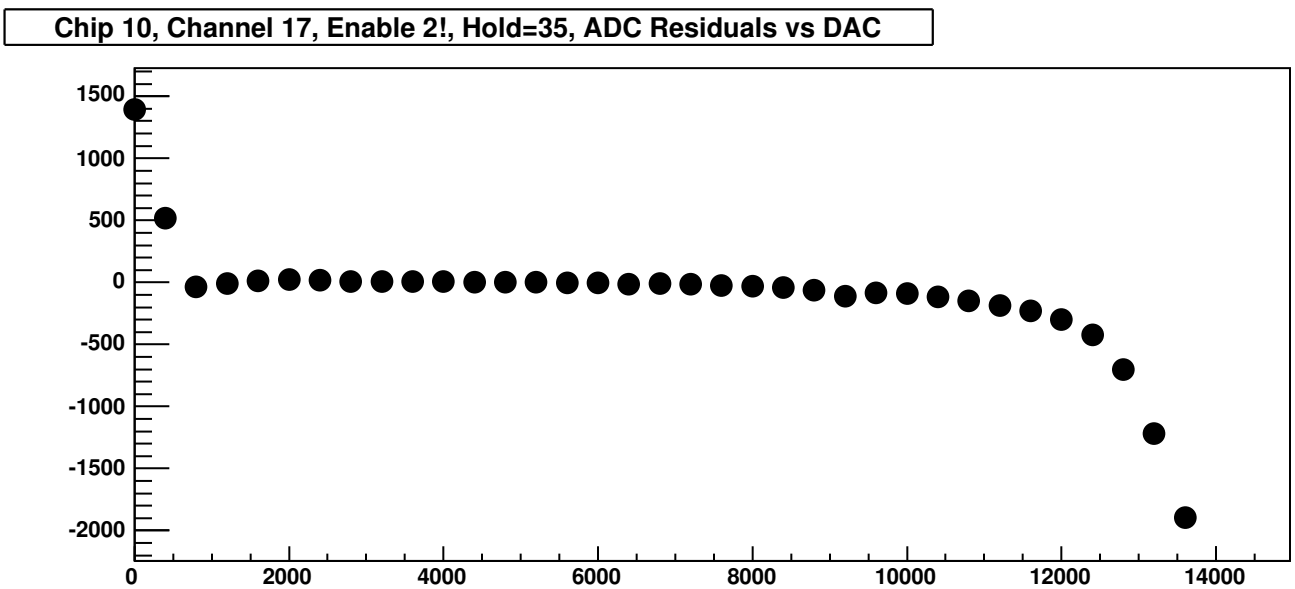

Chip 10, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 47.87 / 23
p0 43.35 ± 0.8791
p1 0.02149 ± 0.0001303
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

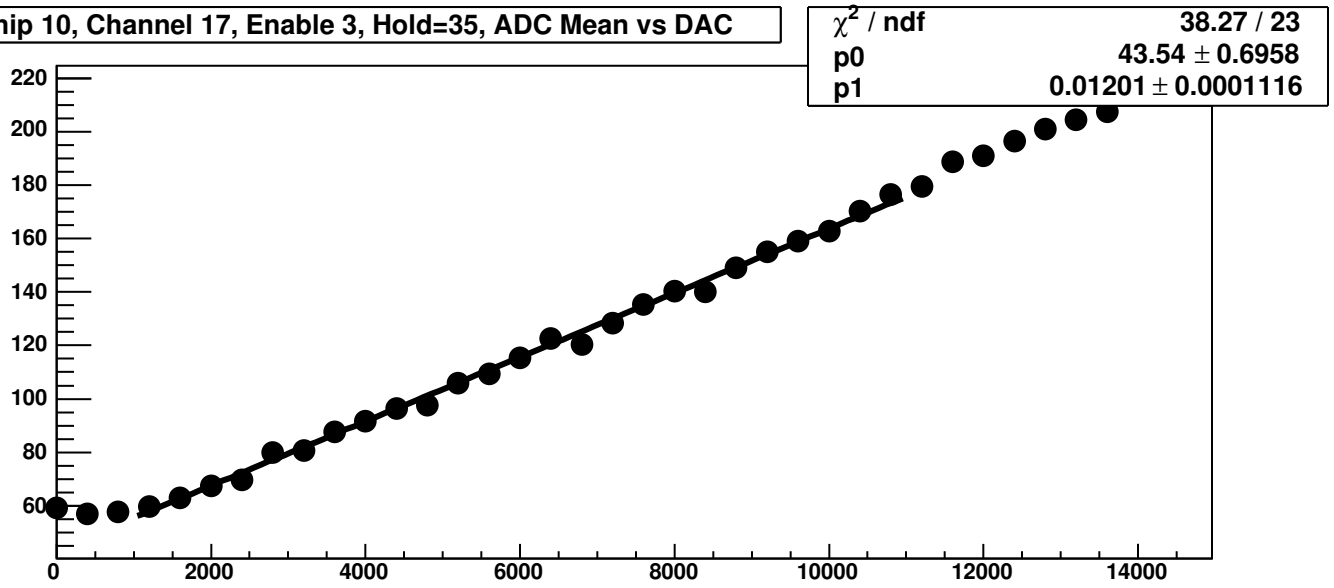

Chip 10, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 141.4 / 23
p0 121.5 ± 2.858
p1 2.238 ± 0.001822
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

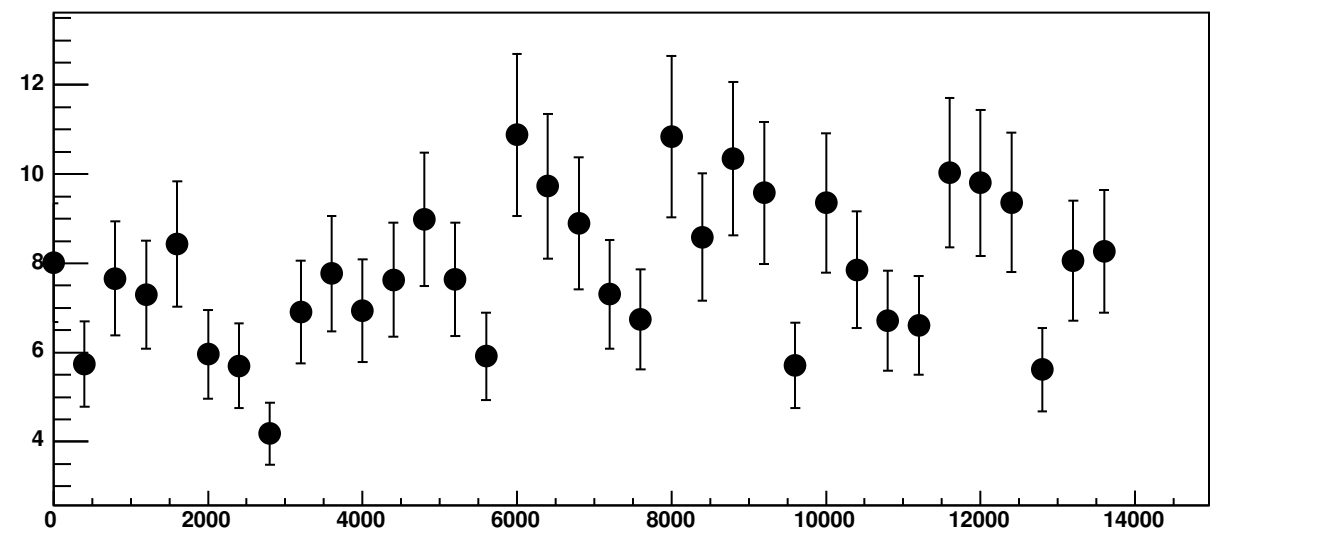

Chip 10, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

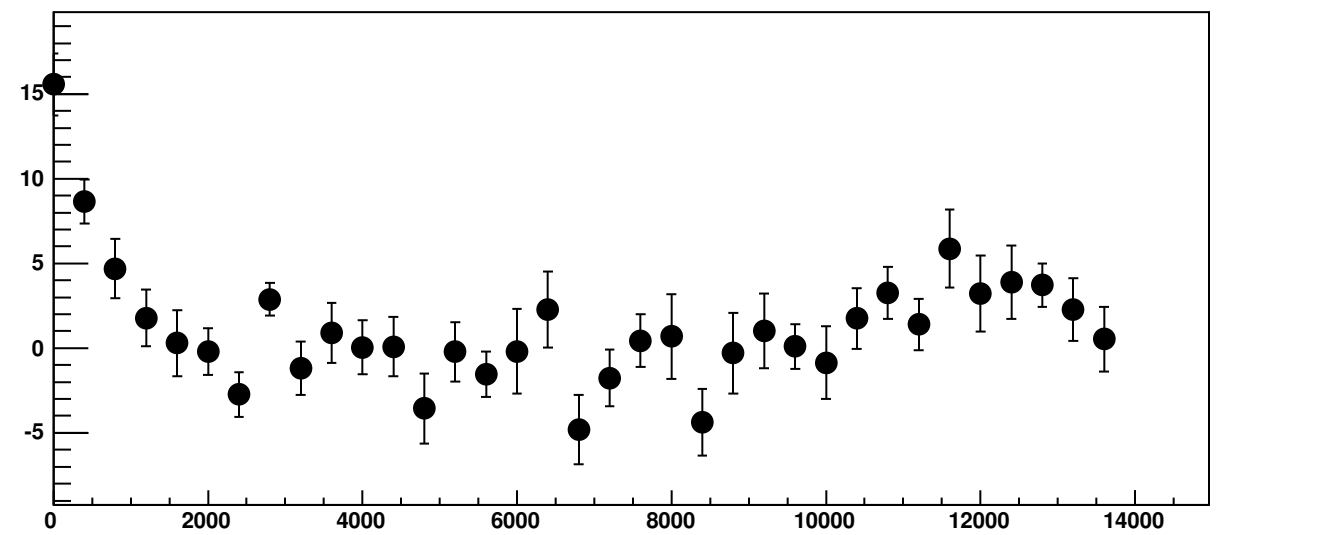
Chip 10, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

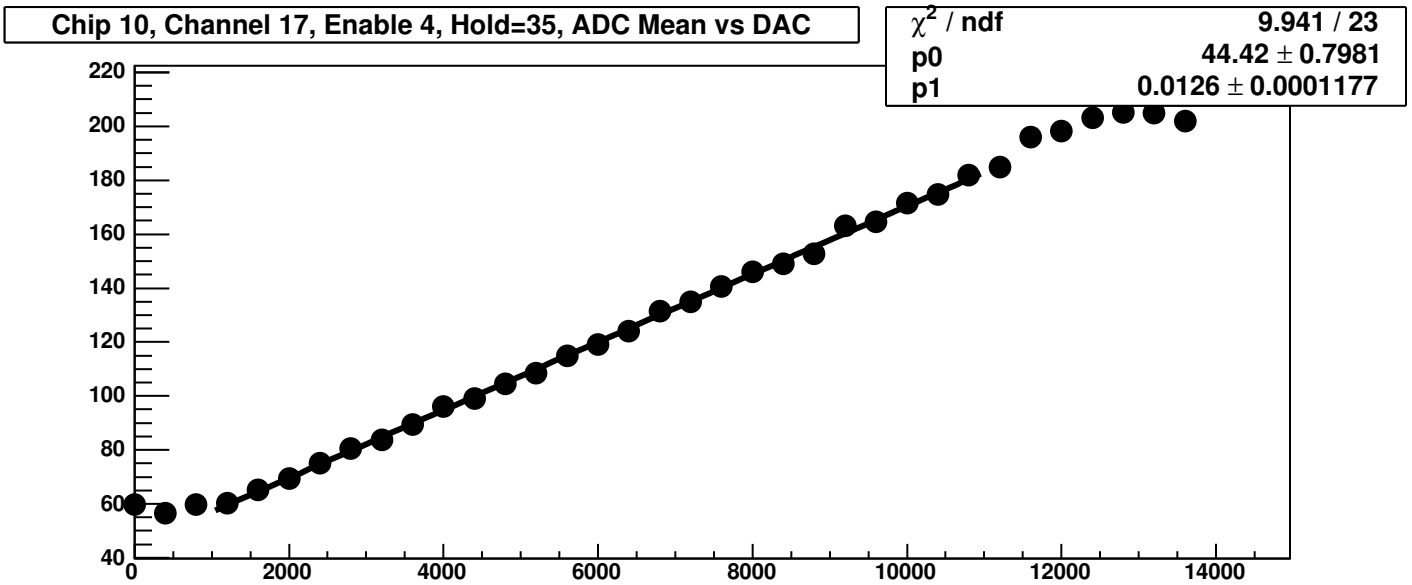


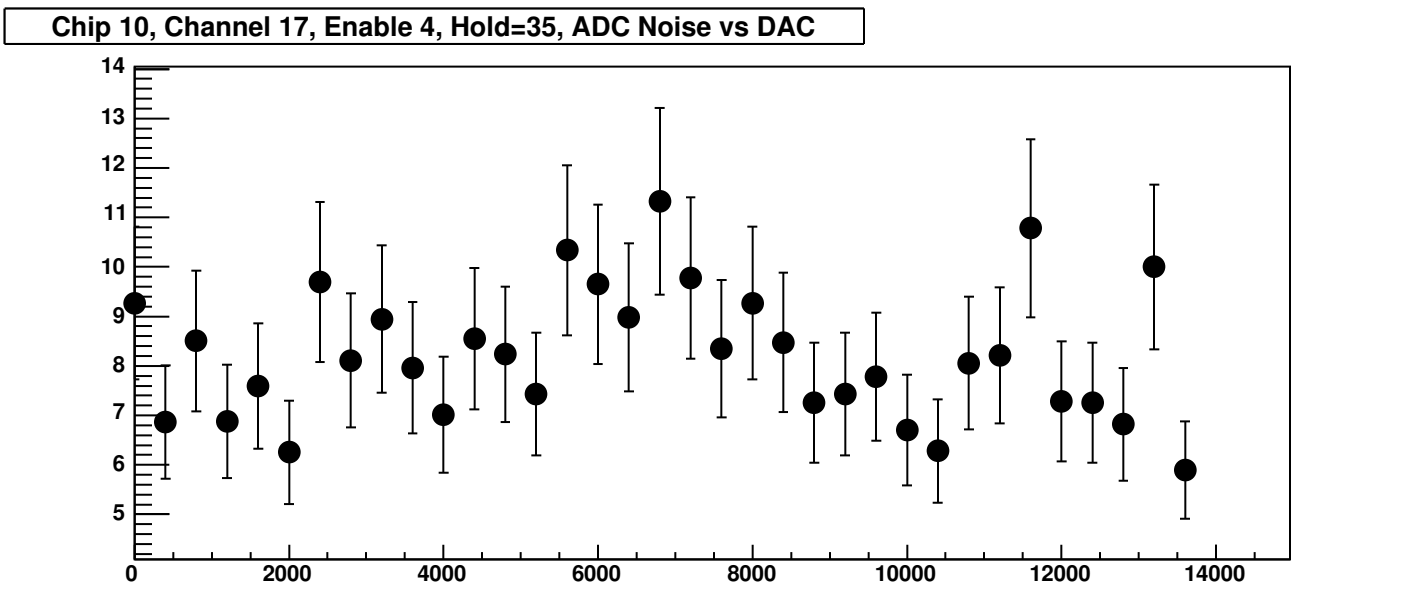
Chip 10, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC

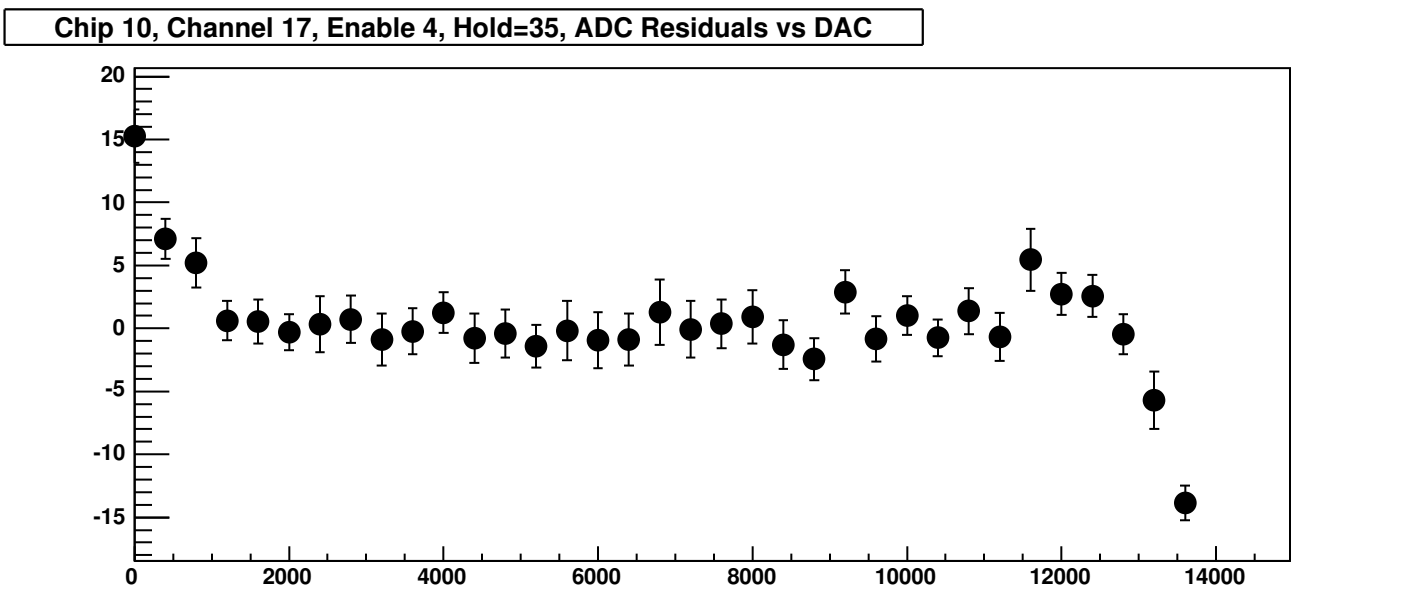


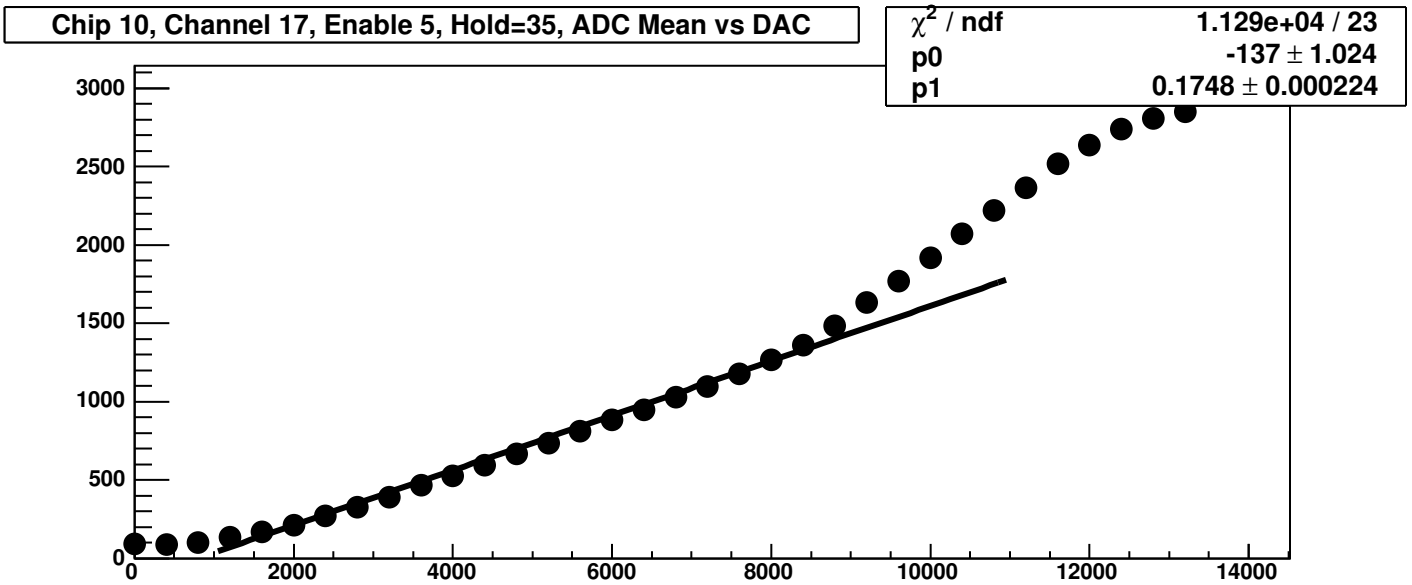
Chip 10, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

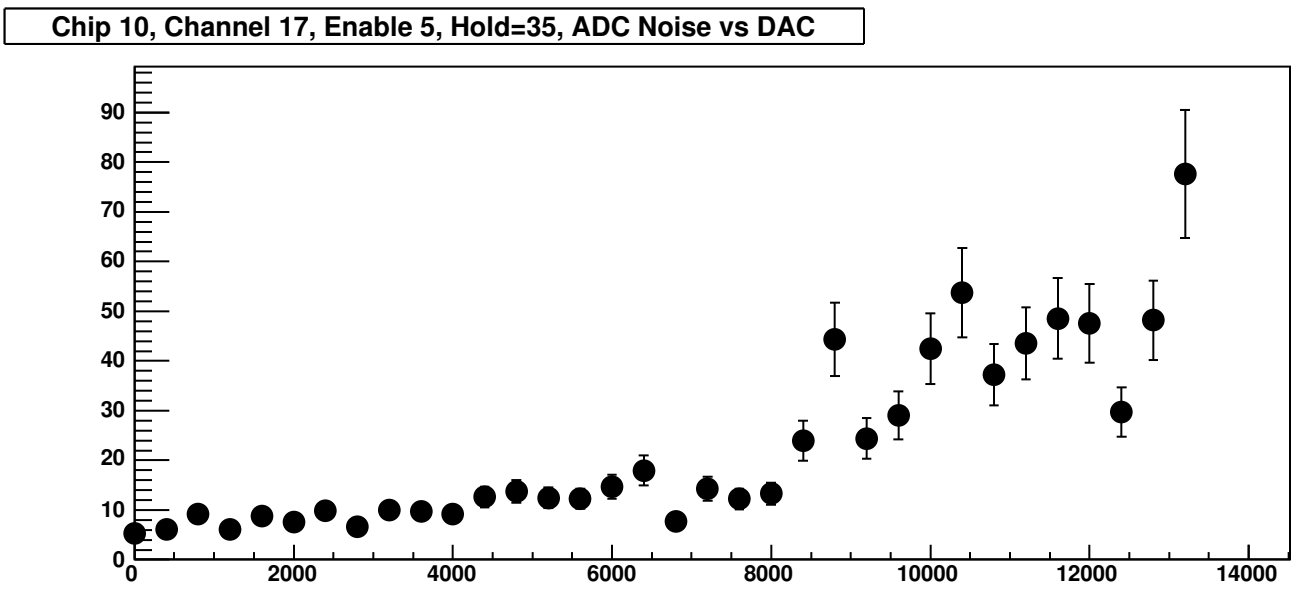


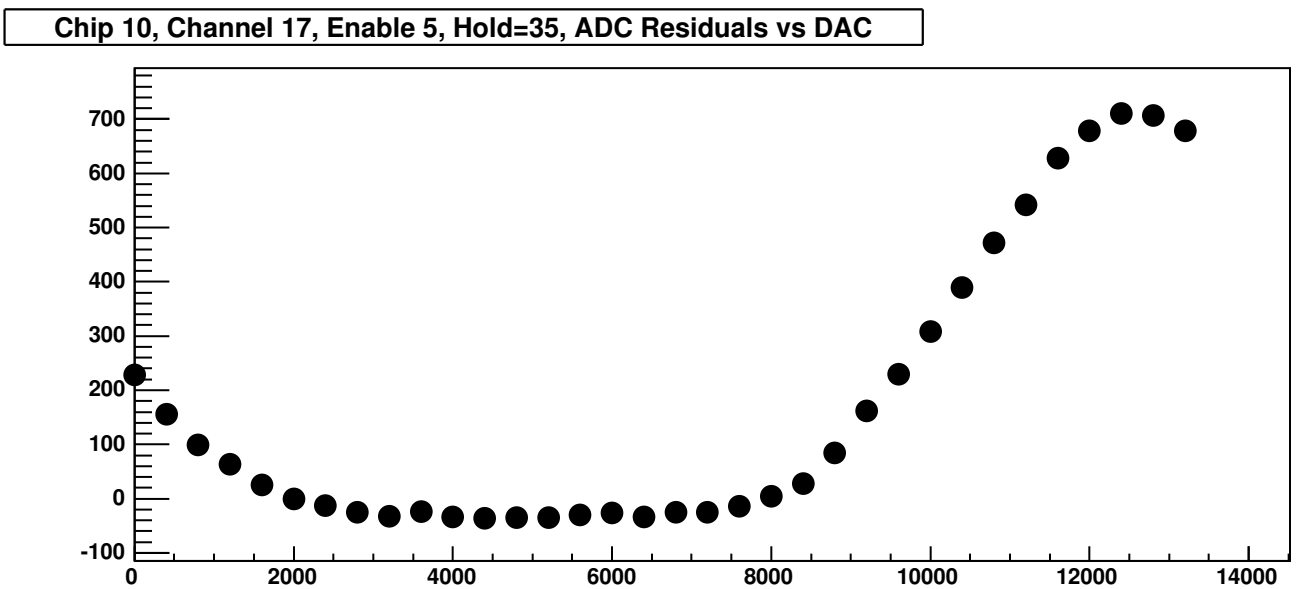

Chip 10, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 9.941 / 23
p0 44.42 ± 0.7981
p1 0.0126 ± 0.0001177
220
200
180
160
140
120
100
80
60
40
0
2000
4000
6000
8000
10000
12000
14000

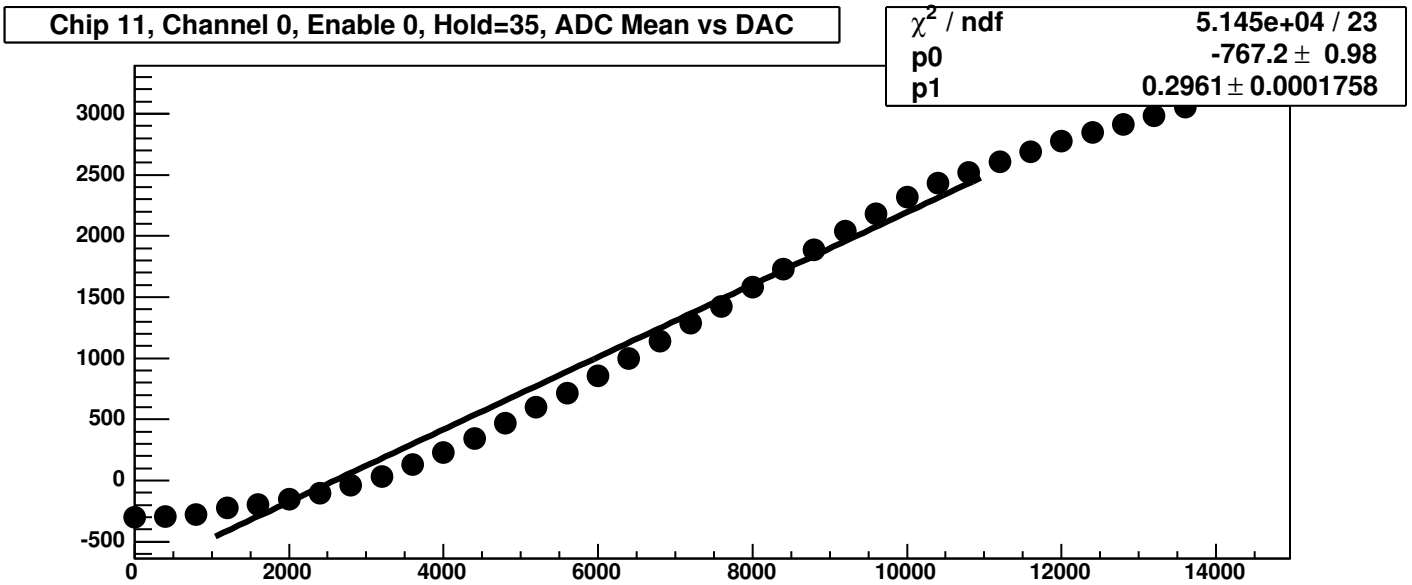

Chip 10, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
14
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

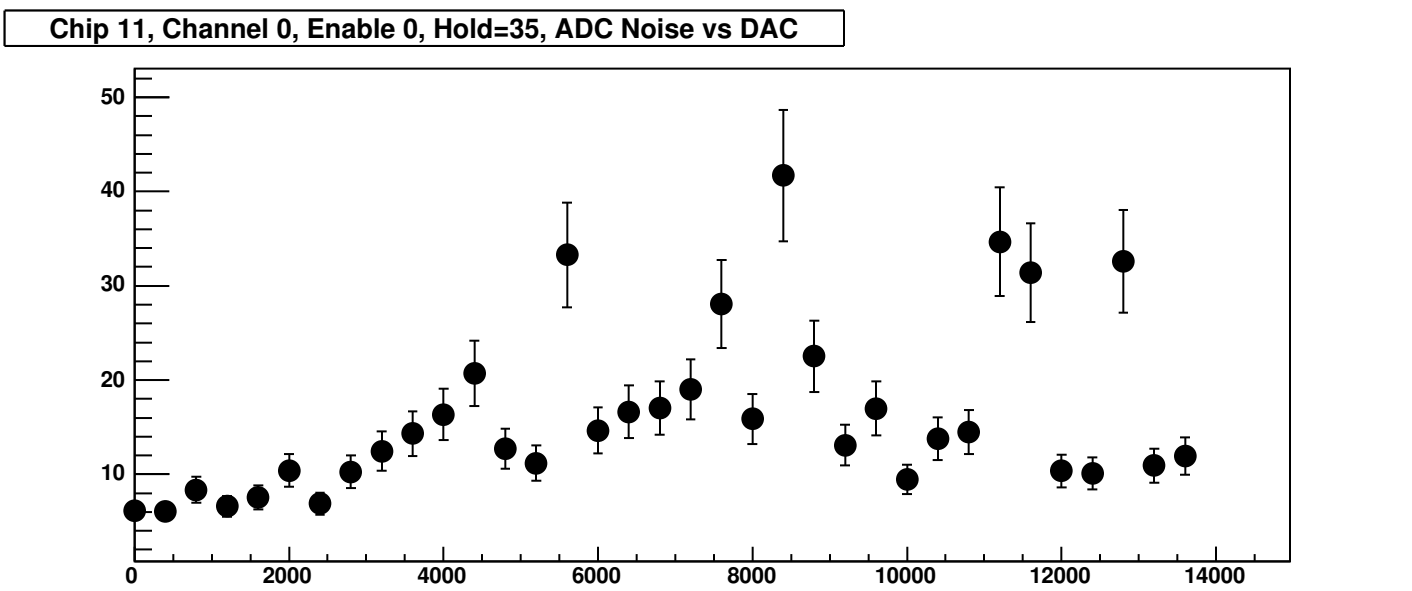

Chip 10, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

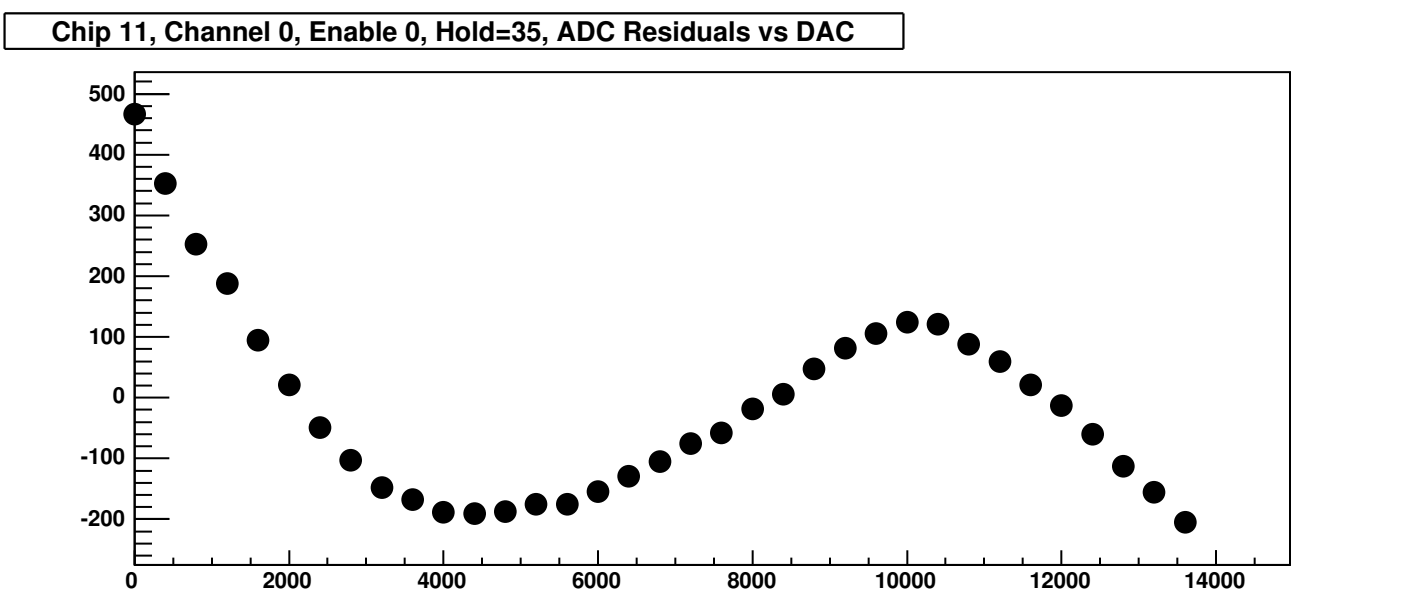

Chip 10, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.129e+04 / 23
p0 -137 ± 1.024
p1 0.1748 ± 0.000224
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

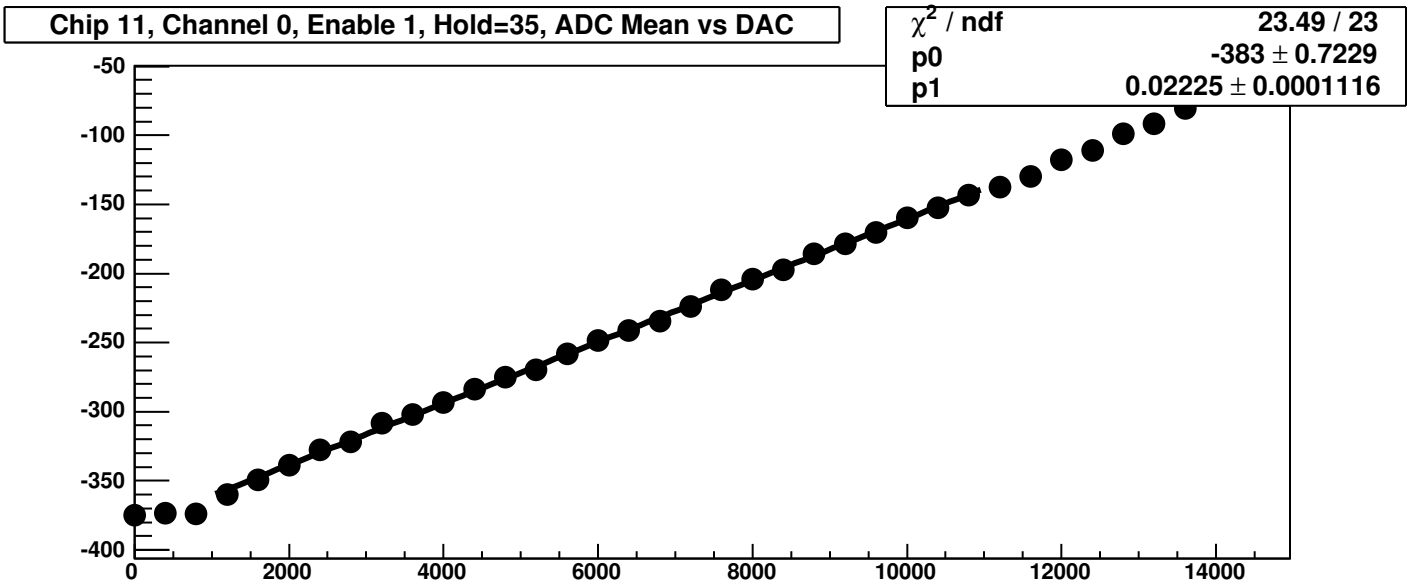

Chip 10, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

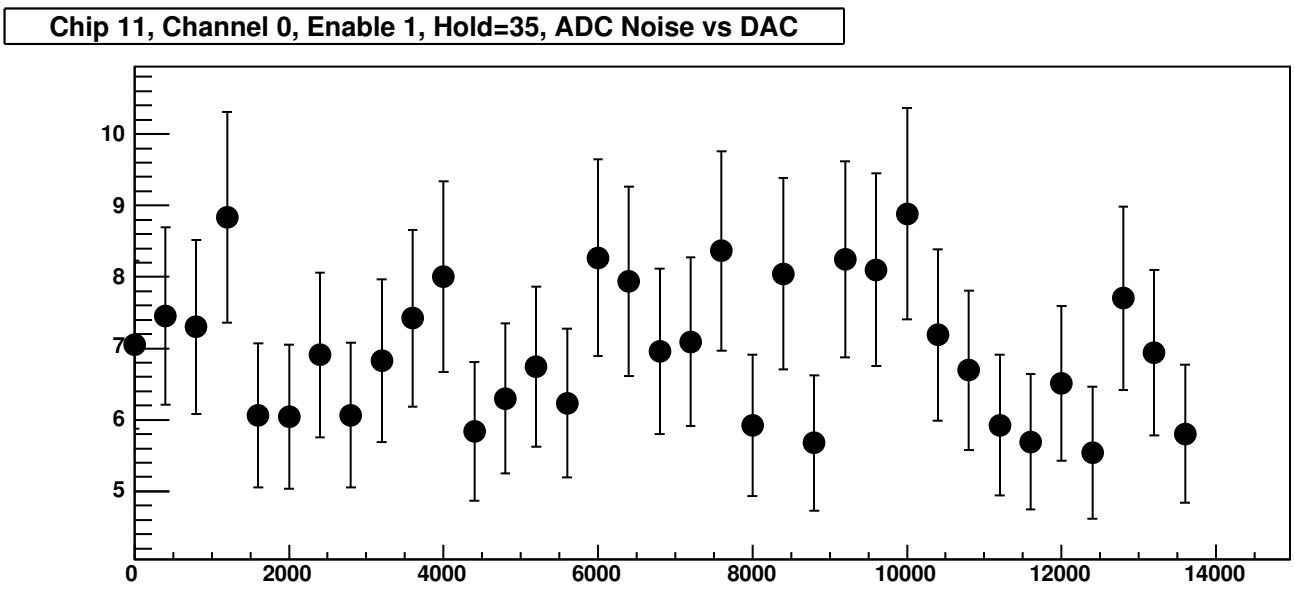

Chip 10, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
700
600
500
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

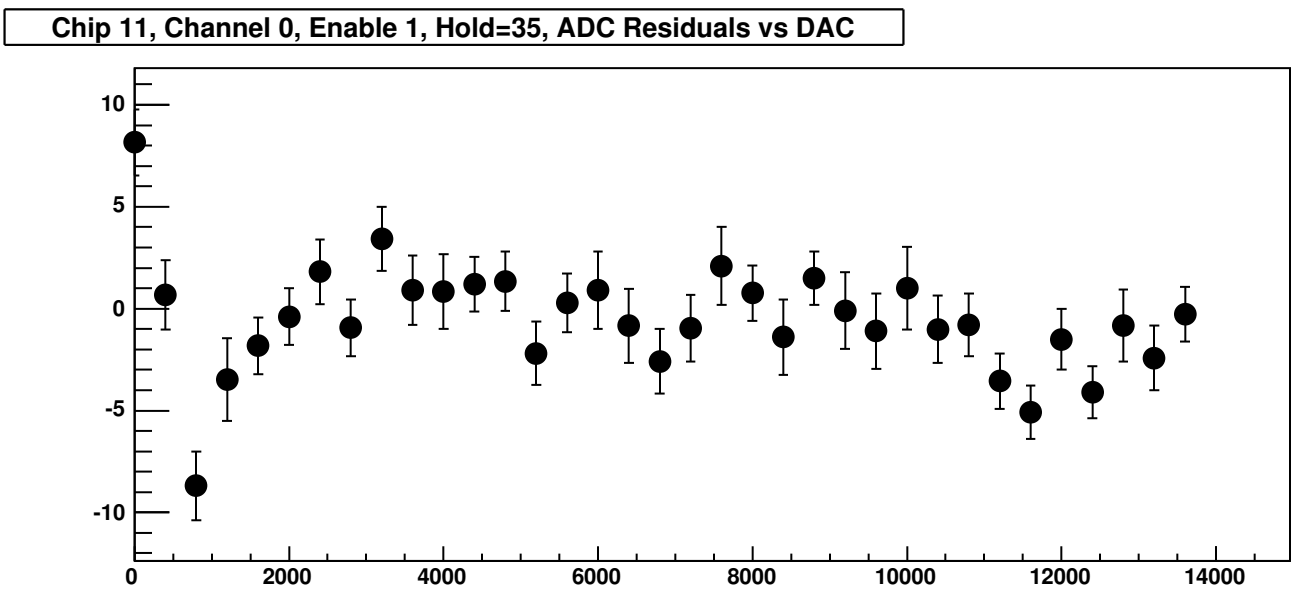

Chip 11, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.145e+04 / 23
p0 -767.2 ± 0.98
p1 0.2961 ± 0.0001758
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

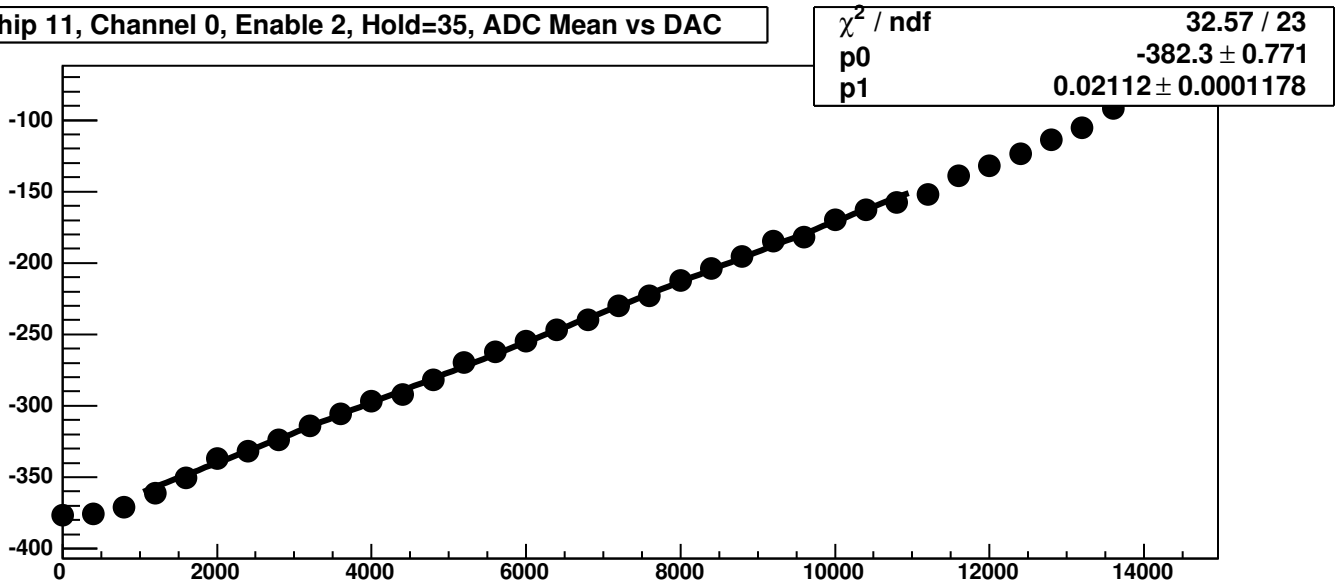

Chip 11, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 23.49 / 23
p0 -383 ± 0.7229
p1 0.02225 ± 0.0001116
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

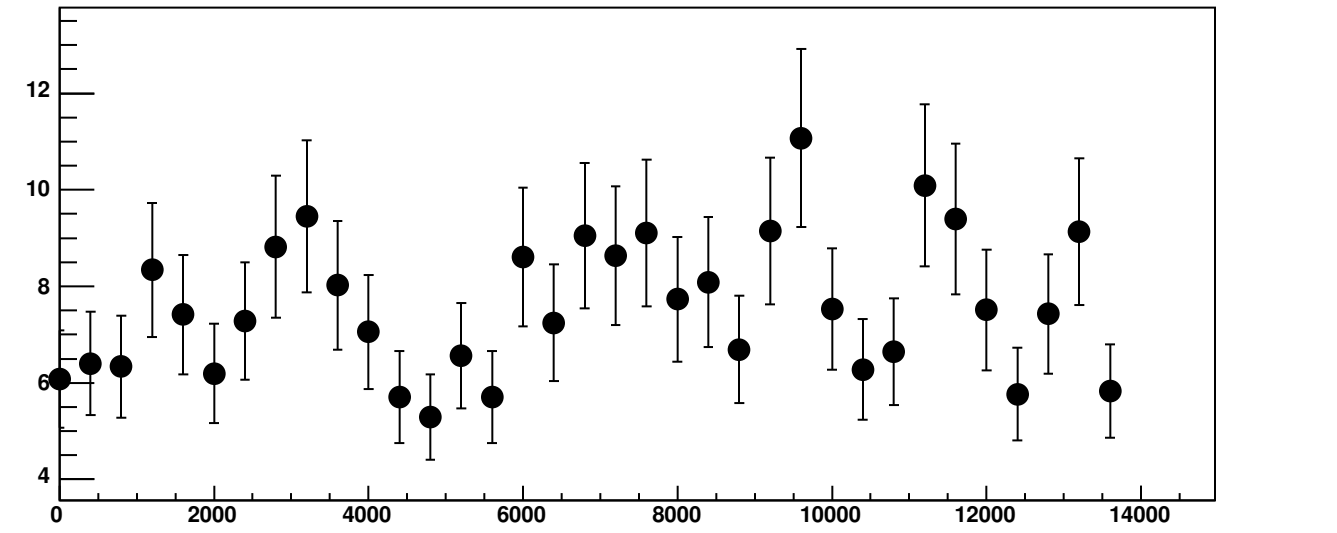

Chip 11, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

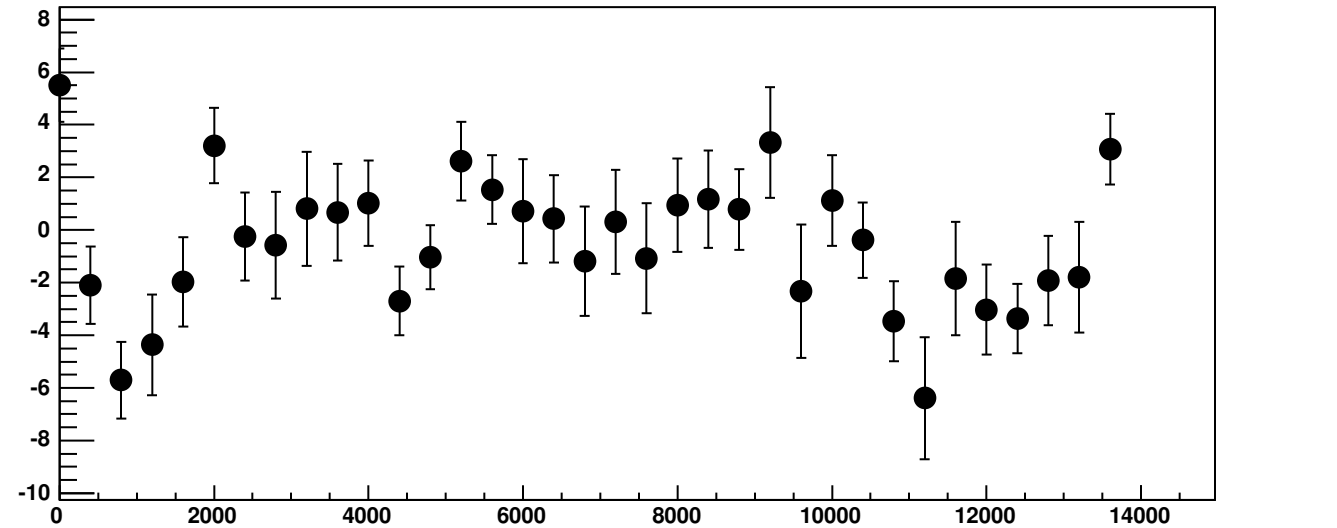
Chip 11, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

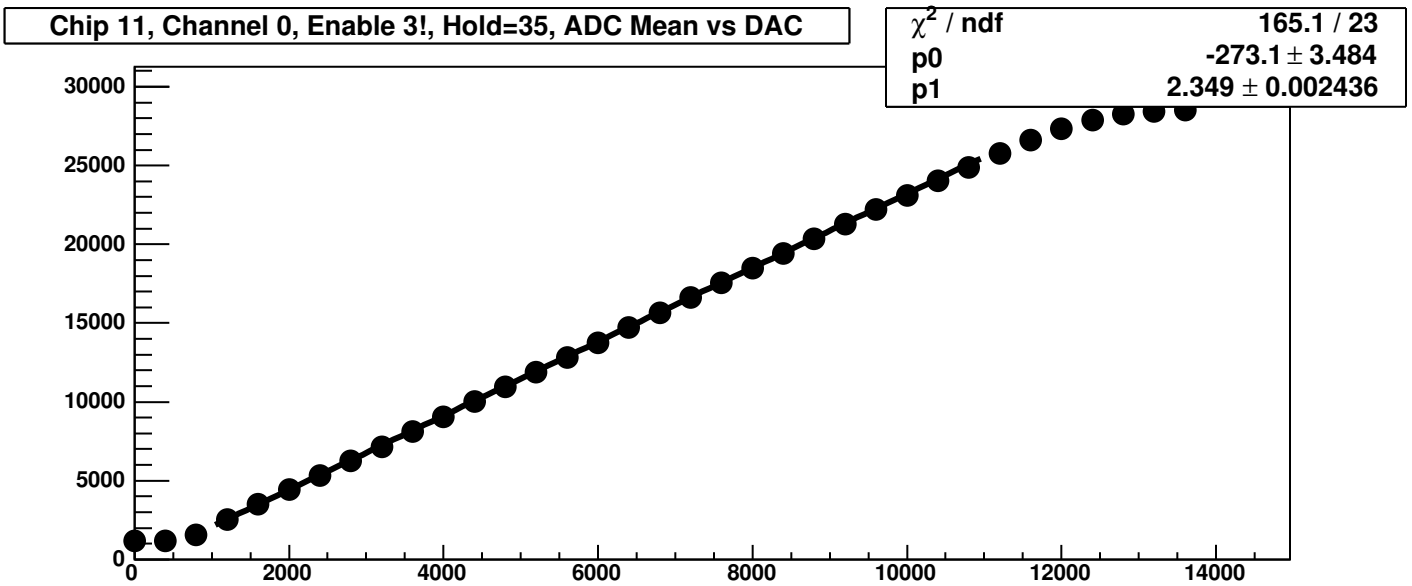


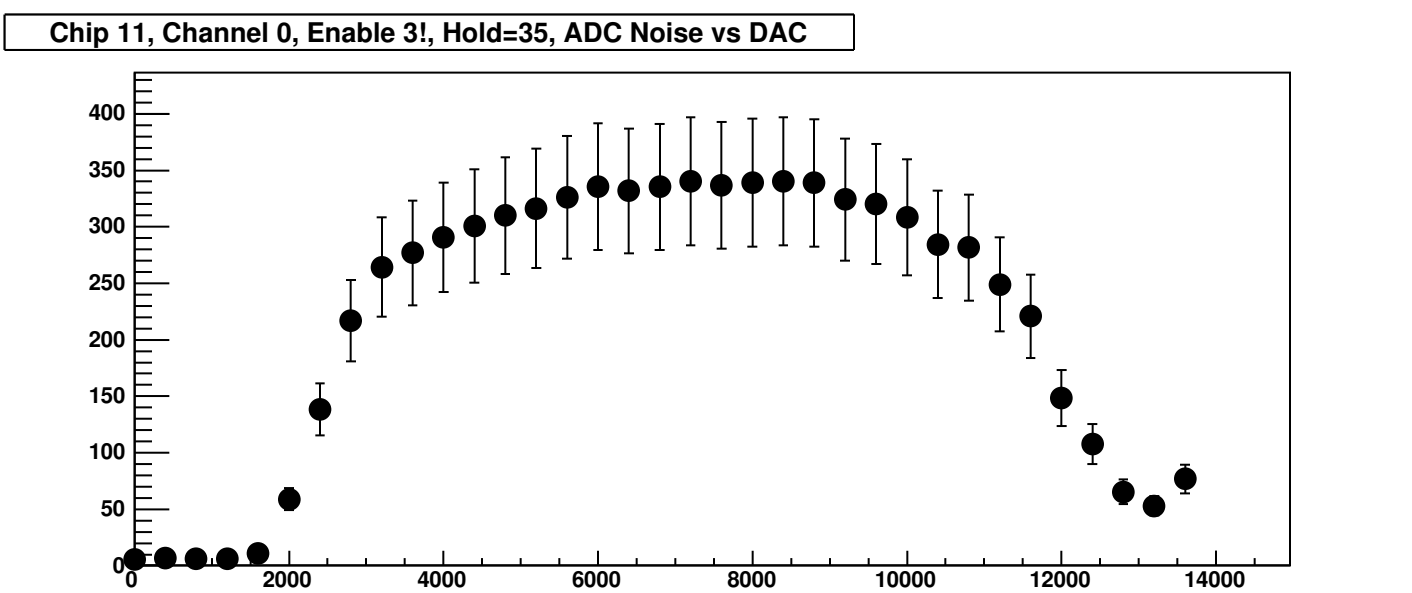
Chip 11, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC

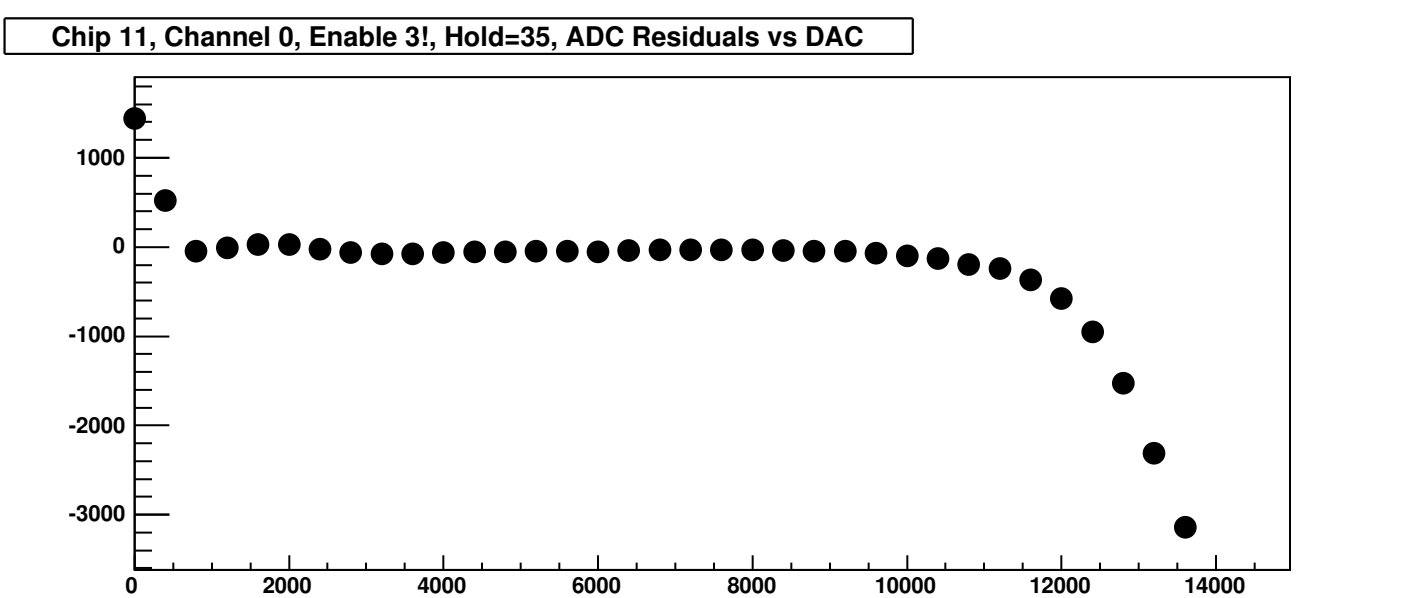


Chip 11, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

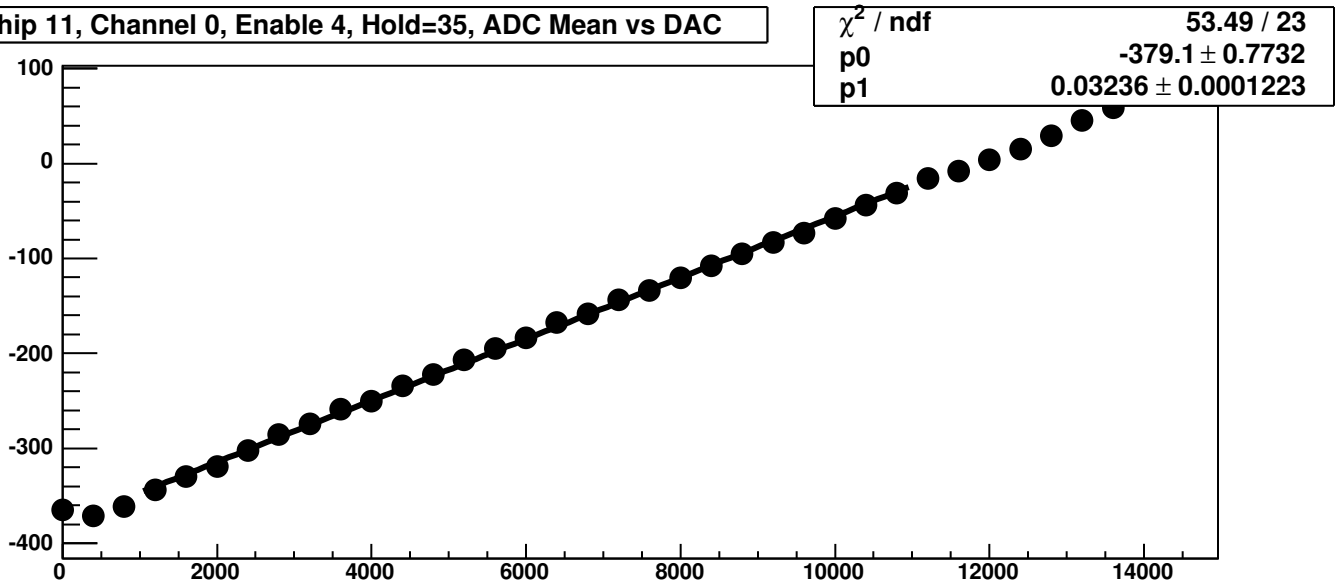



Chip 11, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 165.1 / 23
p0 -273.1 ± 3.484
p1 2.349 ± 0.002436
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

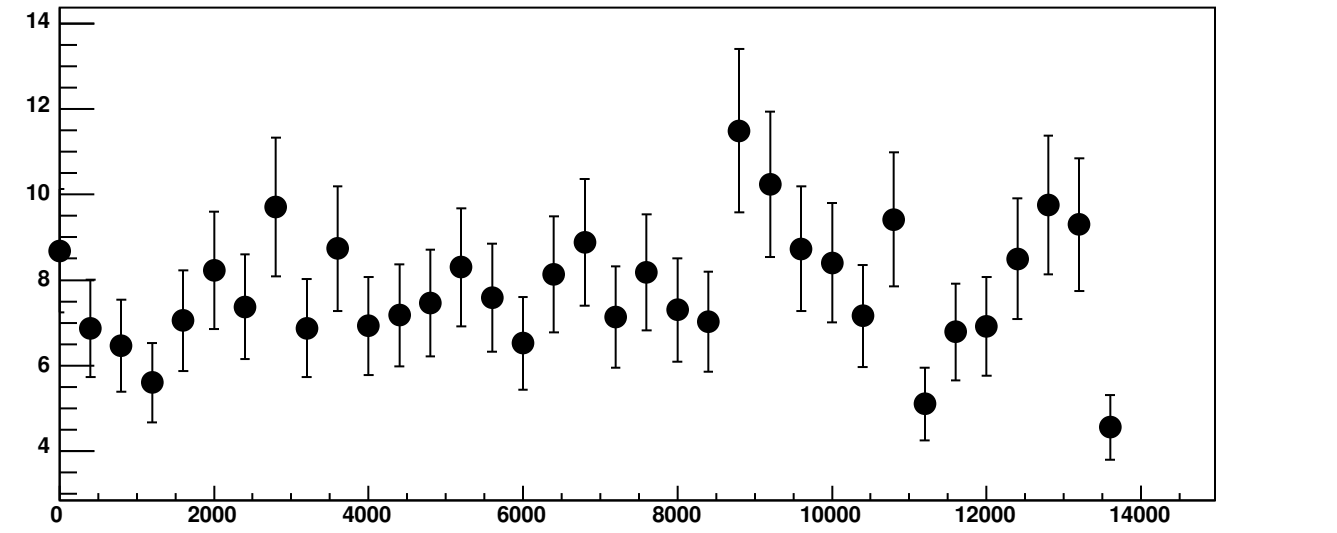

Chip 11, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

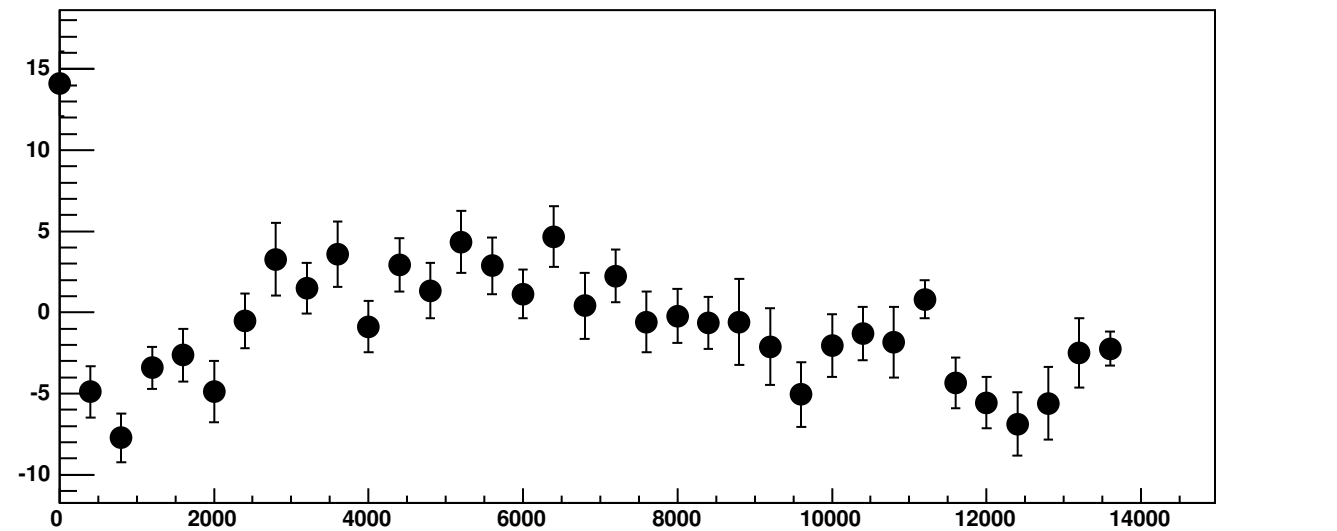
Chip 11, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

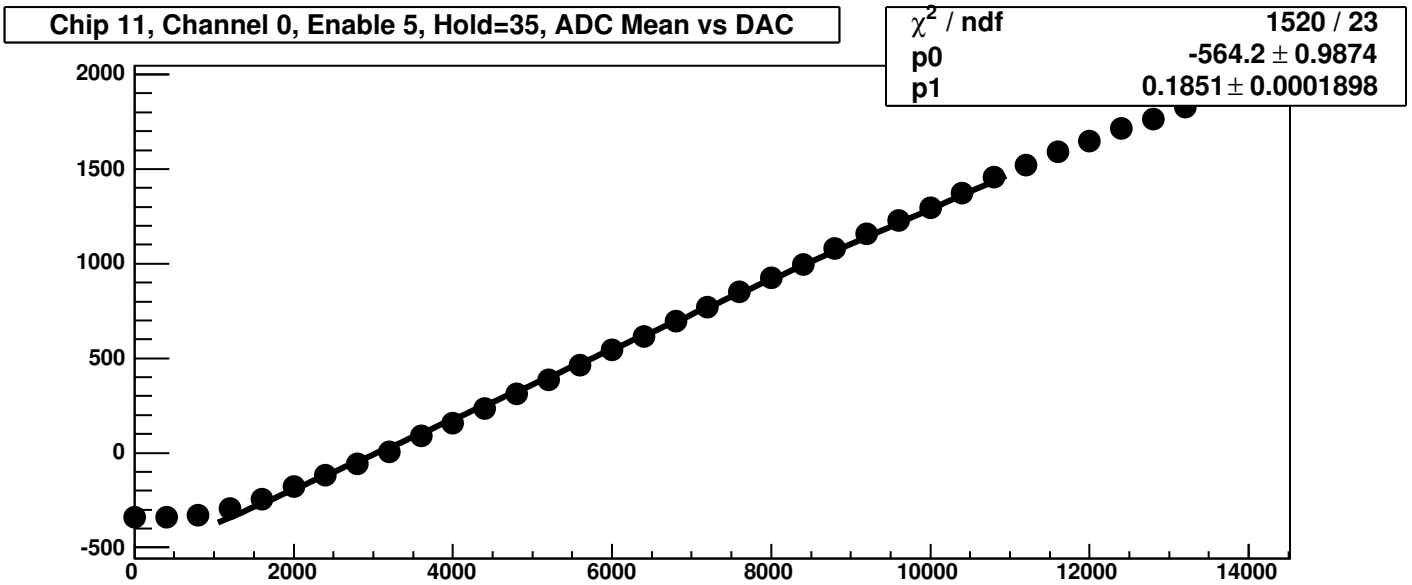


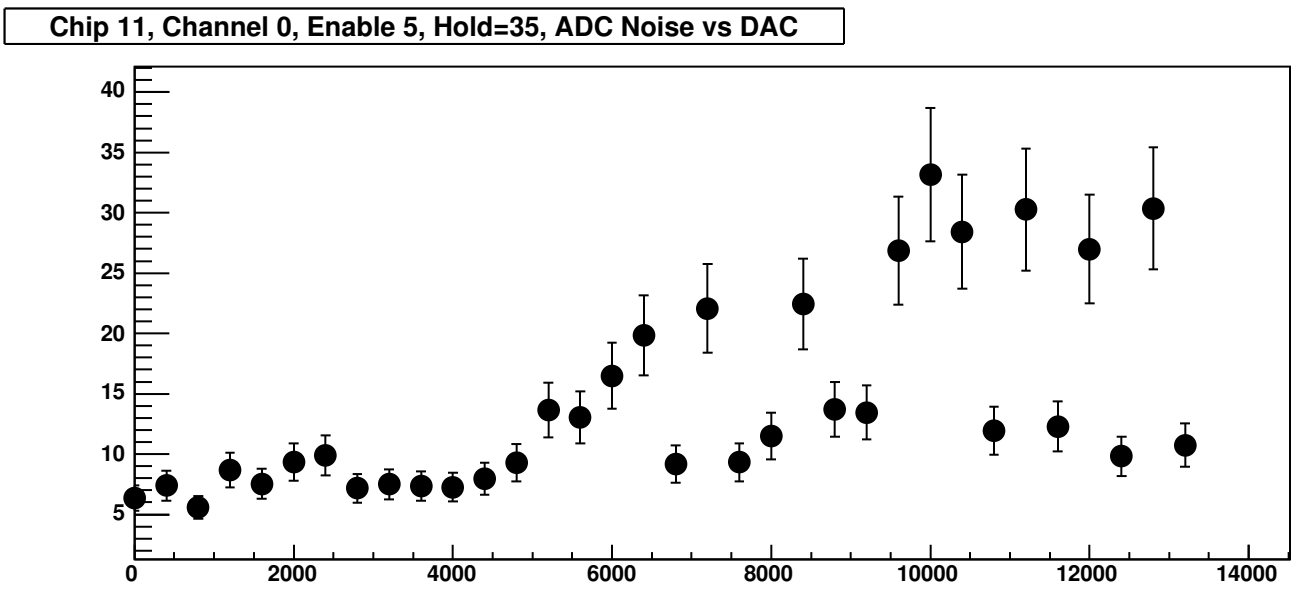
Chip 11, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

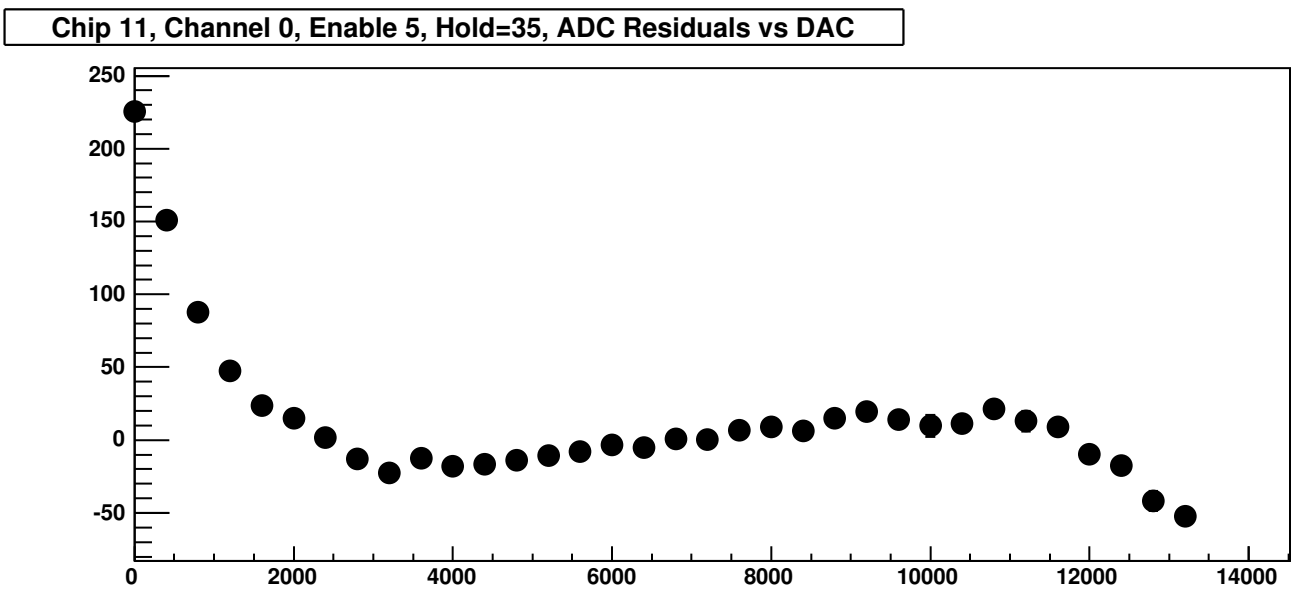


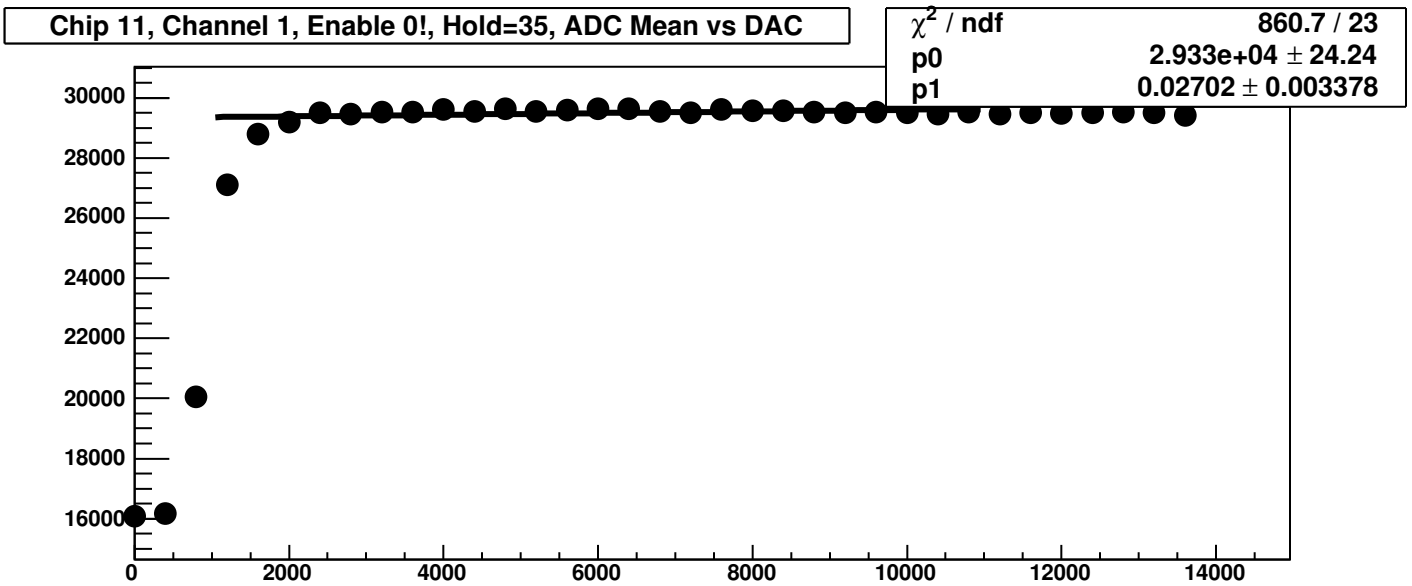
Chip 11, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

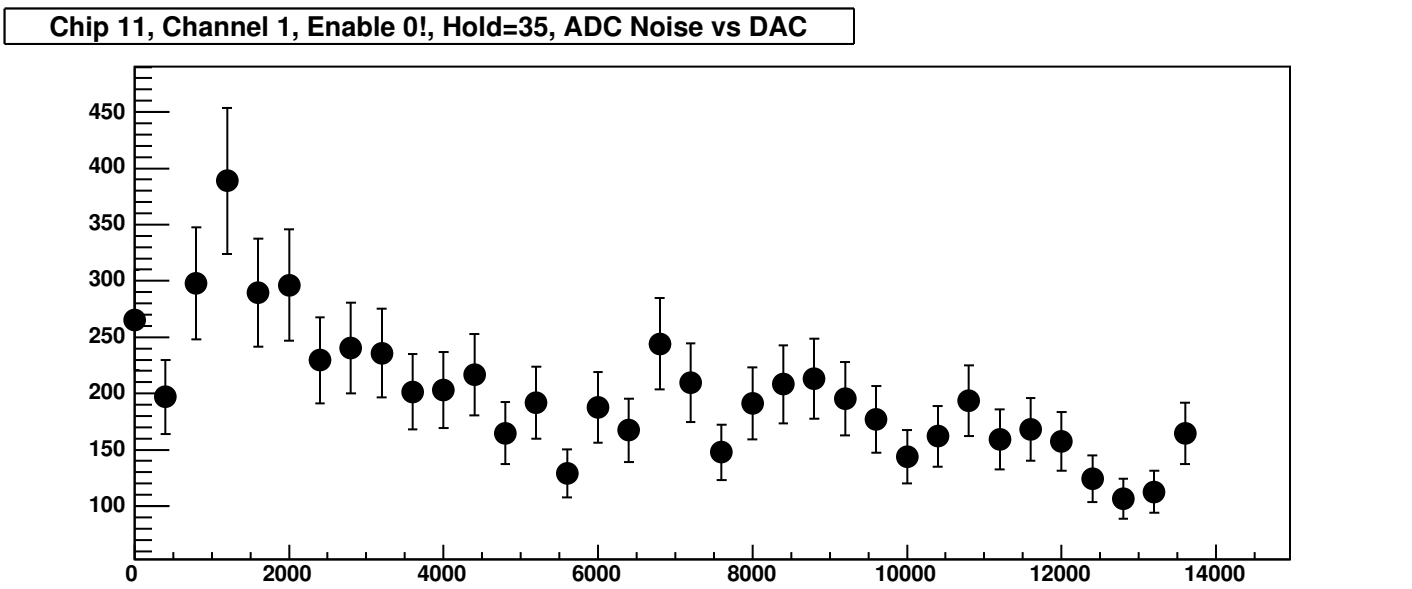


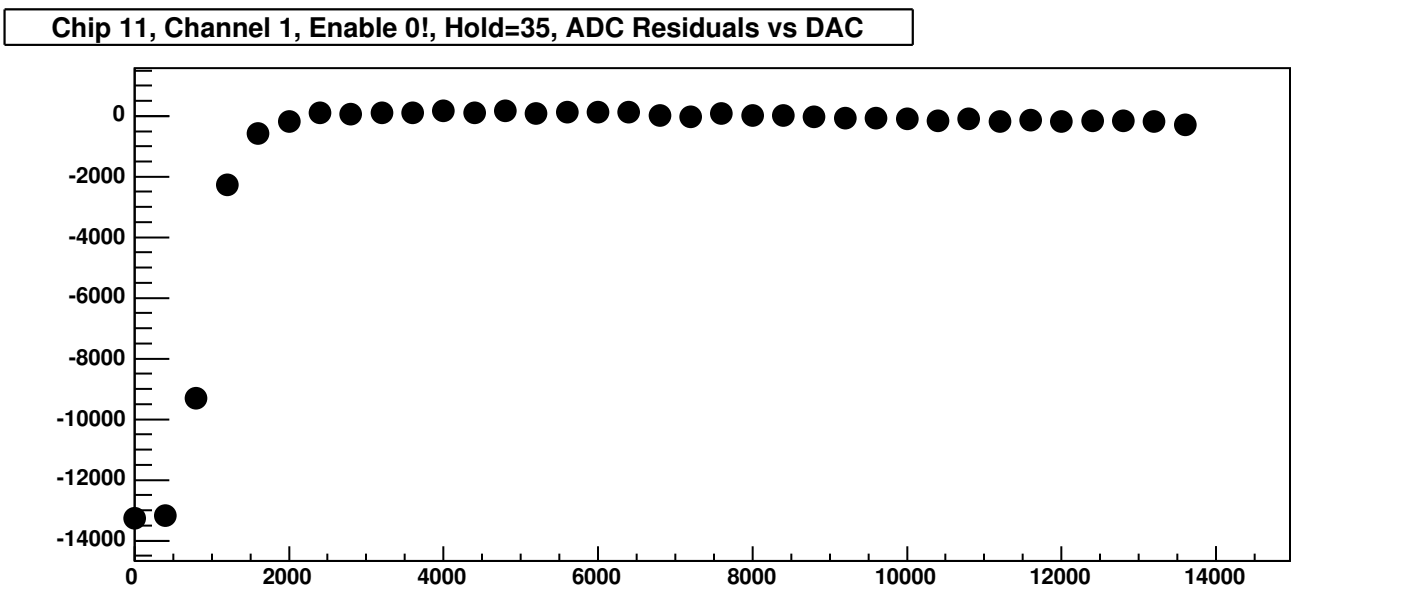

Chip 11, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1520 / 23
p0 -564.2 ± 0.9874
p1 0.1851 ± 0.0001898
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

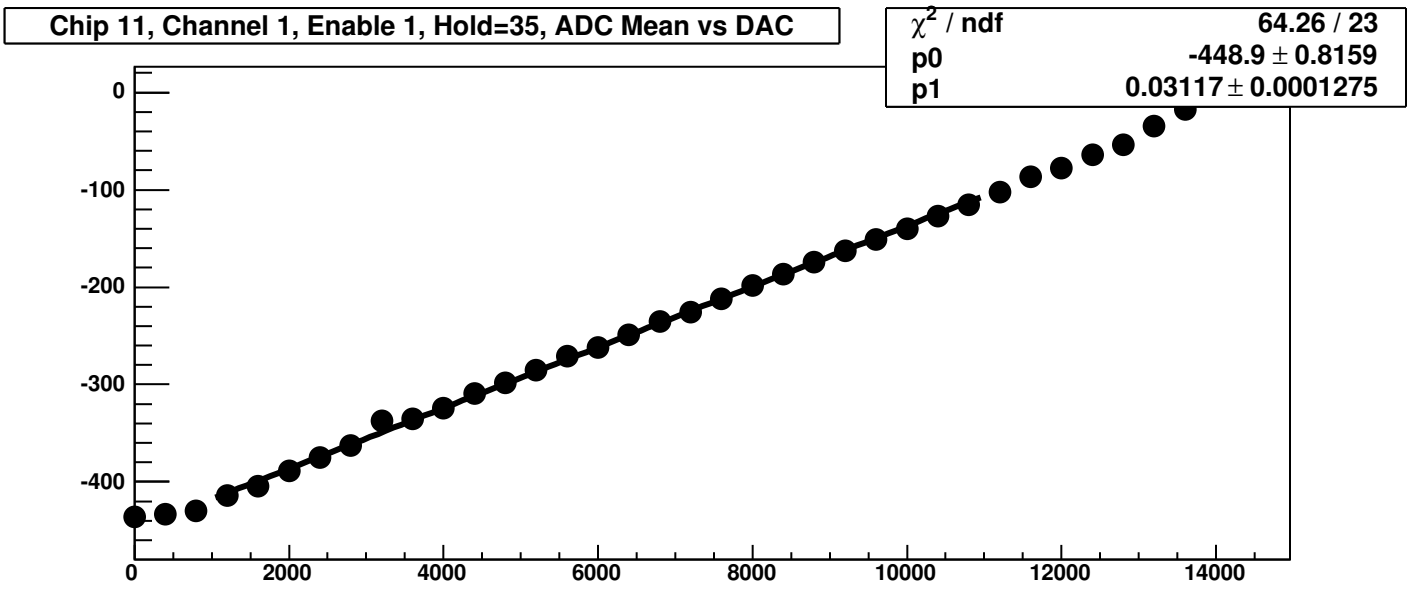

Chip 11, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

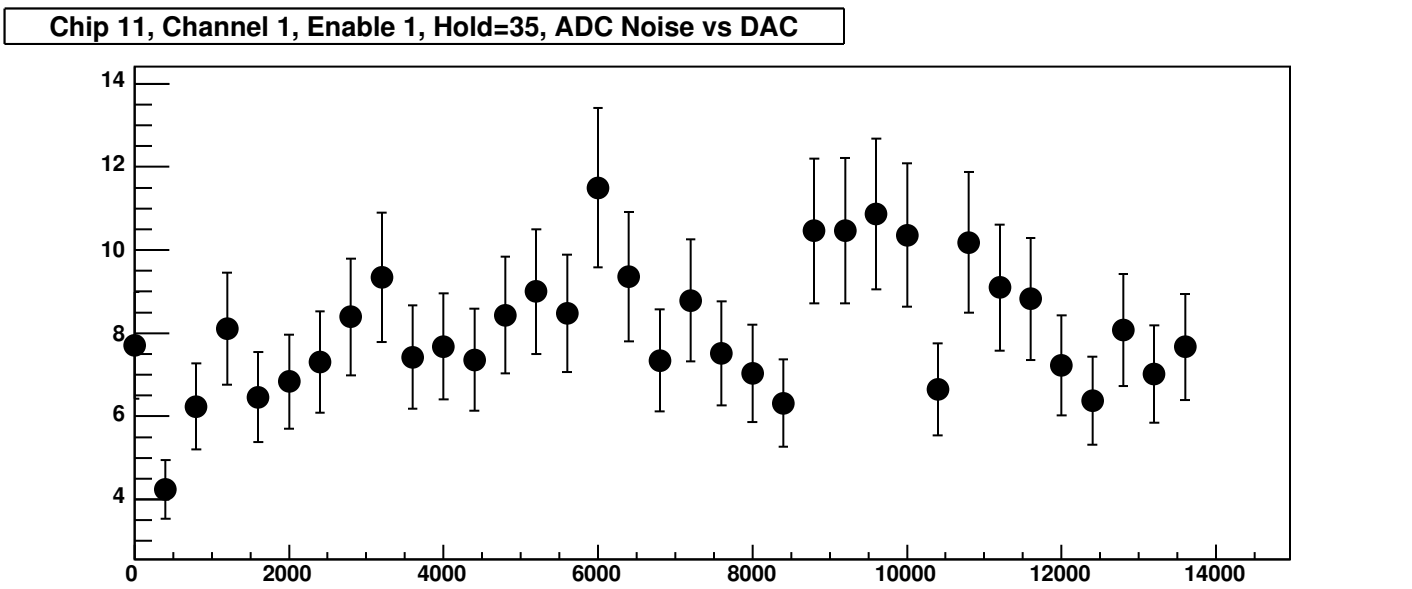

Chip 11, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

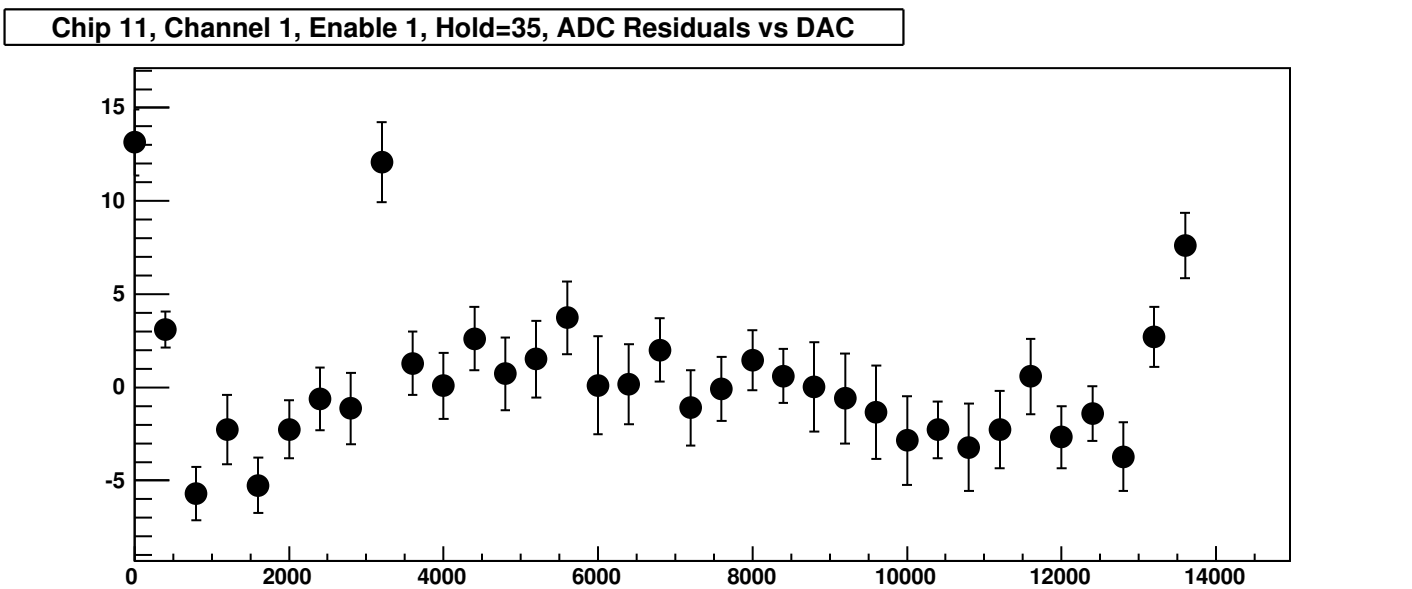

Chip 11, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 860.7 / 23
p0 2.933e+04 ± 24.24
p1 0.02702 ± 0.003378
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

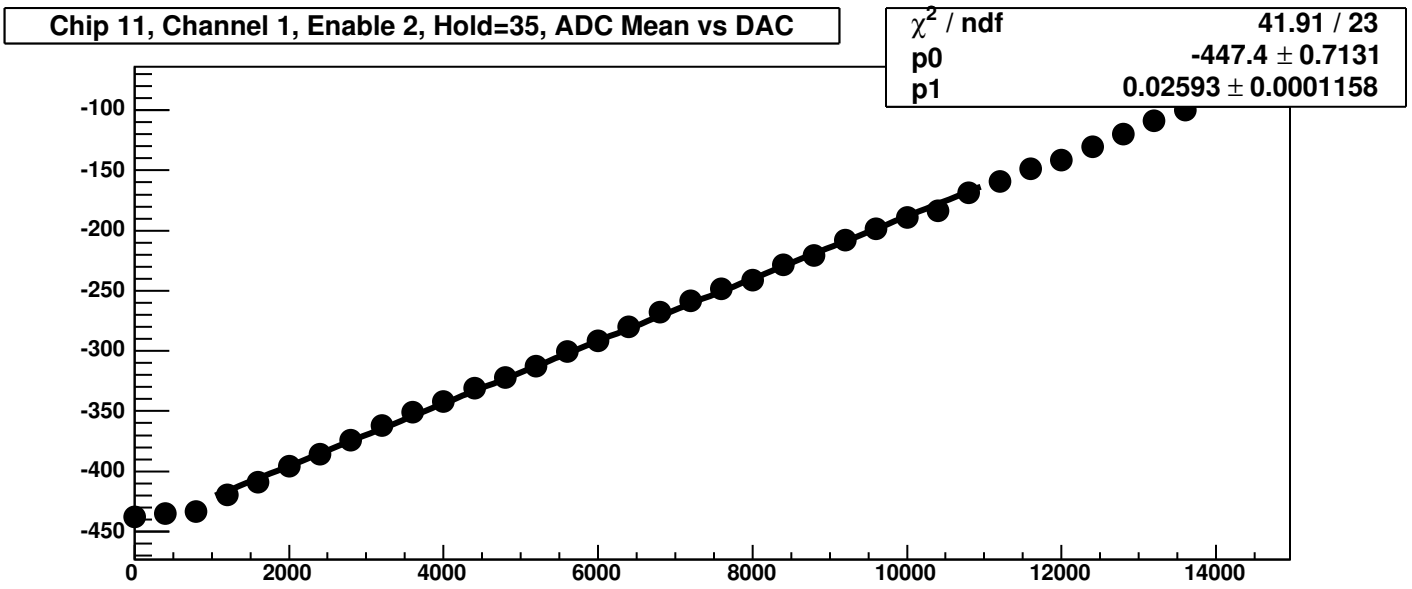

Chip 11, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

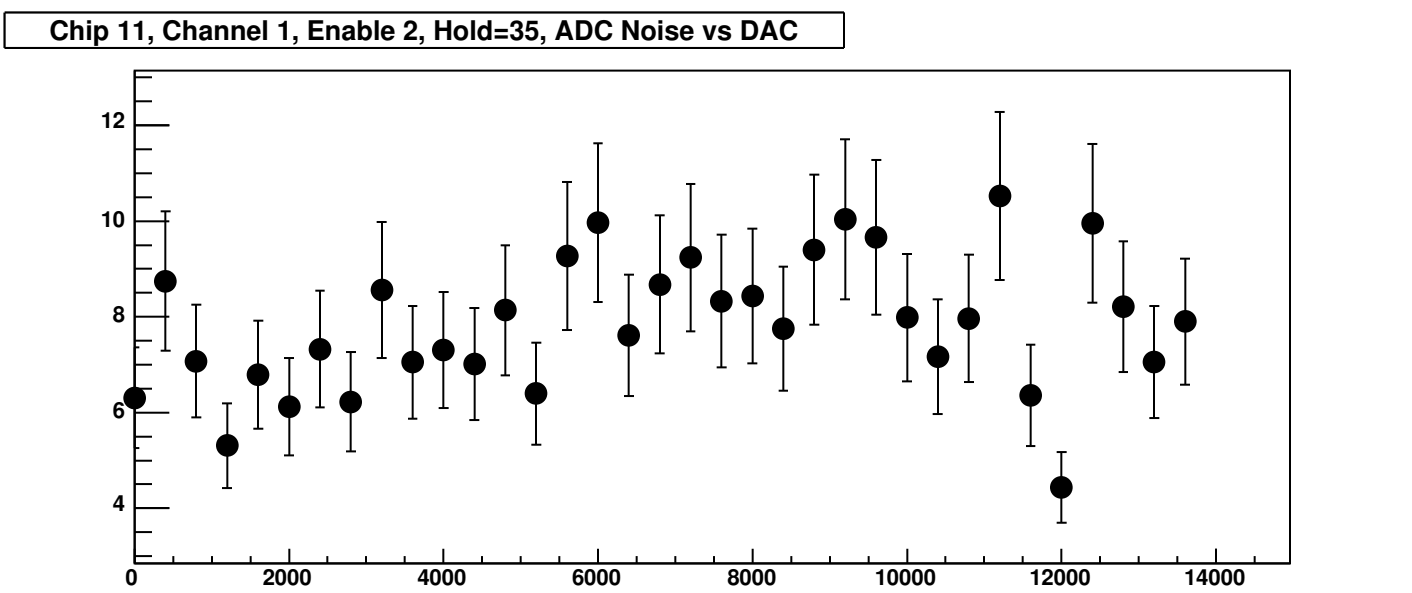

Chip 11, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

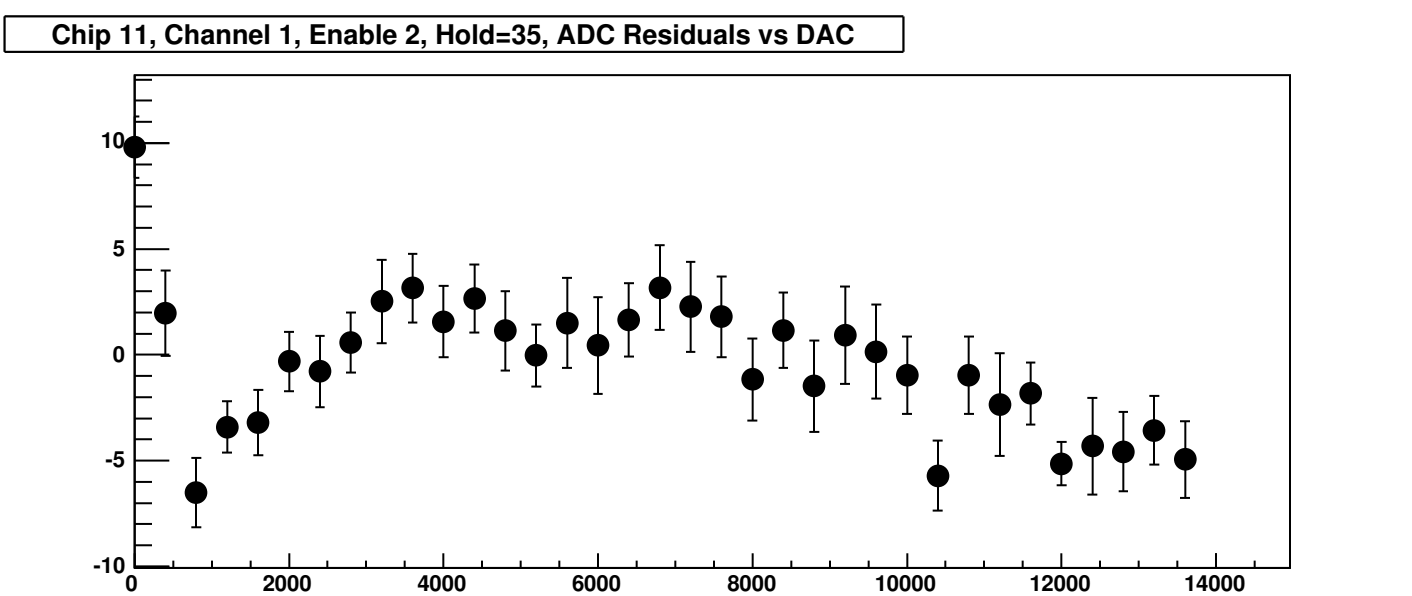

Chip 11, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 64.26 / 23
p0 -448.9 ± 0.8159
p1 0.03117 ± 0.0001275
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

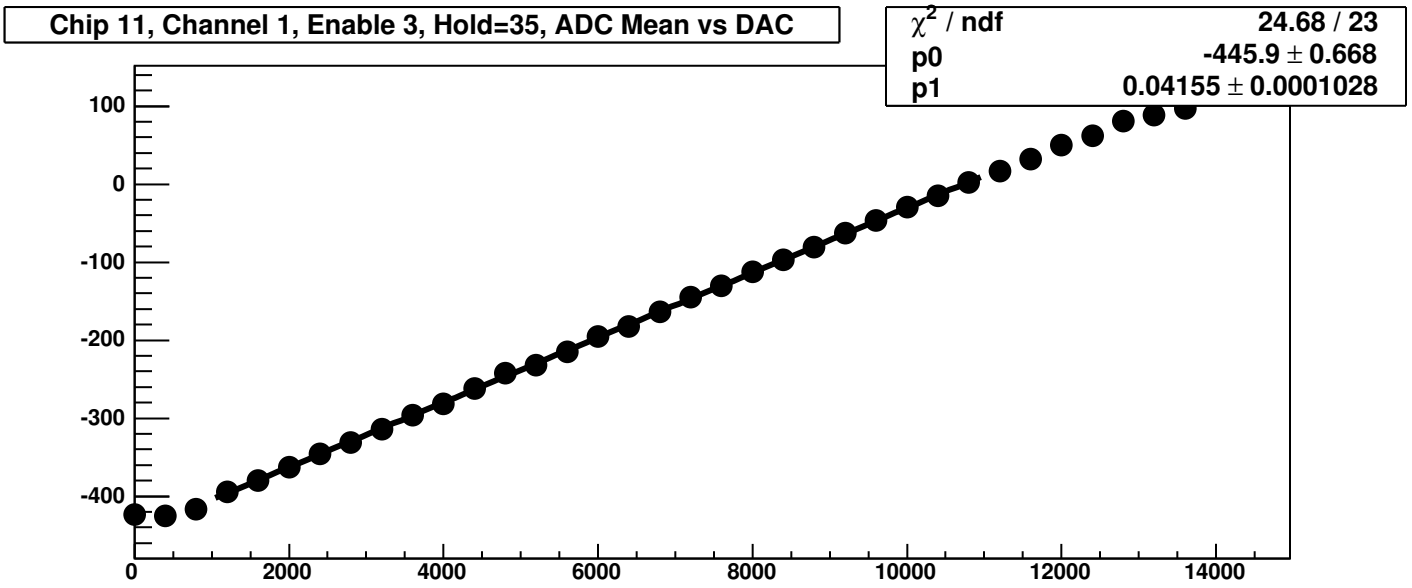

Chip 11, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

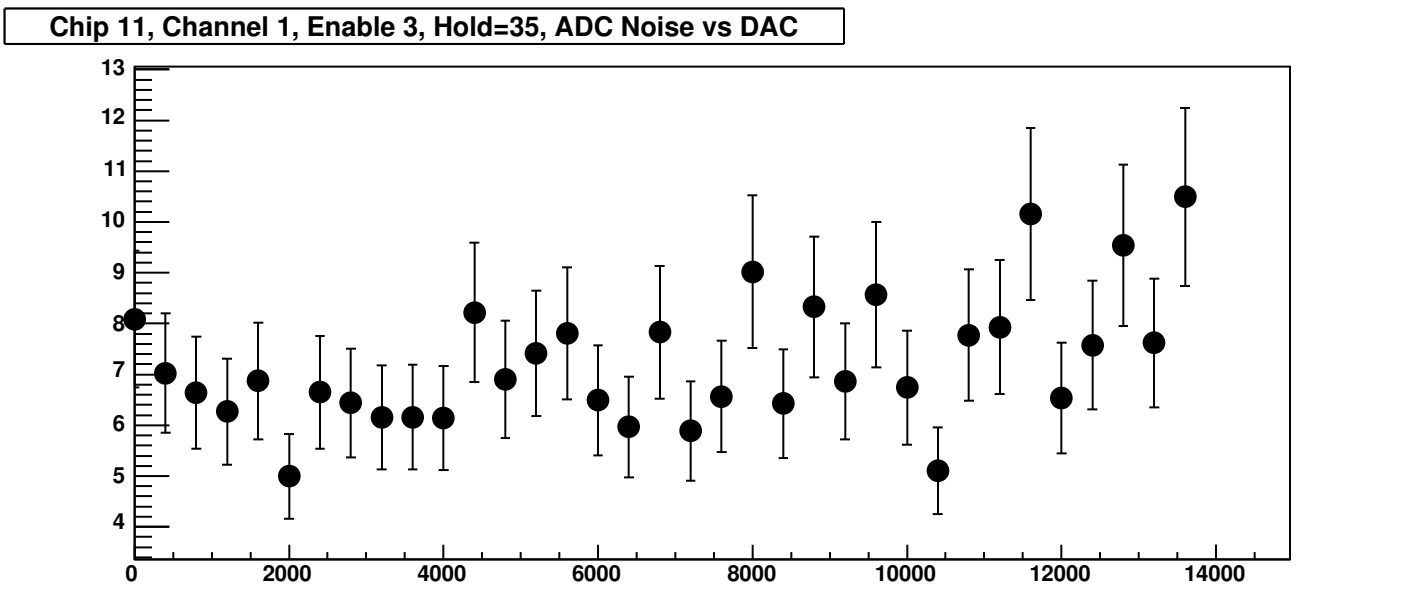

Chip 11, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

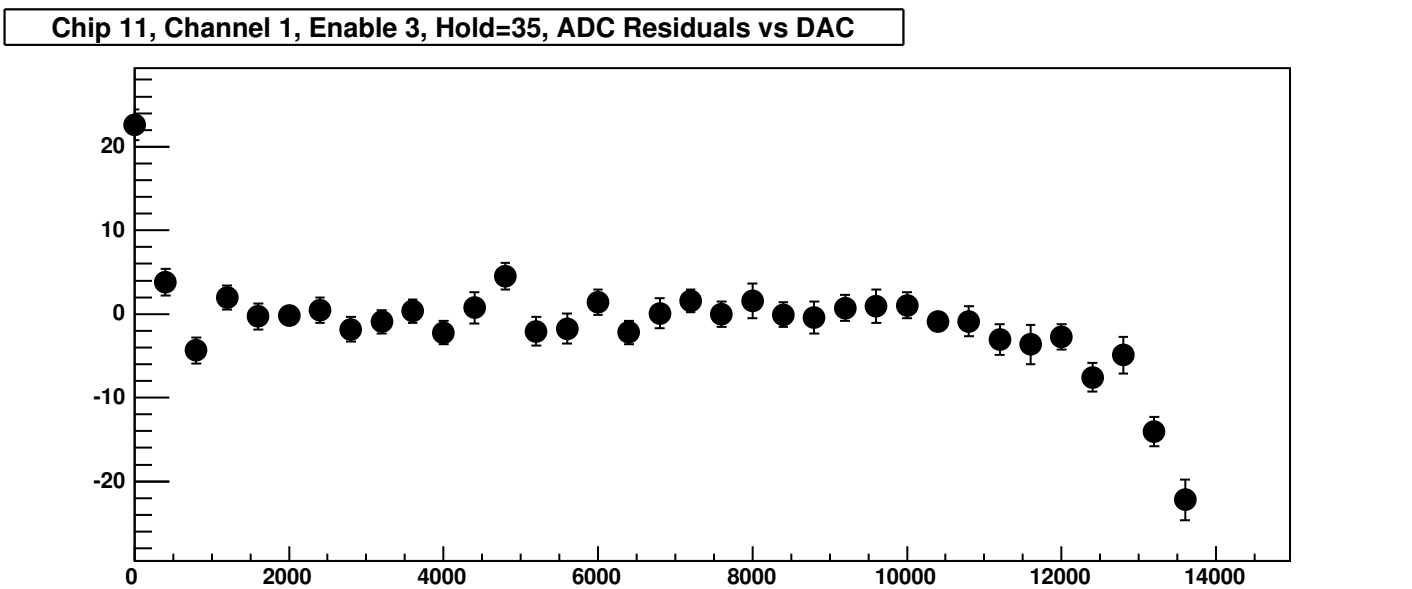

Chip 11, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 41.91 / 23
p0 -447.4 ± 0.7131
p1 0.02593 ± 0.0001158
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

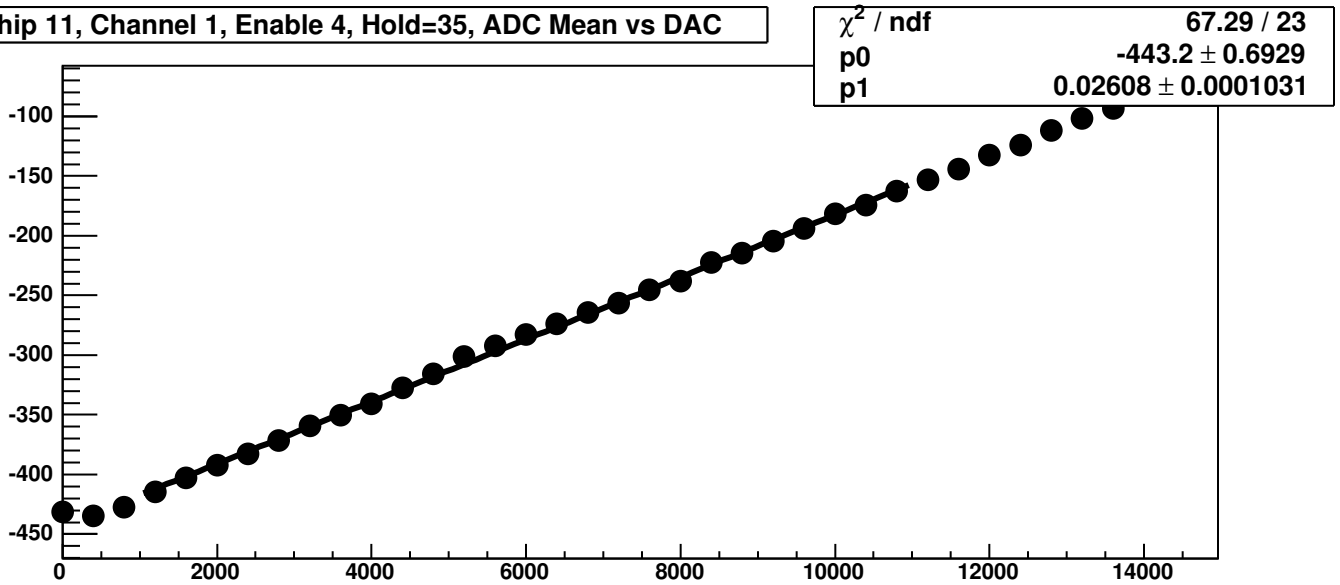

Chip 11, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.68 / 23
p0 -445.9 ± 0.668
p1 0.04155 ± 0.0001028
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

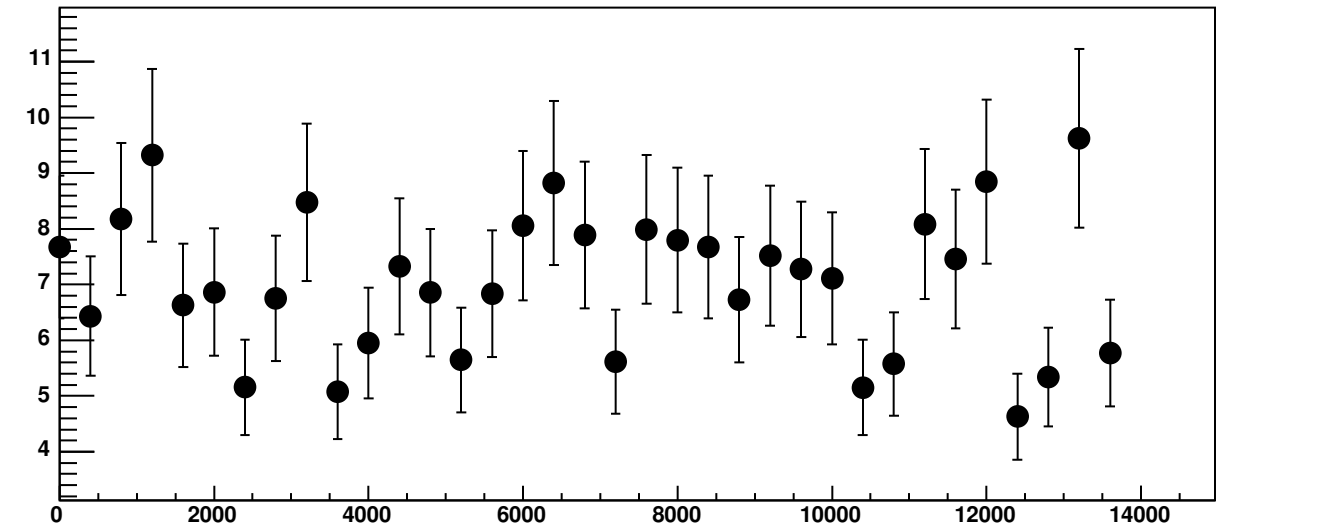

Chip 11, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

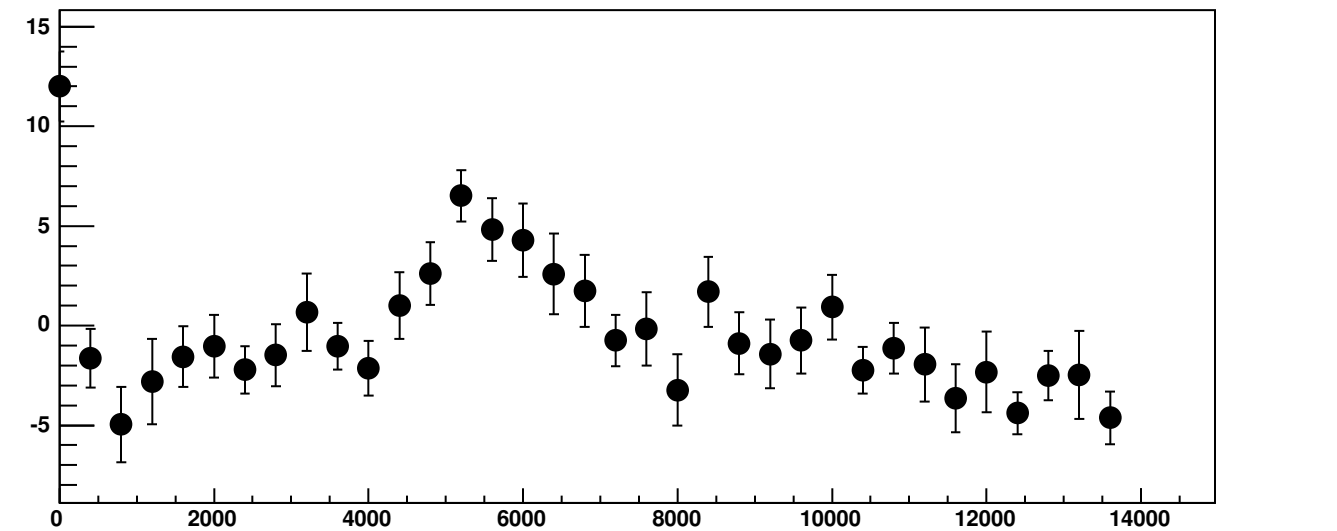
Chip 11, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC



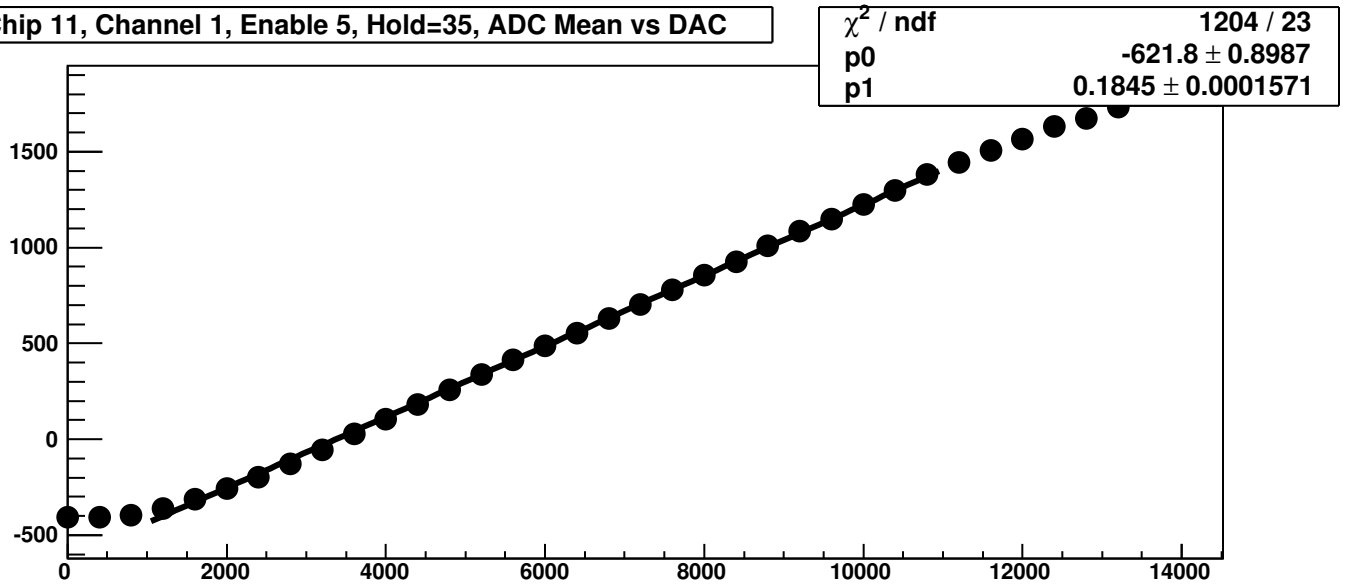
Chip 11, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC



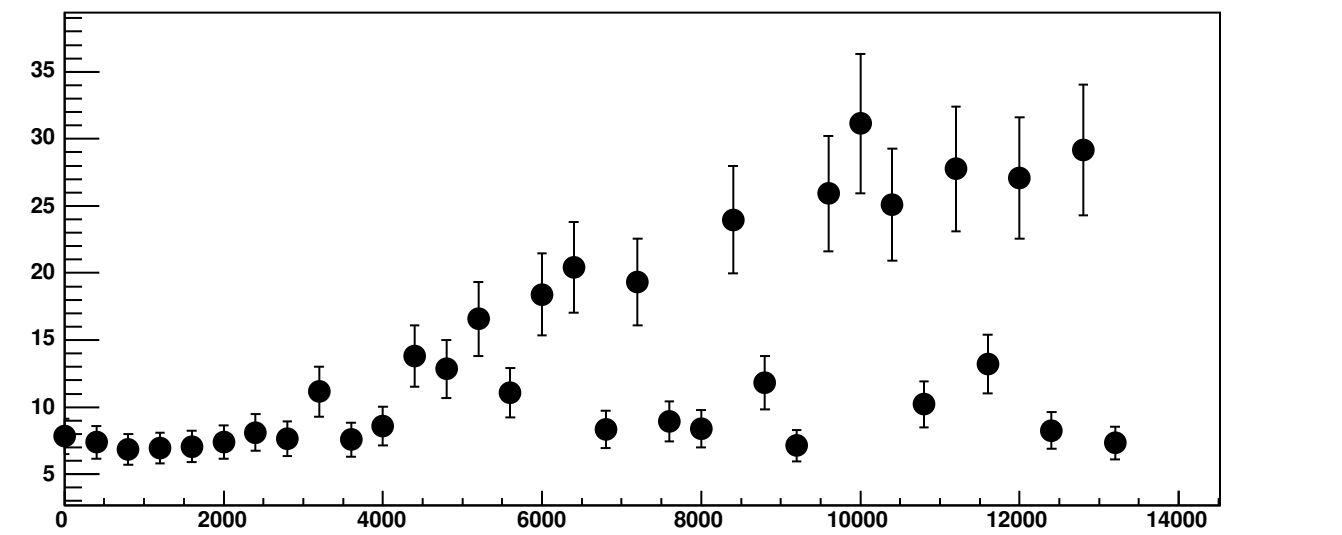
Chip 11, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC



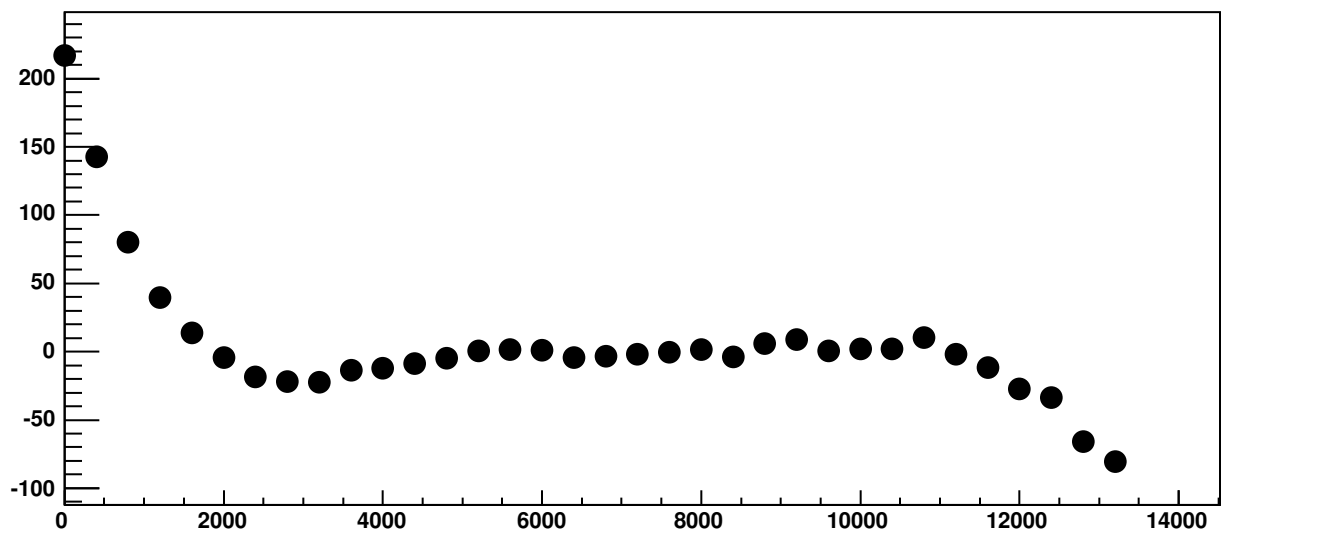
Chip 11, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

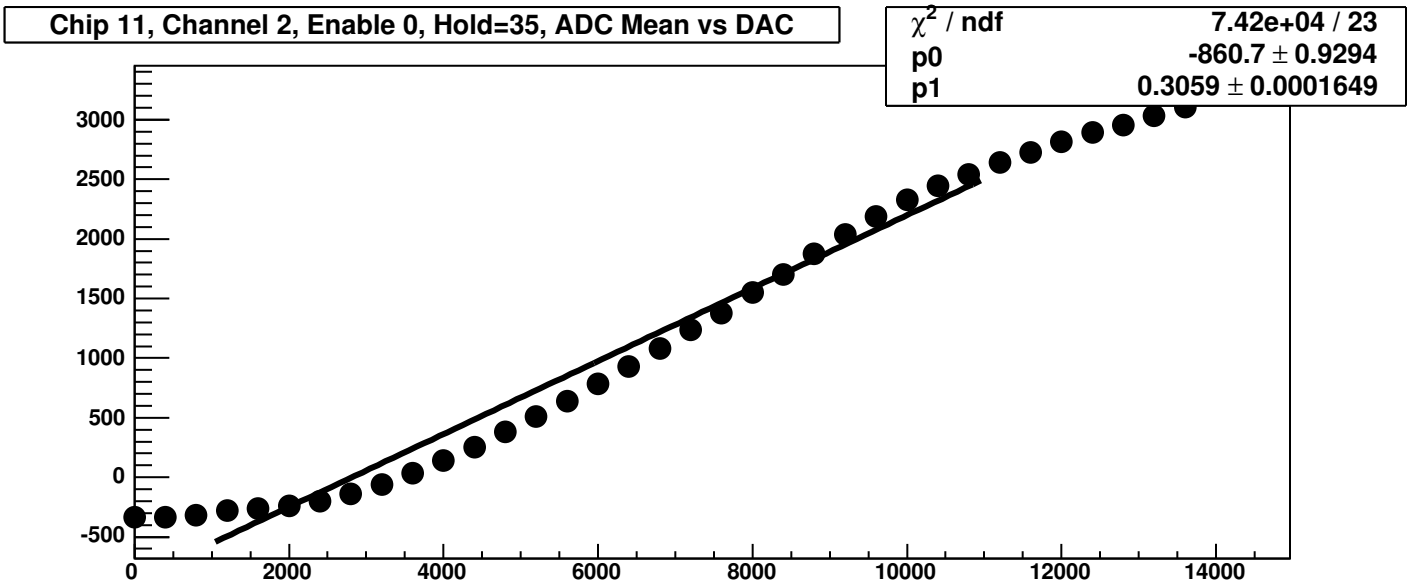


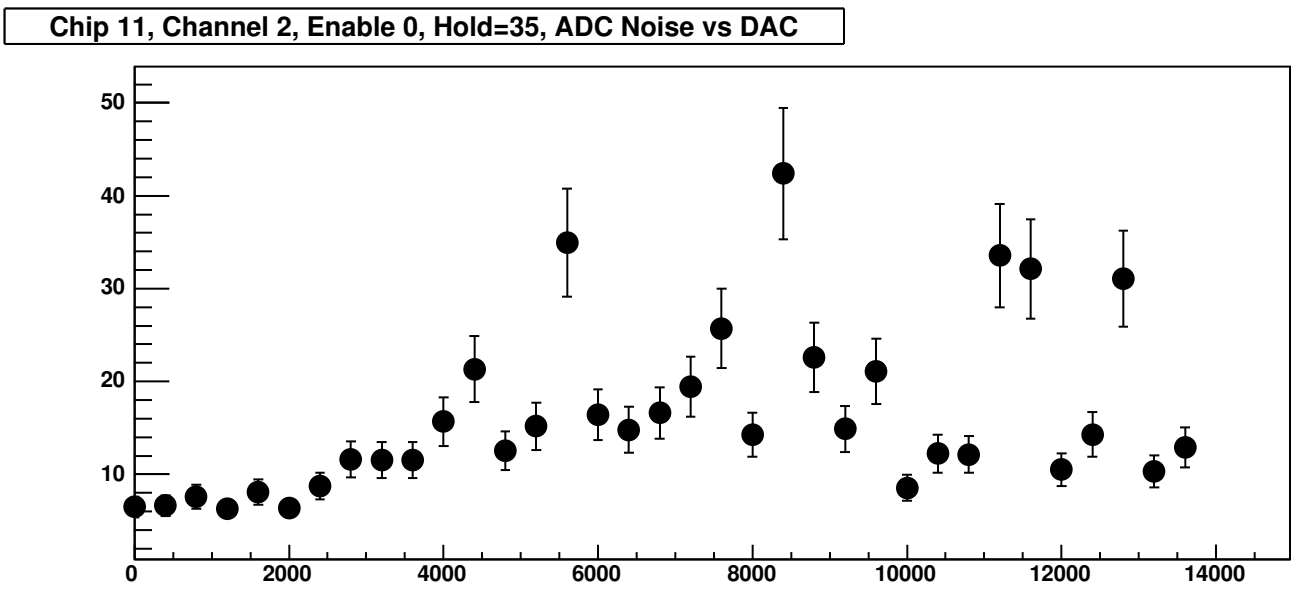
Chip 11, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC

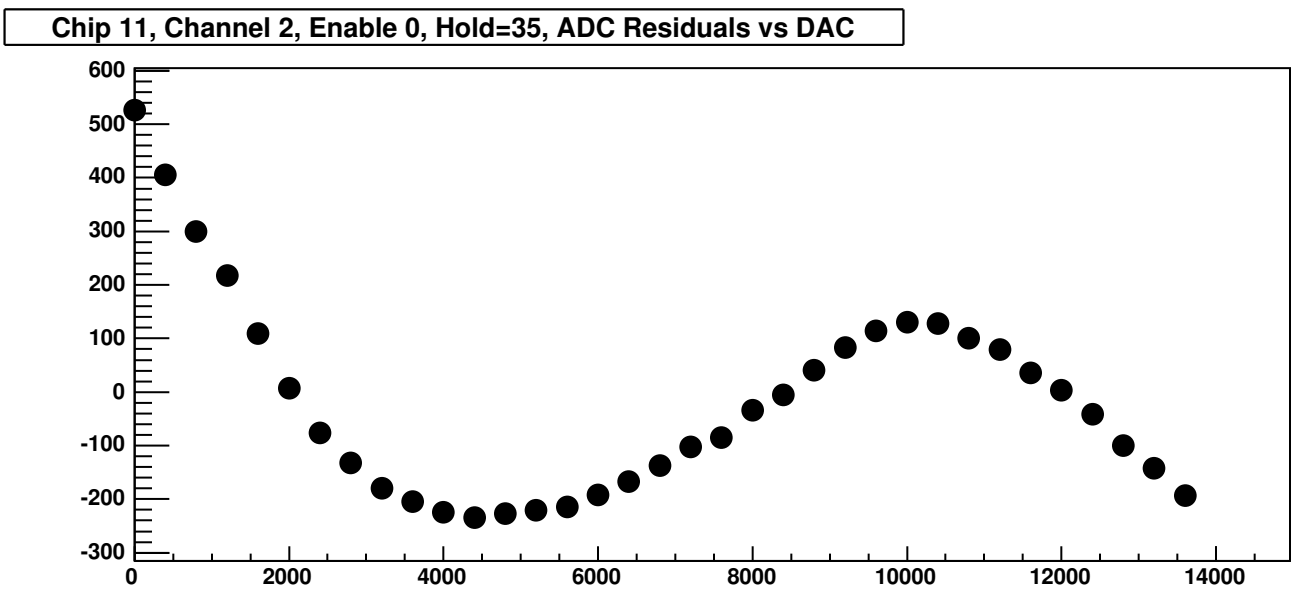


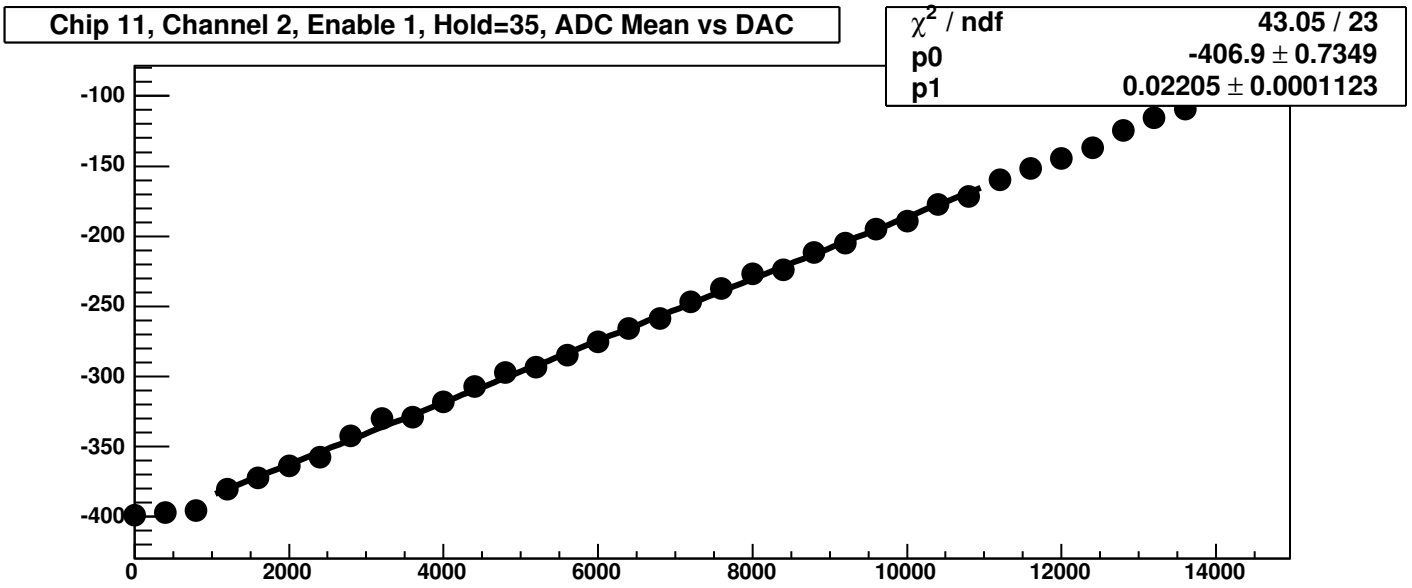
Chip 11, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

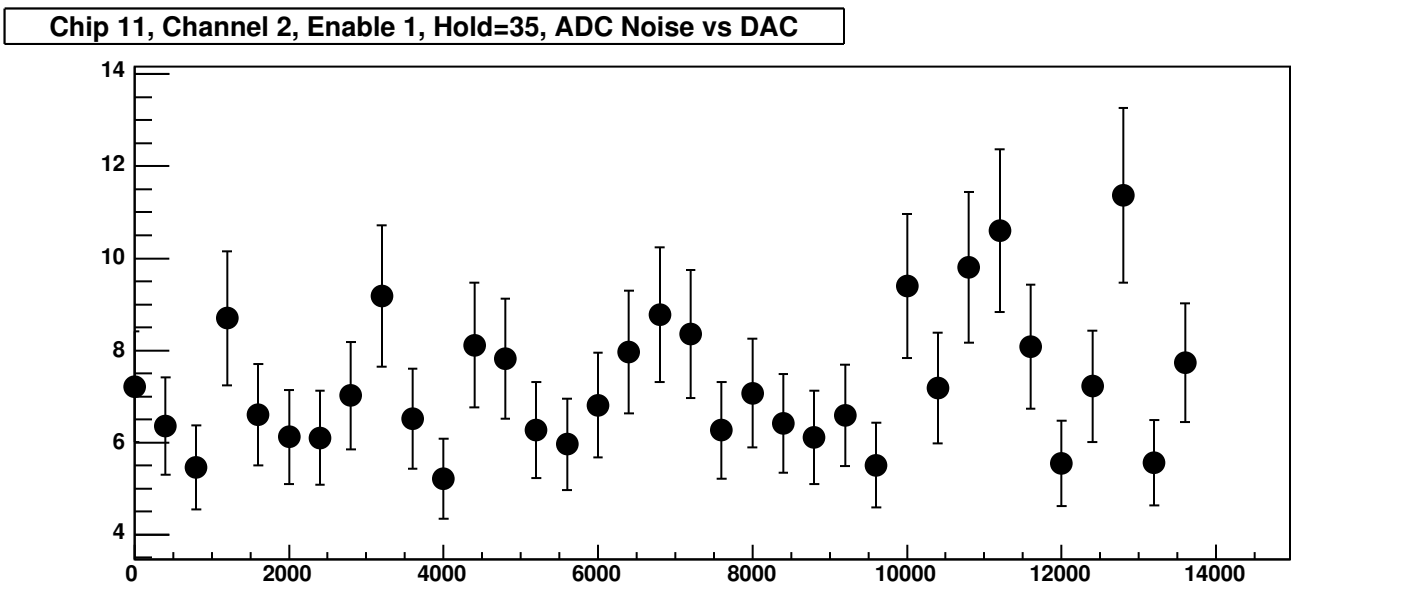


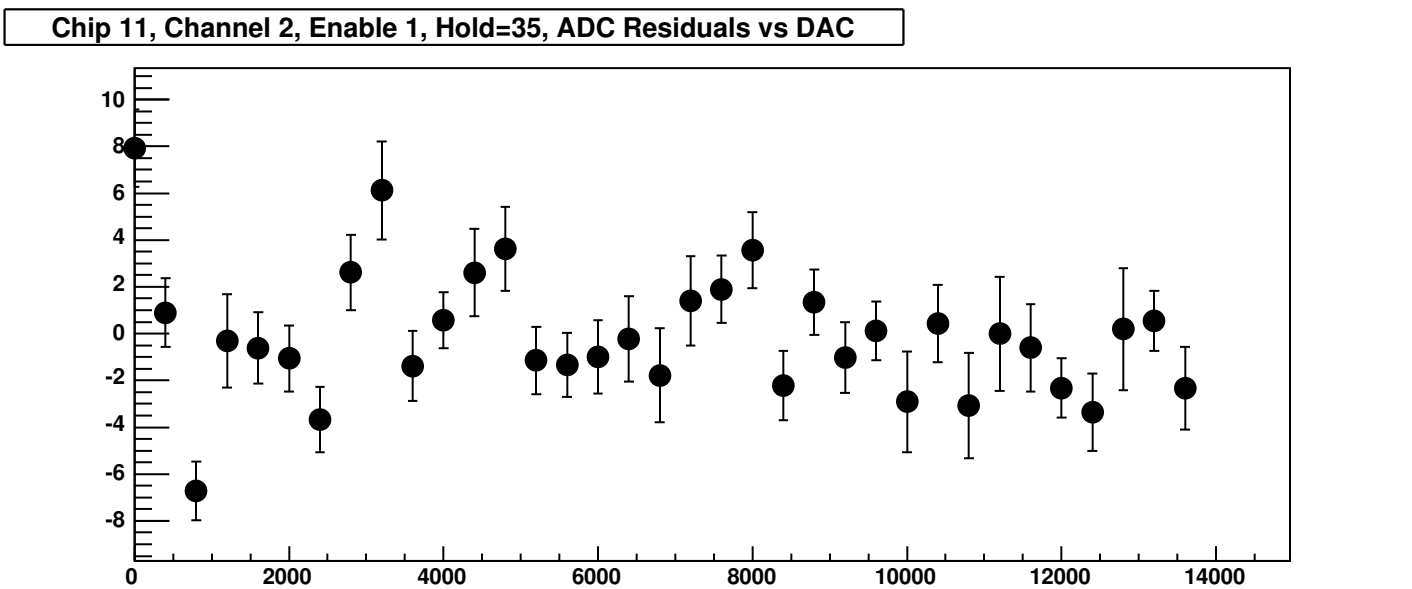

Chip 11, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 7.42e+04 / 23
p0 -860.7 ± 0.9294
p1 0.3059 ± 0.0001649
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

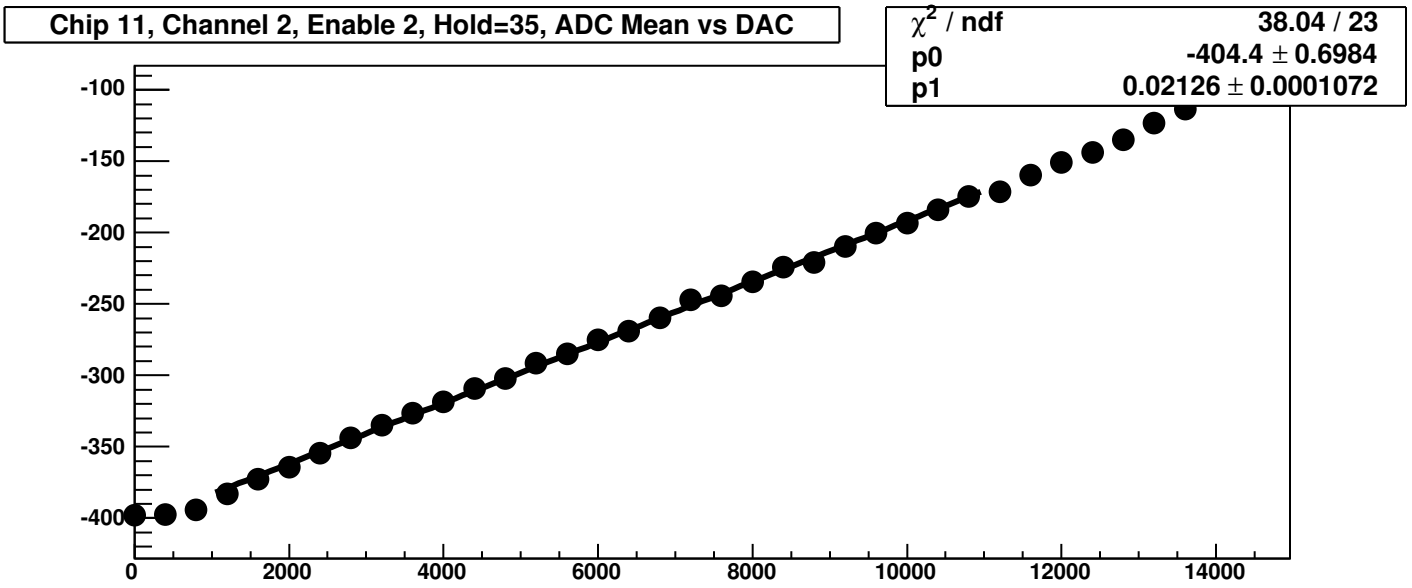

Chip 11, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

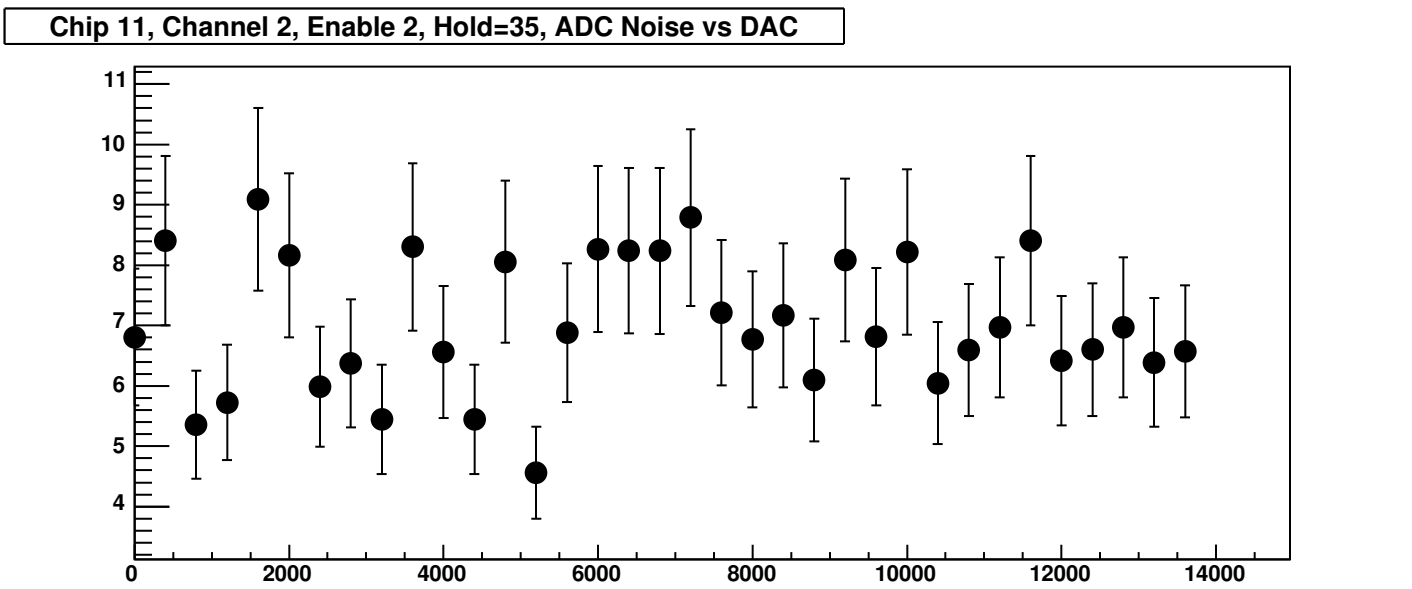

Chip 11, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

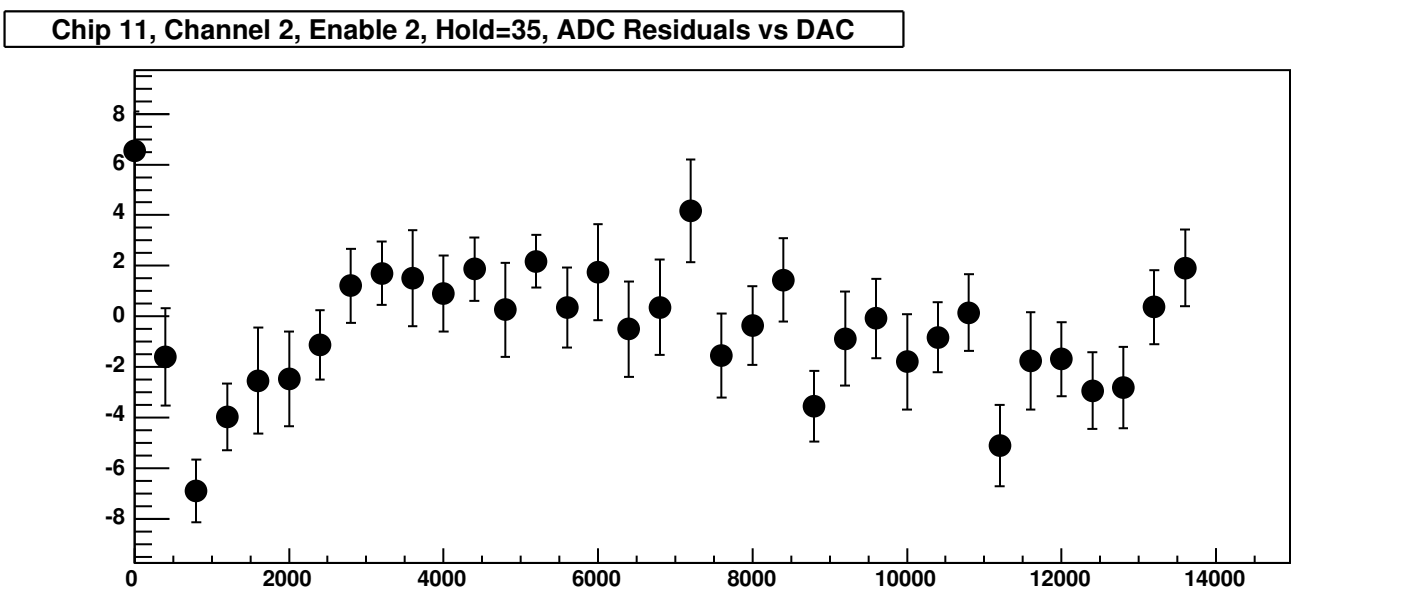

Chip 11, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 43.05 / 23
p0 -406.9 ± 0.7349
p1 0.02205 ± 0.0001123
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

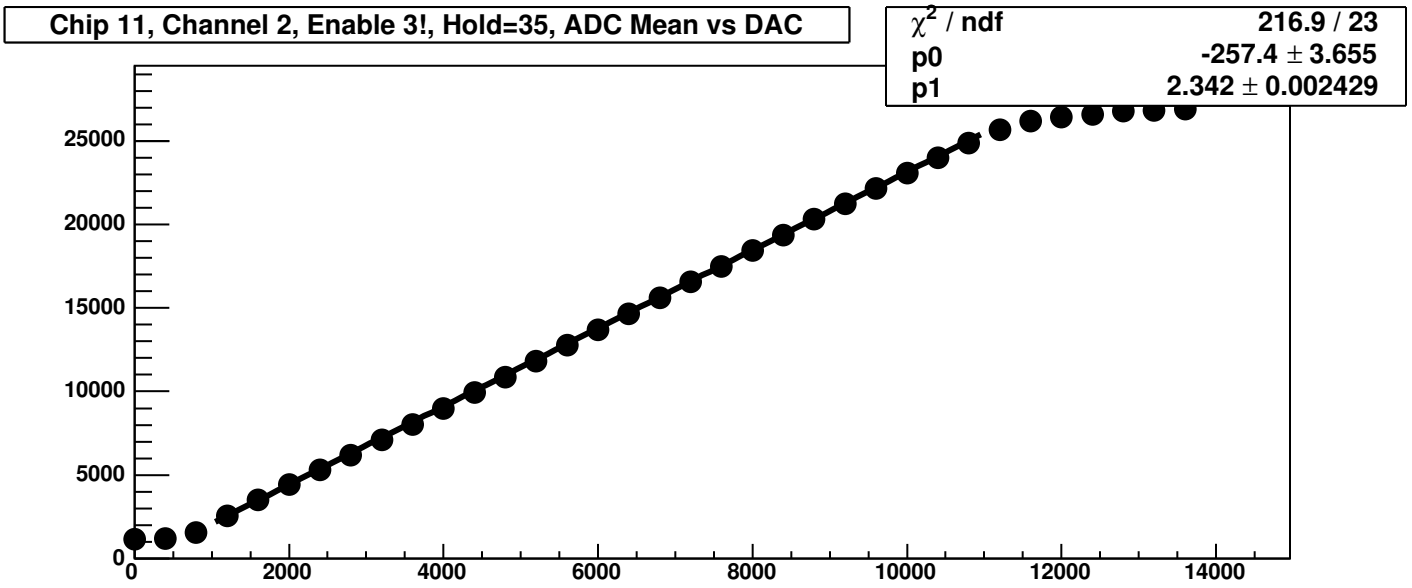

Chip 11, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

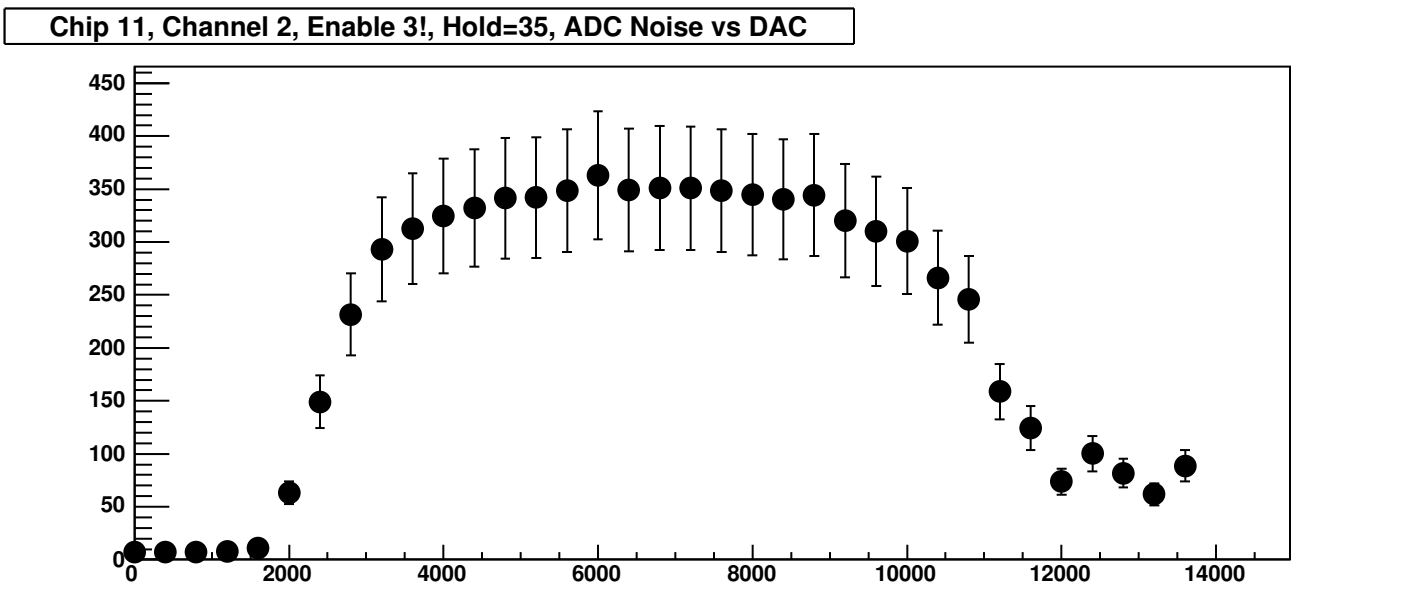

Chip 11, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

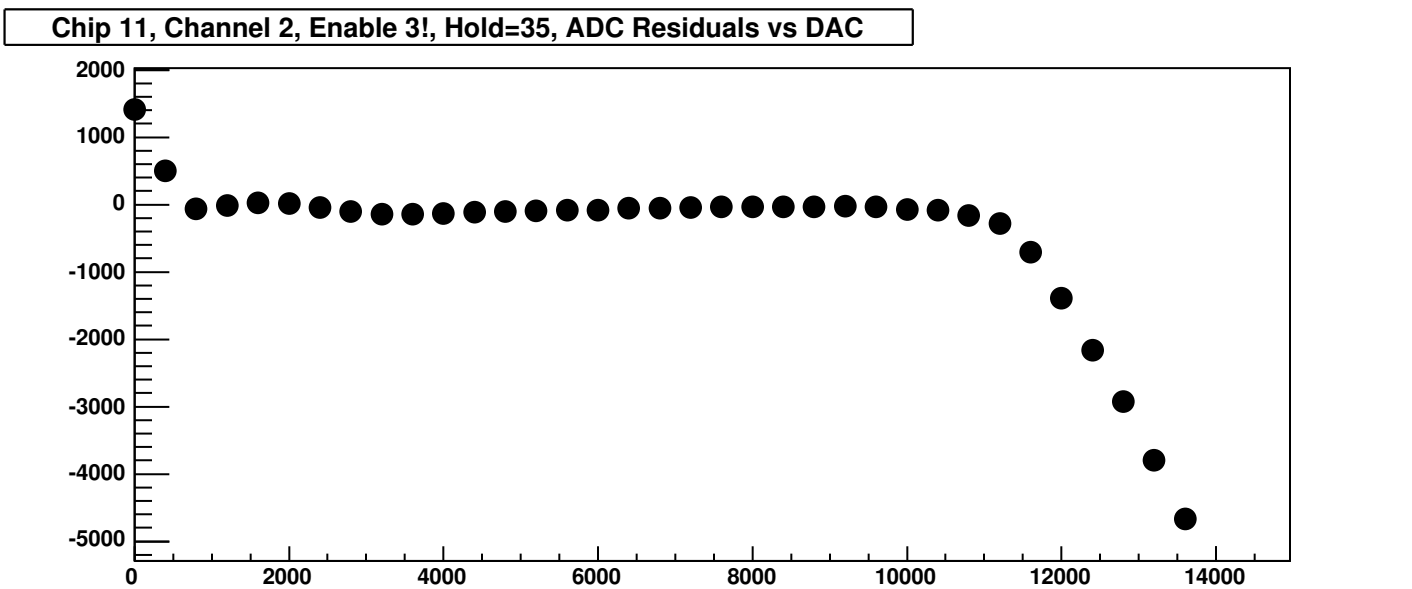

Chip 11, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 38.04 / 23
p0 -404.4 ± 0.6984
p1 0.02126 ± 0.0001072
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

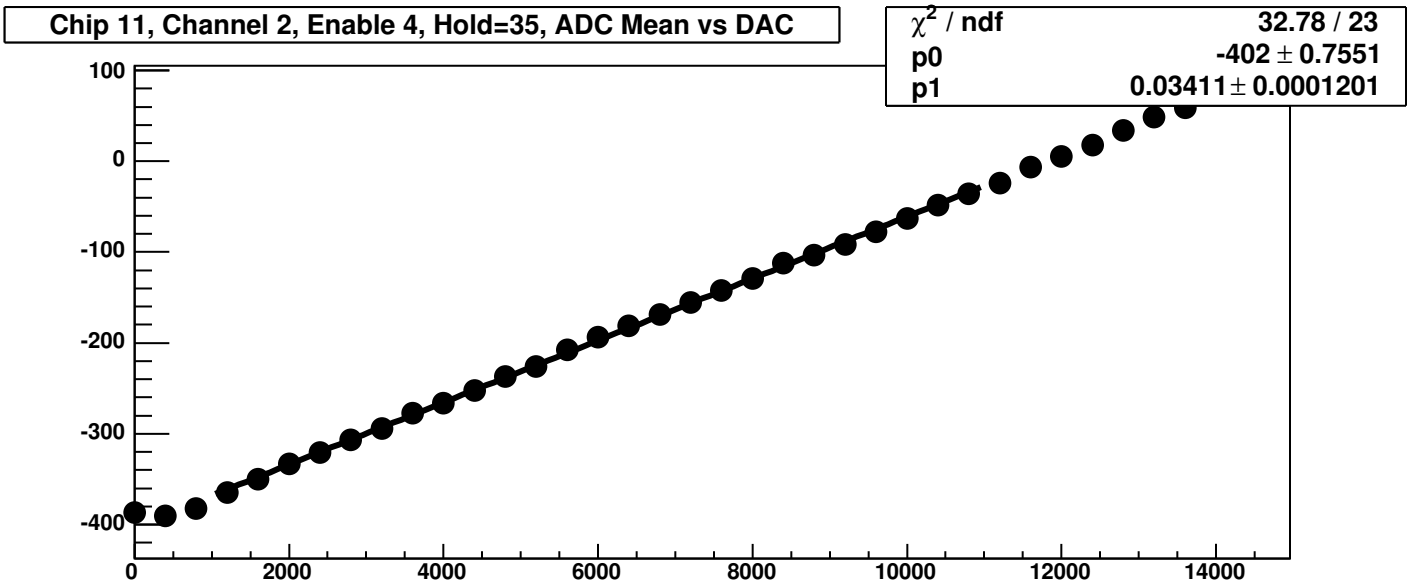

Chip 11, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

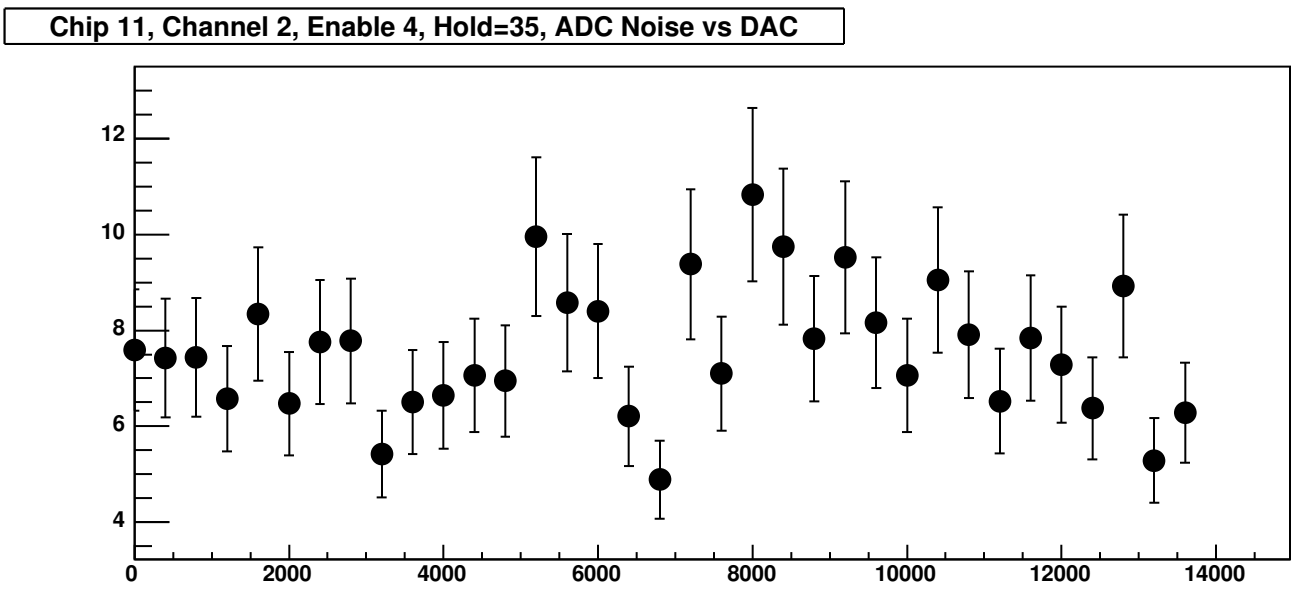

Chip 11, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

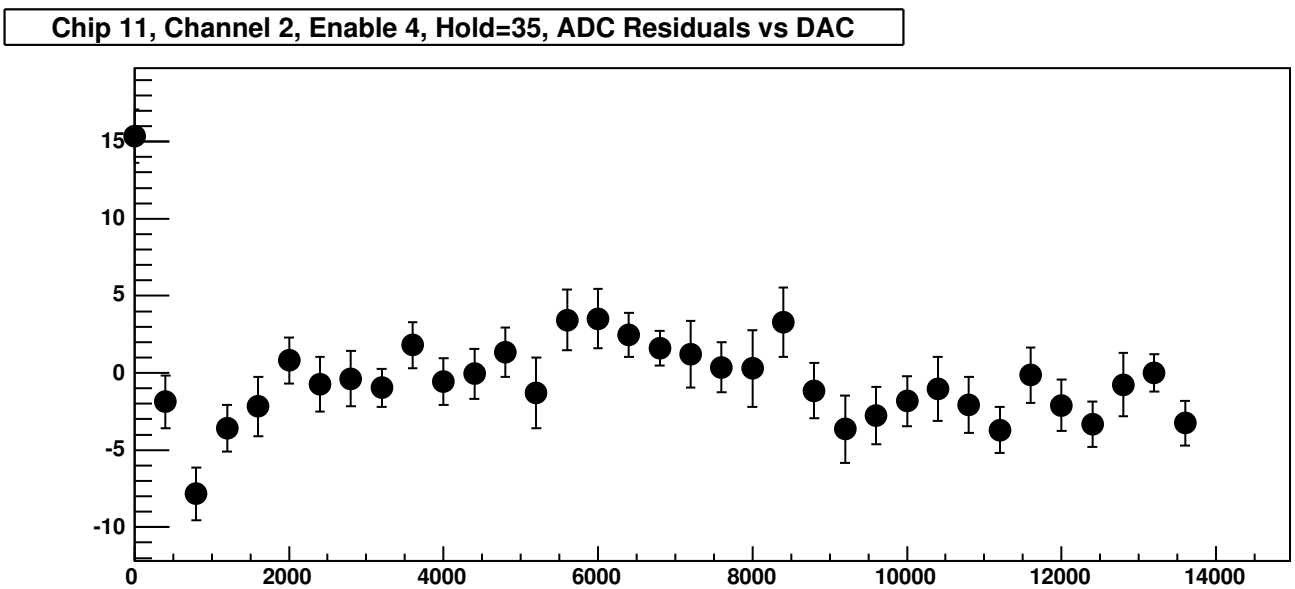

Chip 11, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 216.9 / 23
p0 -257.4 ± 3.655
p1 2.342 ± 0.002429
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

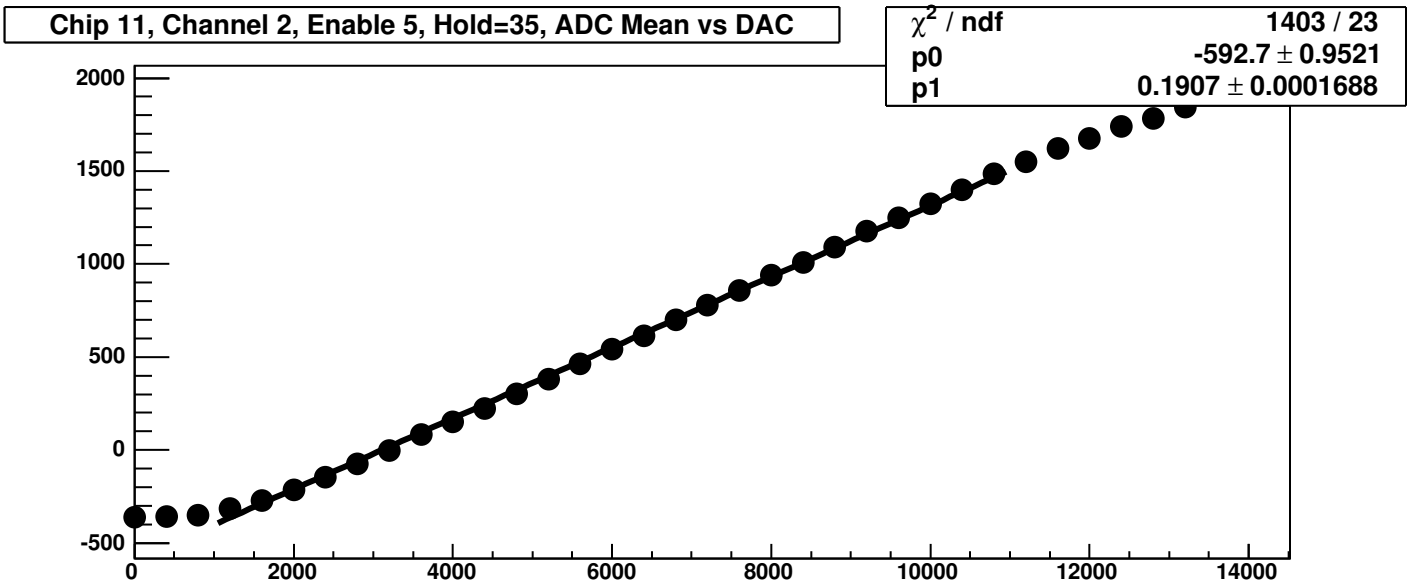

Chip 11, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

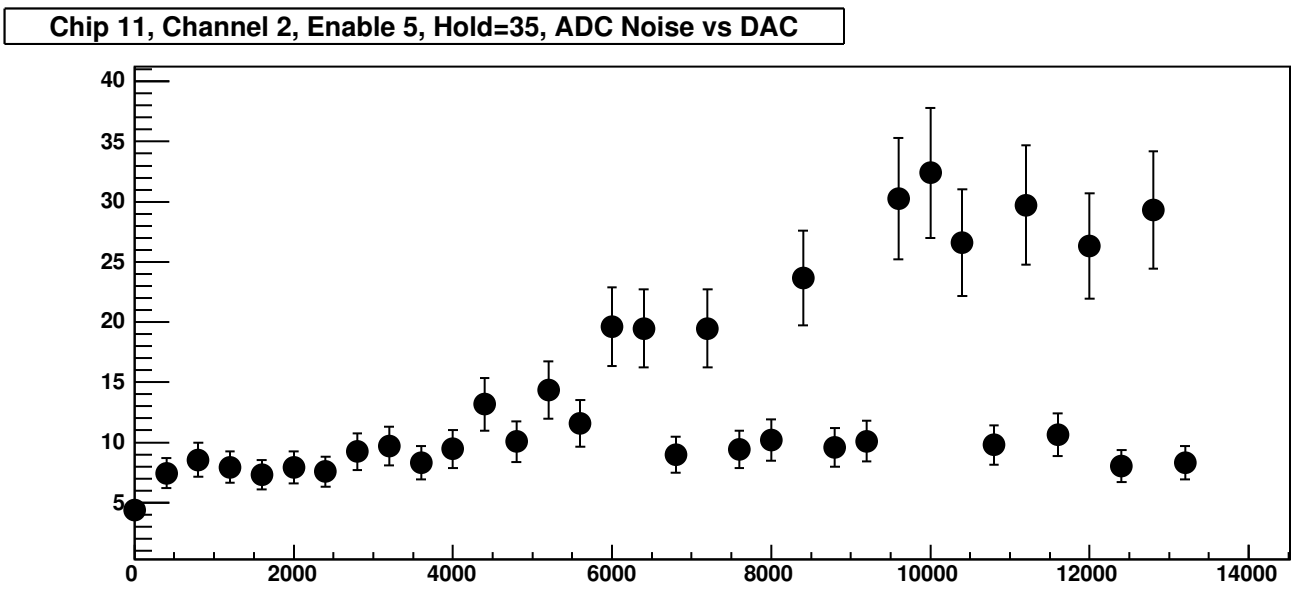

Chip 11, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000

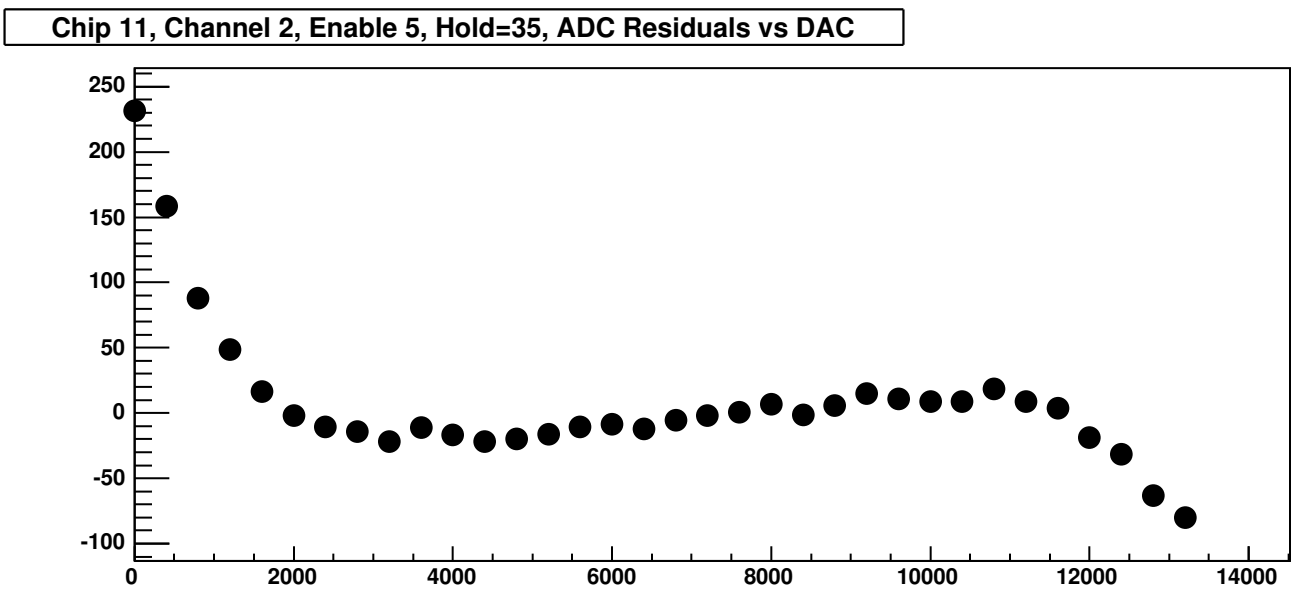

Chip 11, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 32.78 / 23
p0 -402 ± 0.7551
p1 0.03411 ± 0.0001201
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

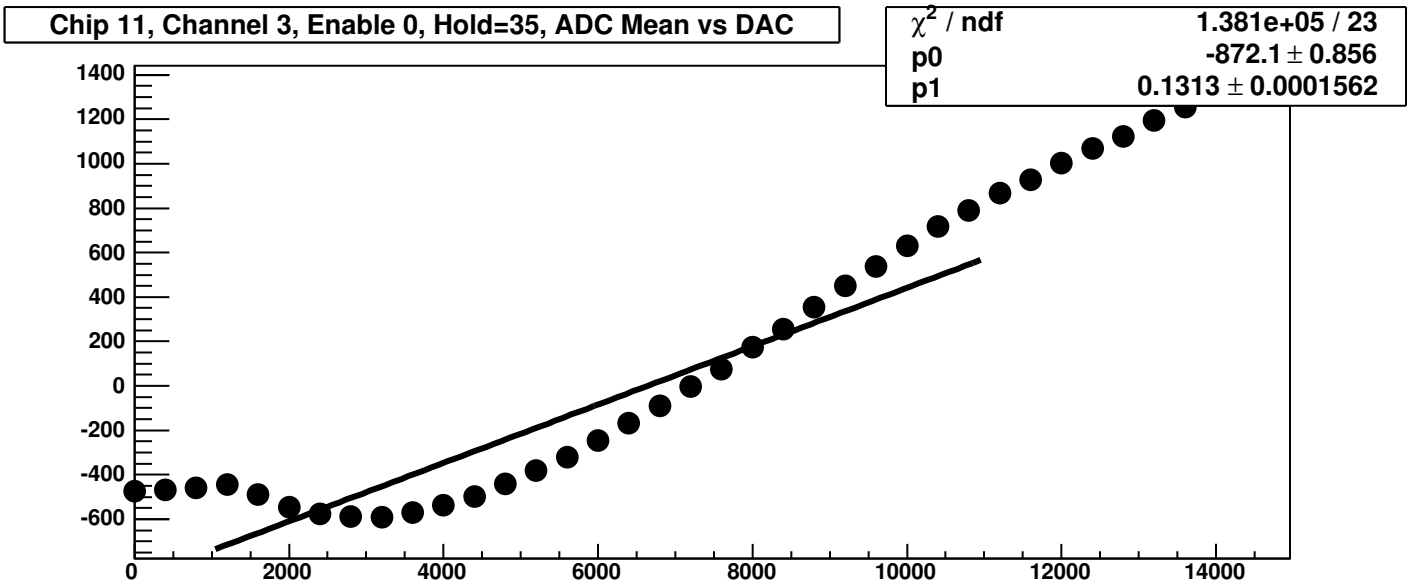

Chip 11, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

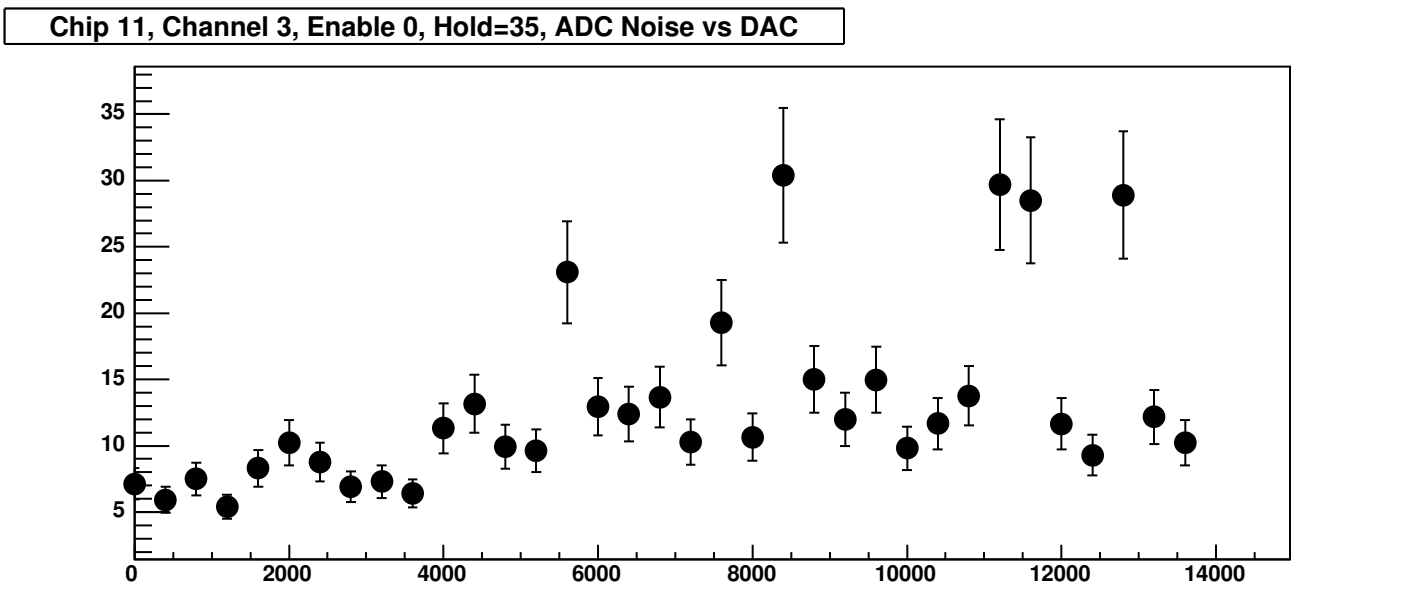

Chip 11, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

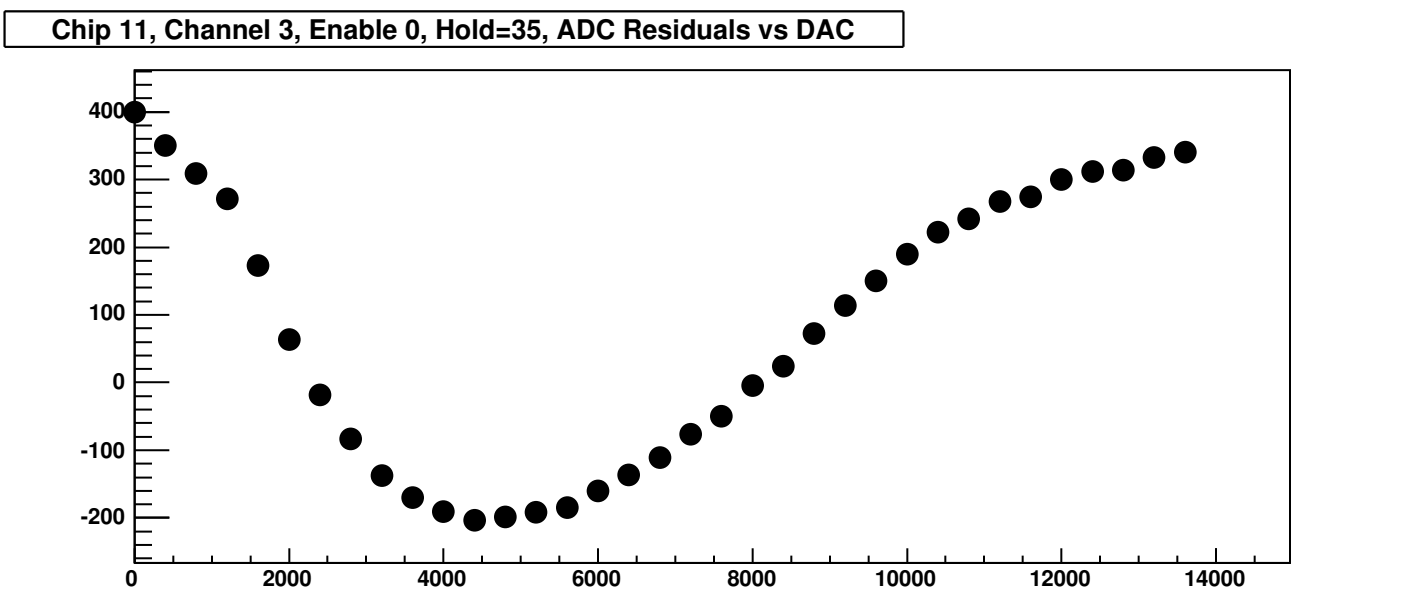

Chip 11, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1403 / 23
p0 -592.7 ± 0.9521
p1 0.1907 ± 0.0001688
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

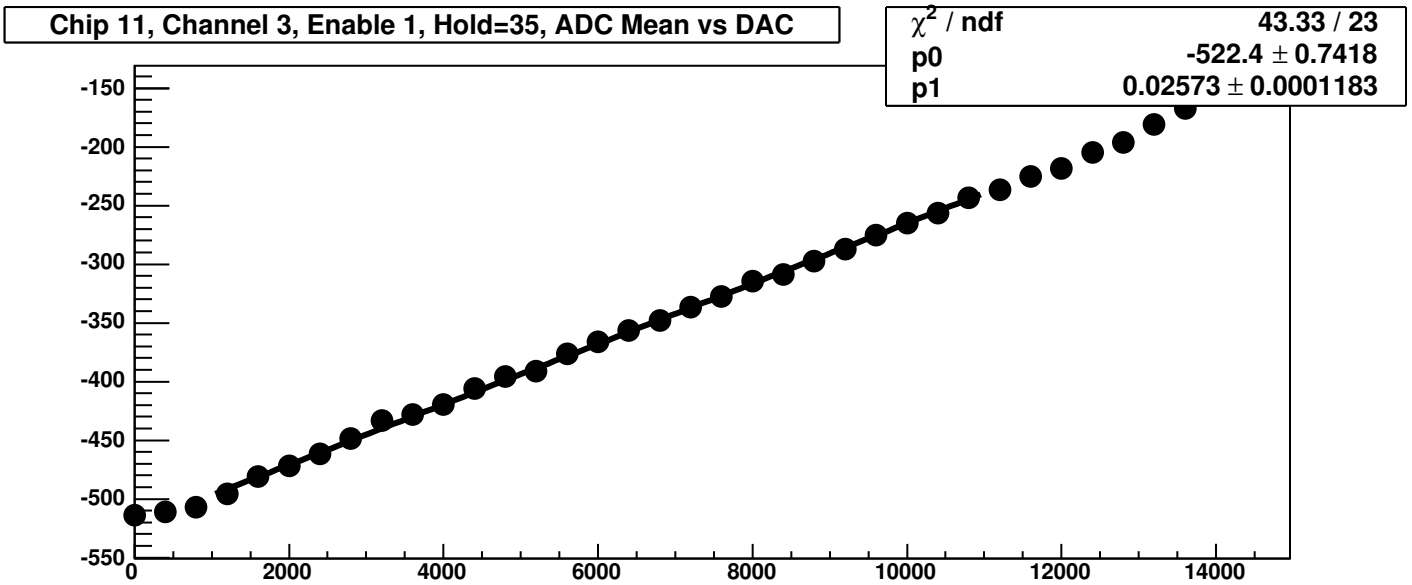

Chip 11, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

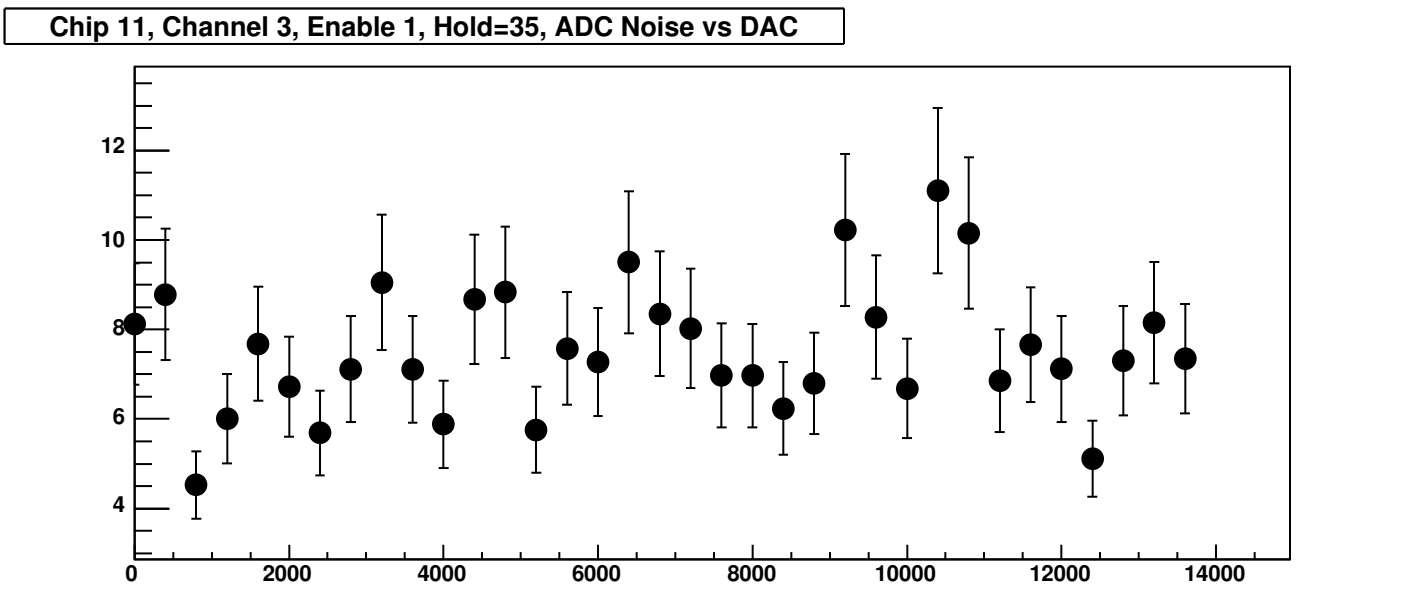

Chip 11, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

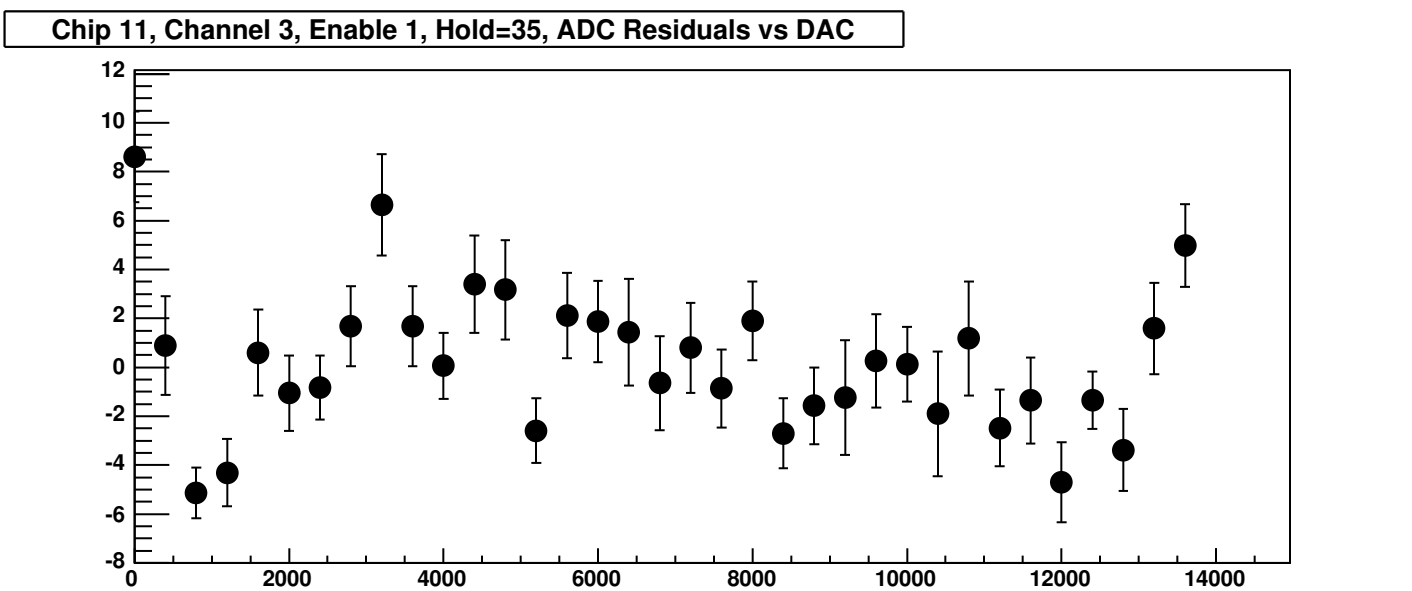

Chip 11, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.381e+05 / 23
p0 -872.1 ± 0.856
p1 0.1313 ± 0.0001562
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

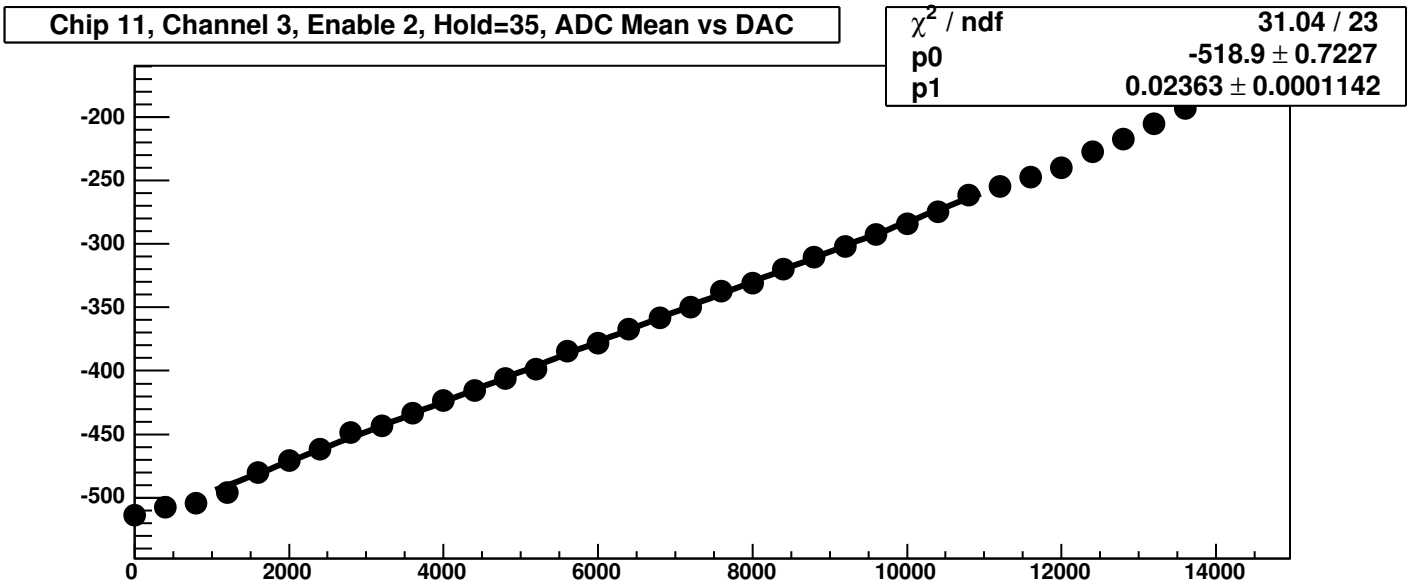

Chip 11, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

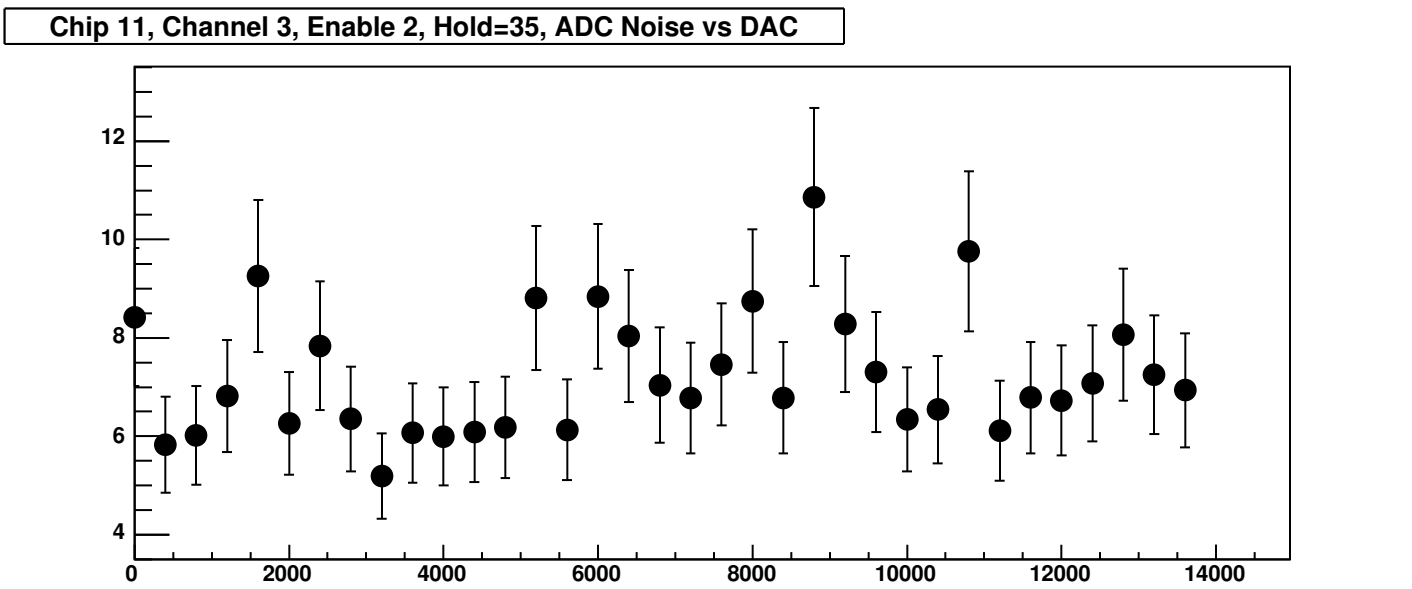

Chip 11, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

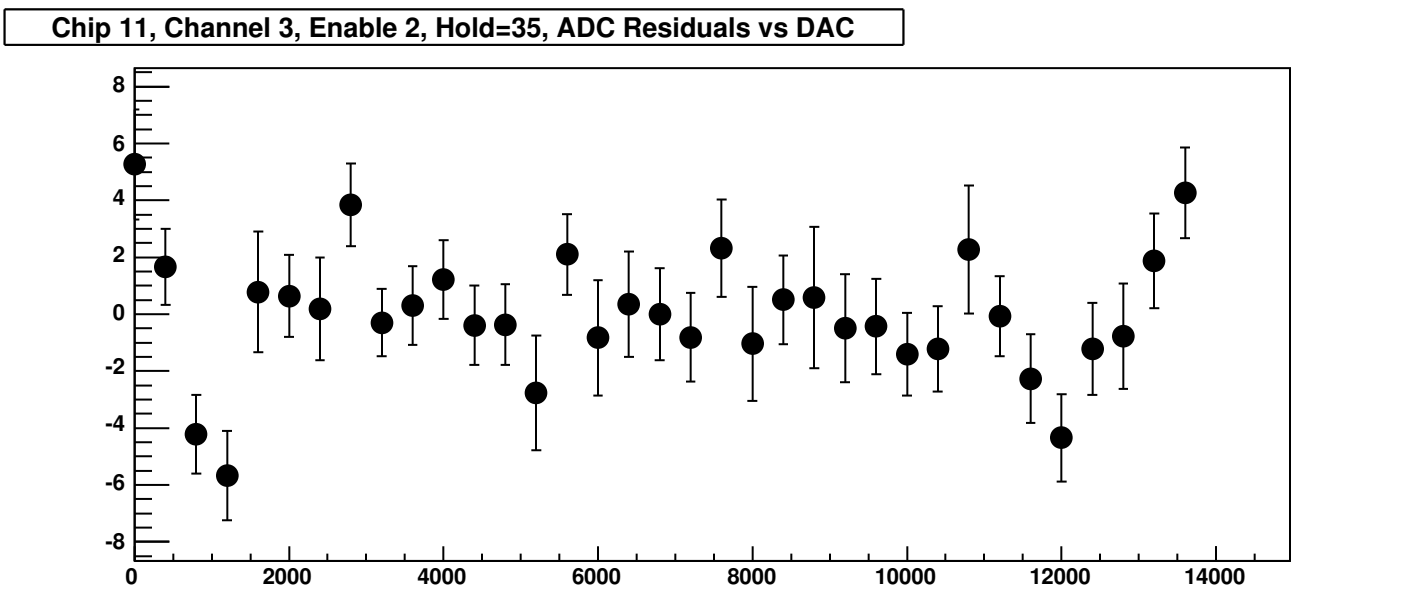

Chip 11, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 43.33 / 23
p0 -522.4 ± 0.7418
p1 0.02573 ± 0.0001183
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

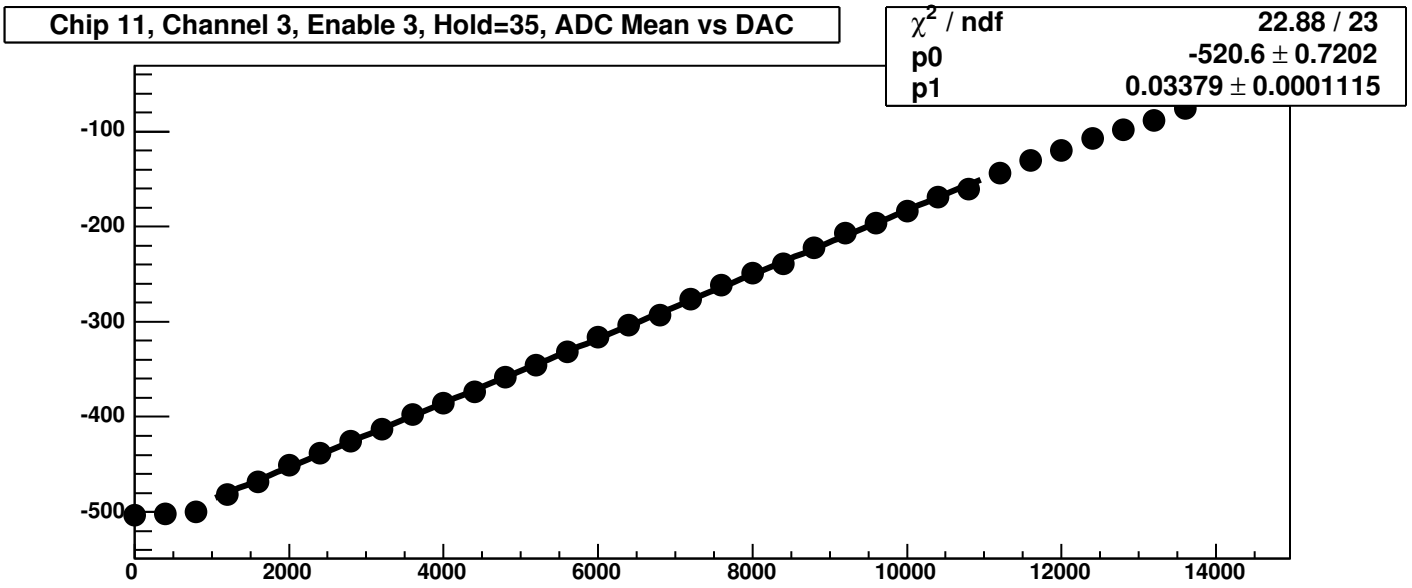

Chip 11, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

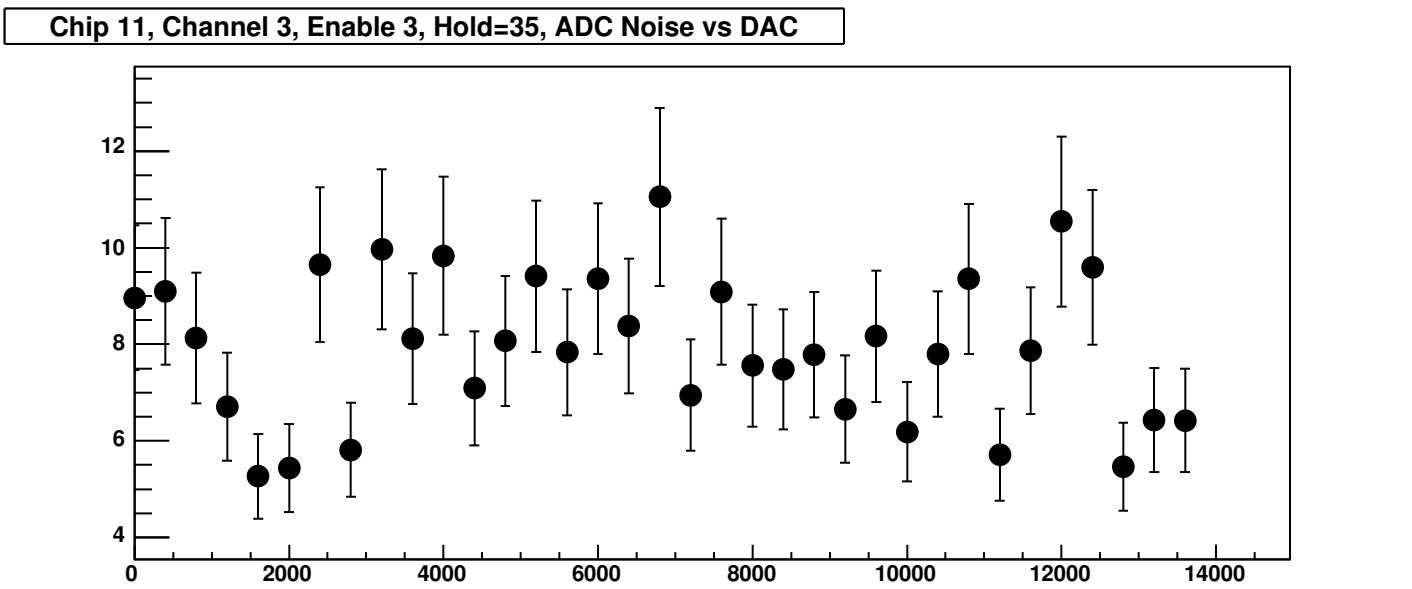

Chip 11, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

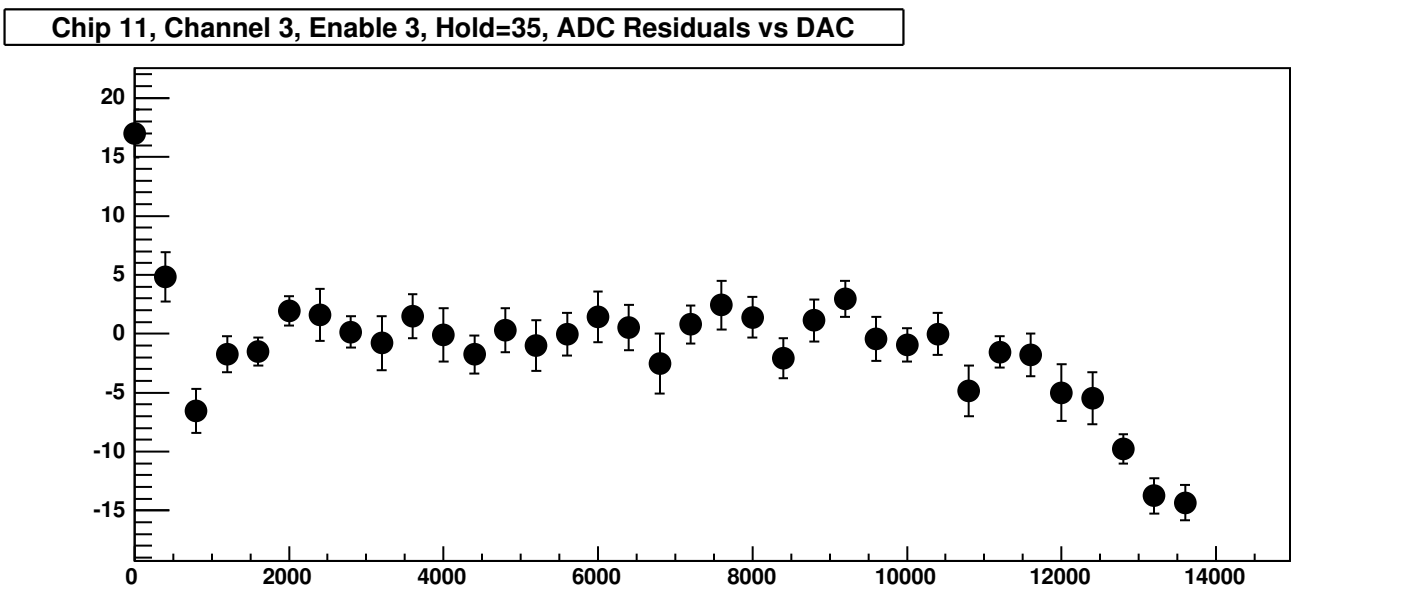

Chip 11, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 31.04 / 23
p0 -518.9 ± 0.7227
p1 0.02363 ± 0.0001142
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

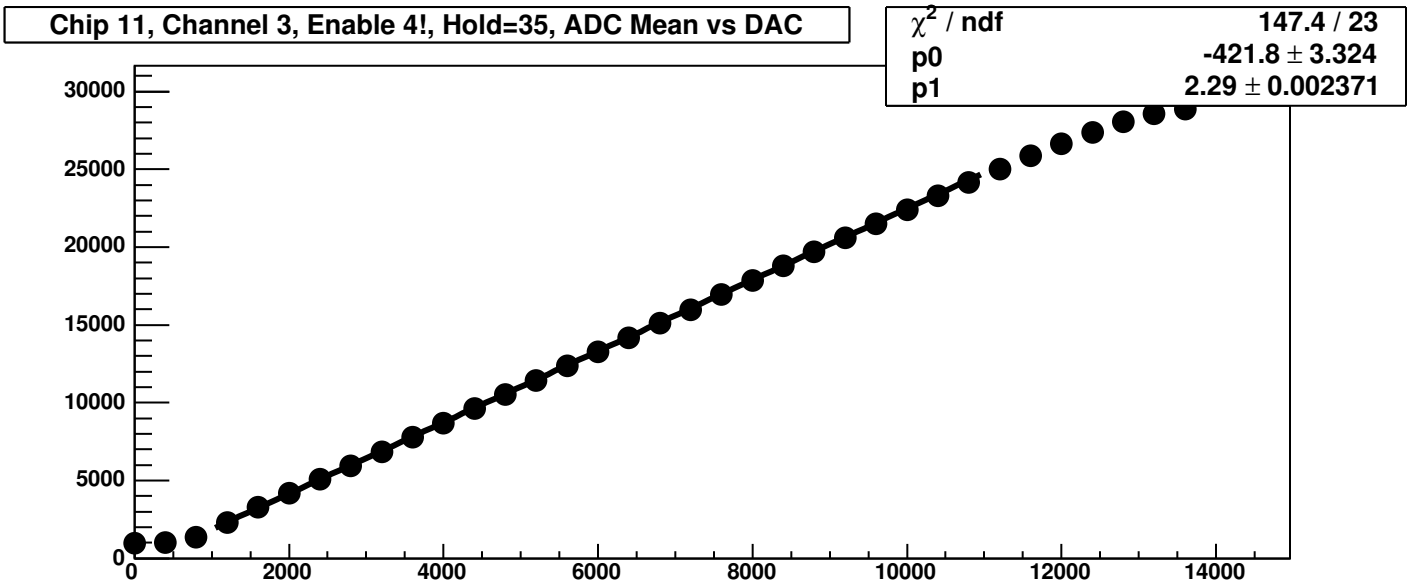

Chip 11, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

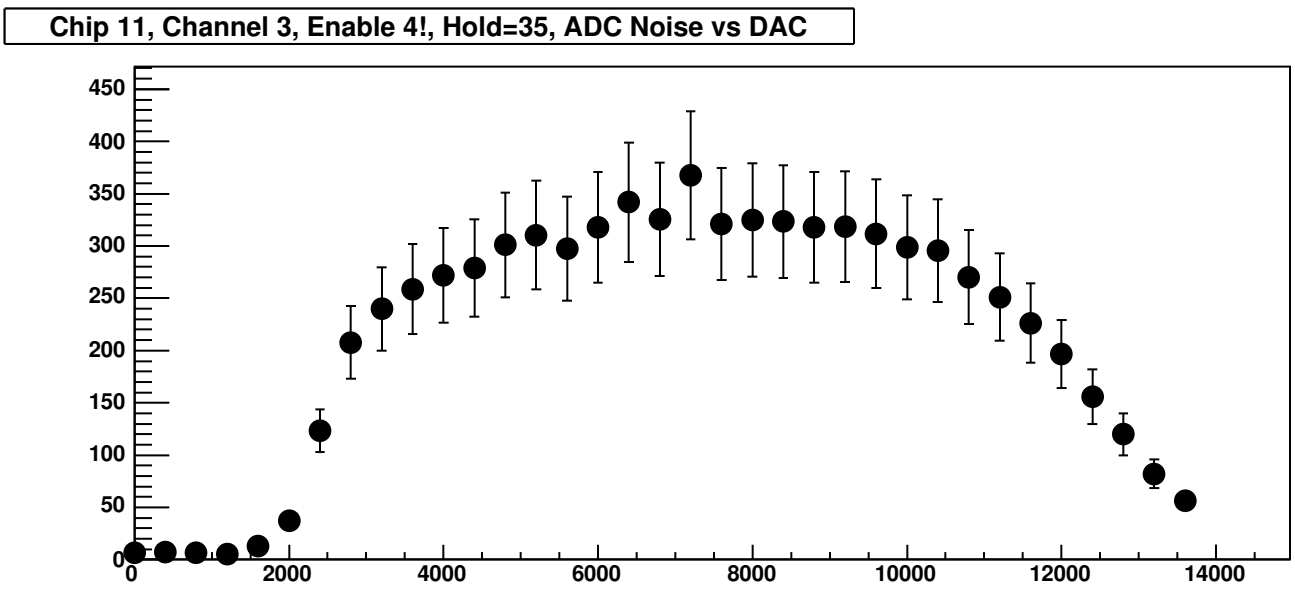

Chip 11, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

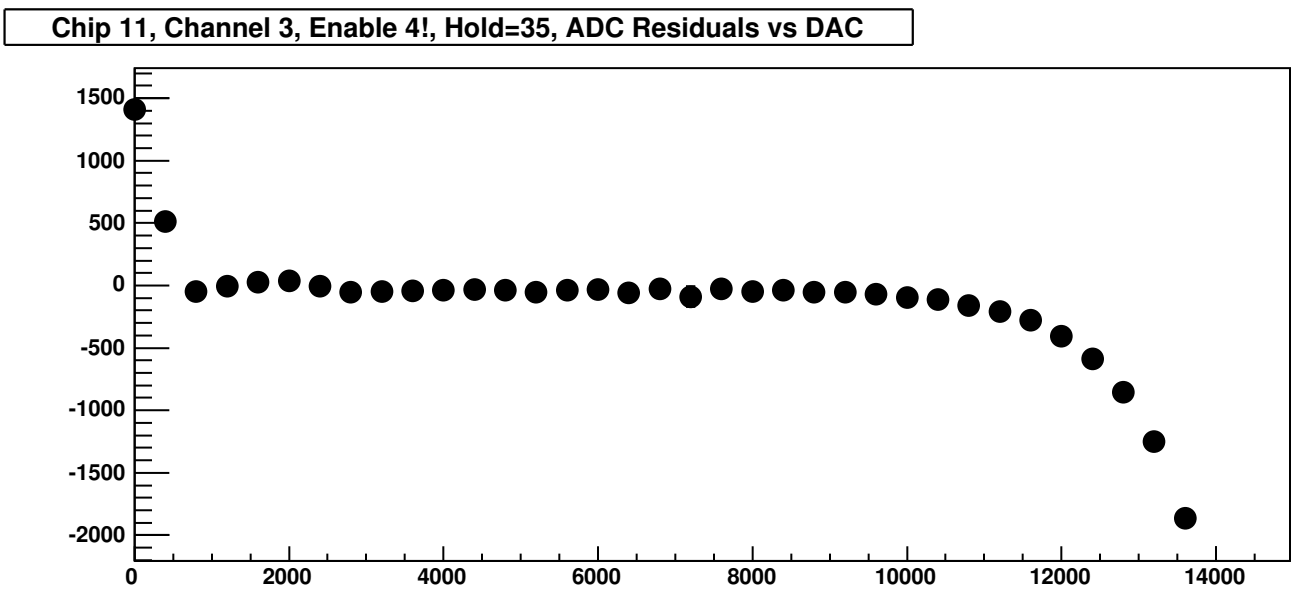

Chip 11, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 22.88 / 23
p0 -520.6 ± 0.7202
p1 0.03379 ± 0.0001115
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000

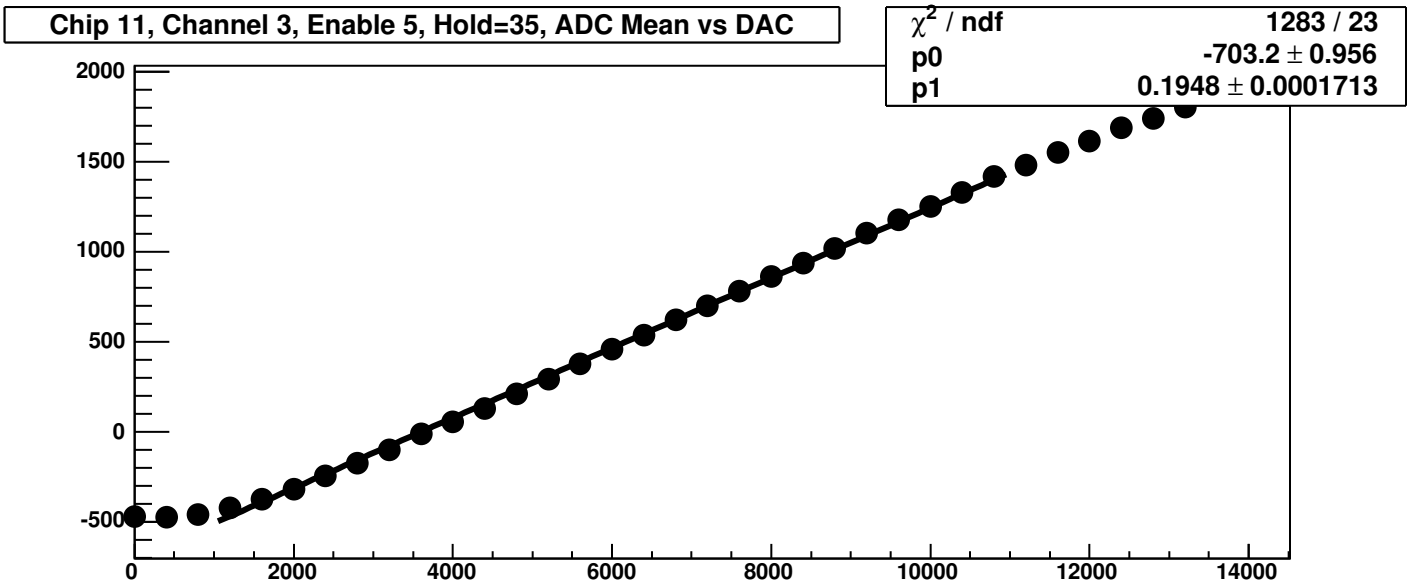

Chip 11, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

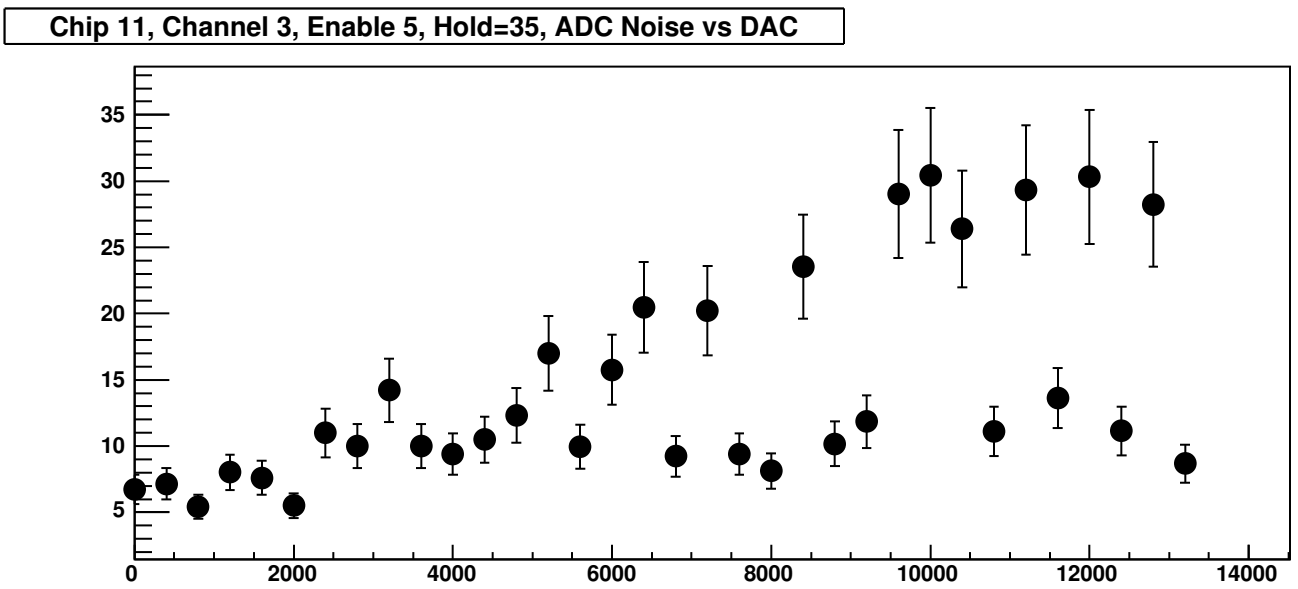

Chip 11, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

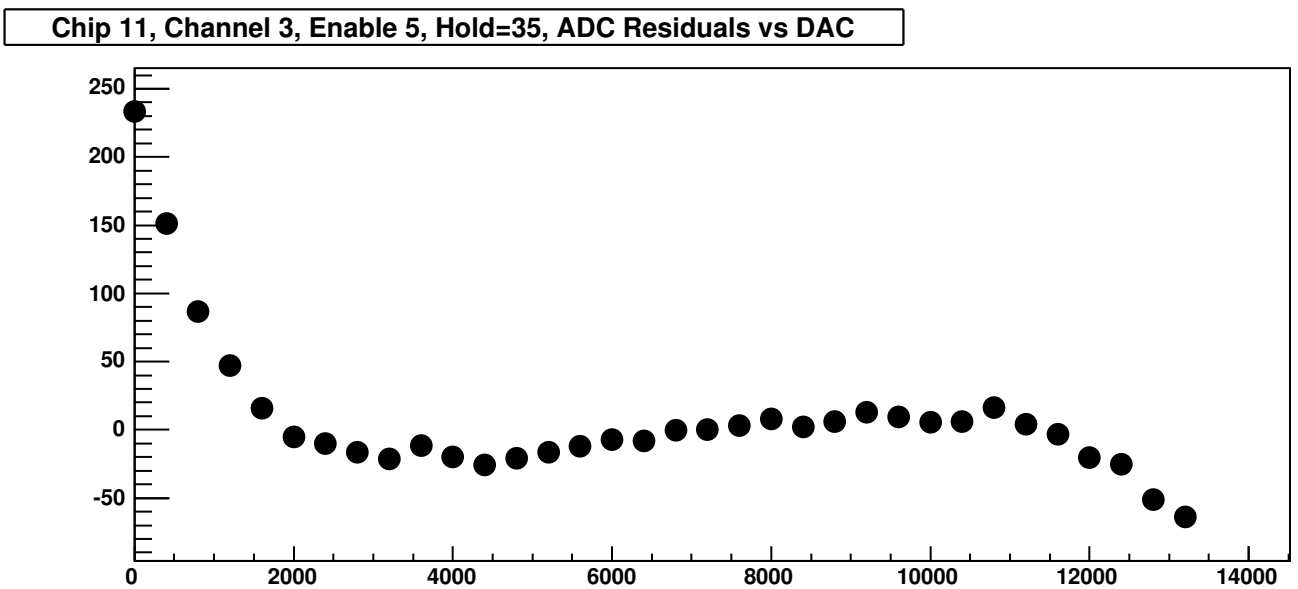

Chip 11, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 147.4 / 23
p0 -421.8 ± 3.324
p1 2.29 ± 0.002371
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

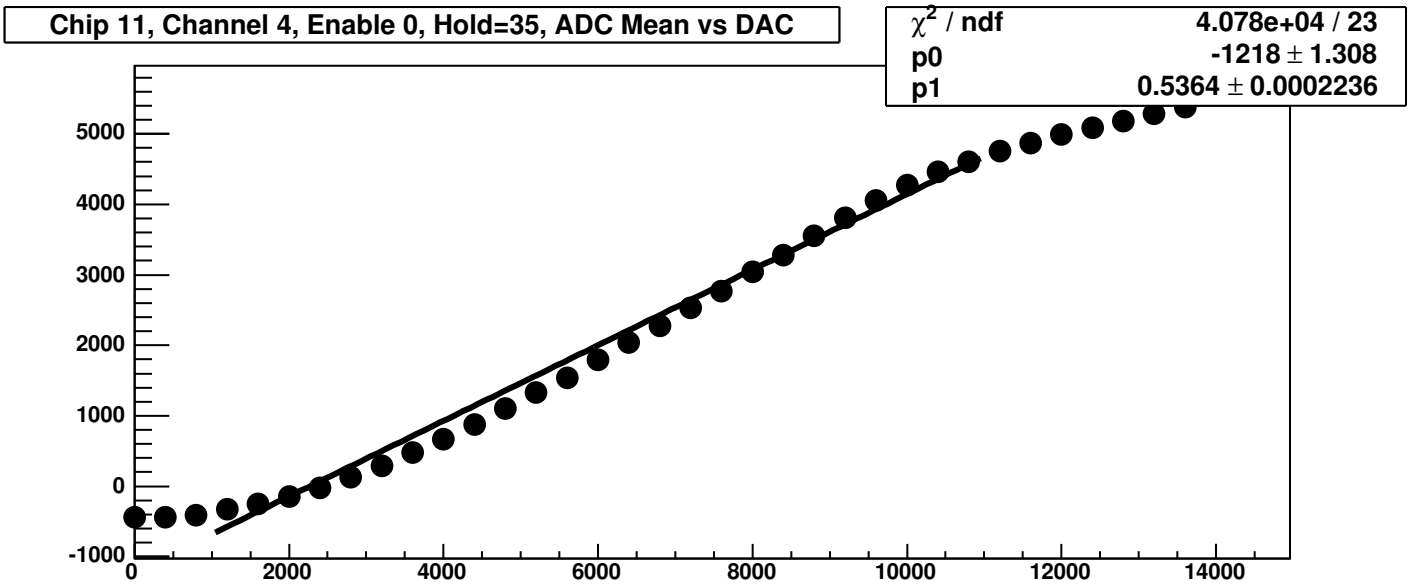

Chip 11, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

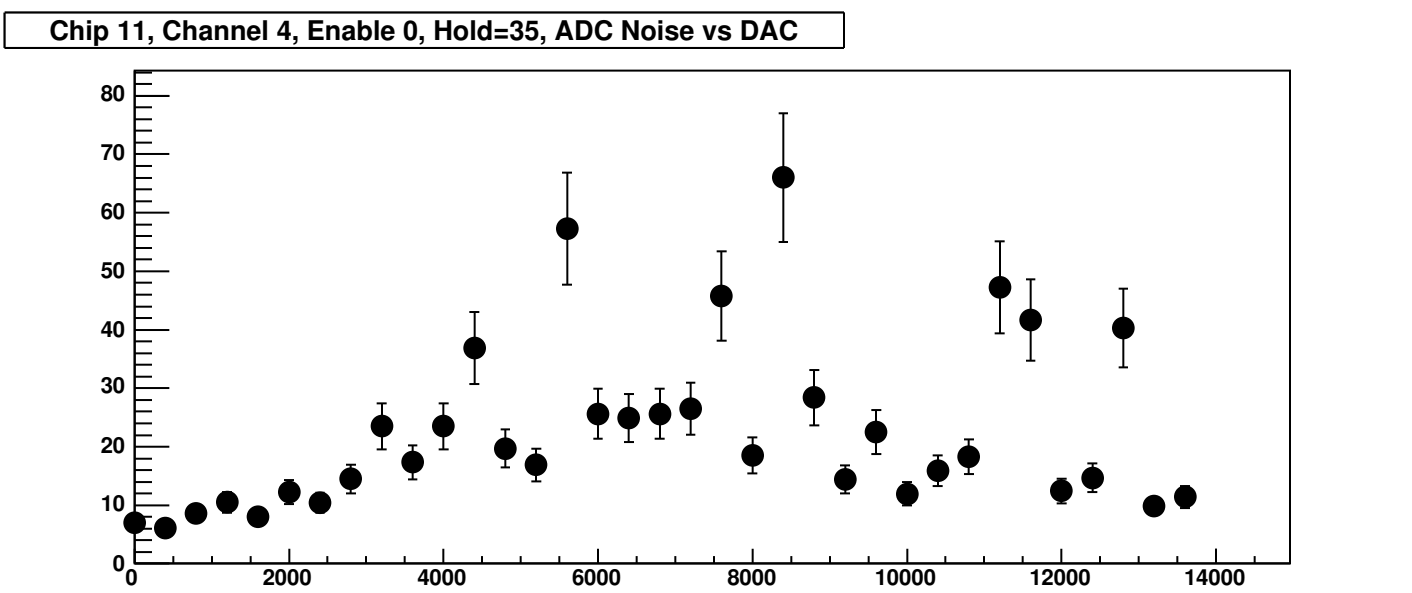

Chip 11, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

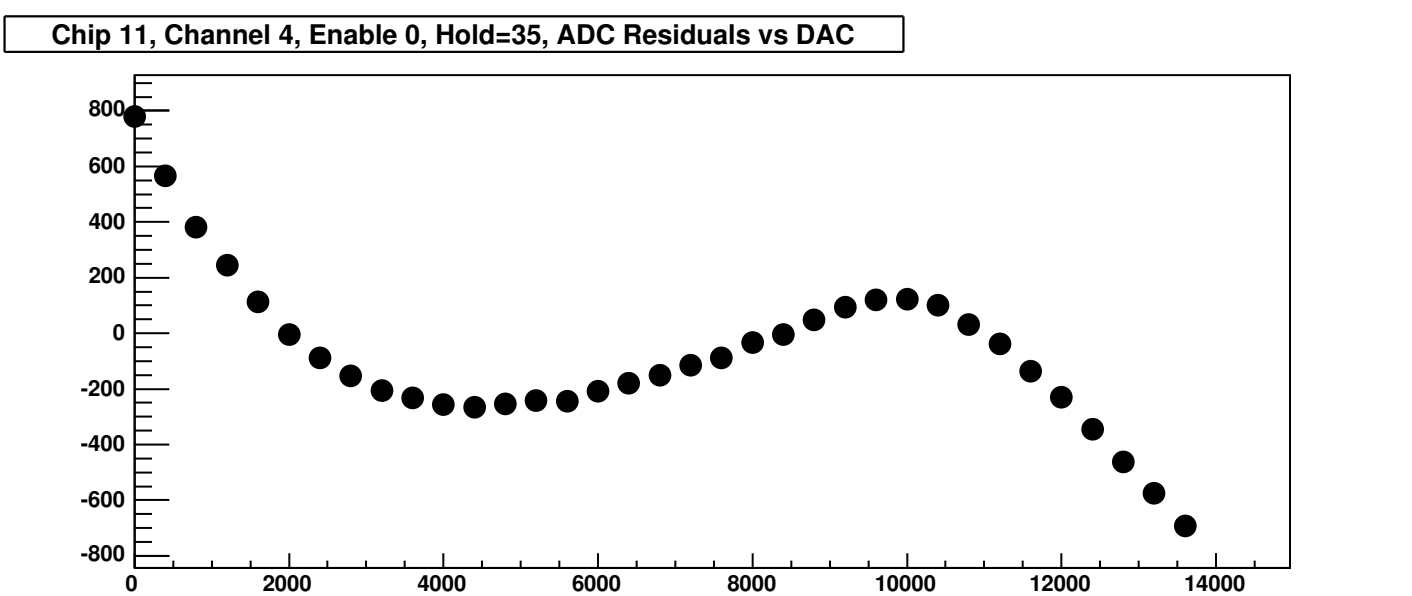

Chip 11, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1283 / 23
p0 -703.2 ± 0.956
p1 0.1948 ± 0.0001713
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

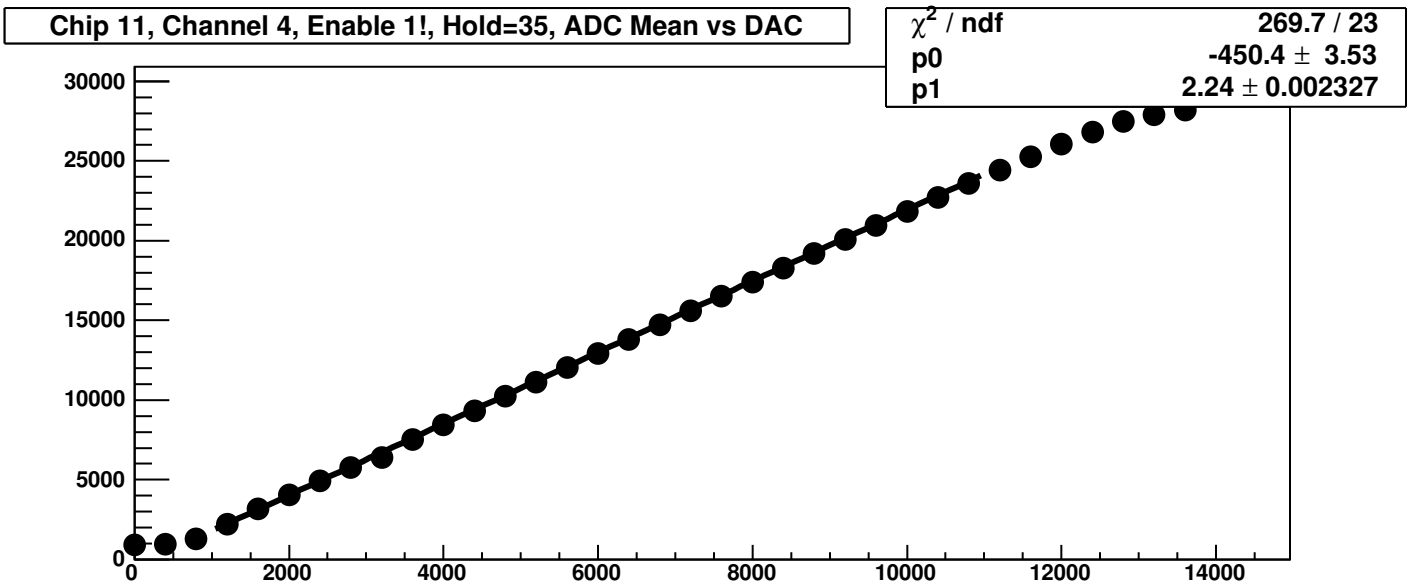

Chip 11, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

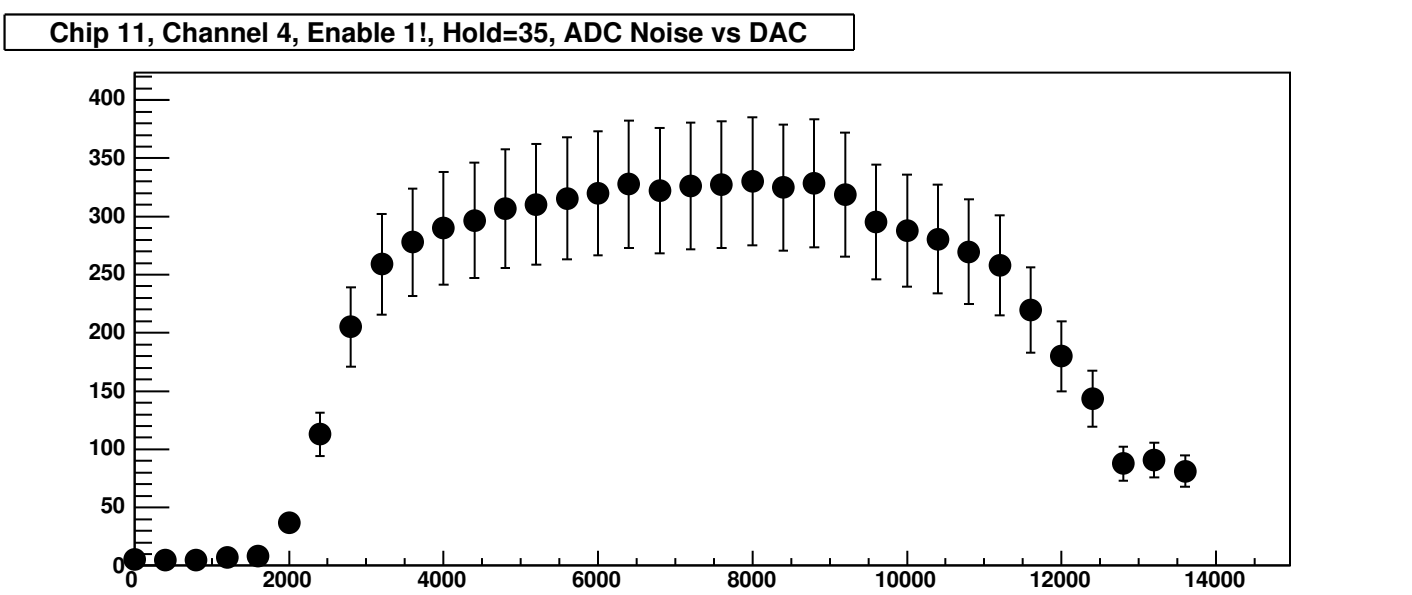

Chip 11, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

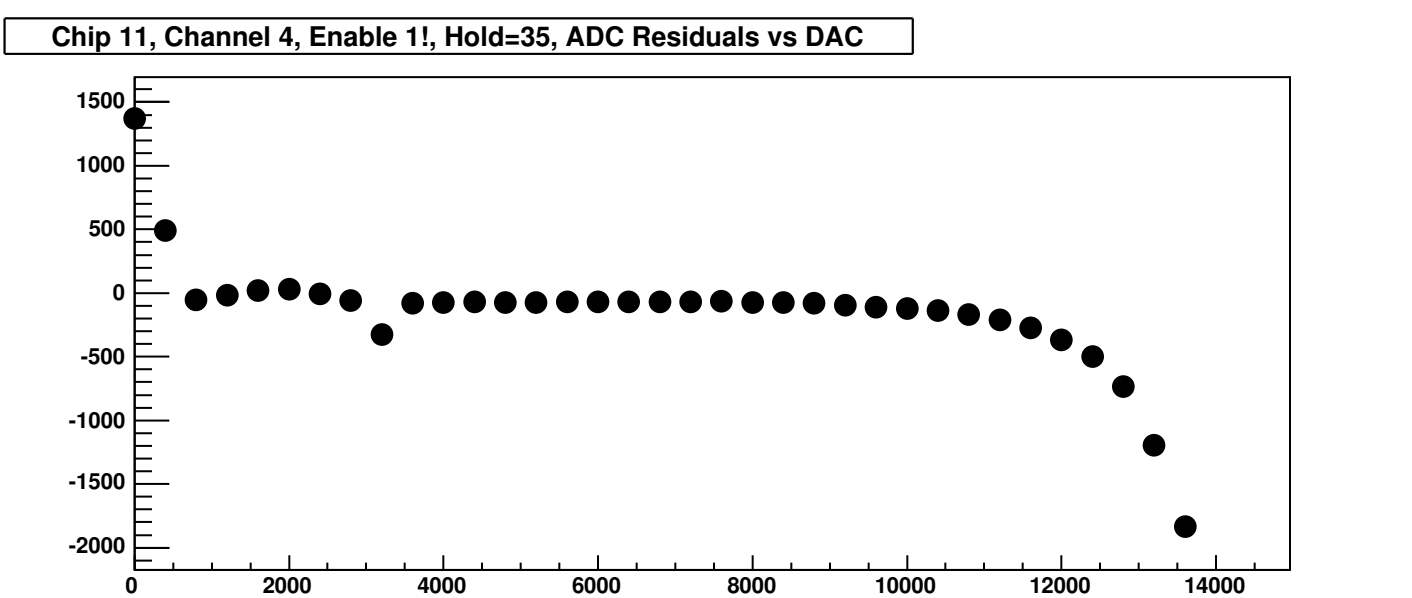

Chip 11, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.078e+04 / 23
p0 -1218 ± 1.308
p1 0.5364 ± 0.0002236
5000
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

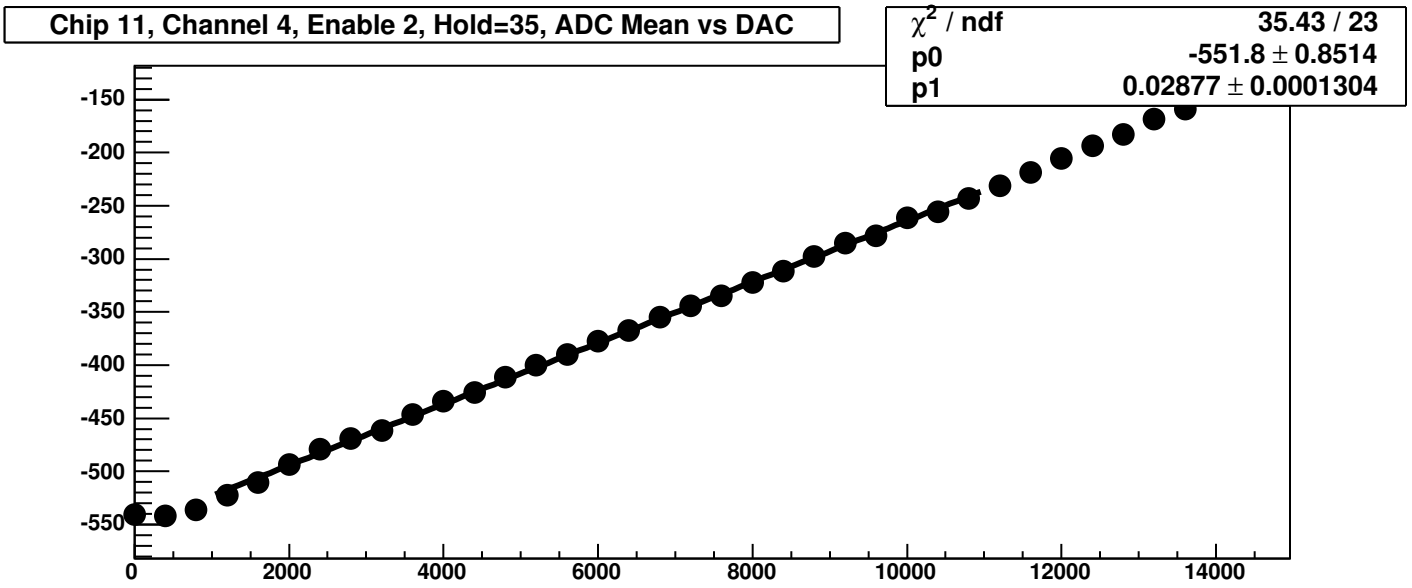

Chip 11, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

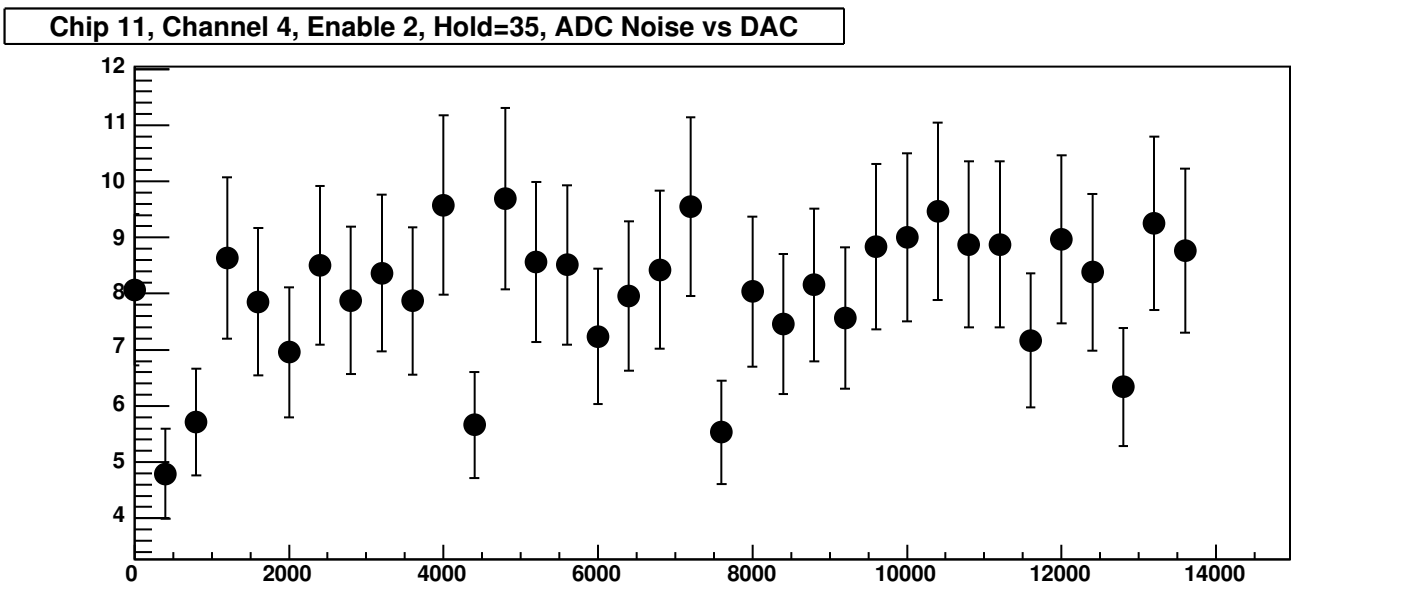

Chip 11, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

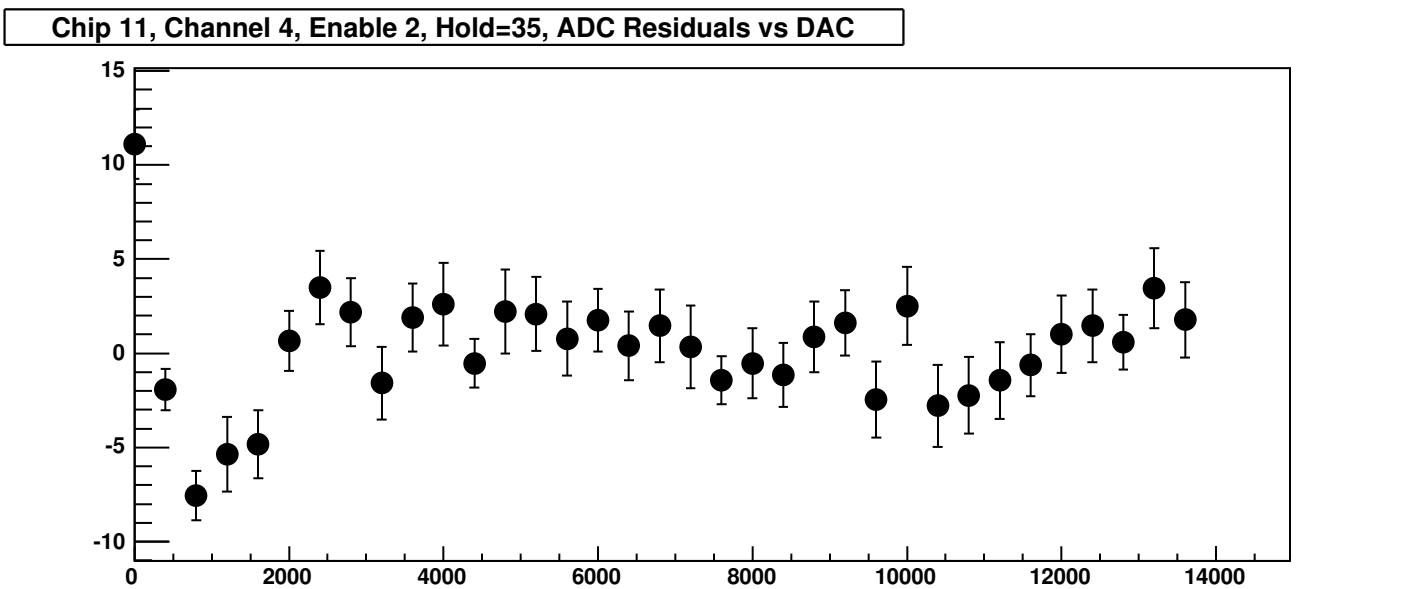

Chip 11, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 269.7 / 23
p0 -450.4 ± 3.53
p1 2.24 ± 0.002327
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

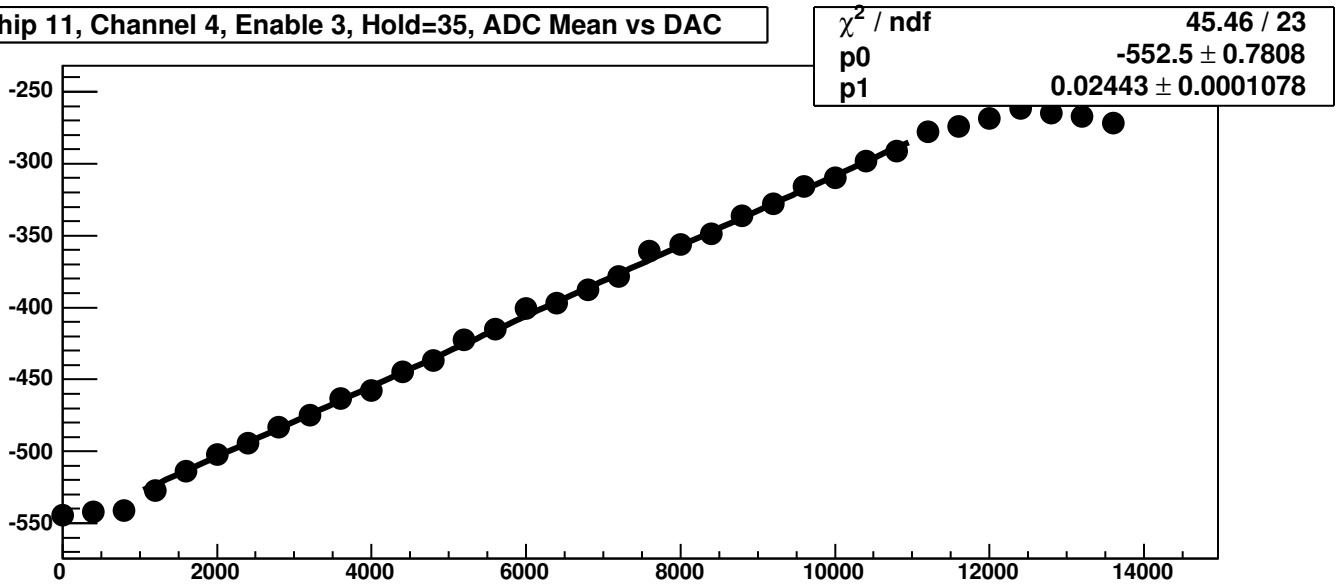

Chip 11, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 35.43 / 23
p0 -551.8 ± 0.8514
p1 0.02877 ± 0.0001304
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

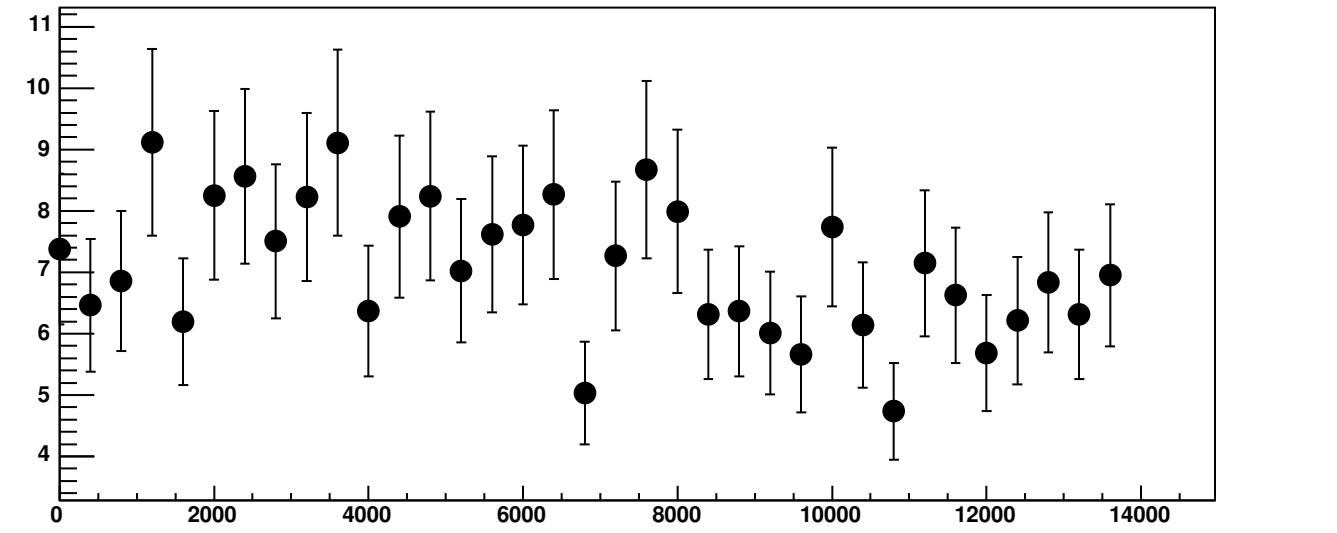

Chip 11, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

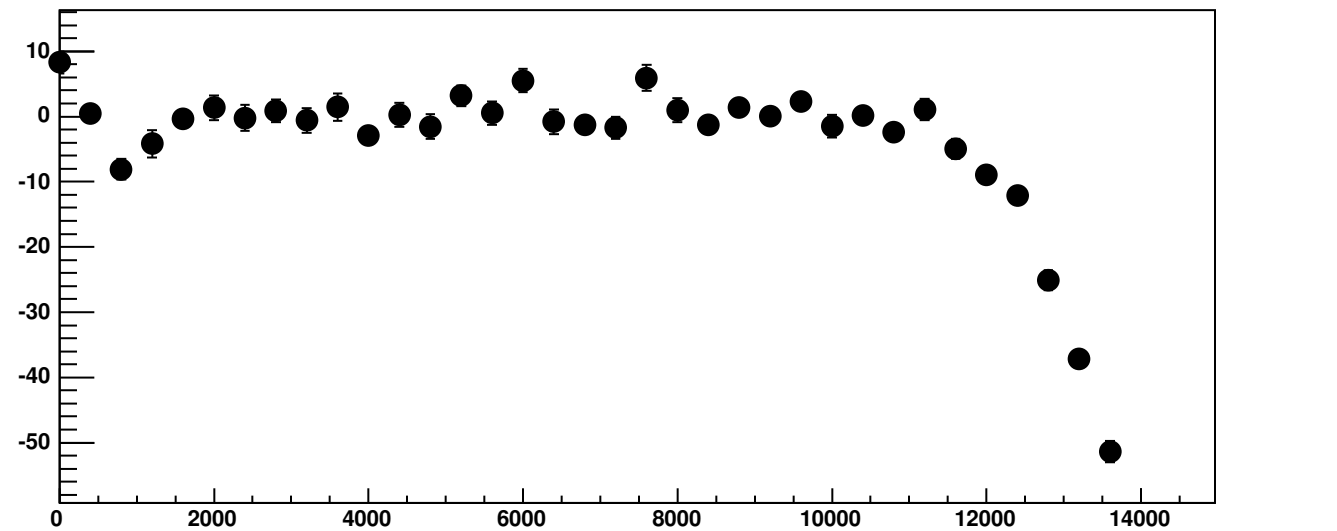
Chip 11, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

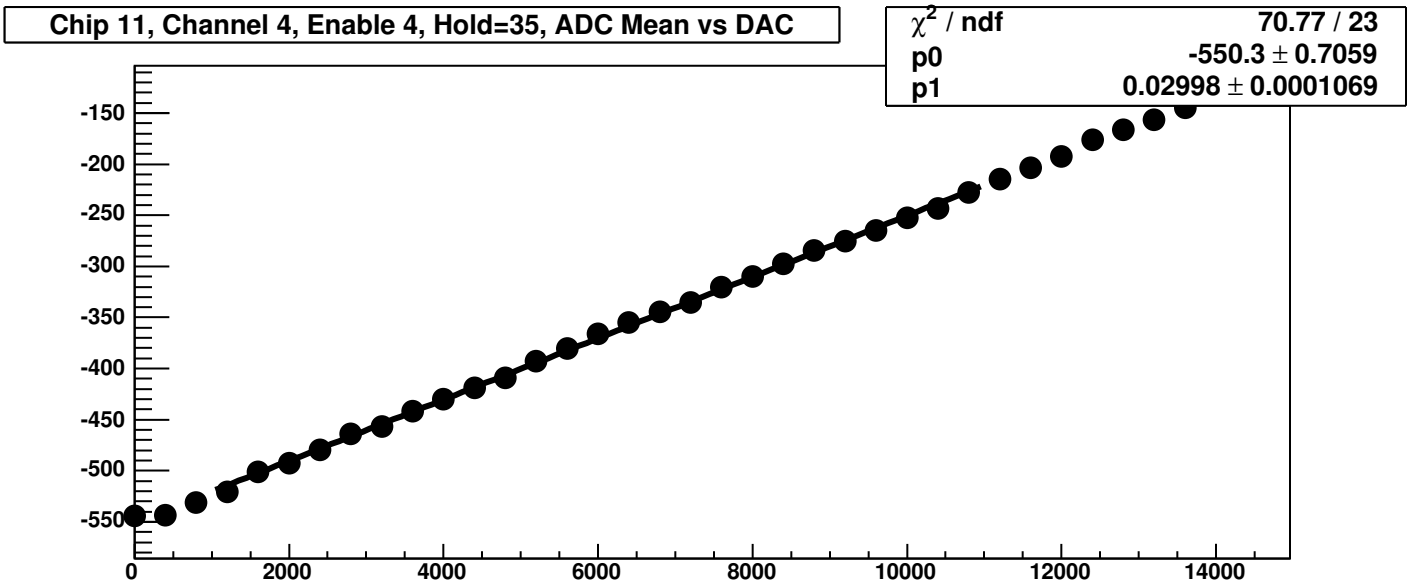


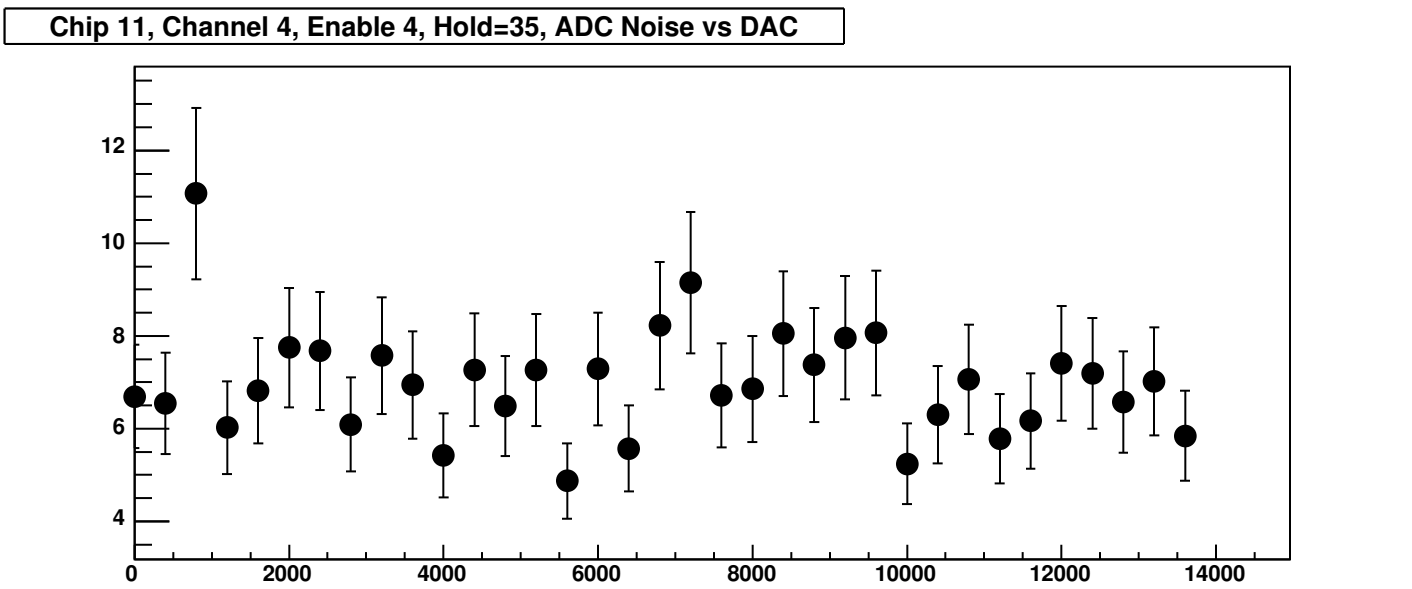
Chip 11, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC

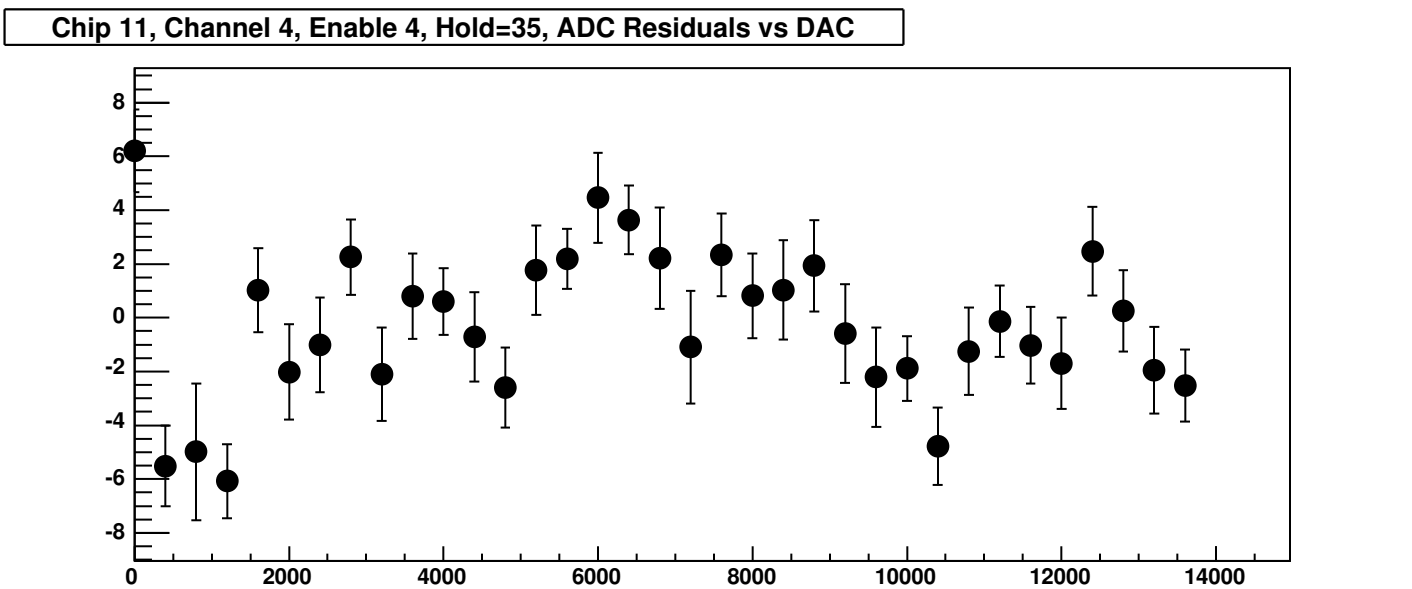


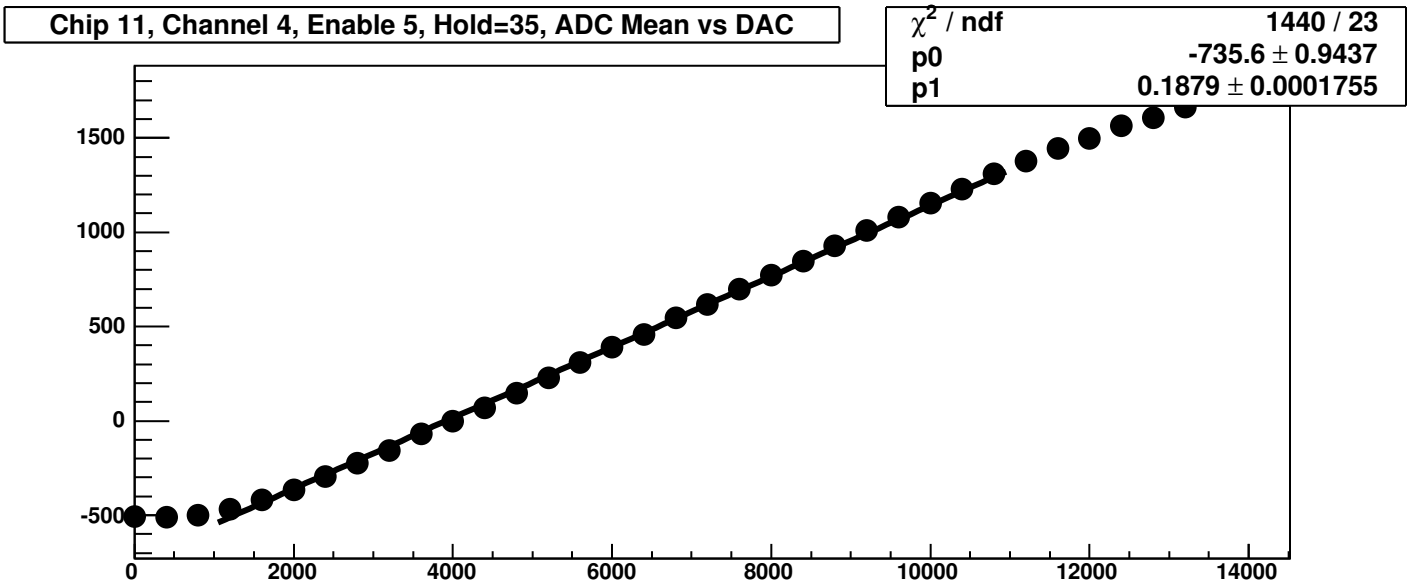
Chip 11, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

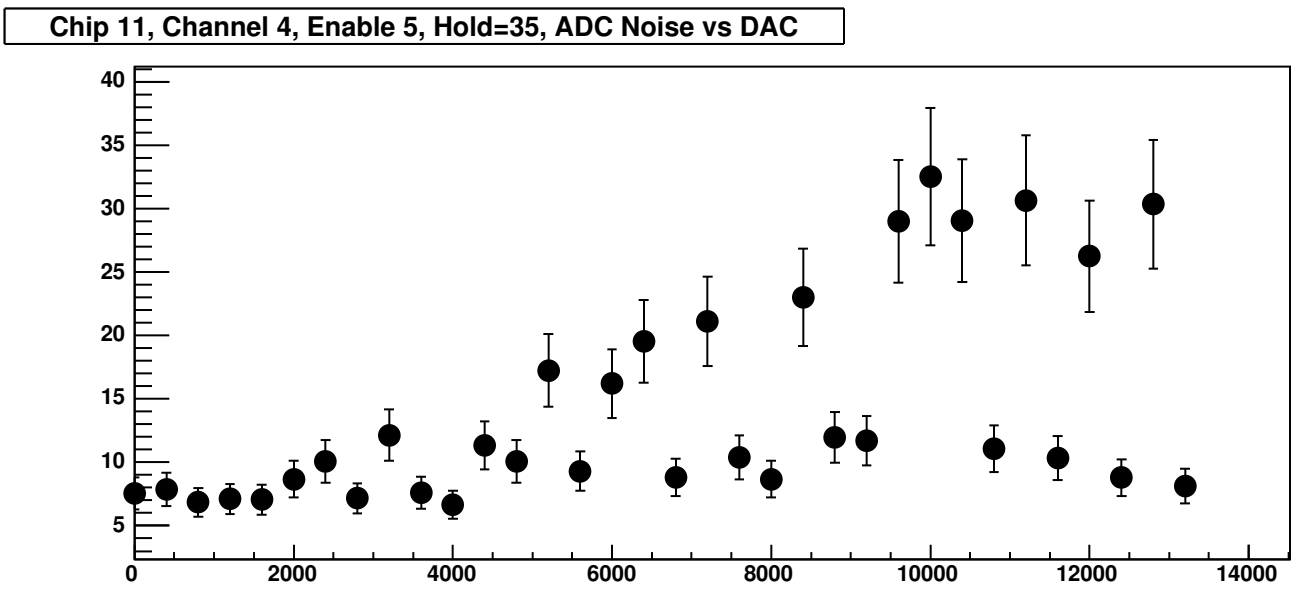


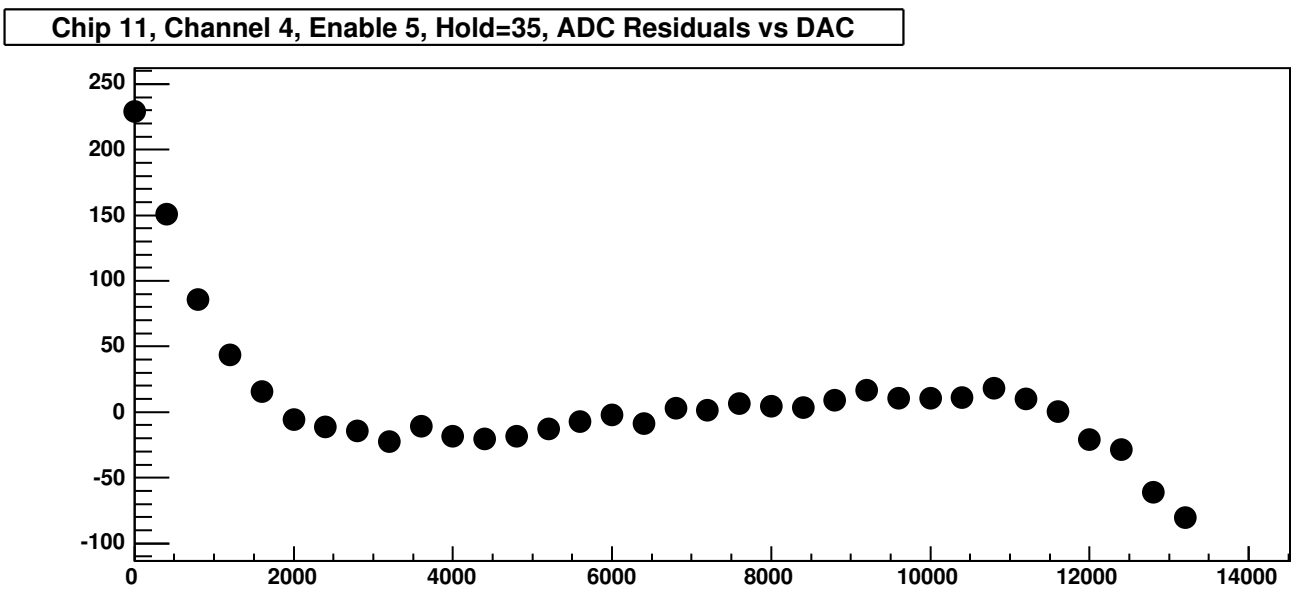

Chip 11, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 70.77 / 23
p0 -550.3 ± 0.7059
p1 0.02998 ± 0.0001069
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

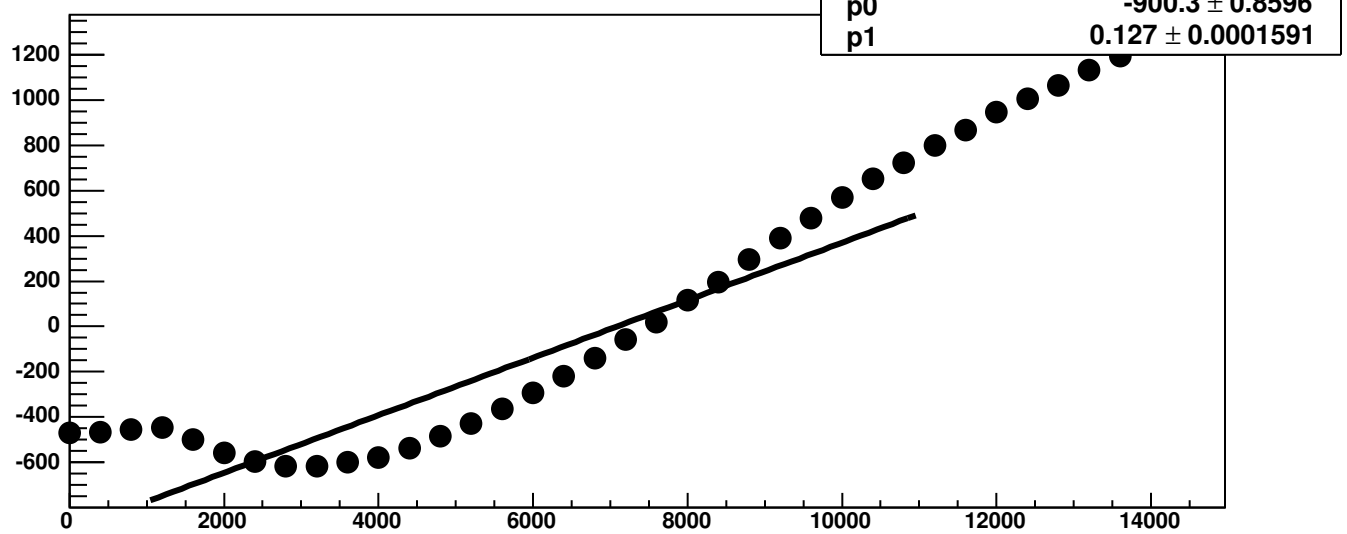

Chip 11, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1440 / 23
p0 -735.6 ± 0.9437
p1 0.1879 ± 0.0001755
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

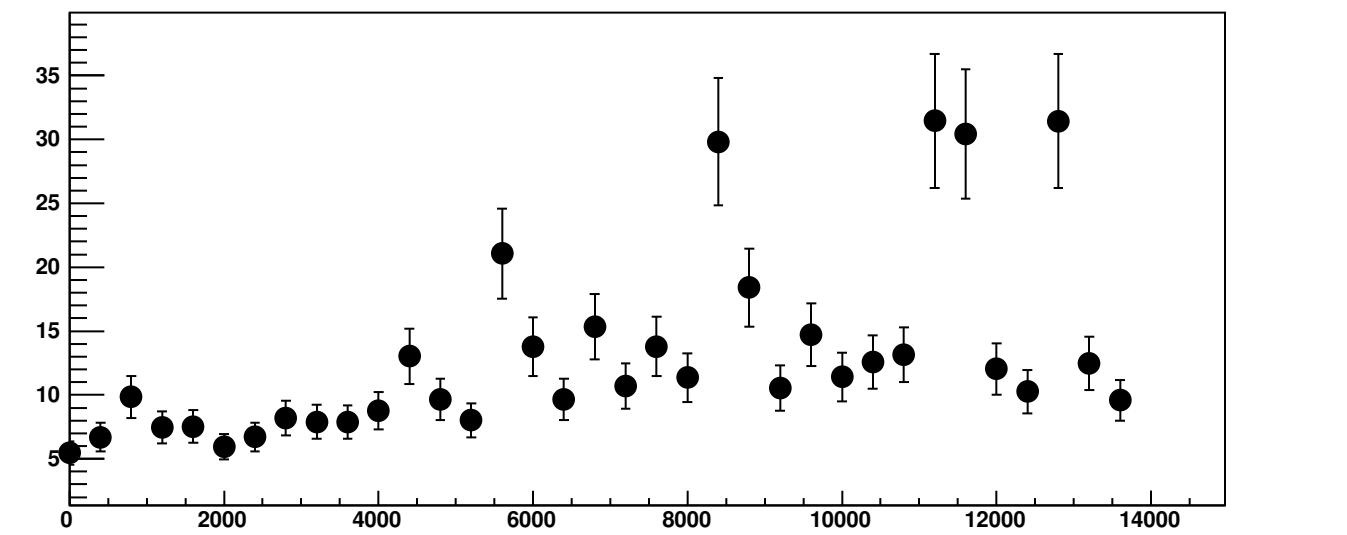

Chip 11, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

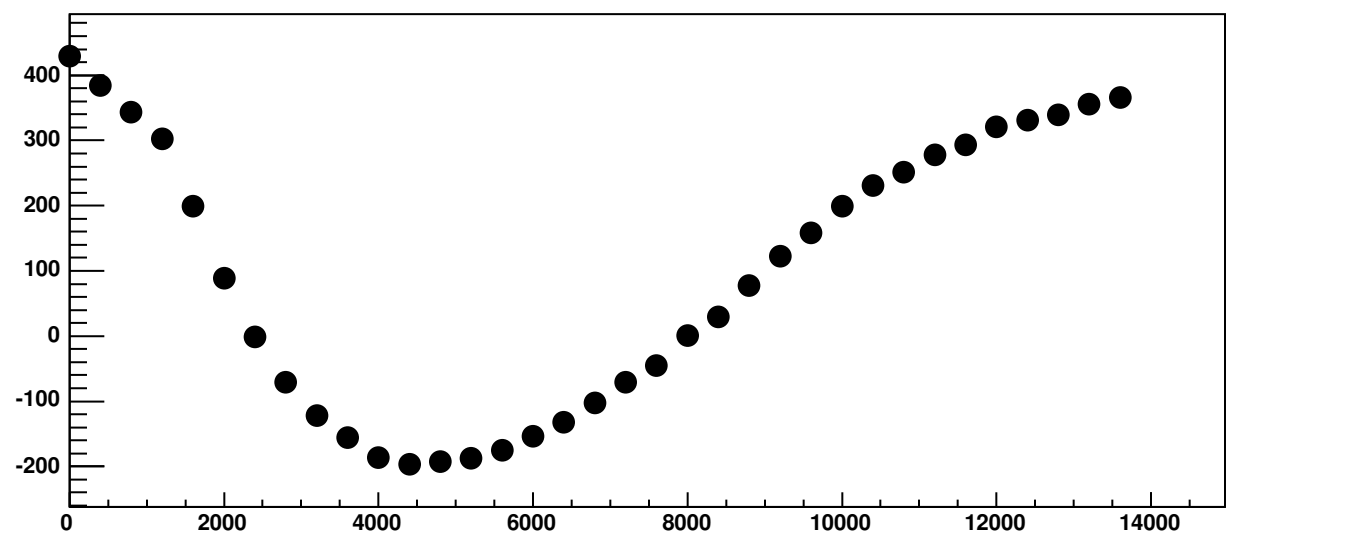
Chip 11, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

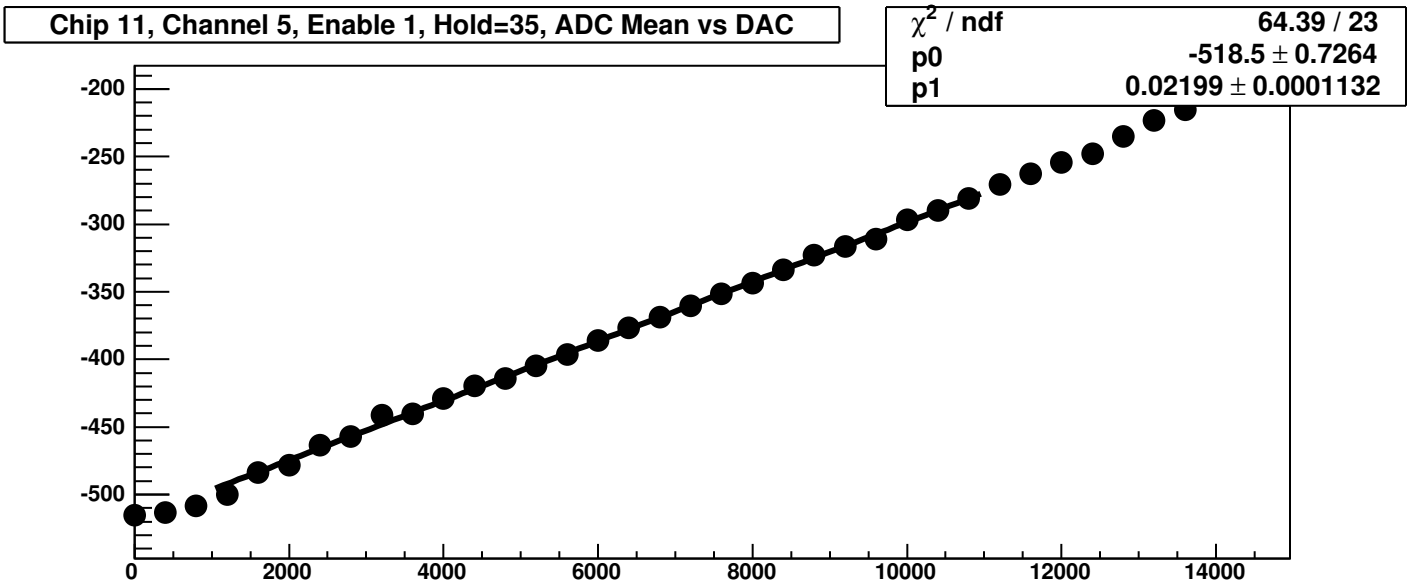


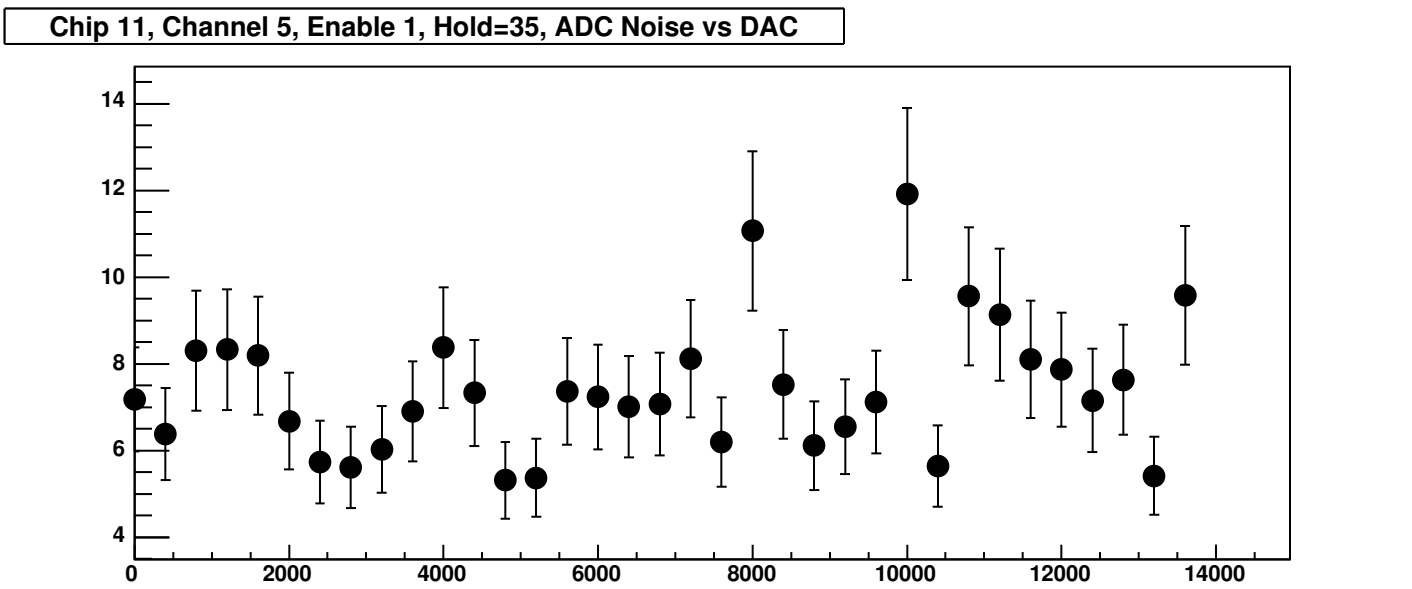
Chip 11, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC

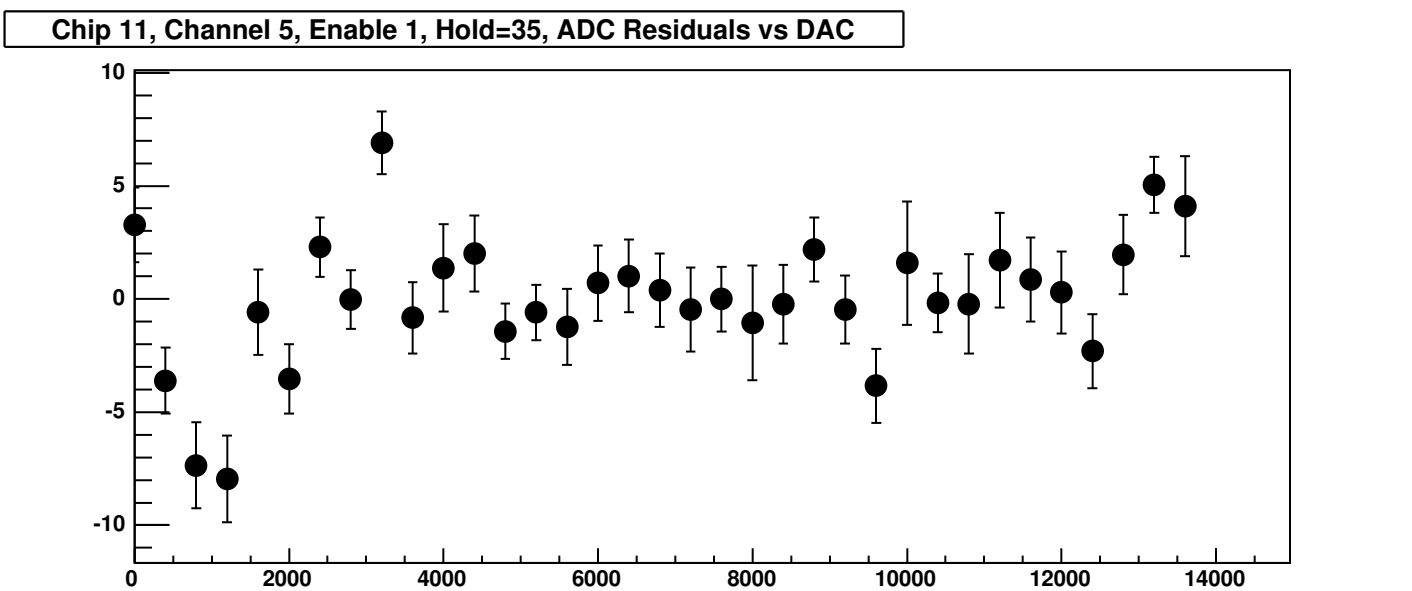


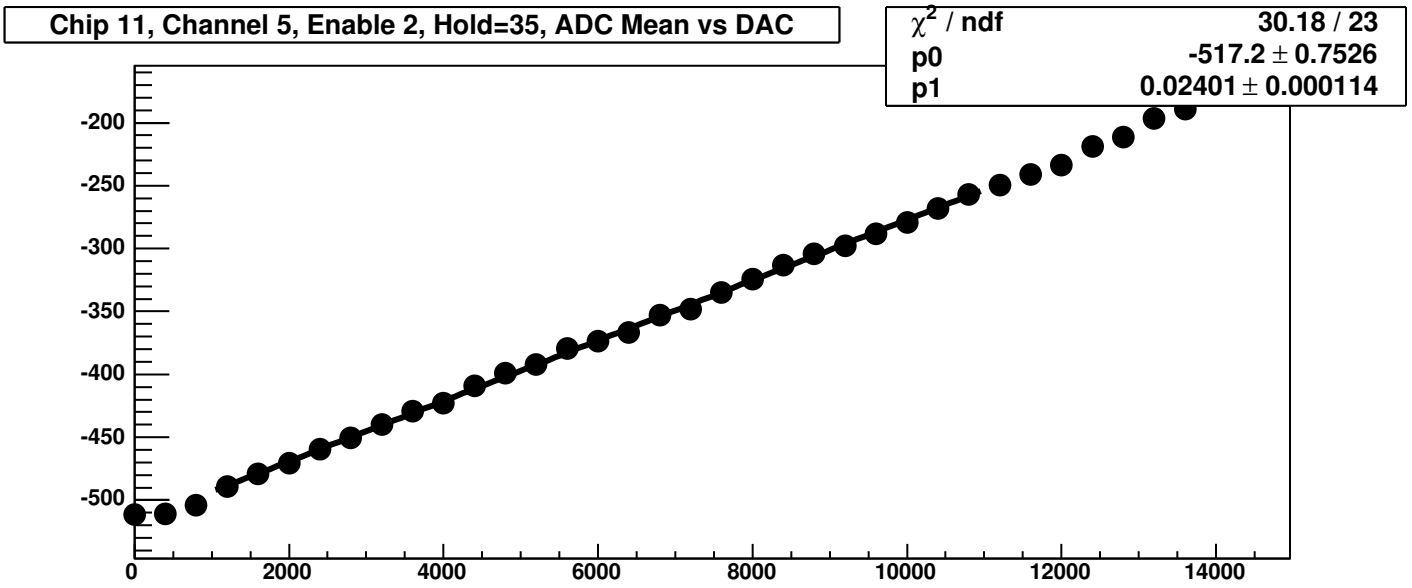
Chip 11, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

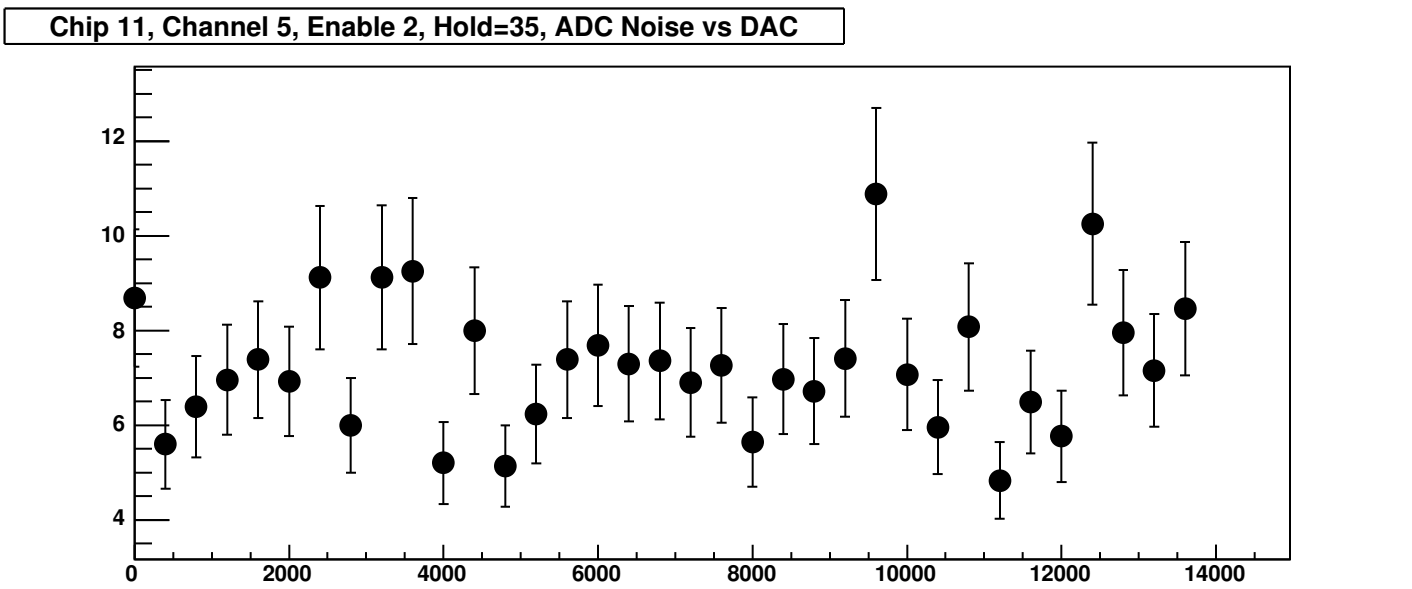


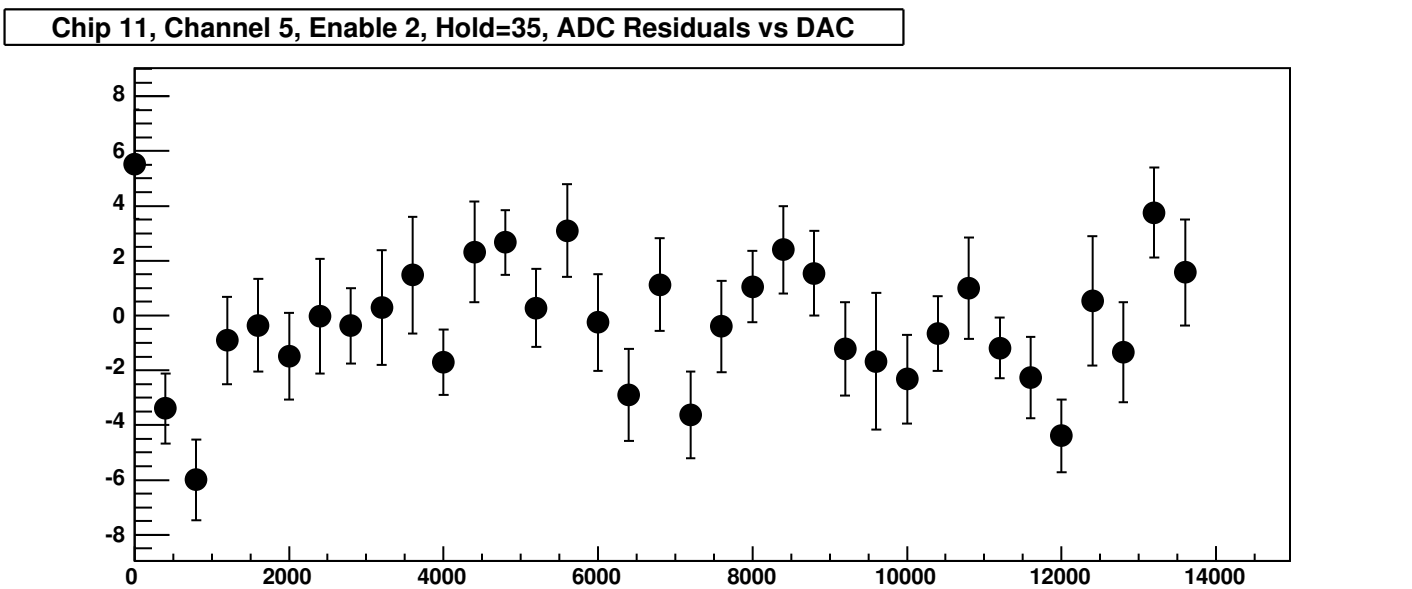

Chip 11, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 64.39 / 23
p0 -518.5 ± 0.7264
p1 0.02199 ± 0.0001132
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

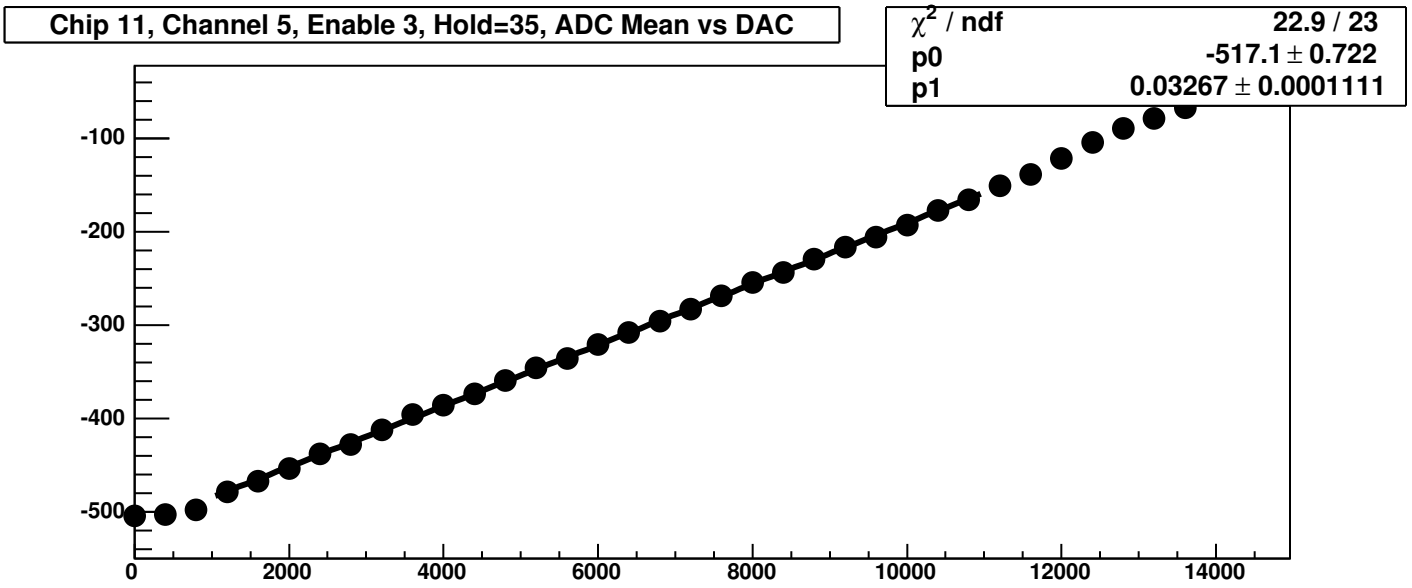

Chip 11, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

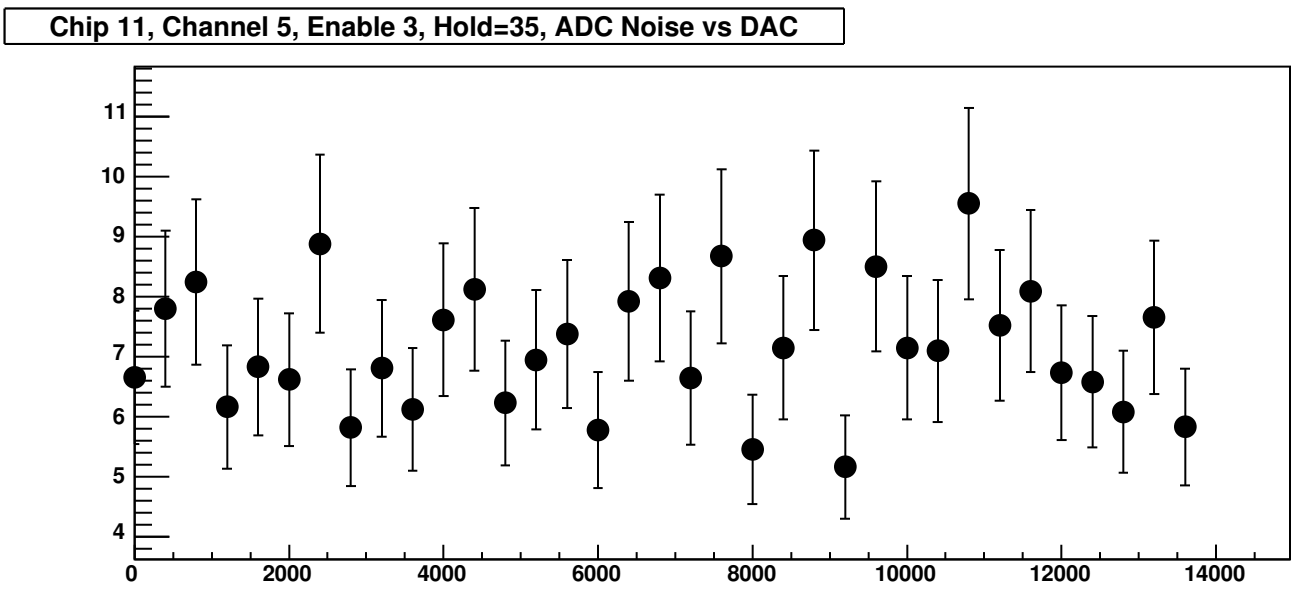

Chip 11, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

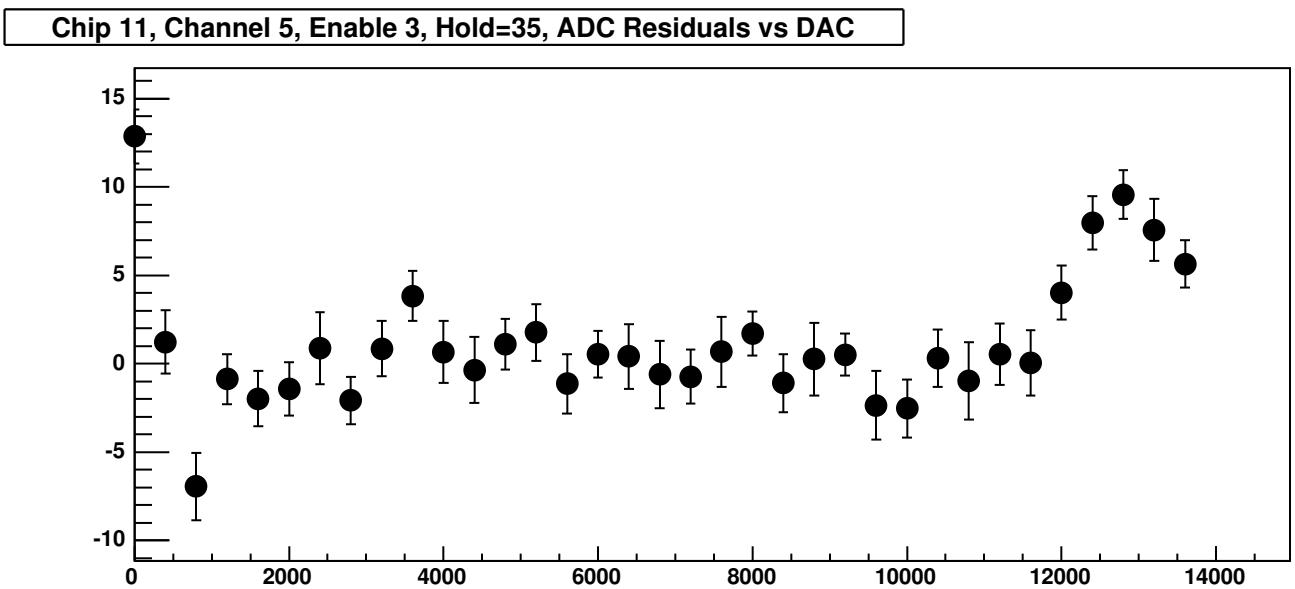

Chip 11, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 30.18 / 23
p0 -517.2 ± 0.7526
p1 0.02401 ± 0.000114
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

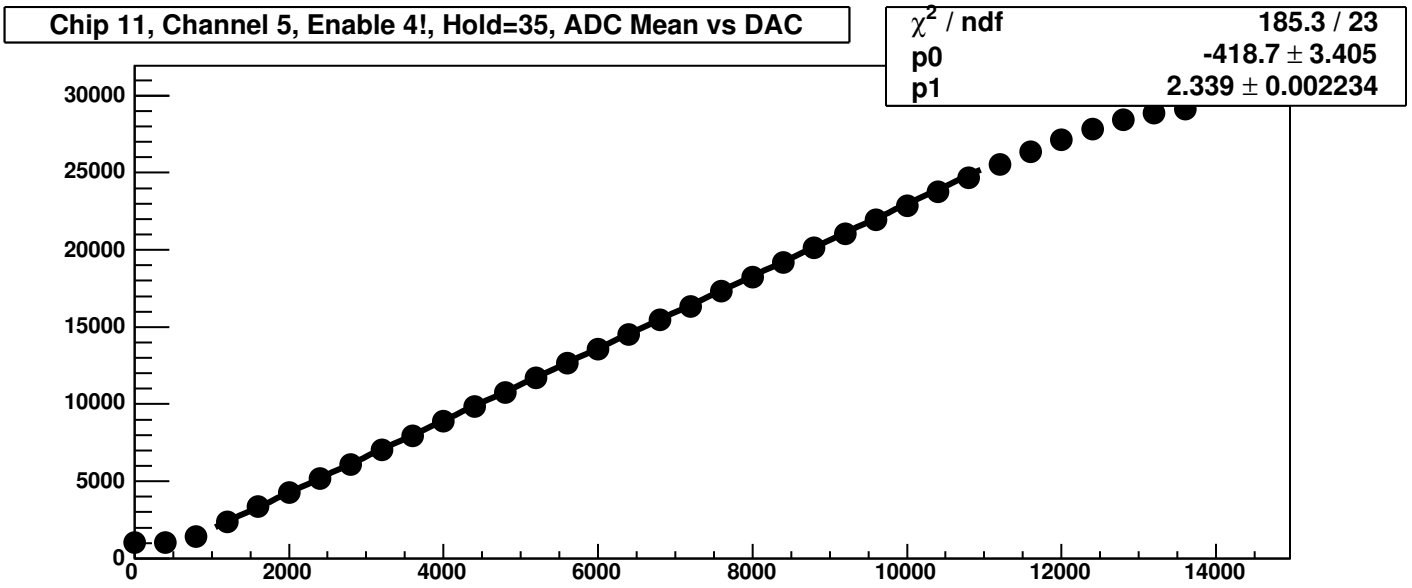

Chip 11, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

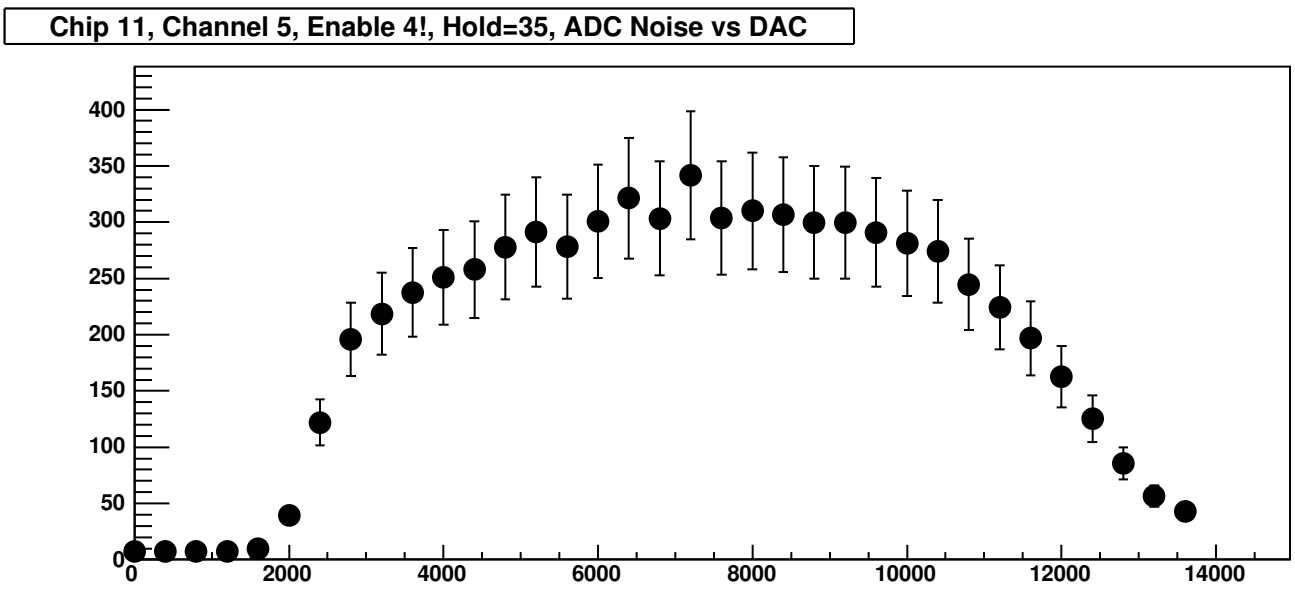

Chip 11, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

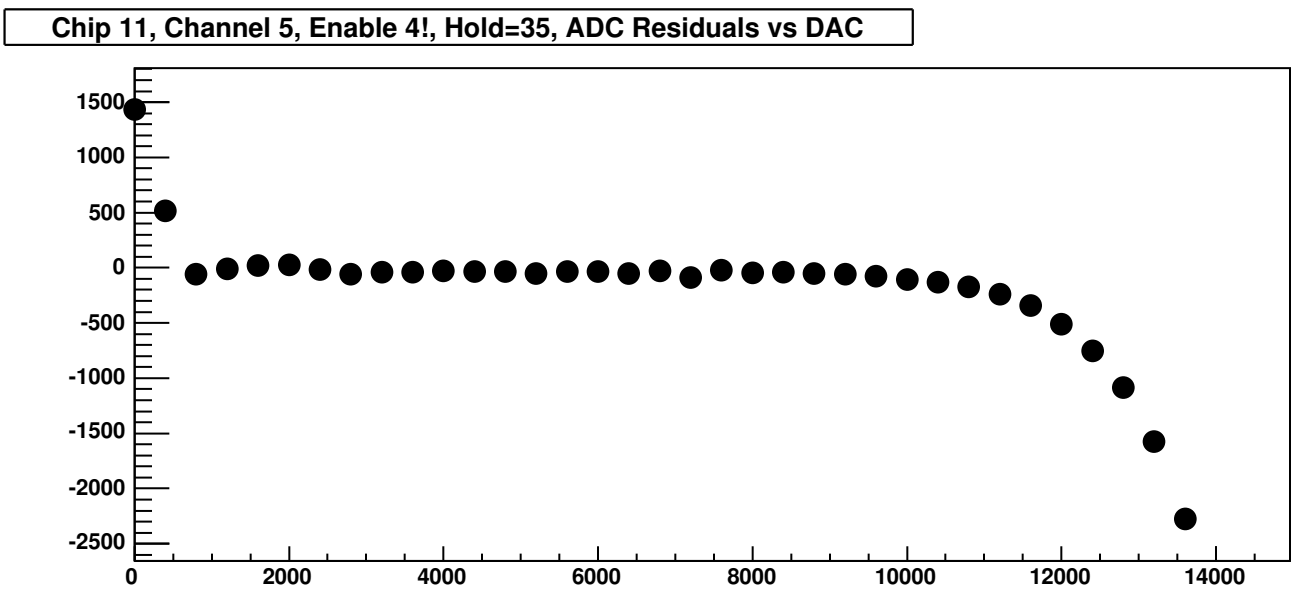

Chip 11, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 22.9 / 23
p0 -517.1 ± 0.722
p1 0.03267 ± 0.0001111
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000

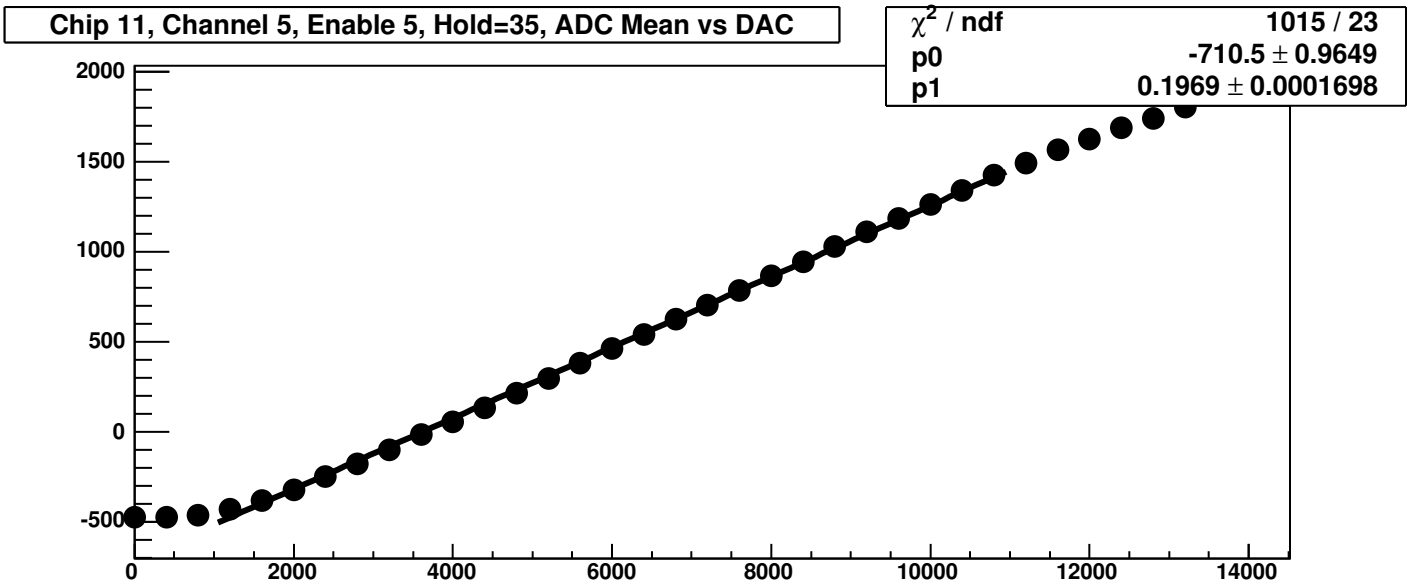

Chip 11, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

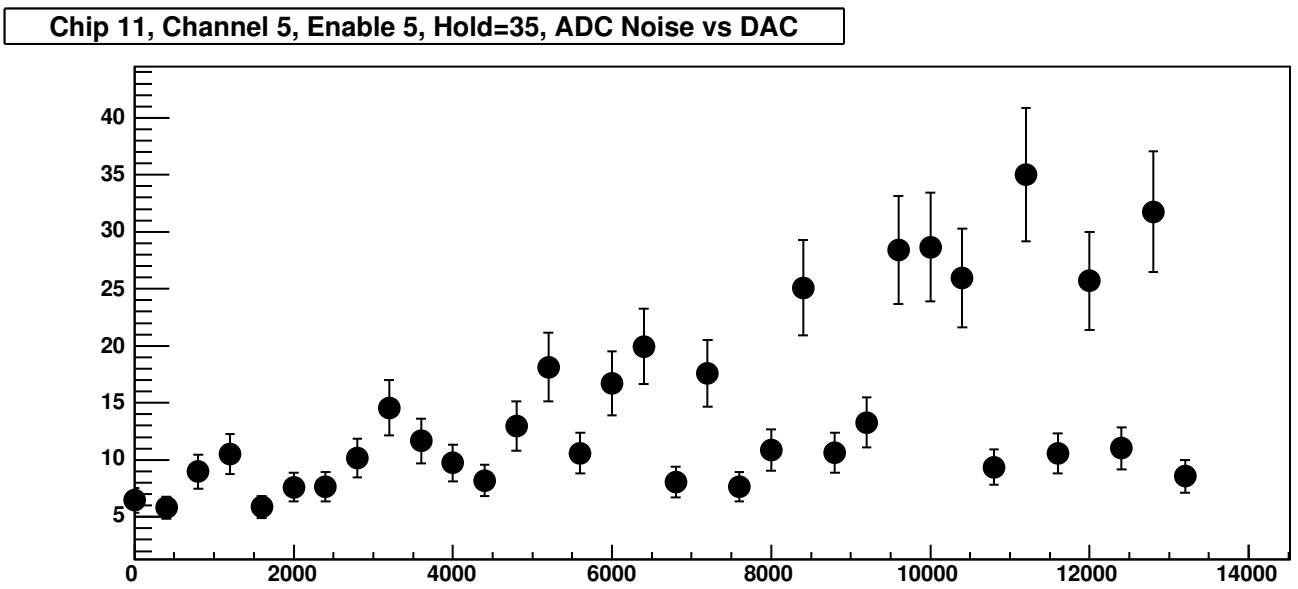

Chip 11, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

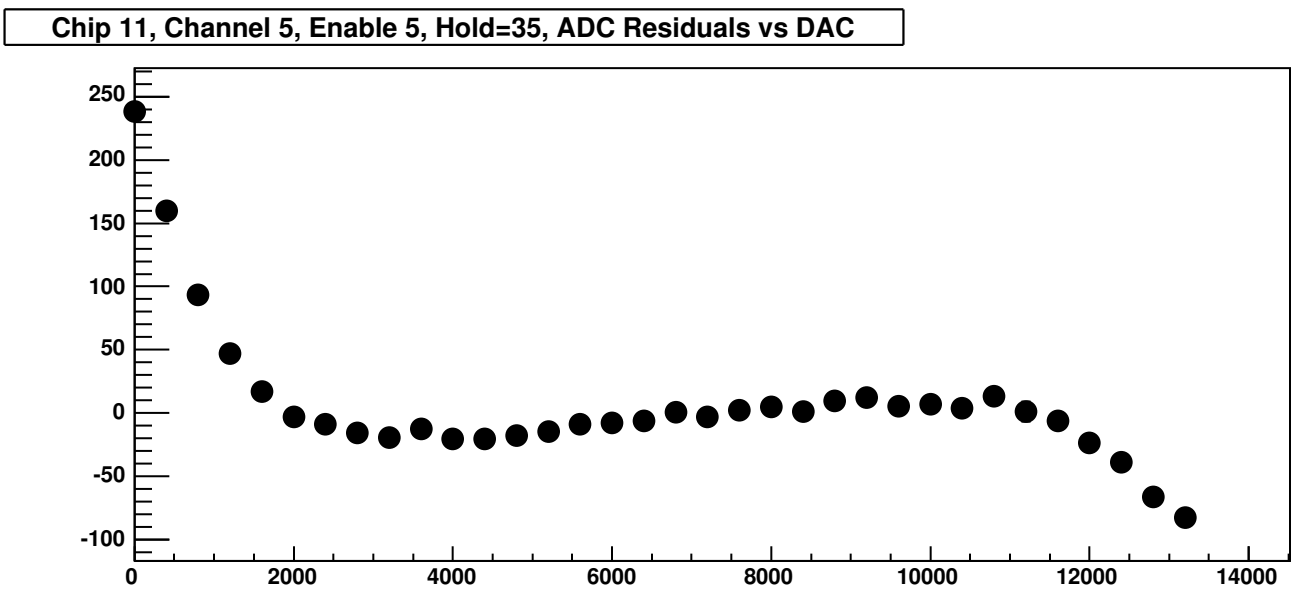

Chip 11, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 185.3 / 23
p0 -418.7 ± 3.405
p1 2.339 ± 0.002234
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

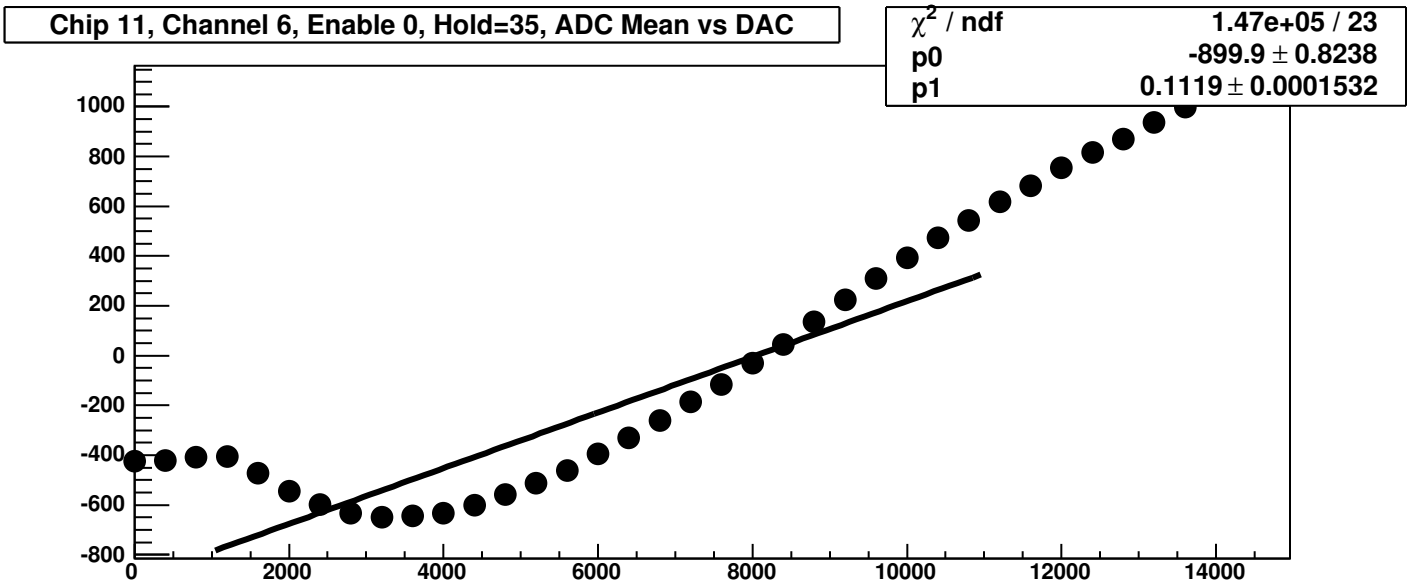

Chip 11, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

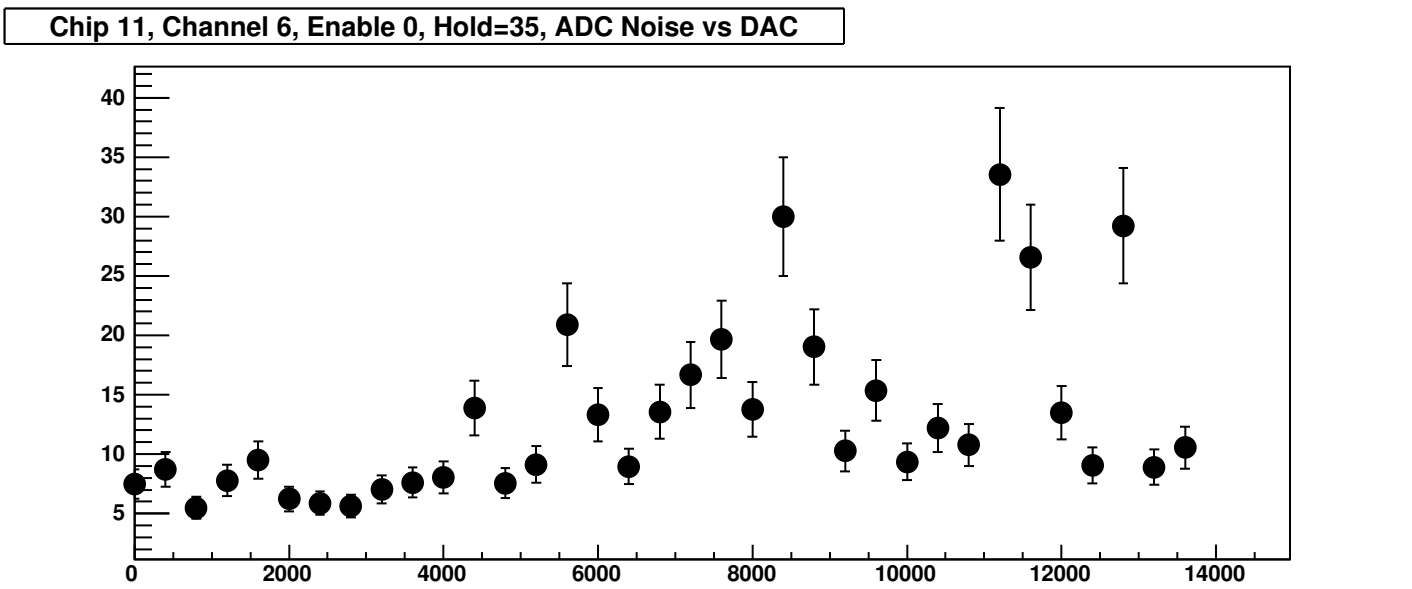

Chip 11, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

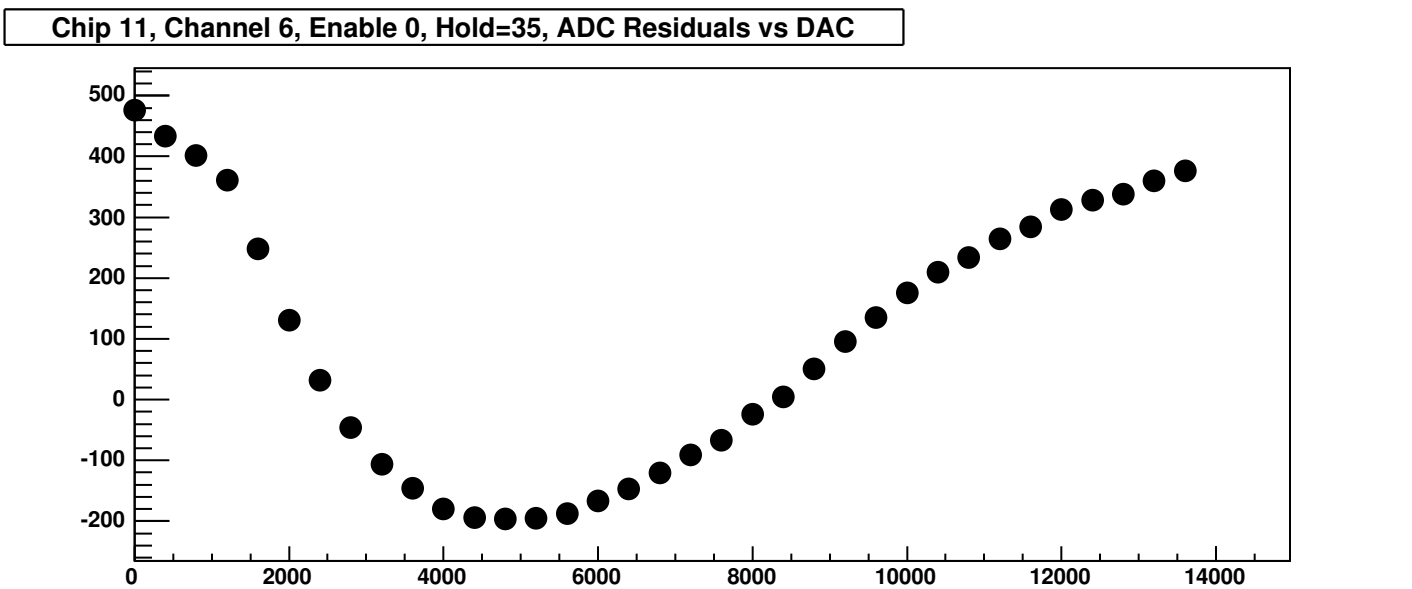

Chip 11, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1015 / 23
p0 -710.5 ± 0.9649
p1 0.1969 ± 0.0001698
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

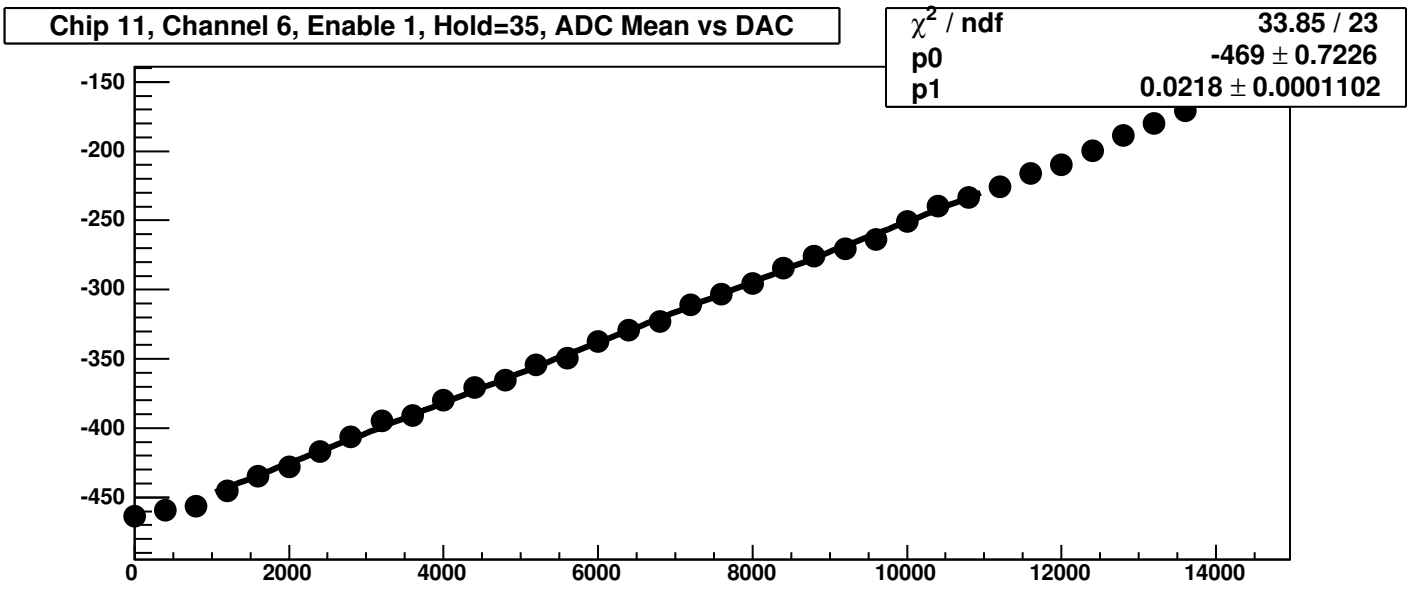

Chip 11, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

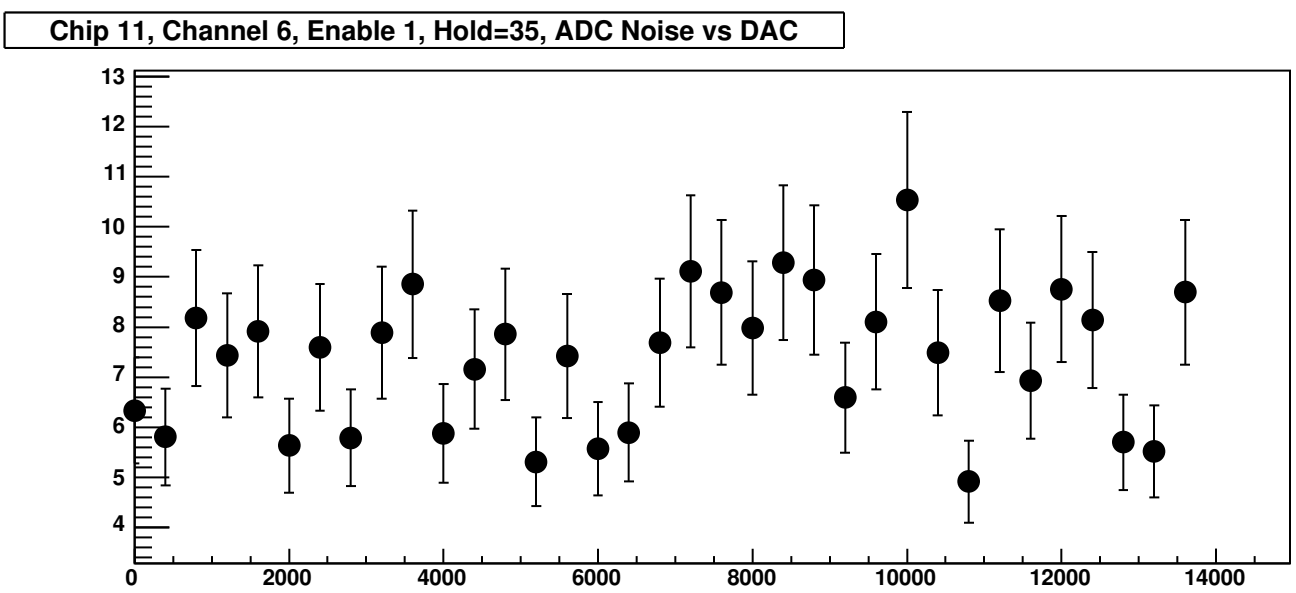

Chip 11, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

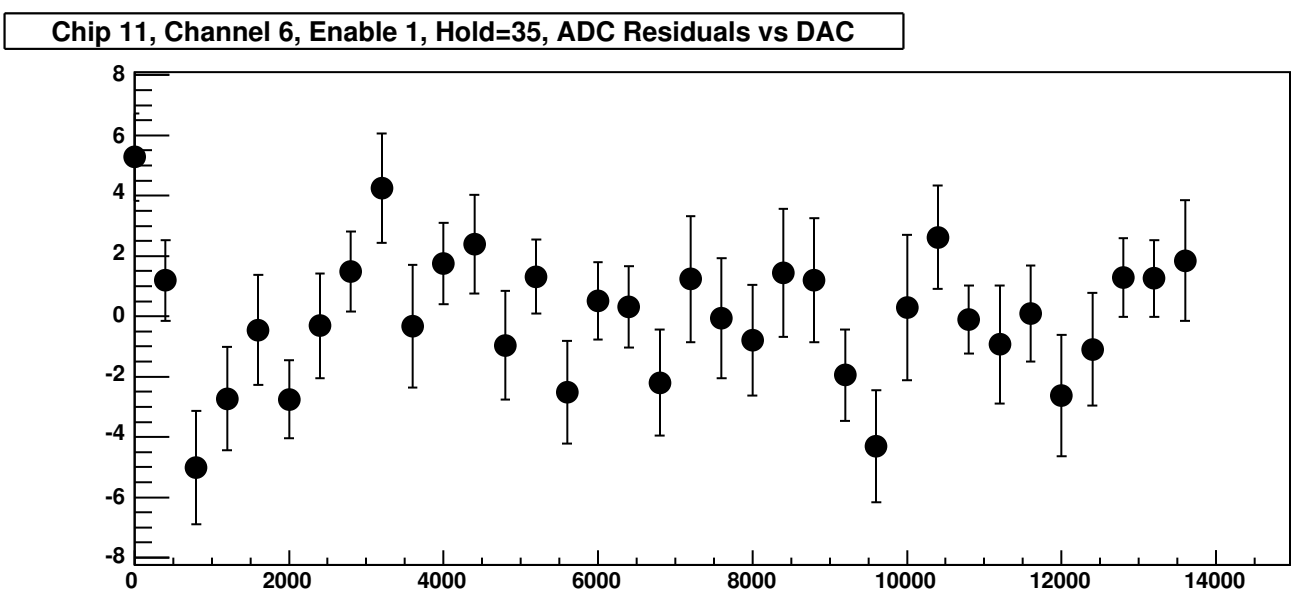

Chip 11, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.47e+05 / 23
p0 -899.9 ± 0.8238
p1 0.1119 ± 0.0001532
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

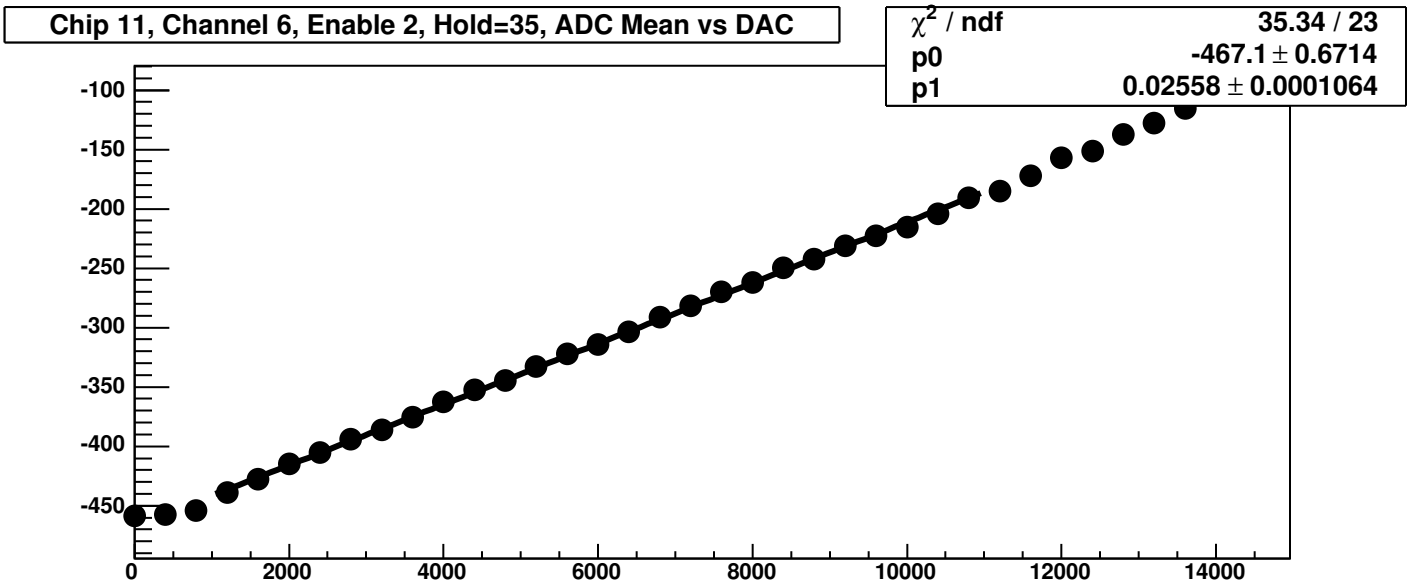

Chip 11, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

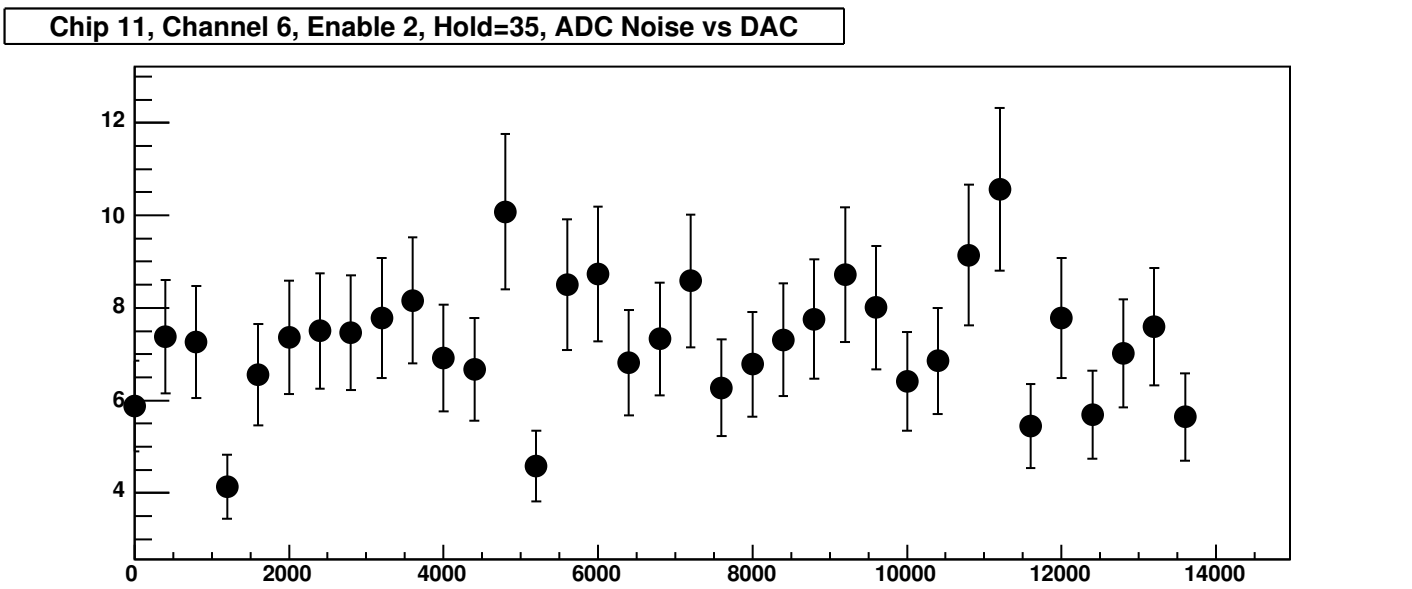

Chip 11, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

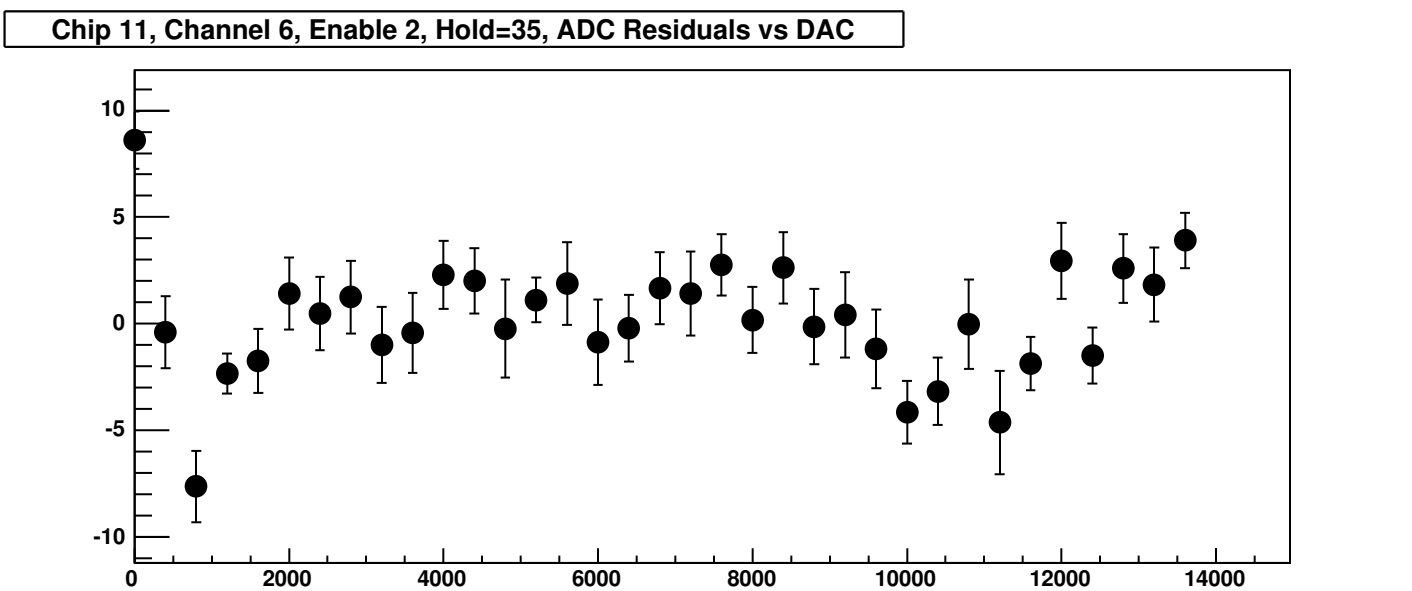

Chip 11, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.85 / 23
p0 -469 ± 0.7226
p1 0.0218 ± 0.0001102
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

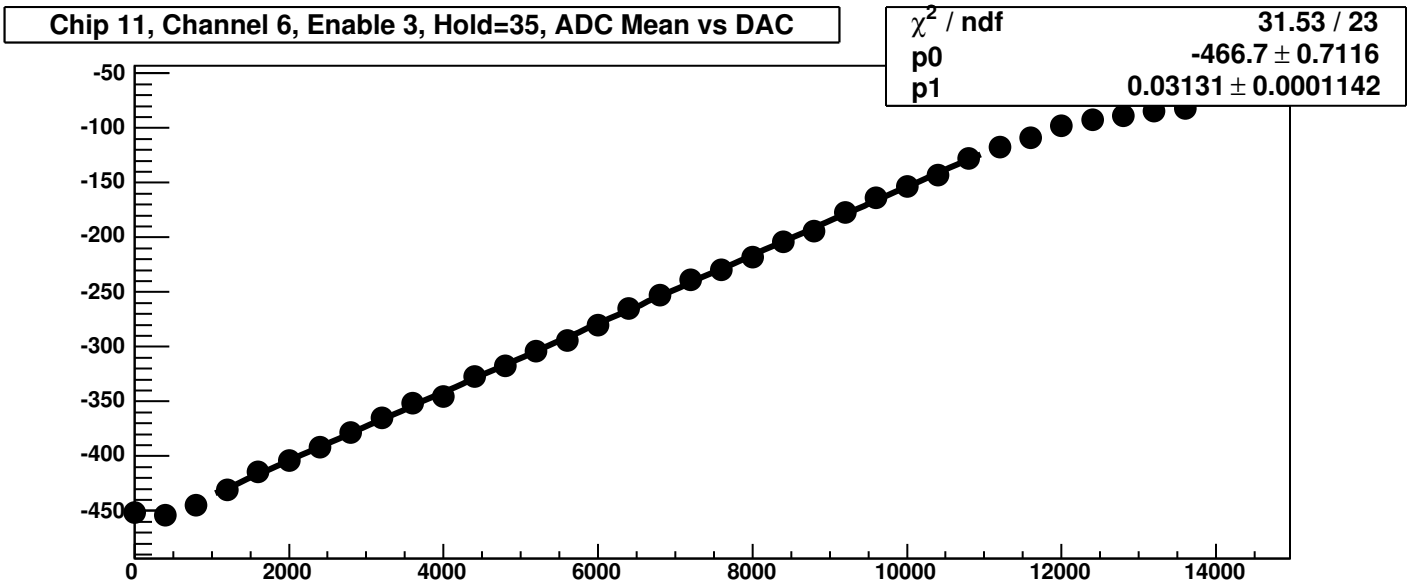

Chip 11, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

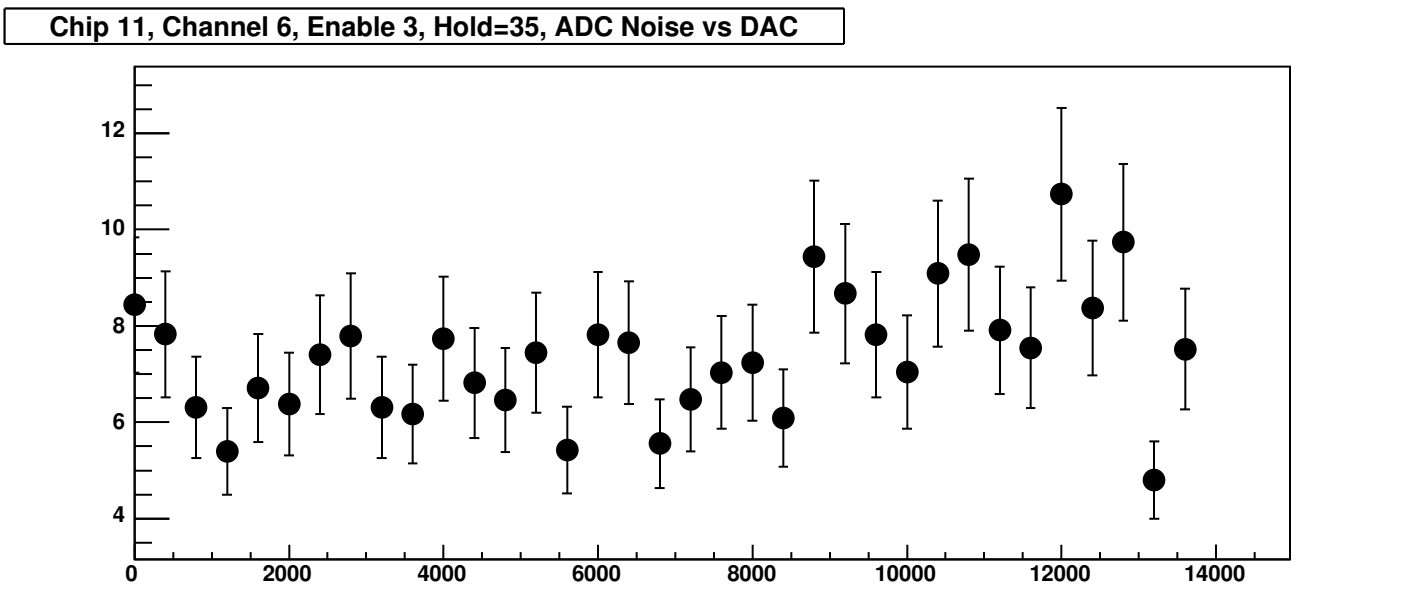

Chip 11, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

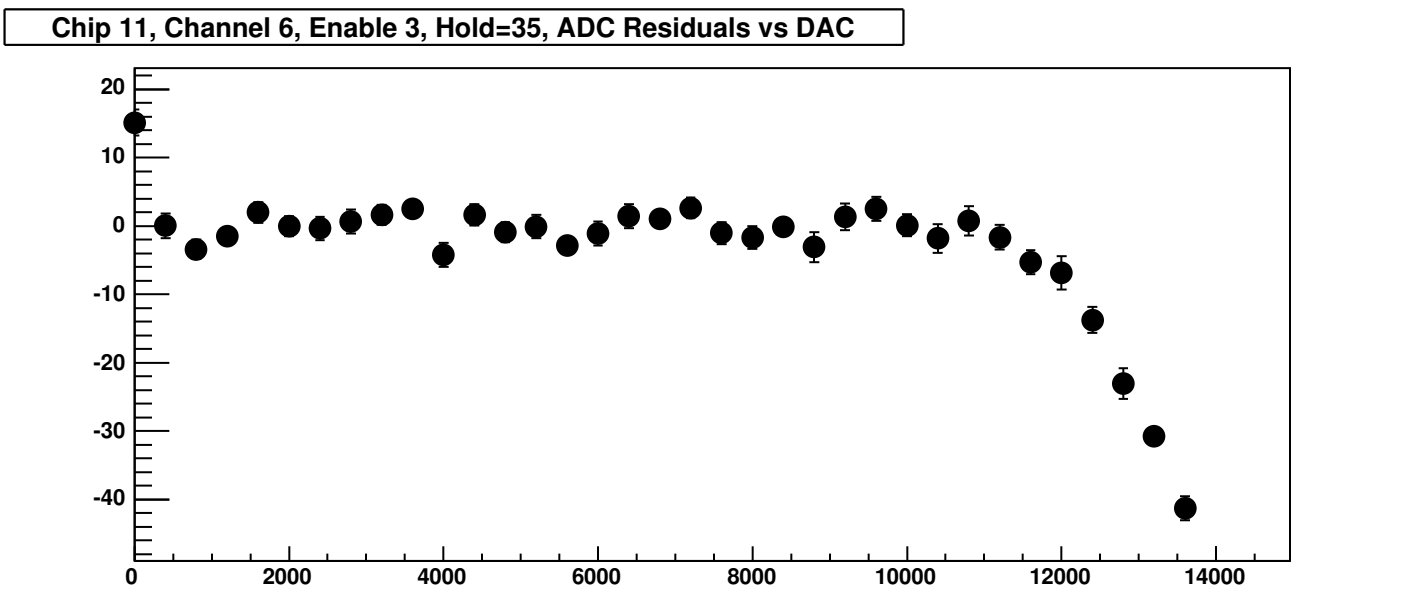

Chip 11, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 35.34 / 23
p0 -467.1 ± 0.6714
p1 0.02558 ± 0.0001064
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

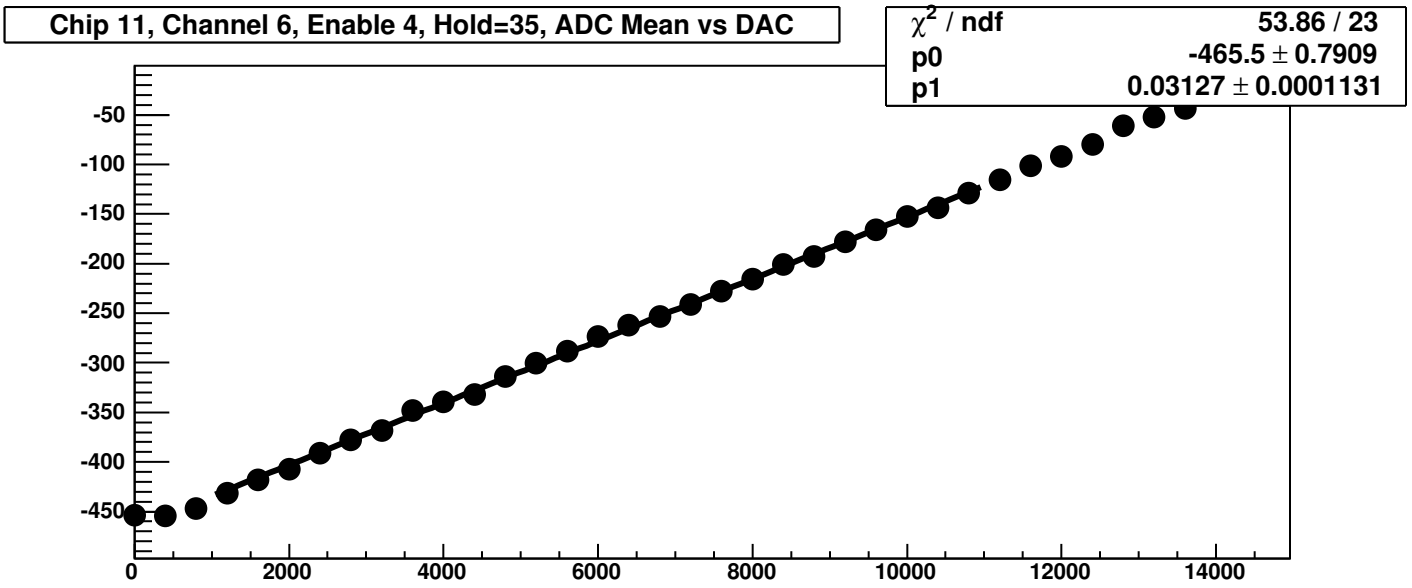

Chip 11, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

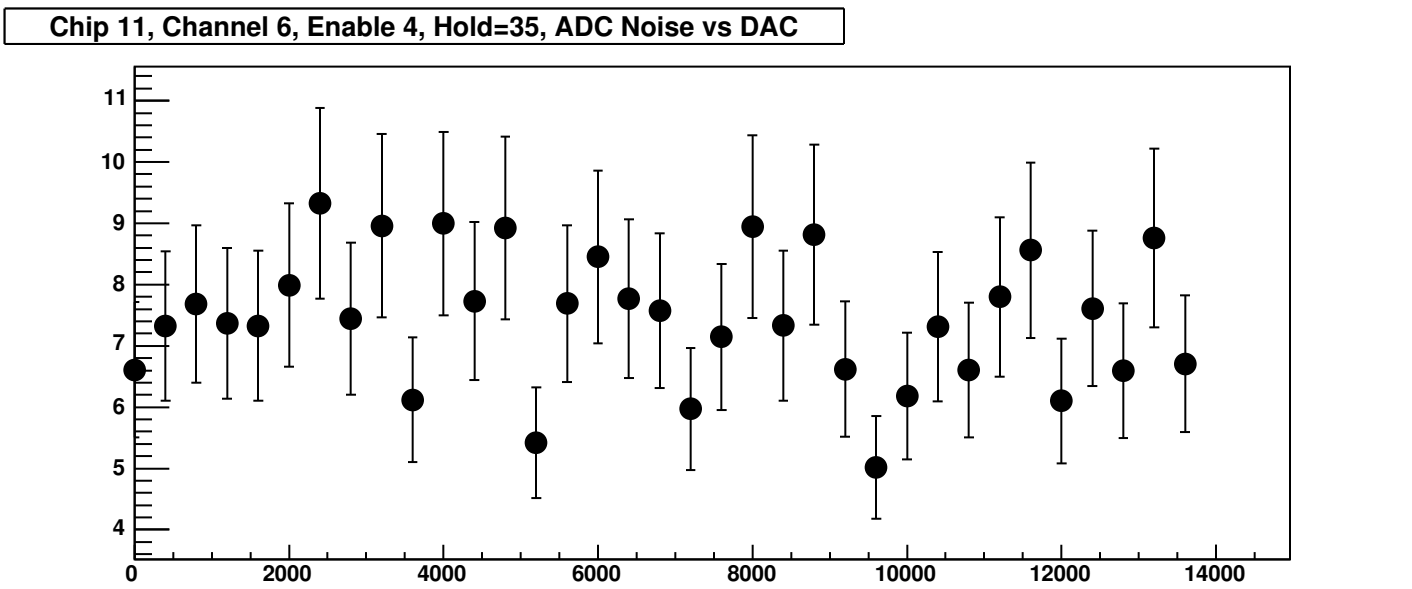

Chip 11, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

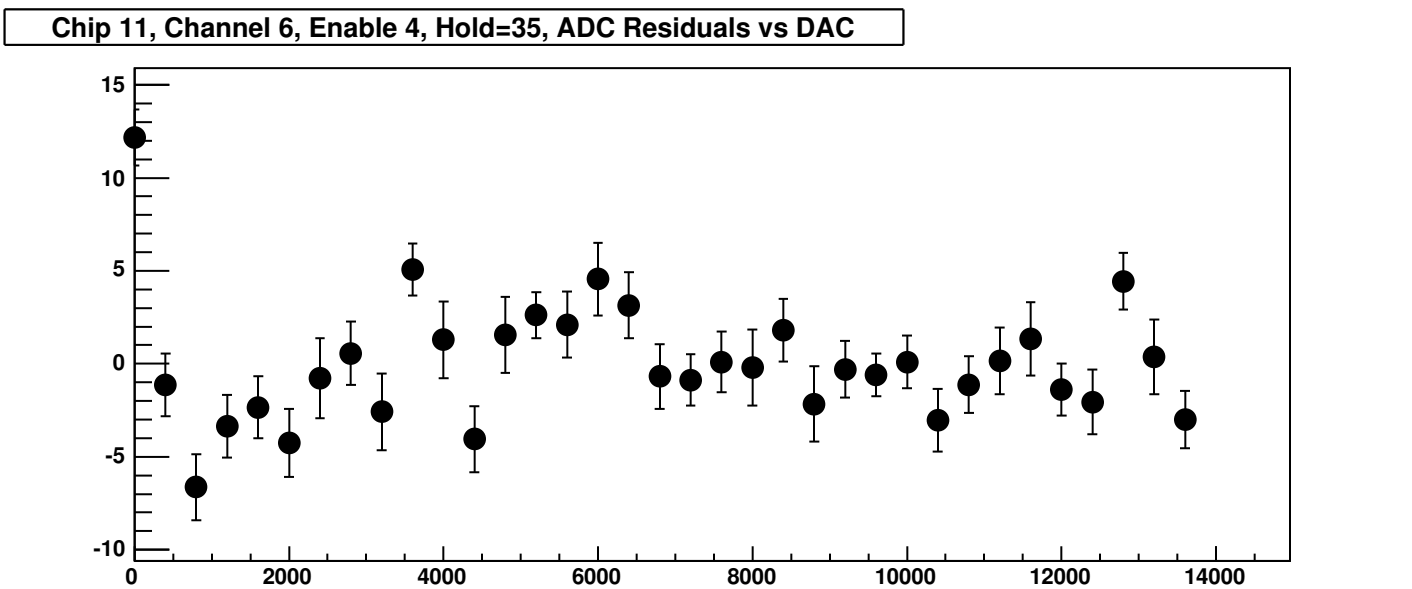

Chip 11, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.53 / 23
p0 -466.7 ± 0.7116
p1 0.03131 ± 0.0001142
-50
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

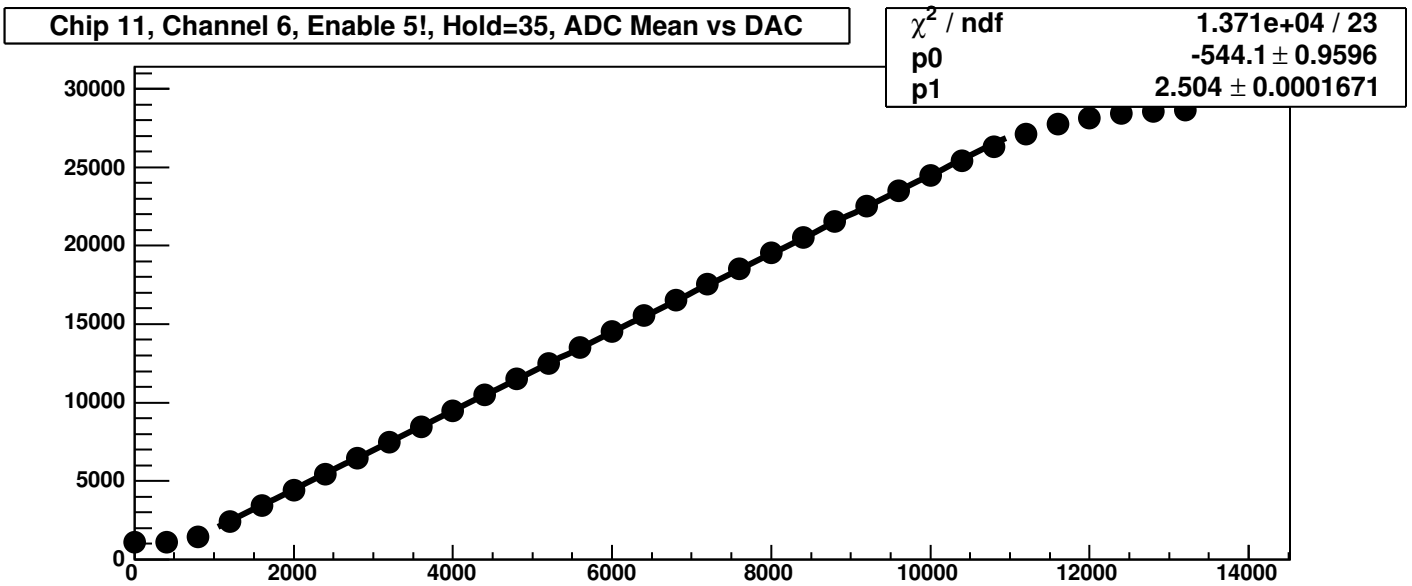

Chip 11, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

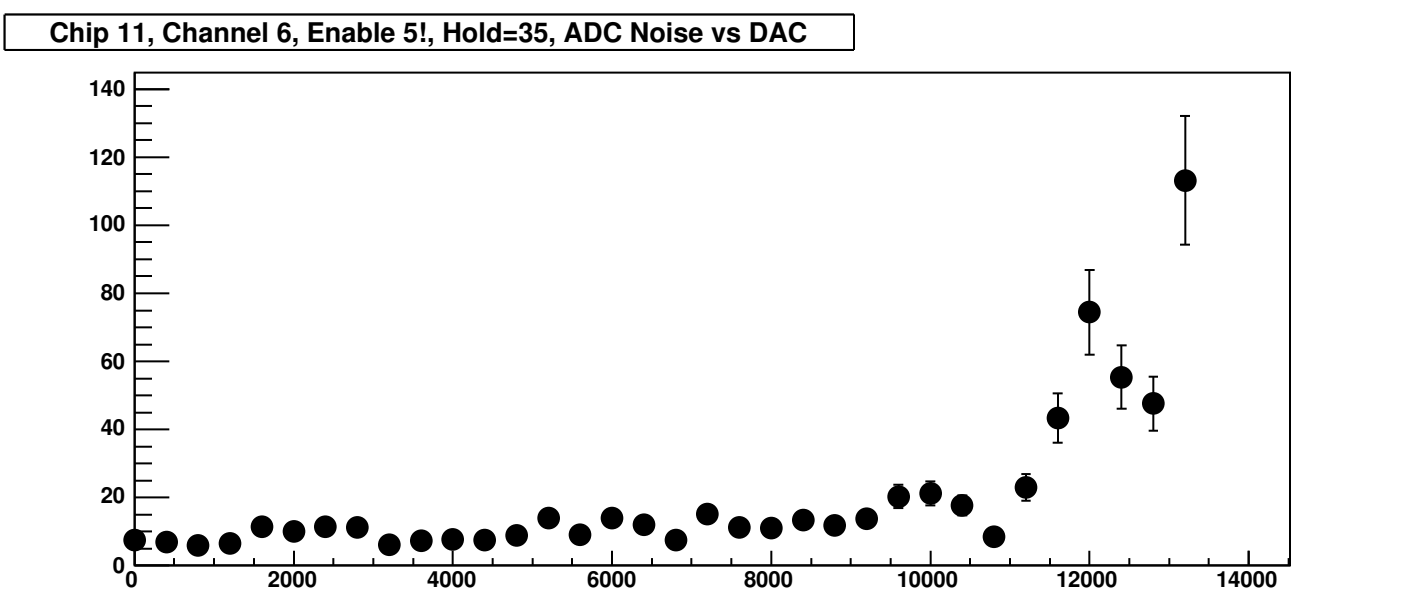

Chip 11, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

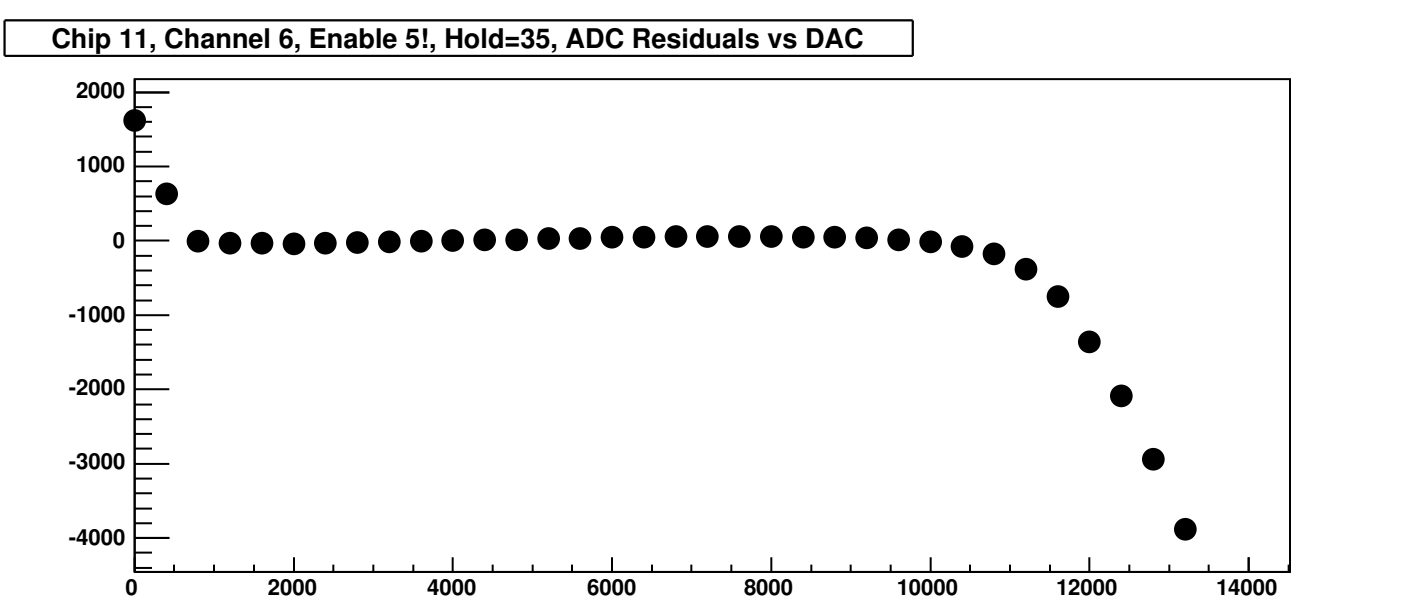

Chip 11, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 53.86 / 23
p0 -465.5 ± 0.7909
p1 0.03127 ± 0.0001131
-50
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

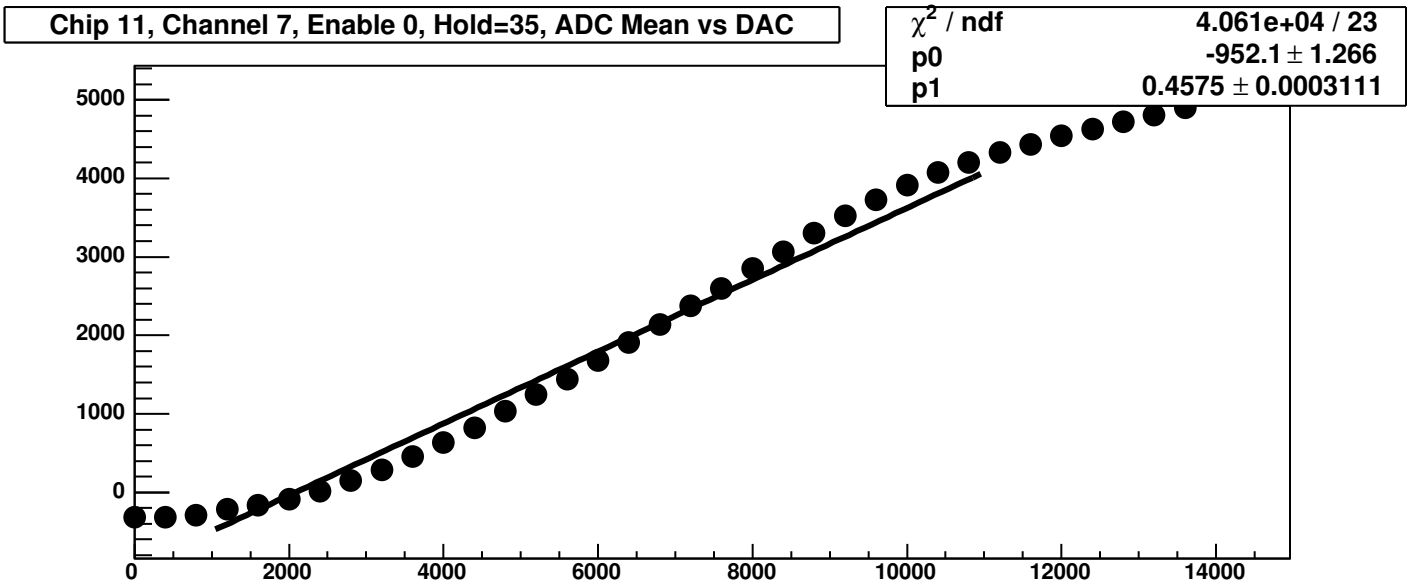

Chip 11, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

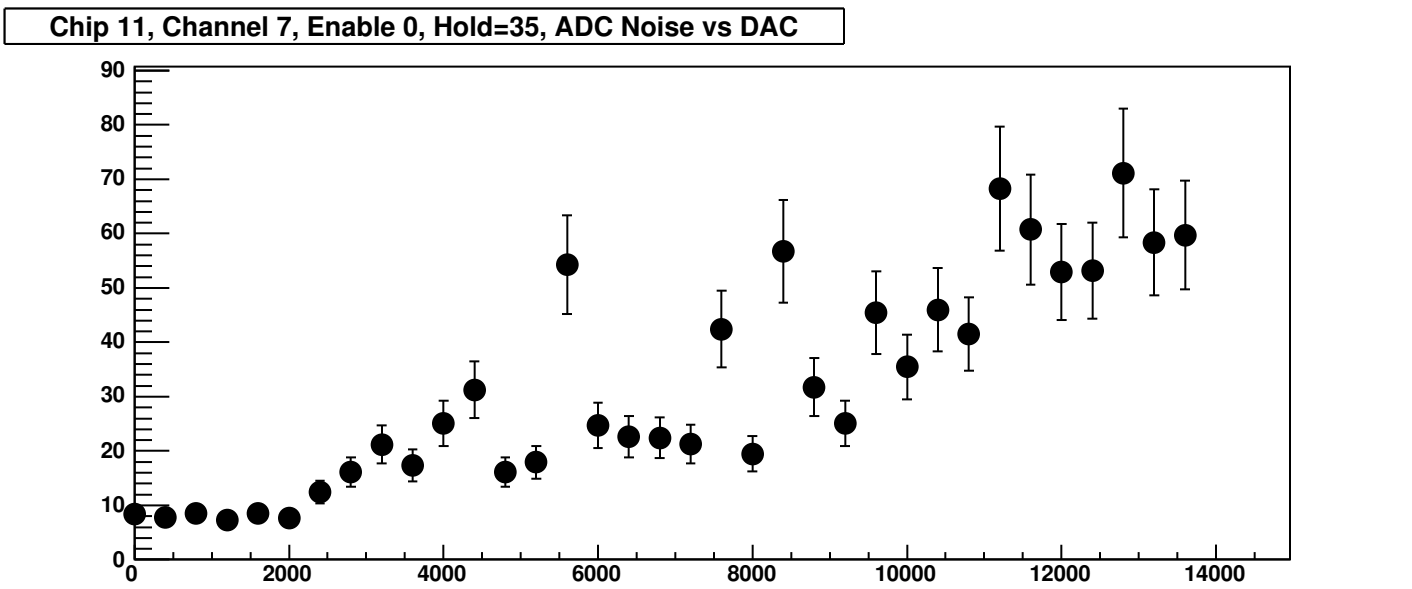

Chip 11, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

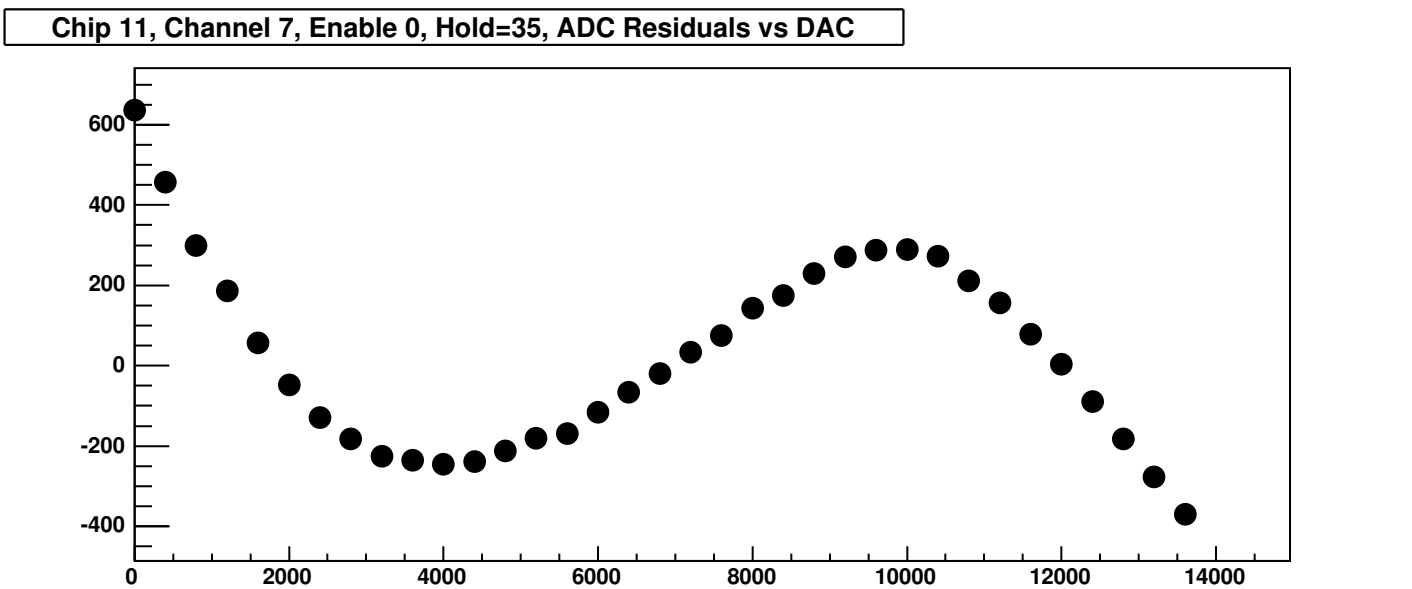

Chip 11, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1.371e+04 / 23
p0 -544.1 ± 0.9596
p1 2.504 ± 0.0001671
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

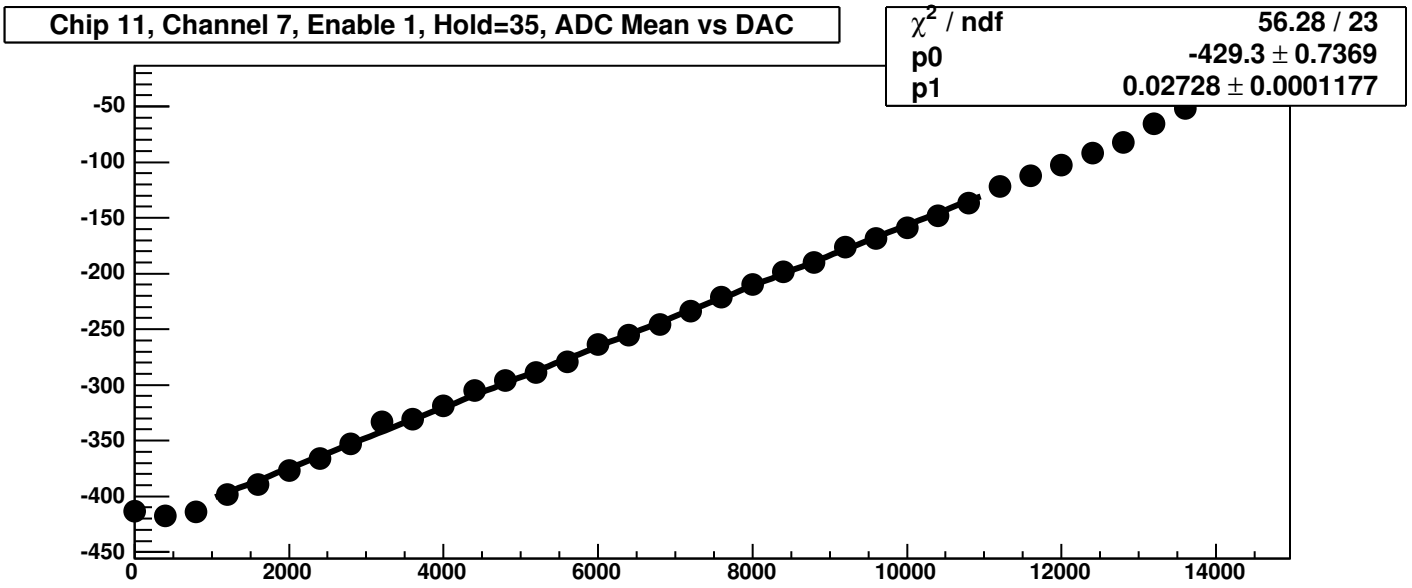

Chip 11, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

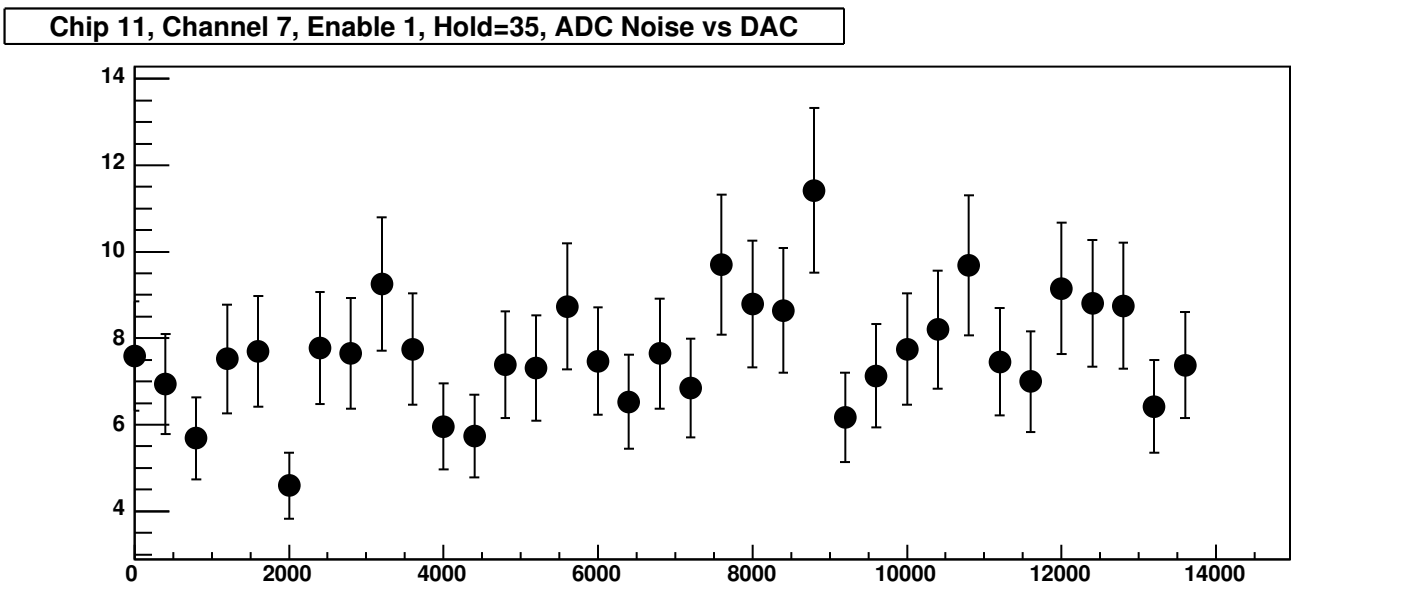

Chip 11, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

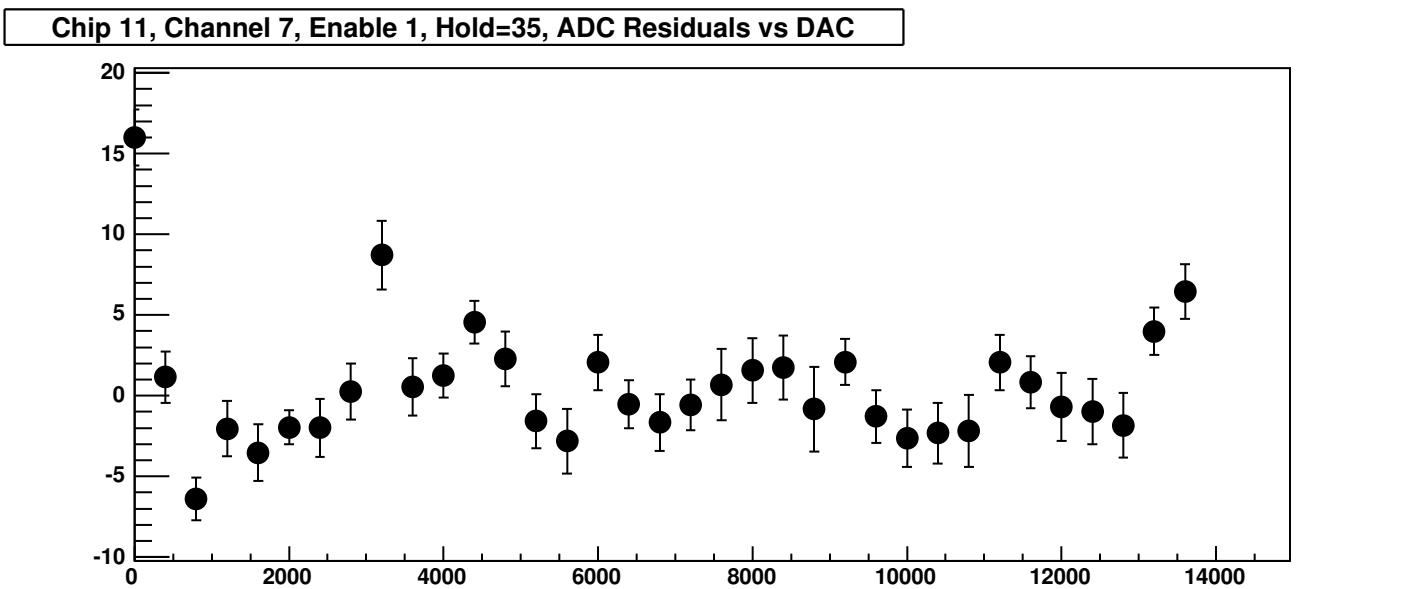

Chip 11, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.061e+04 / 23
p0 -952.1 ± 1.266
p1 0.4575 ± 0.0003111
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

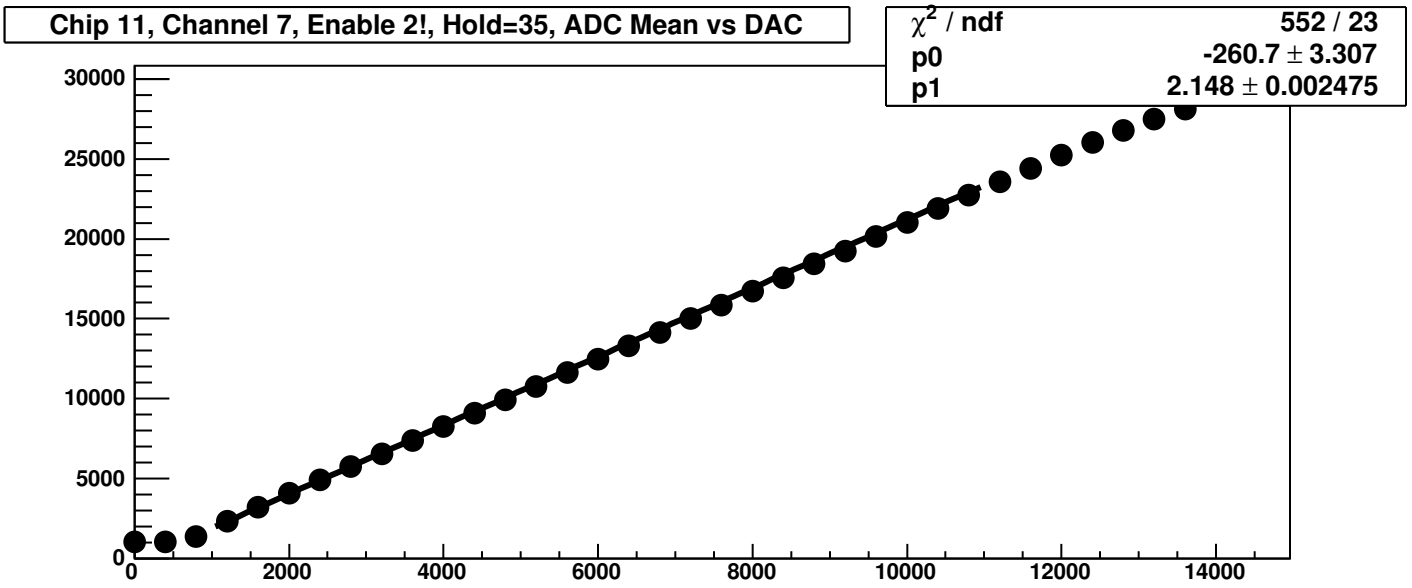

Chip 11, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

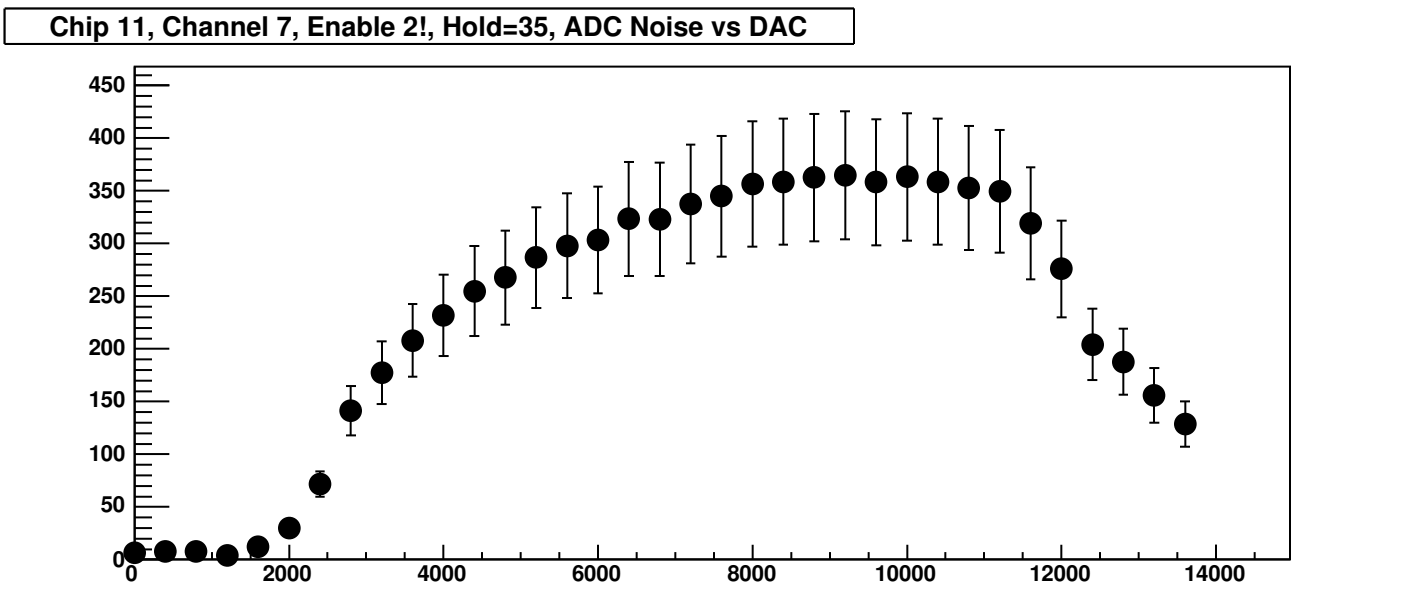

Chip 11, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

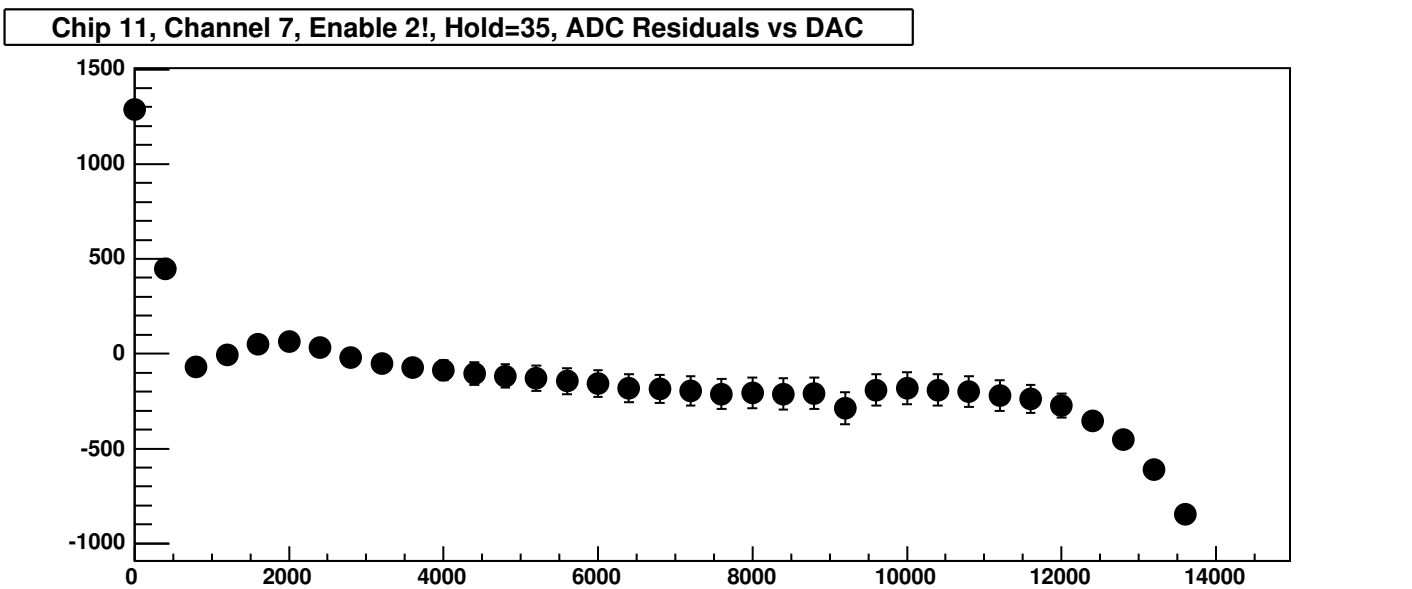

Chip 11, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 56.28 / 23
p0 -429.3 ± 0.7369
p1 0.02728 ± 0.0001177
-50
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

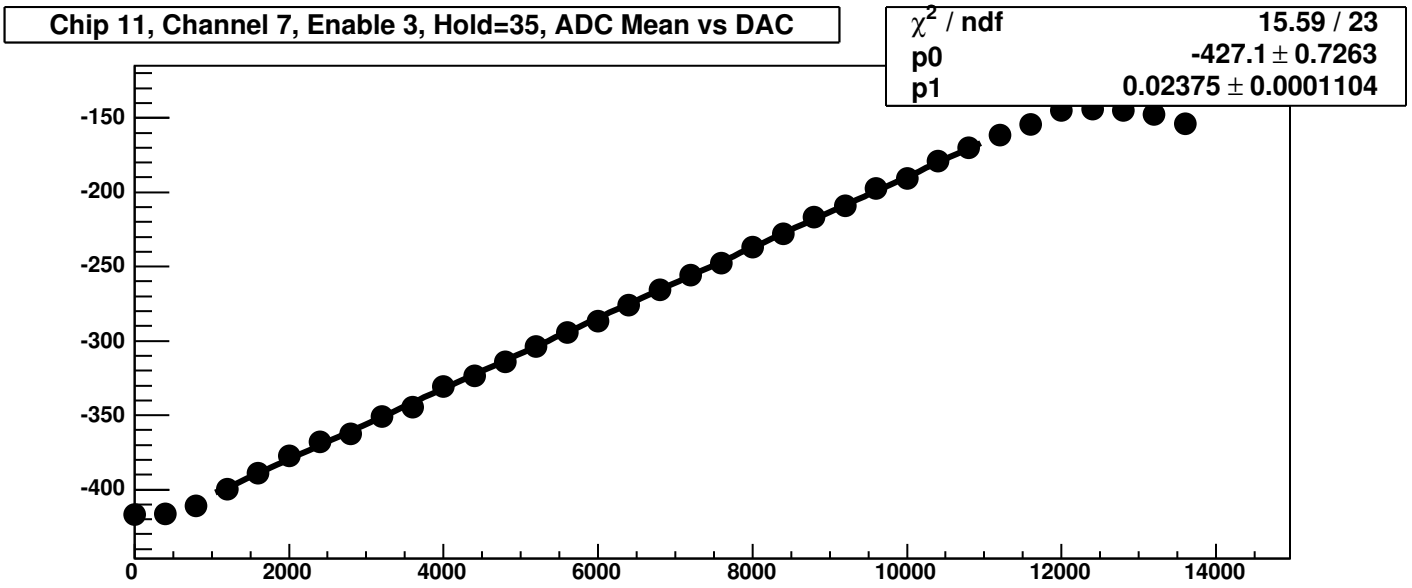

Chip 11, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

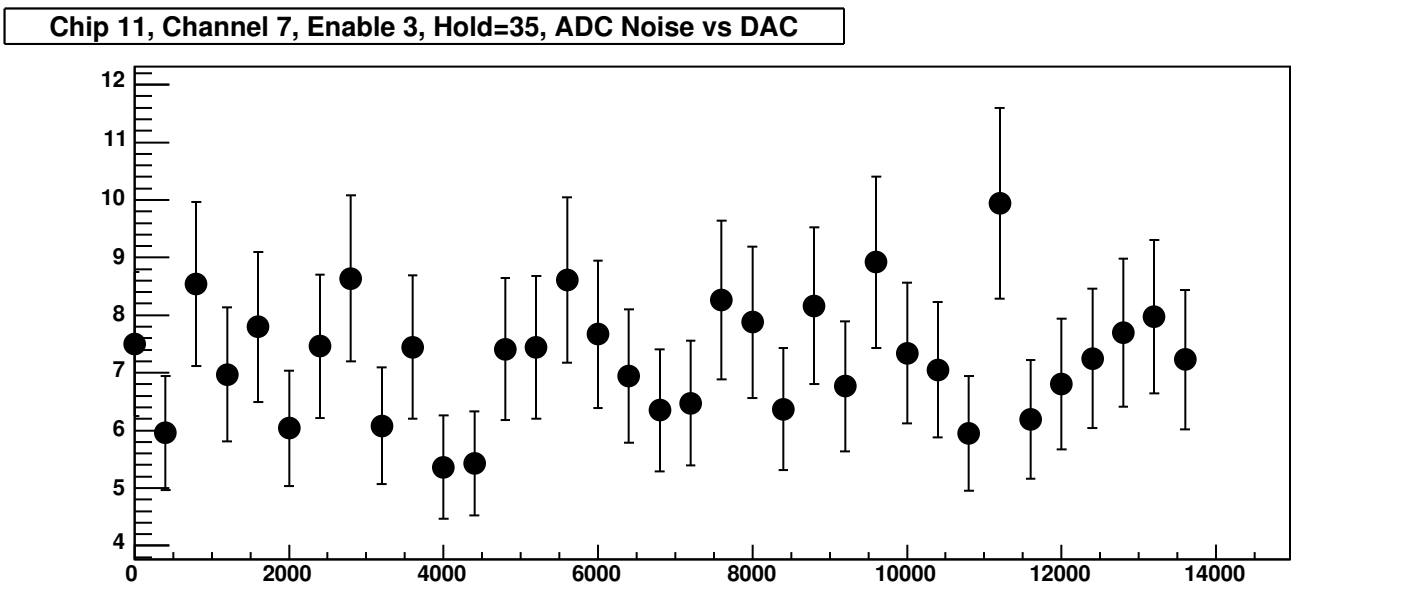

Chip 11, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

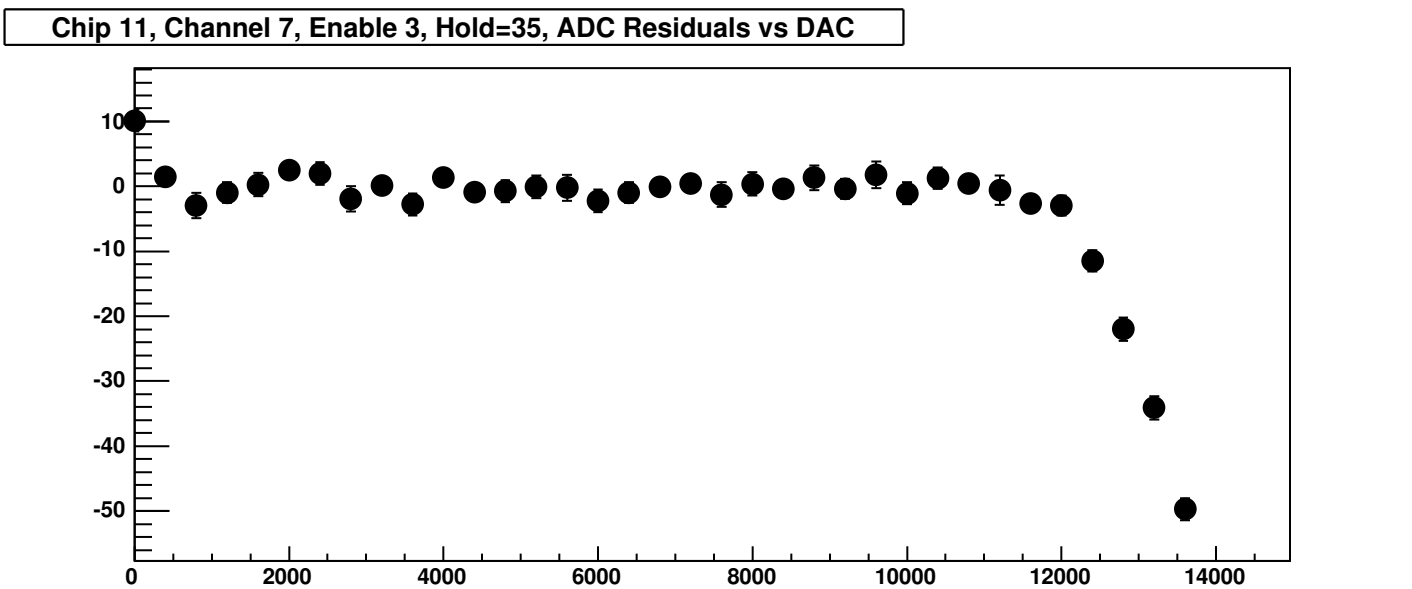

Chip 11, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 552 / 23
p0 -260.7 ± 3.307
p1 2.148 ± 0.002475
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

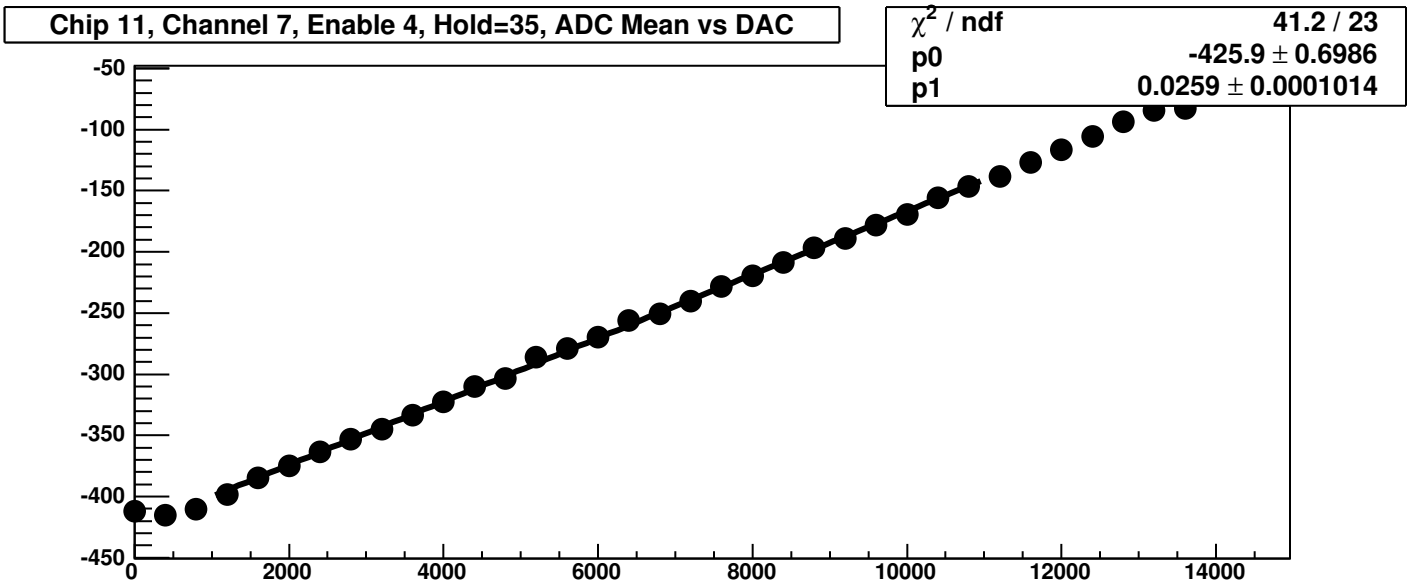

Chip 11, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

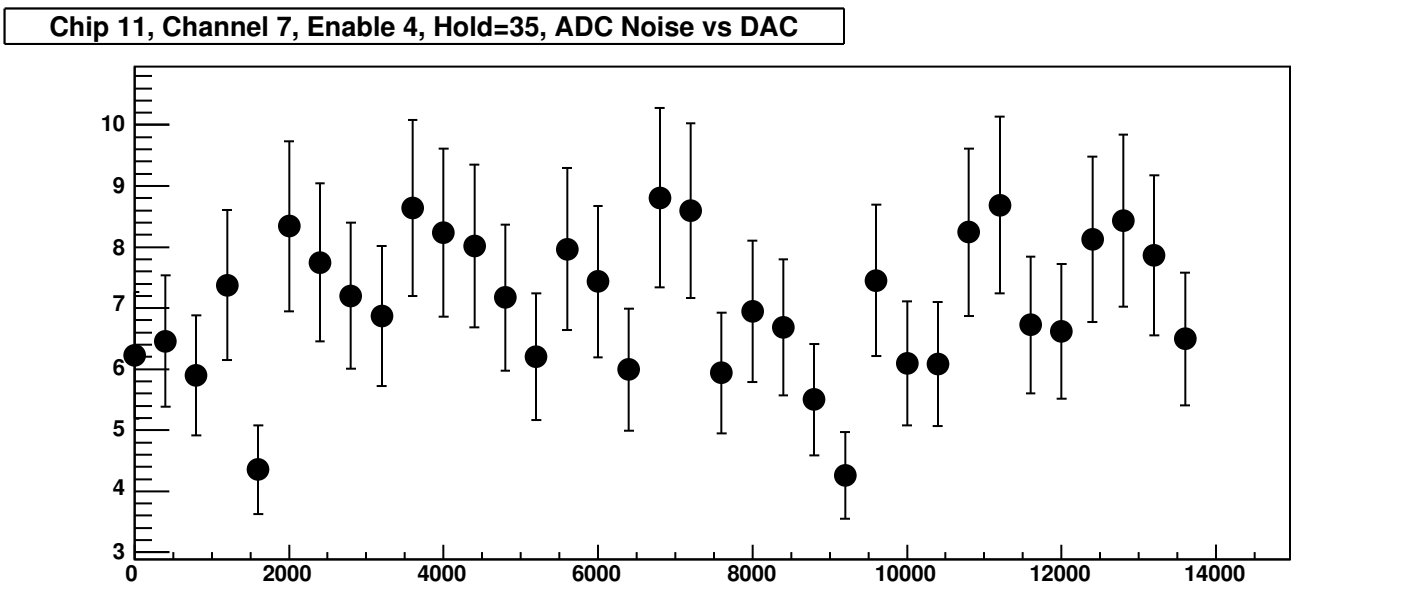

Chip 11, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

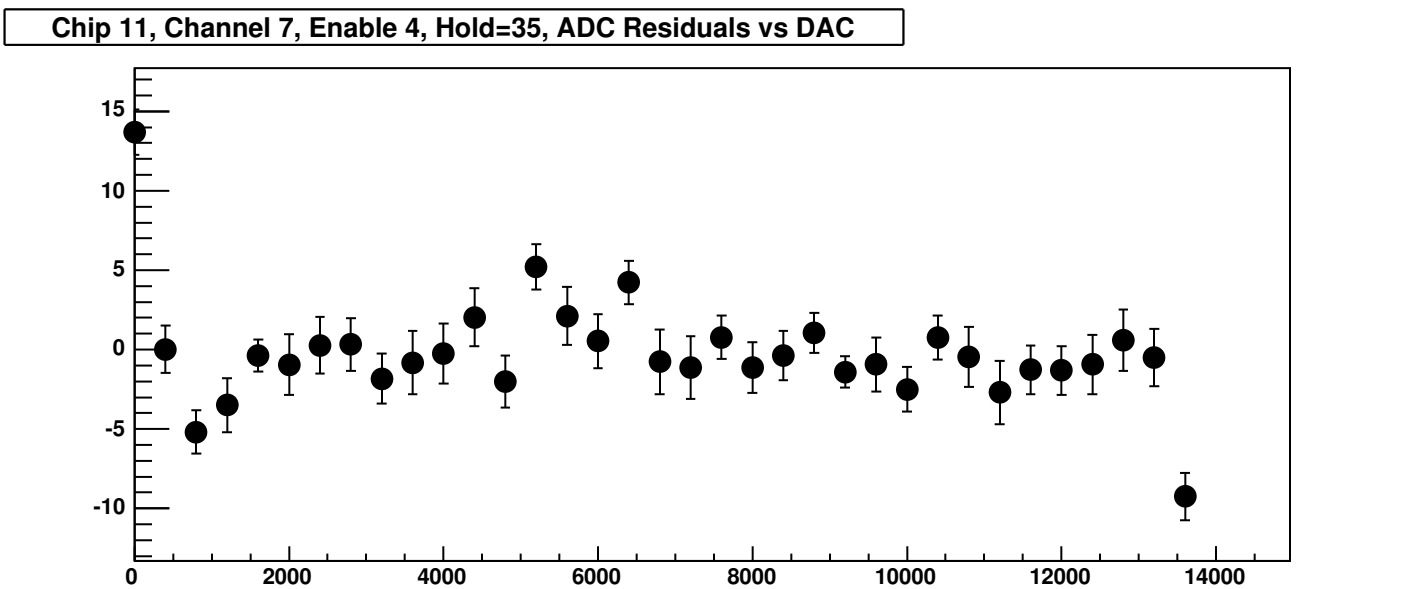

Chip 11, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 15.59 / 23
p0 -427.1 ± 0.7263
p1 0.02375 ± 0.0001104
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

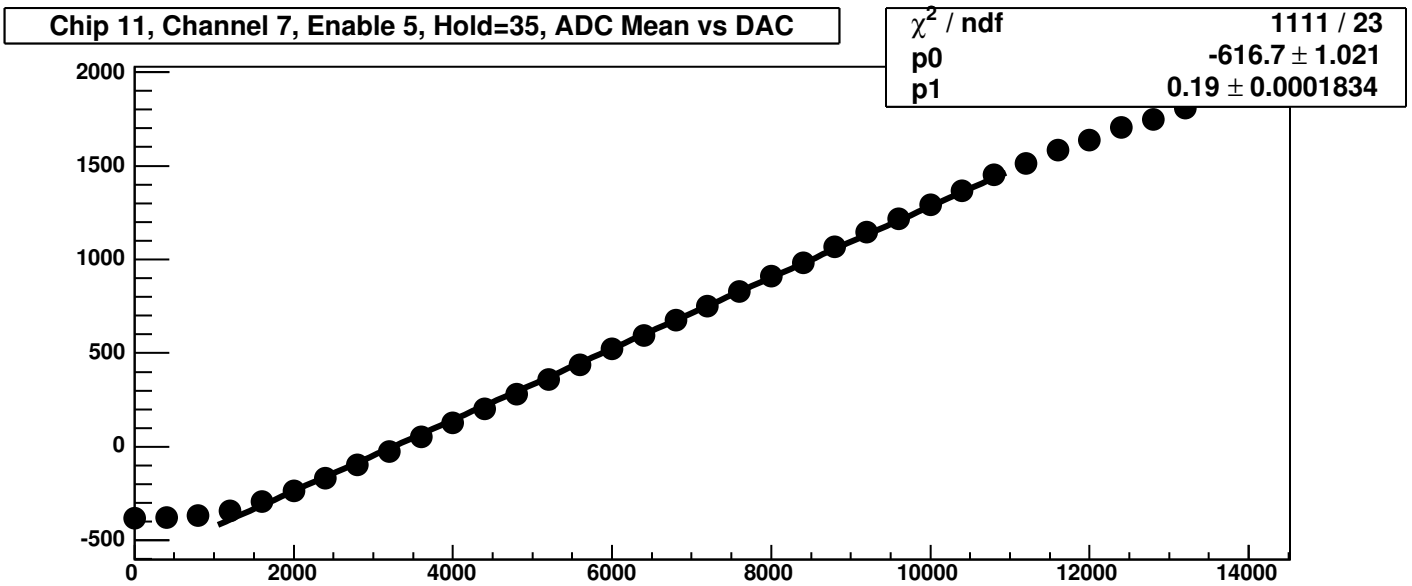

Chip 11, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

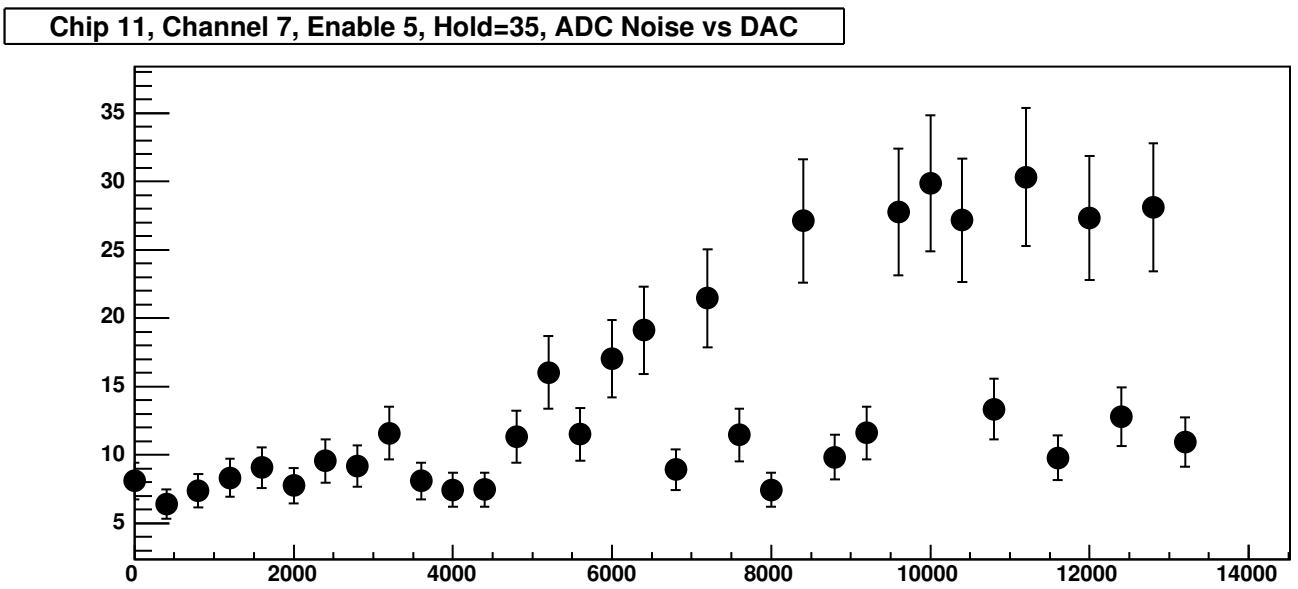

Chip 11, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

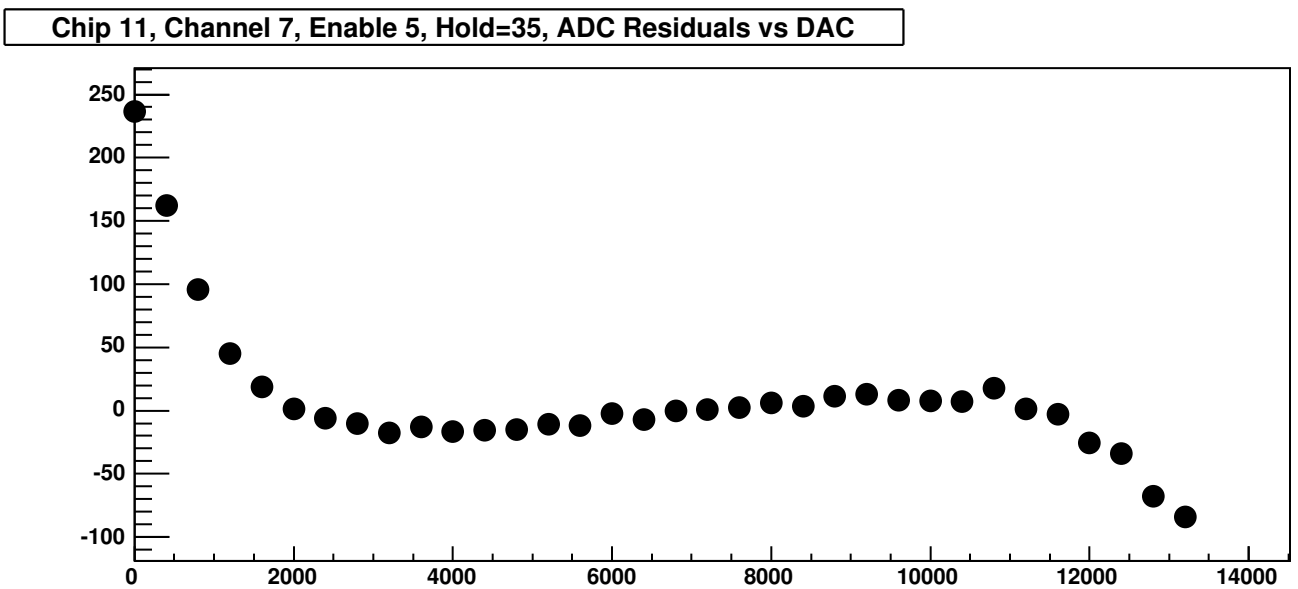

Chip 11, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 41.2 / 23
p0 -425.9 ± 0.6986
p1 0.0259 ± 0.0001014
-50
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

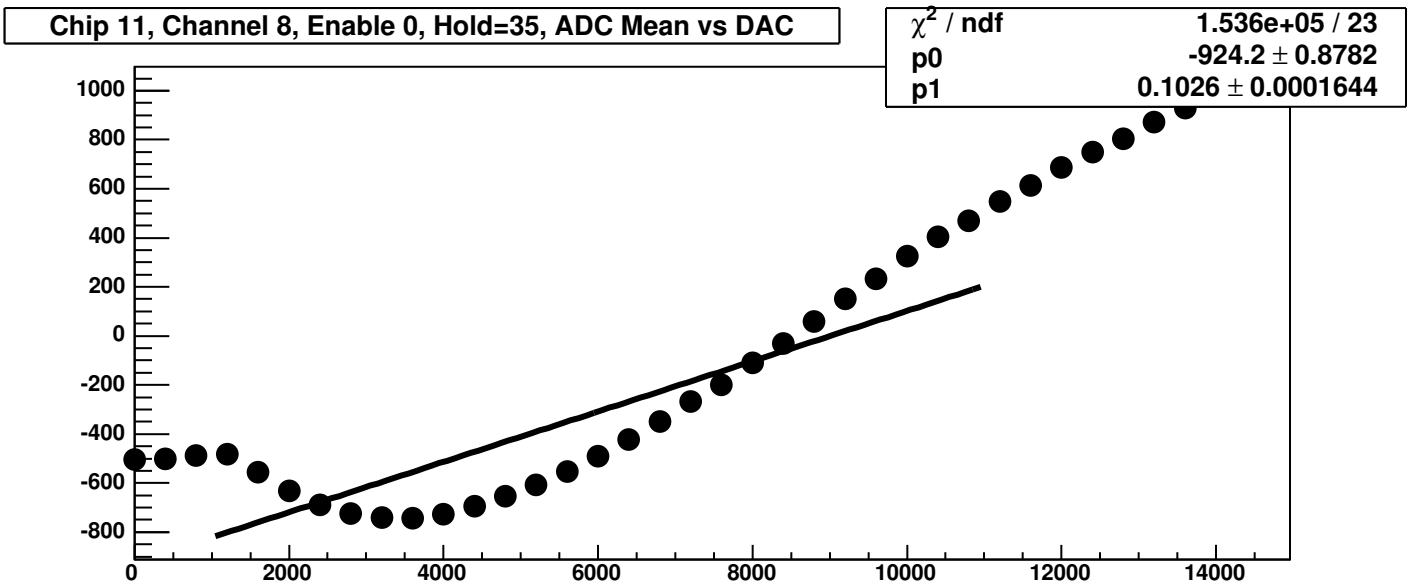

Chip 11, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

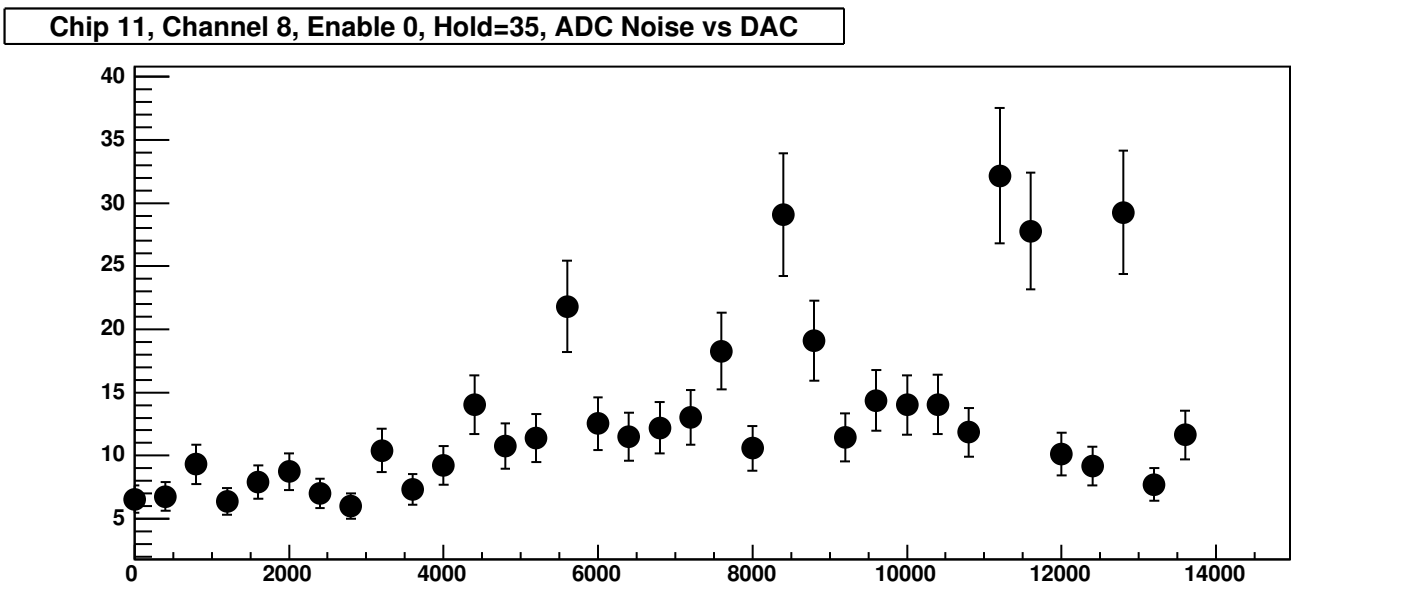

Chip 11, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

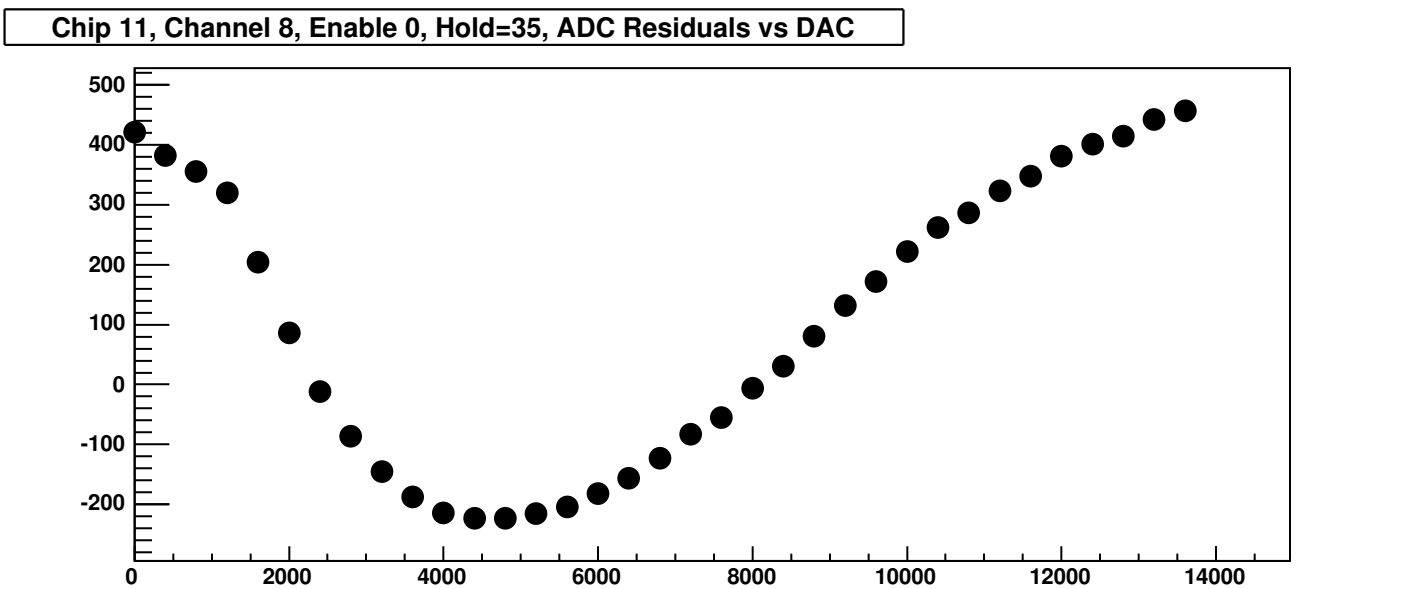

Chip 11, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1111 / 23
p0 -616.7 ± 1.021
p1 0.19 ± 0.0001834
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

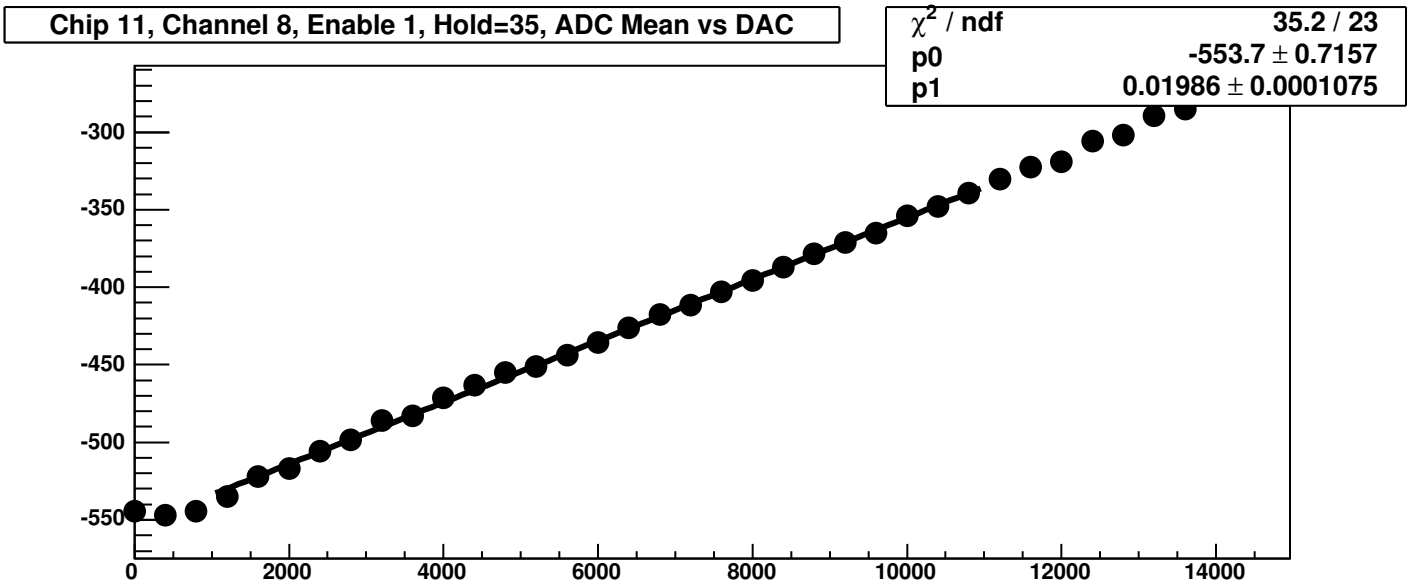

Chip 11, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

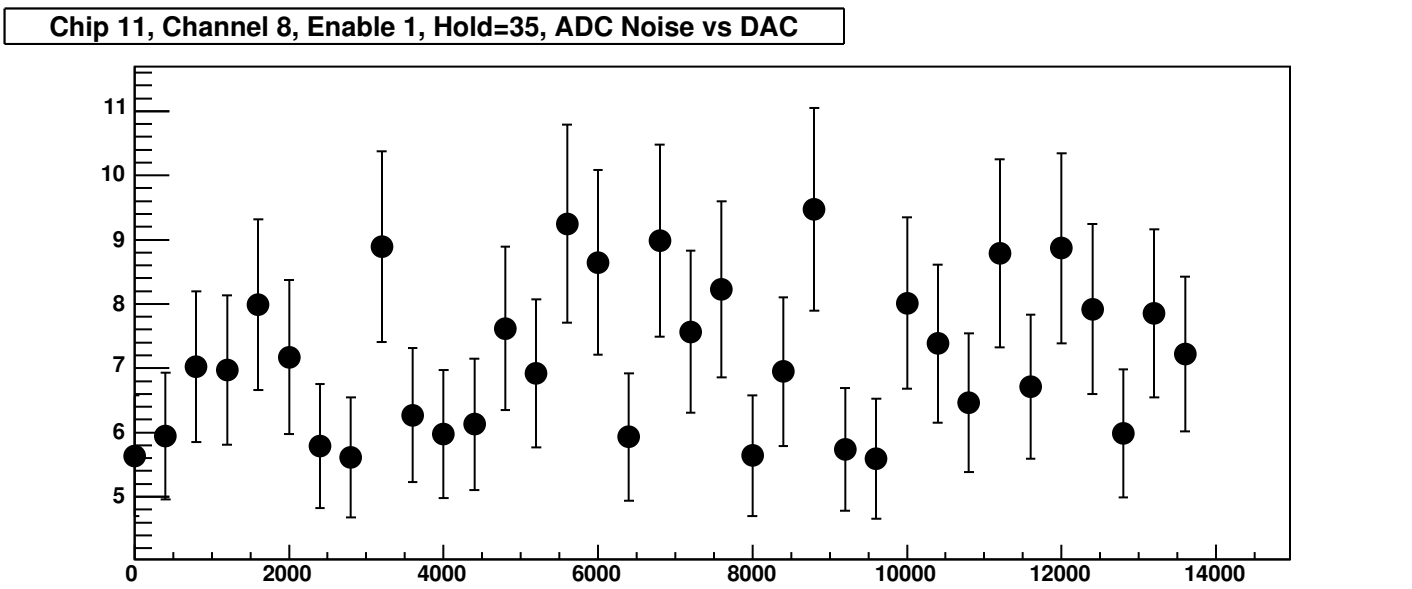

Chip 11, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

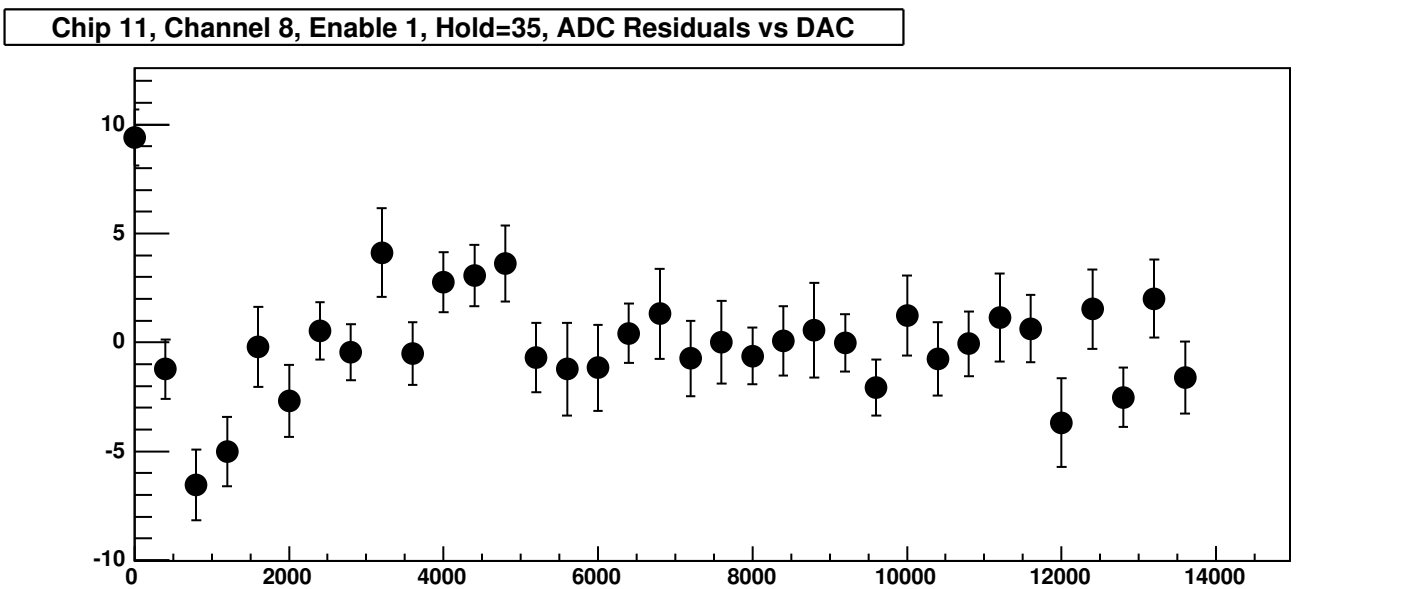

Chip 11, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.536e+05 / 23
p0 -924.2 ± 0.8782
p1 0.1026 ± 0.0001644
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

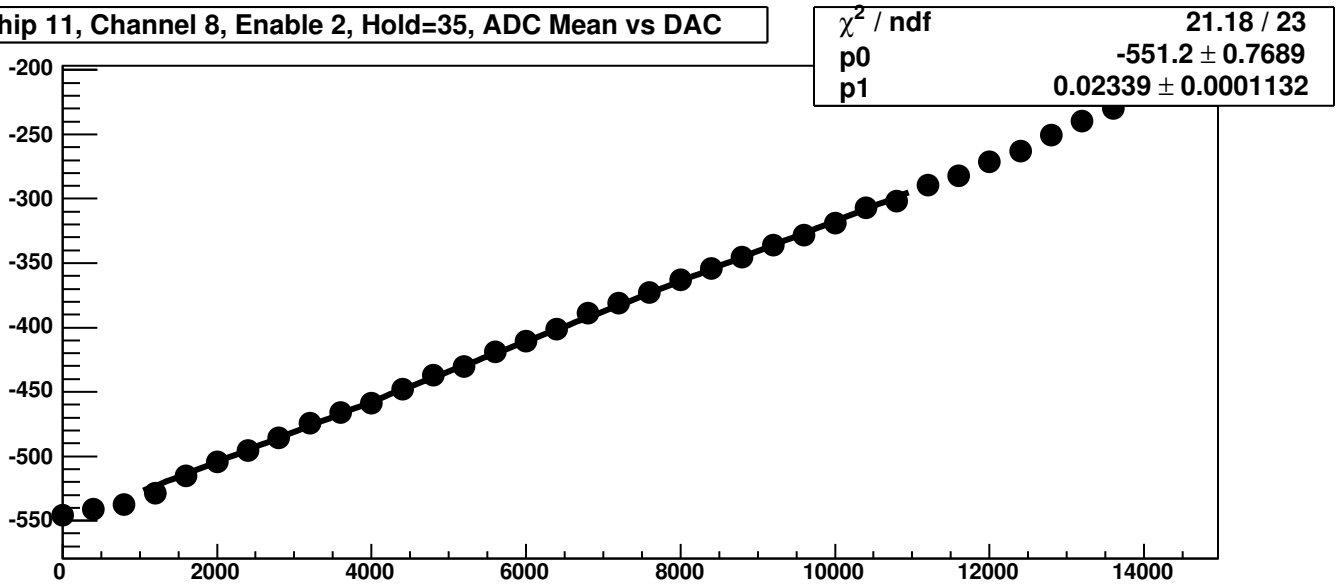

Chip 11, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 35.2 / 23
p0 -553.7 ± 0.7157
p1 0.01986 ± 0.0001075
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

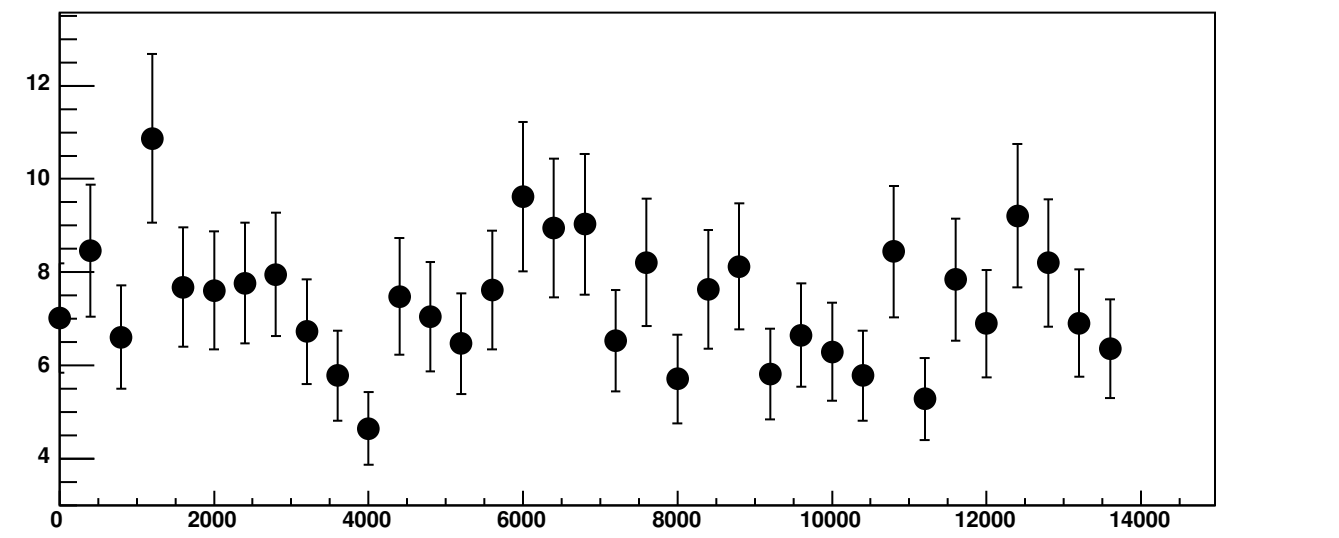

Chip 11, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

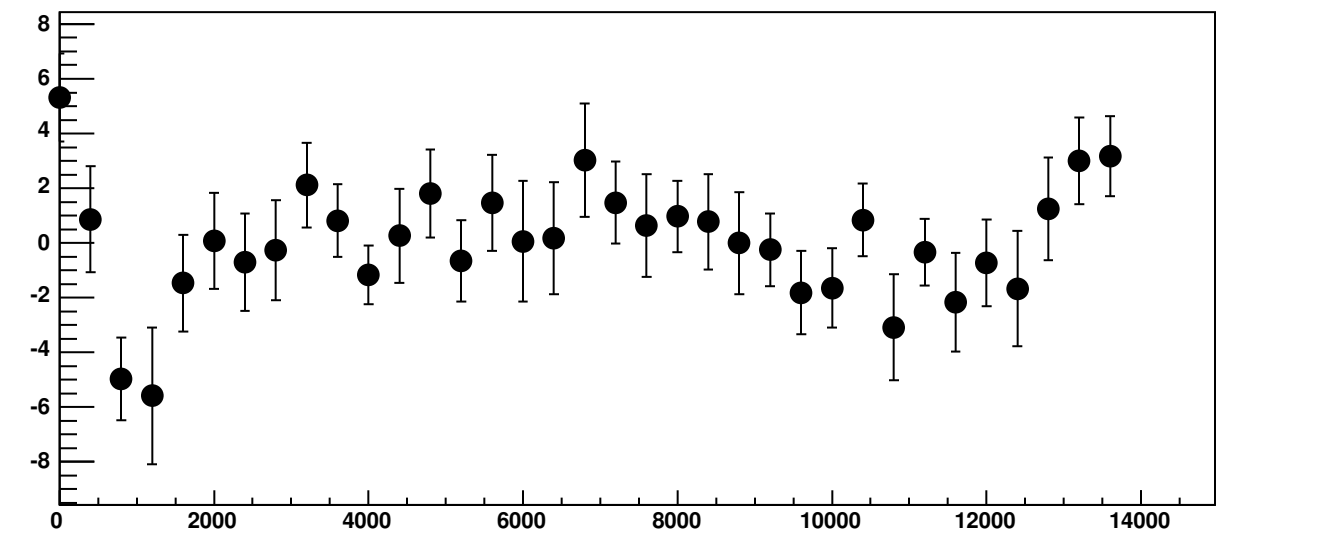
Chip 11, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC

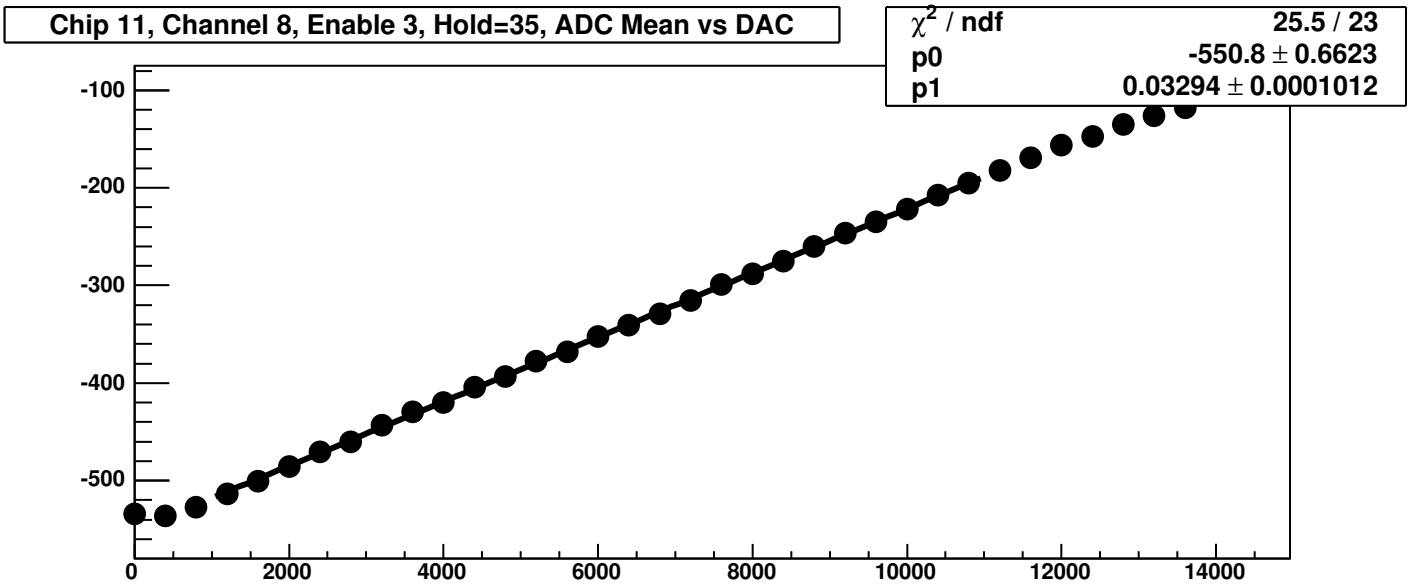


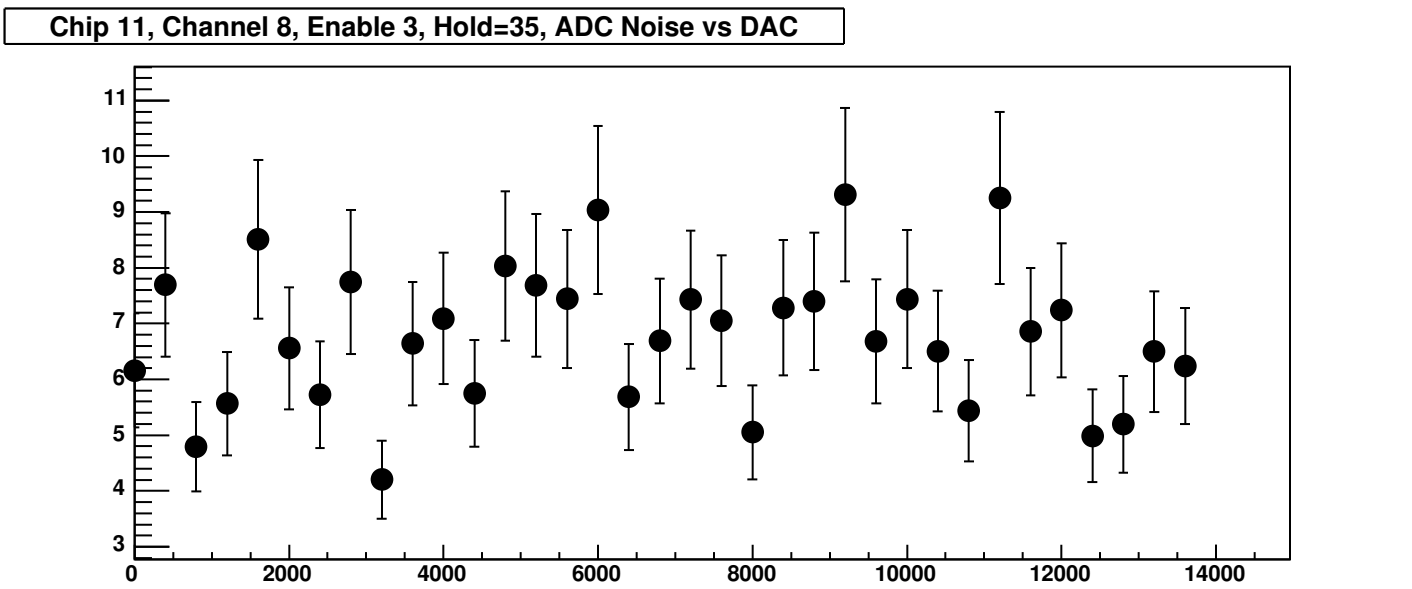
Chip 11, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC

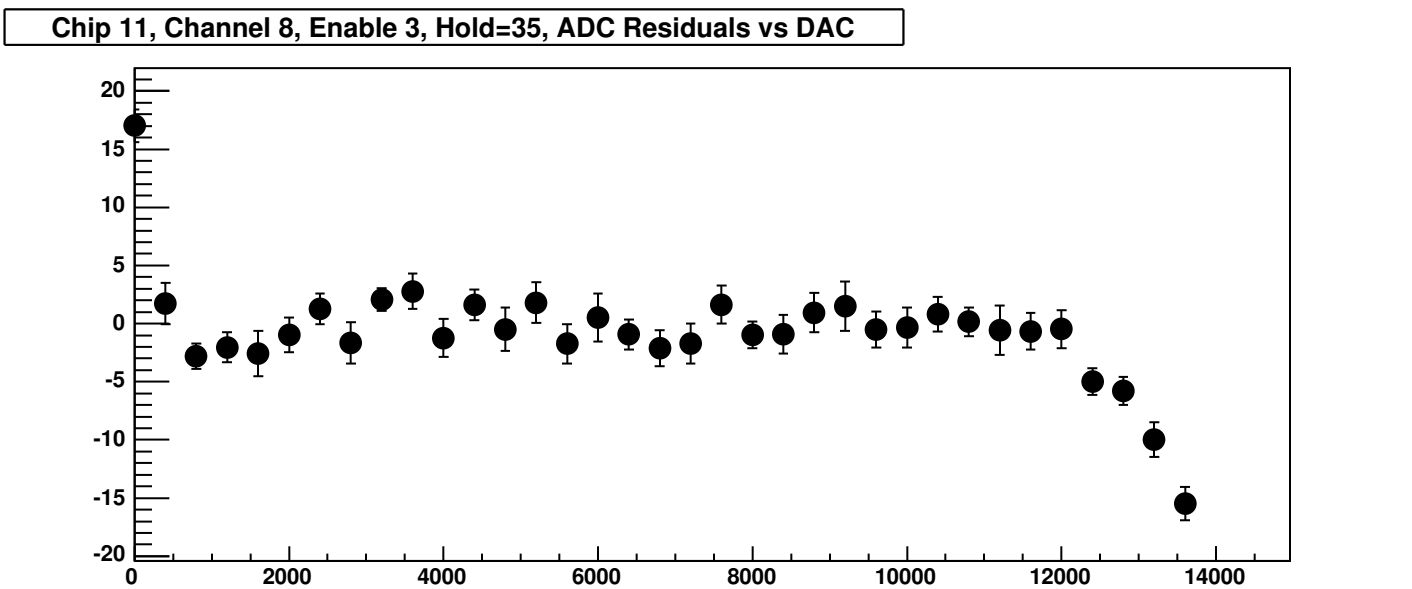


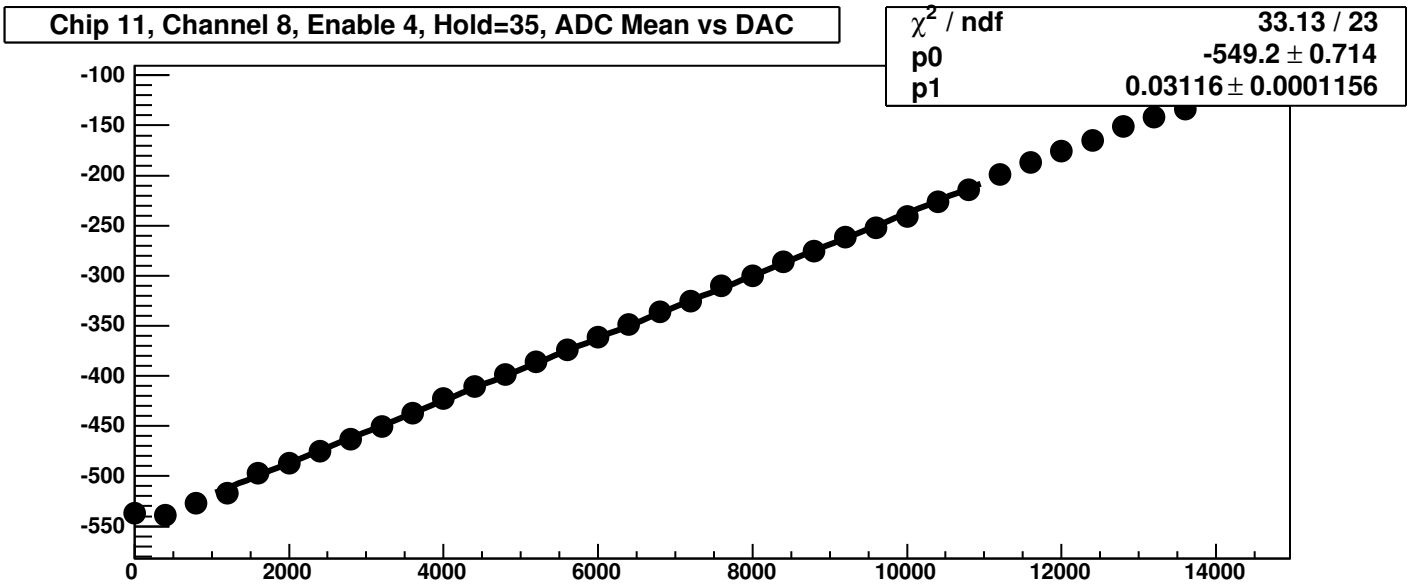
Chip 11, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC

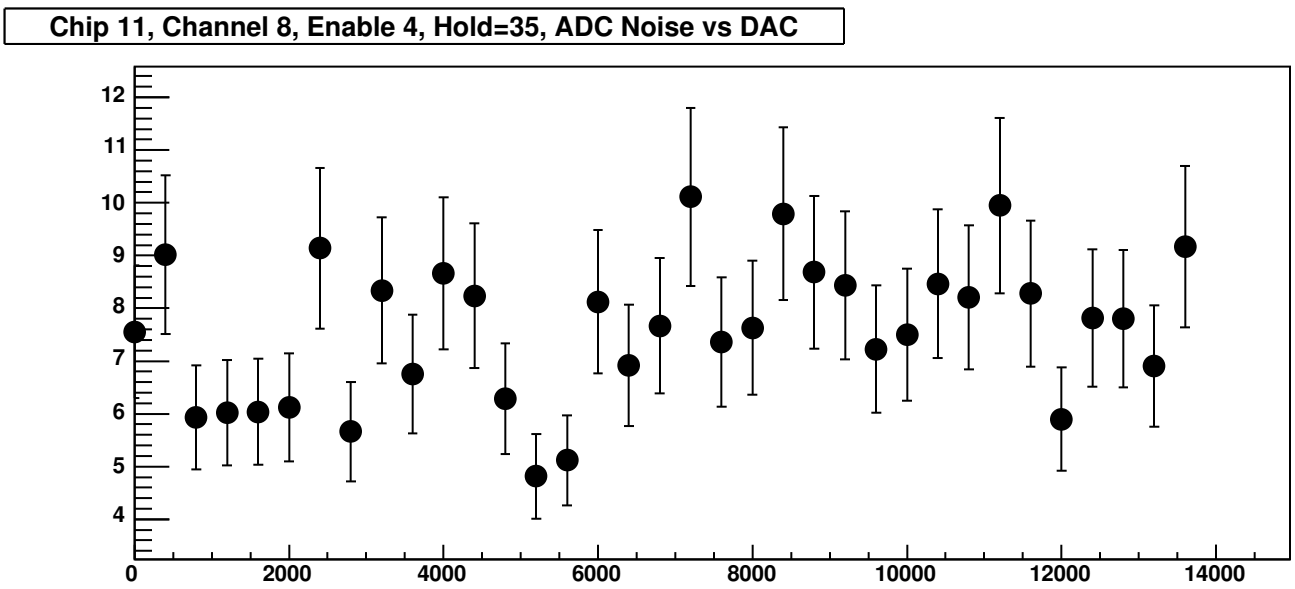


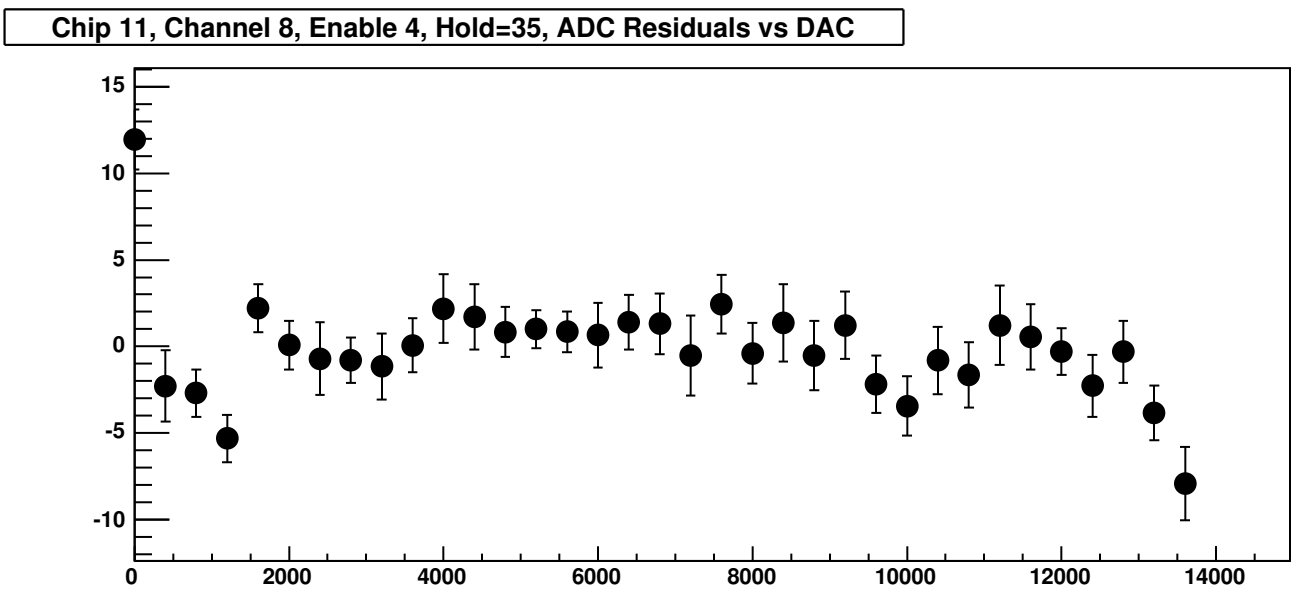

Chip 11, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.5 / 23
p0 -550.8 ± 0.6623
p1 0.03294 ± 0.0001012
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000

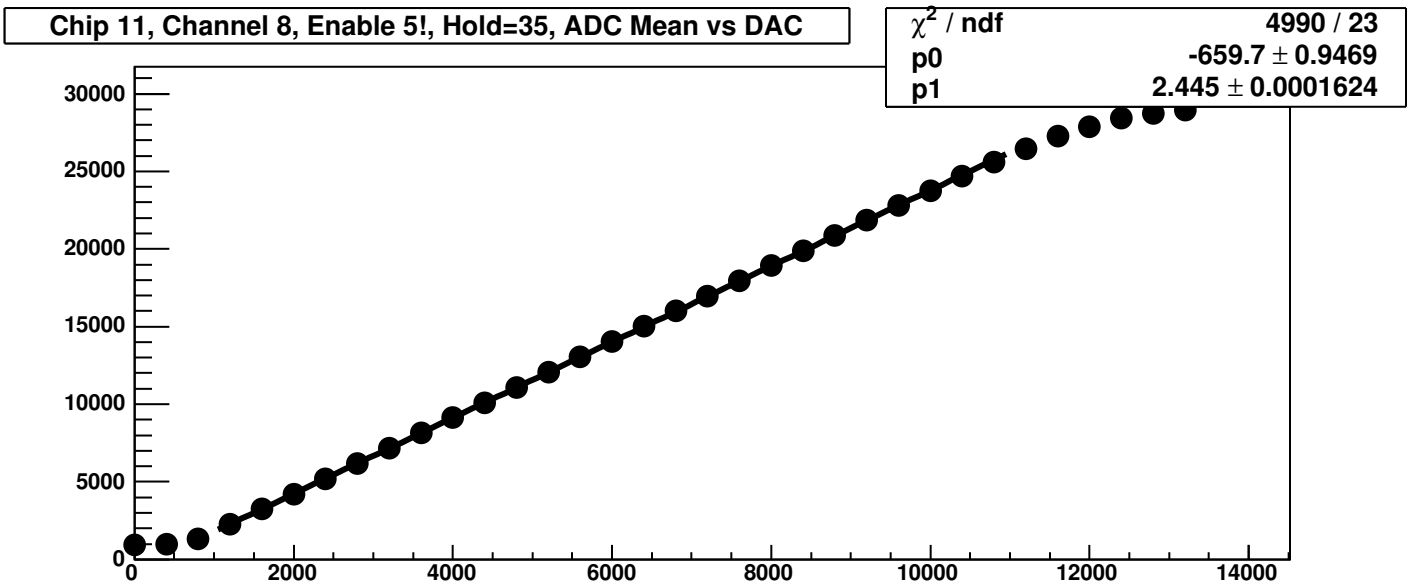

Chip 11, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

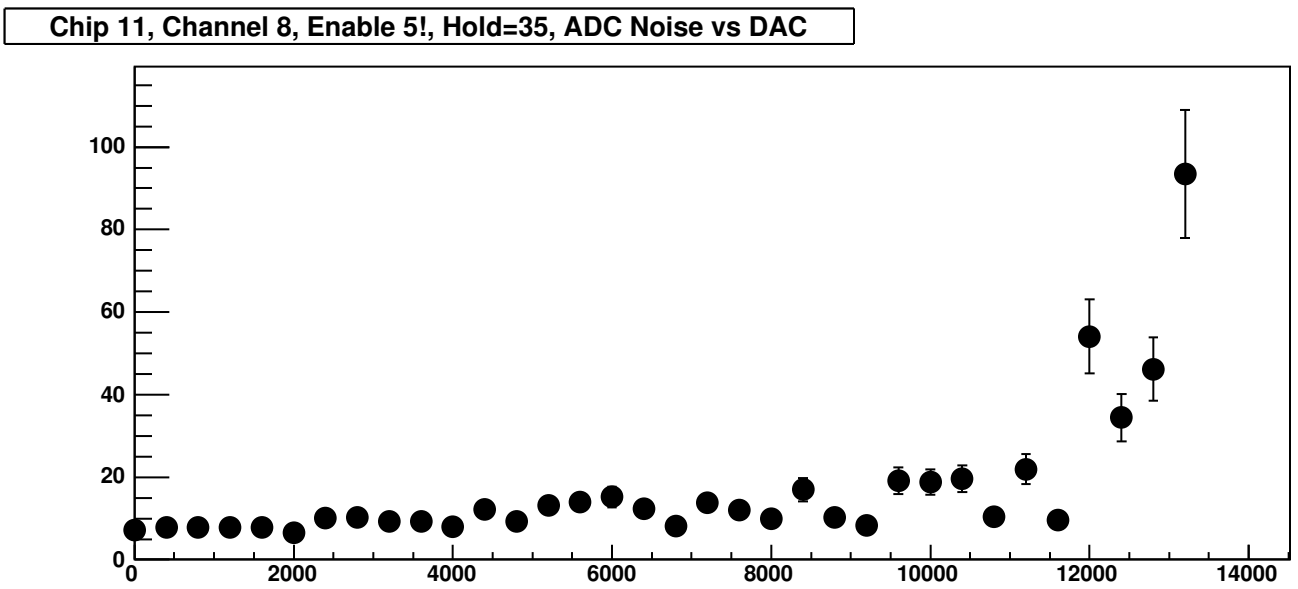

Chip 11, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

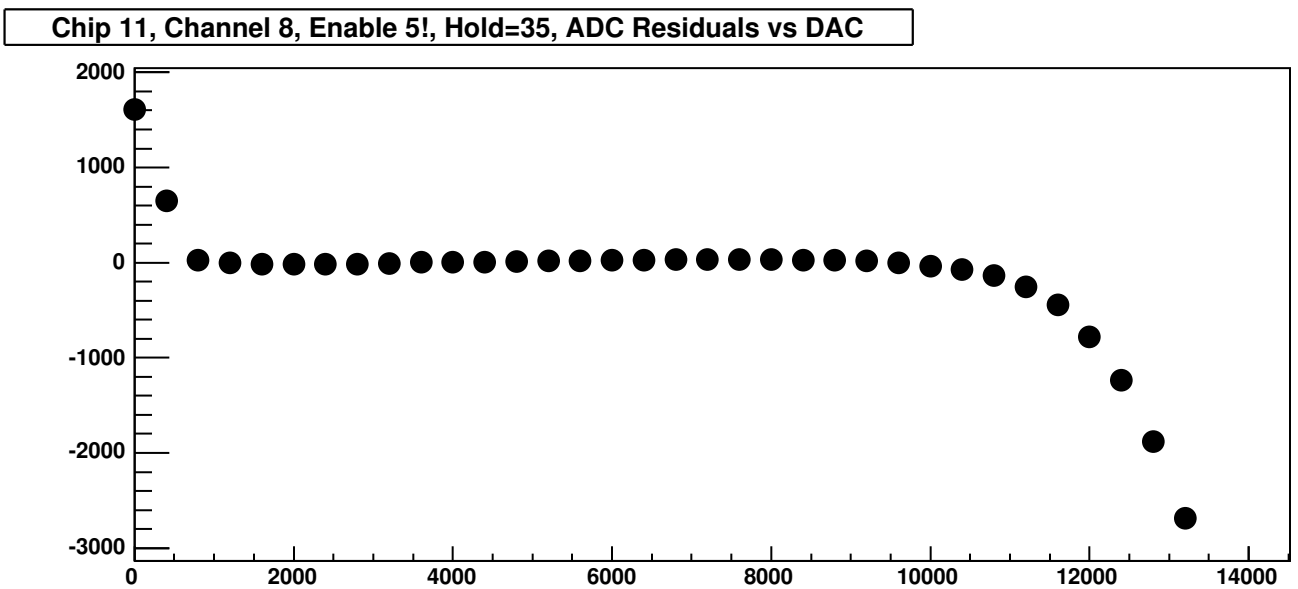

Chip 11, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 33.13 / 23
p0 -549.2 ± 0.714
p1 0.03116 ± 0.0001156
-100
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

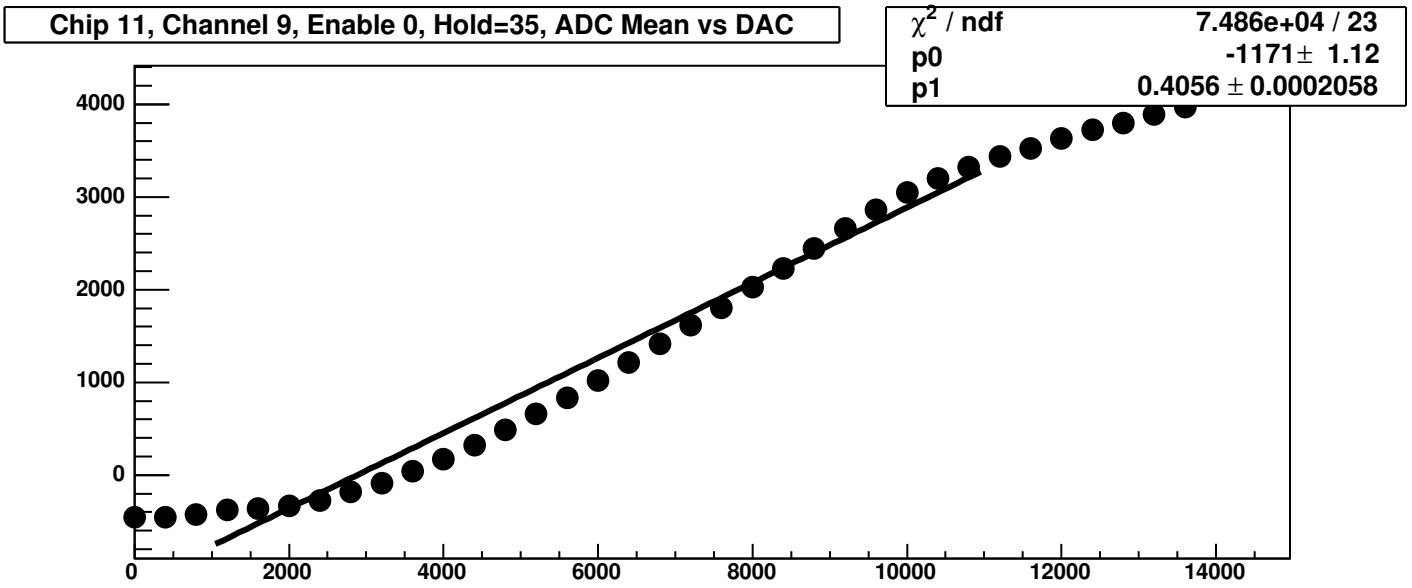

Chip 11, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

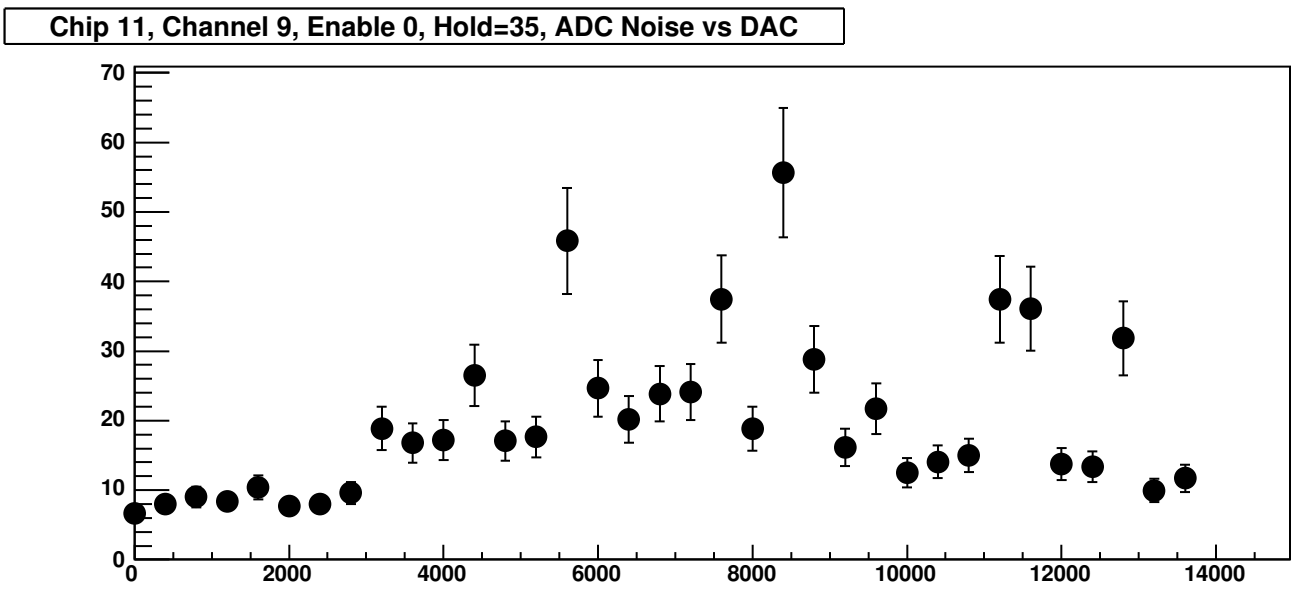

Chip 11, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

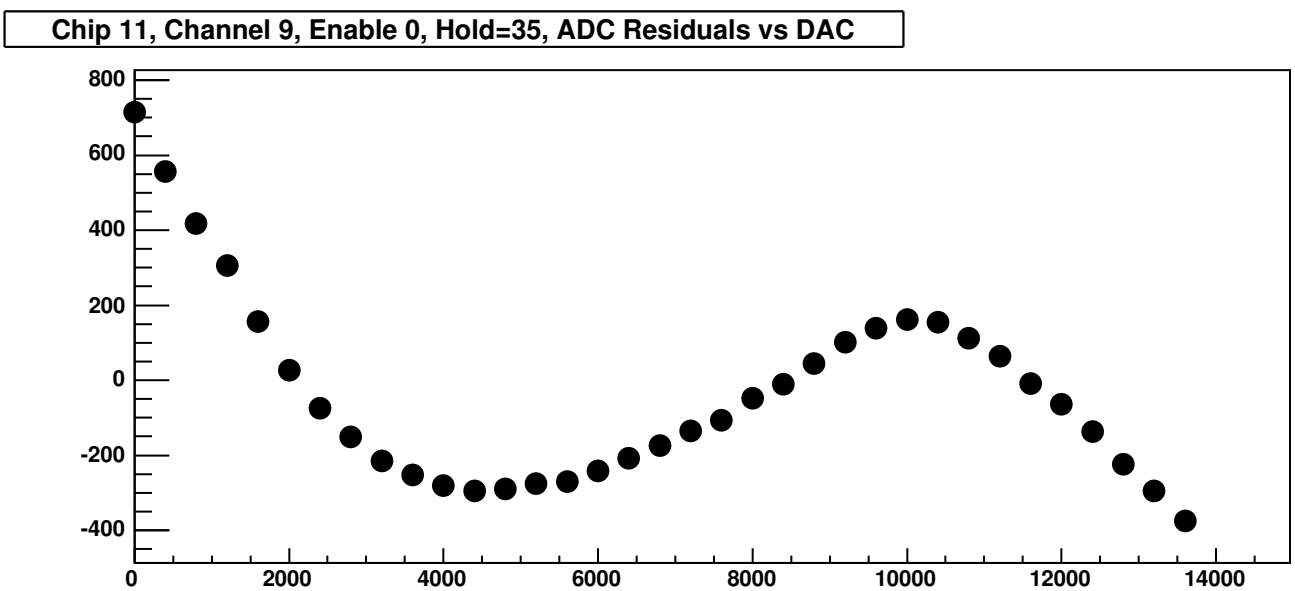

Chip 11, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4990 / 23
p0 -659.7 ± 0.9469
p1 2.445 ± 0.0001624
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

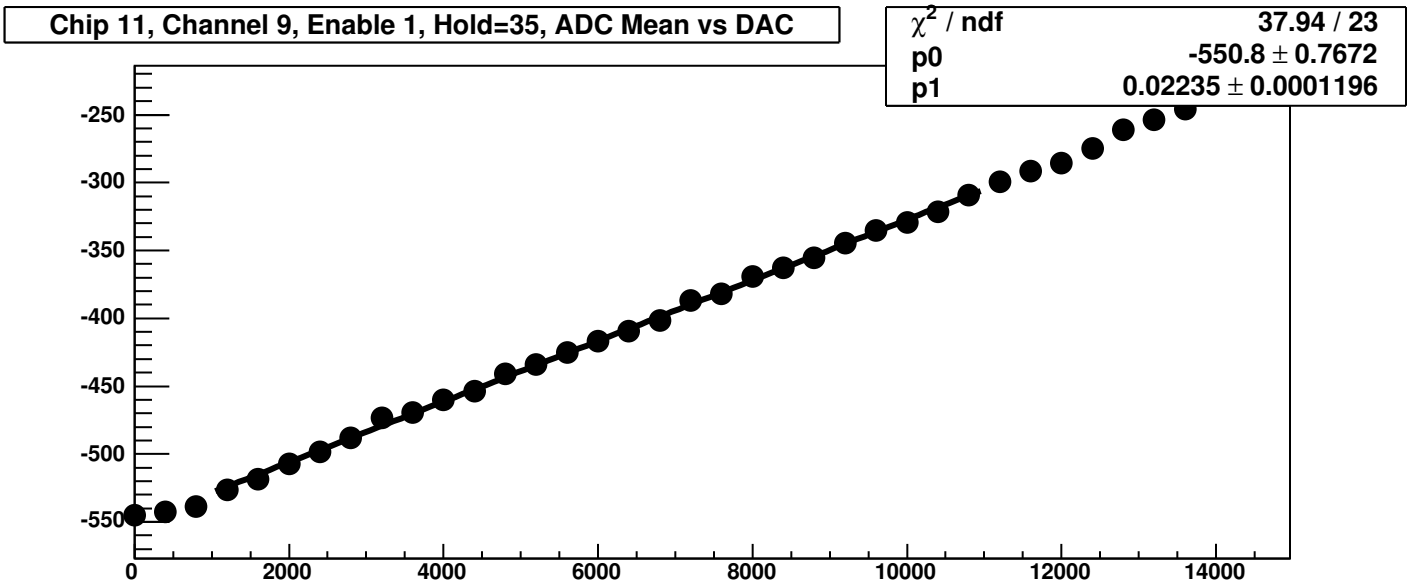

Chip 11, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

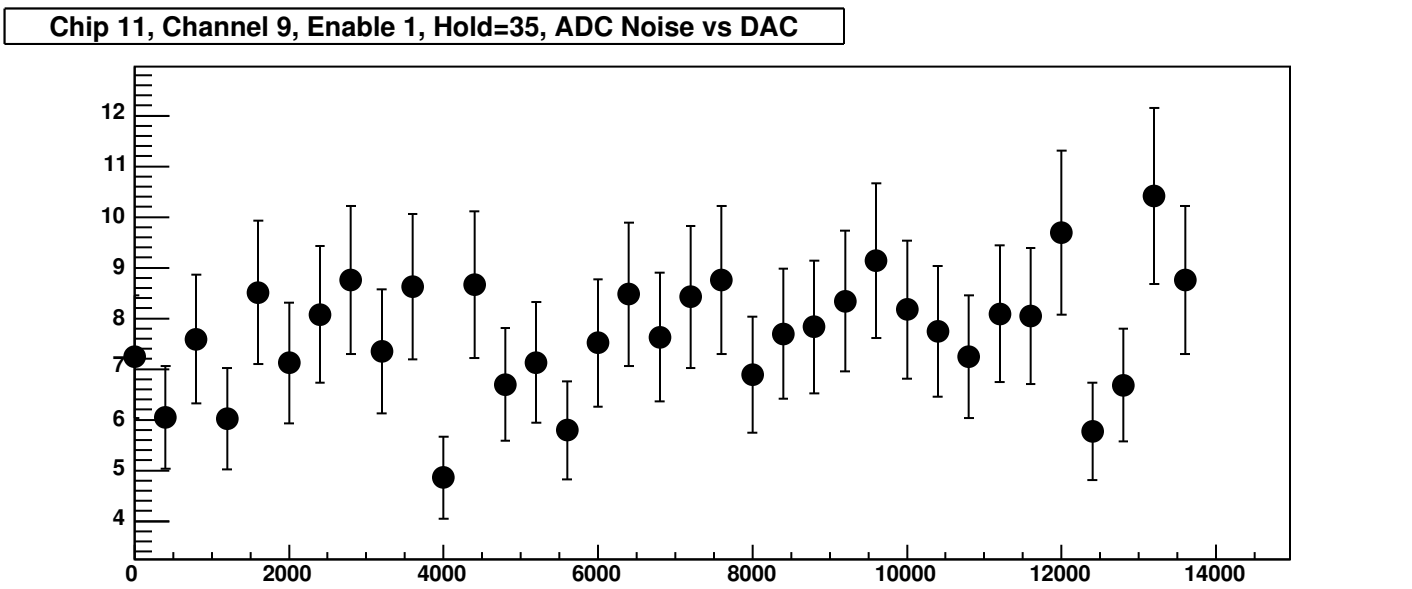

Chip 11, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

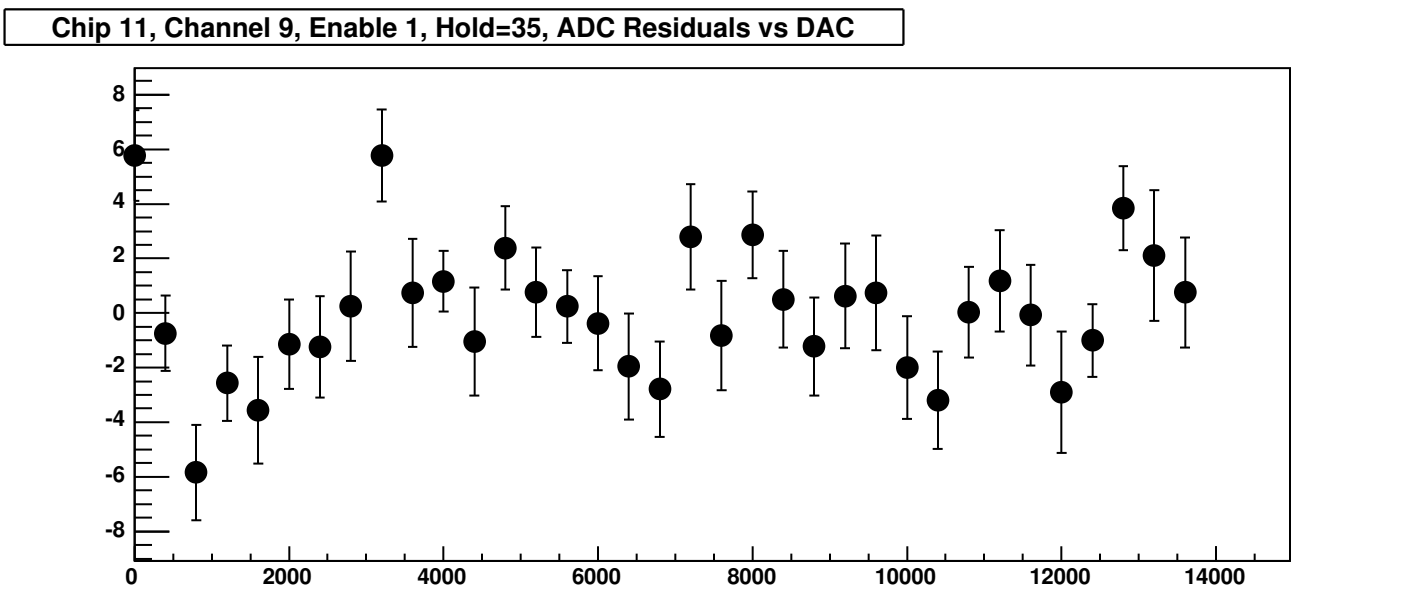

Chip 11, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 7.486e+04 / 23
p0 -1171± 1.12
p1 0.4056 ± 0.0002058
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

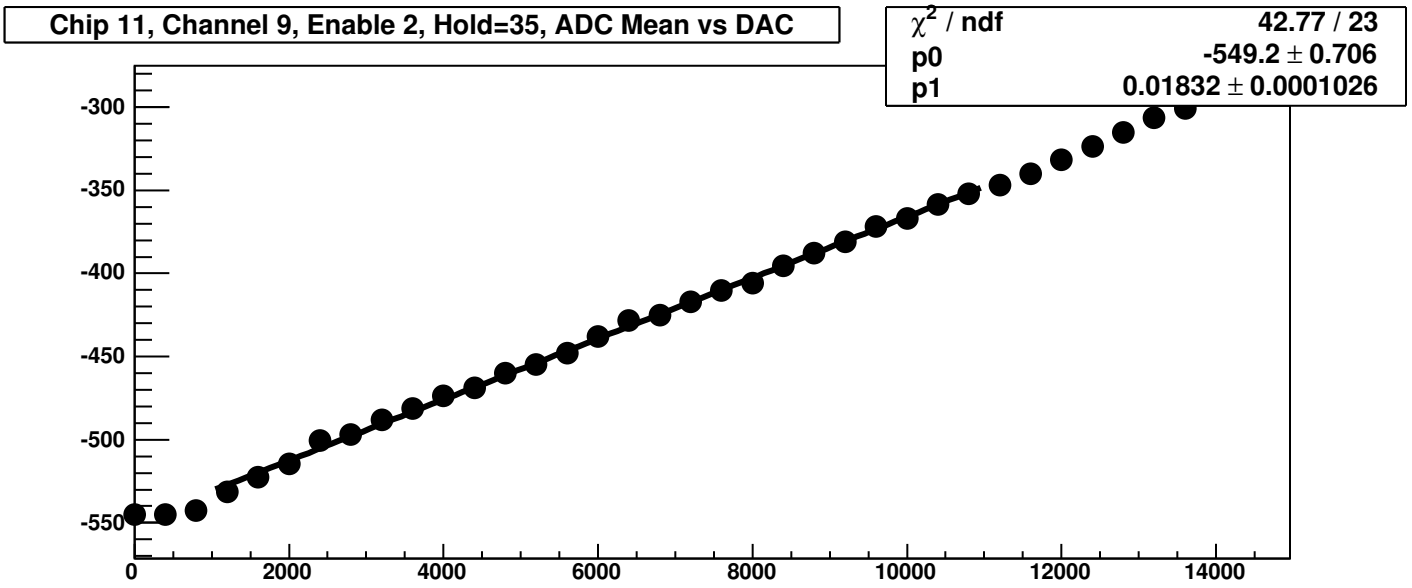

Chip 11, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

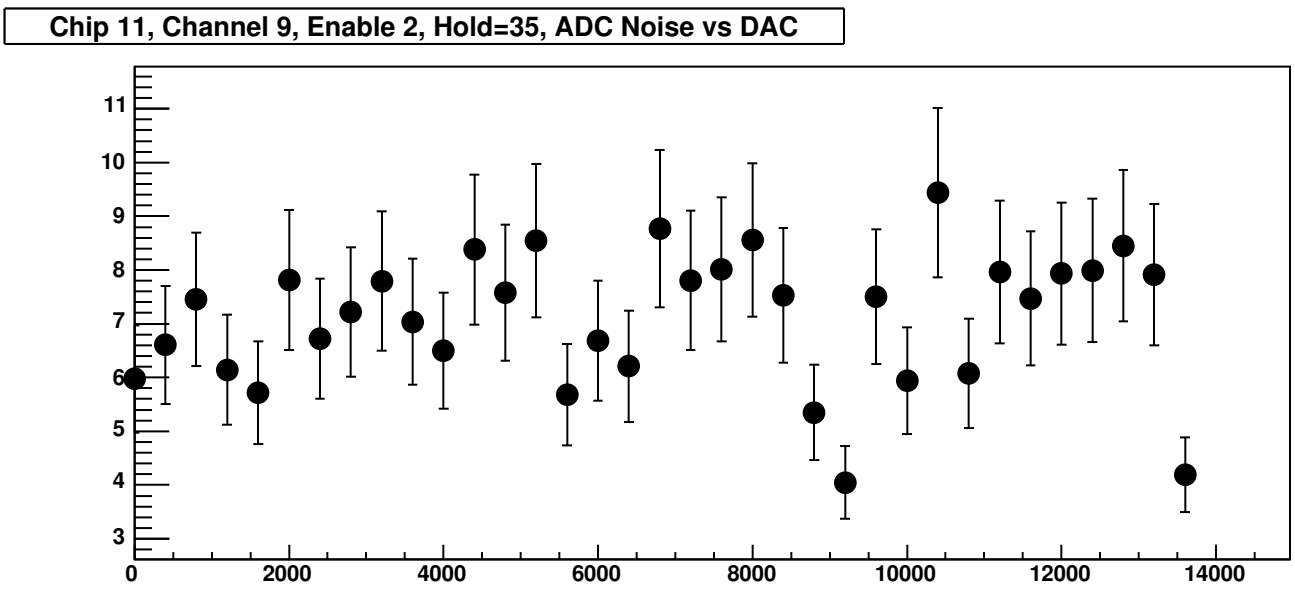

Chip 11, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

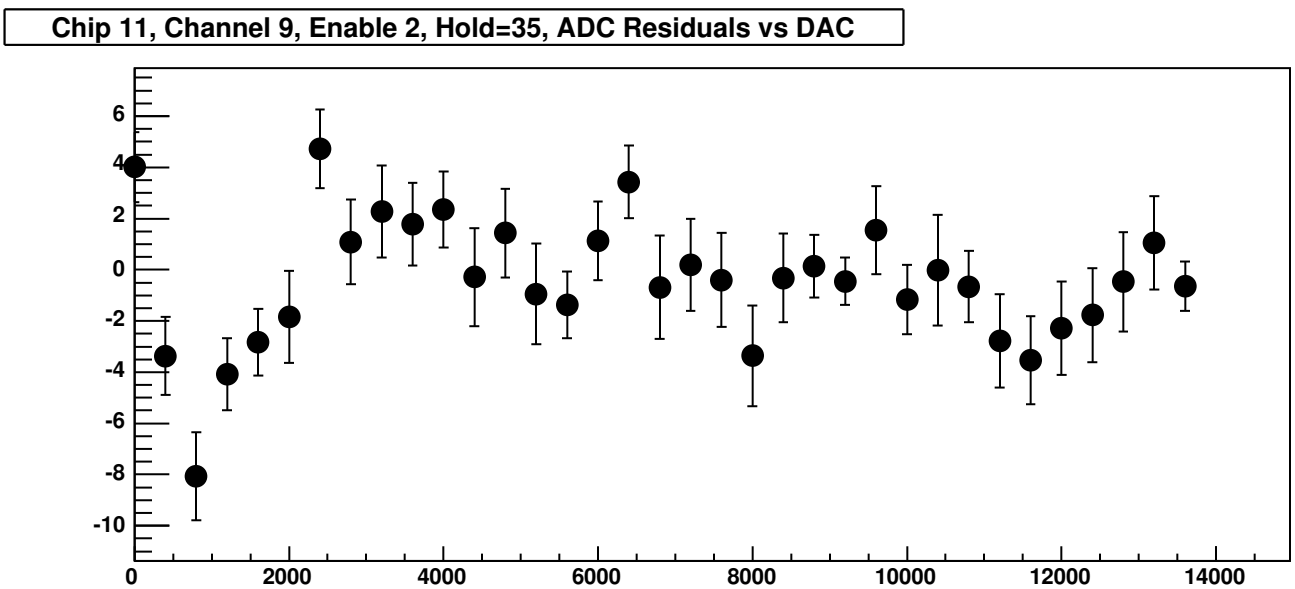

Chip 11, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 37.94 / 23
p0 -550.8 ± 0.7672
p1 0.02235 ± 0.0001196
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

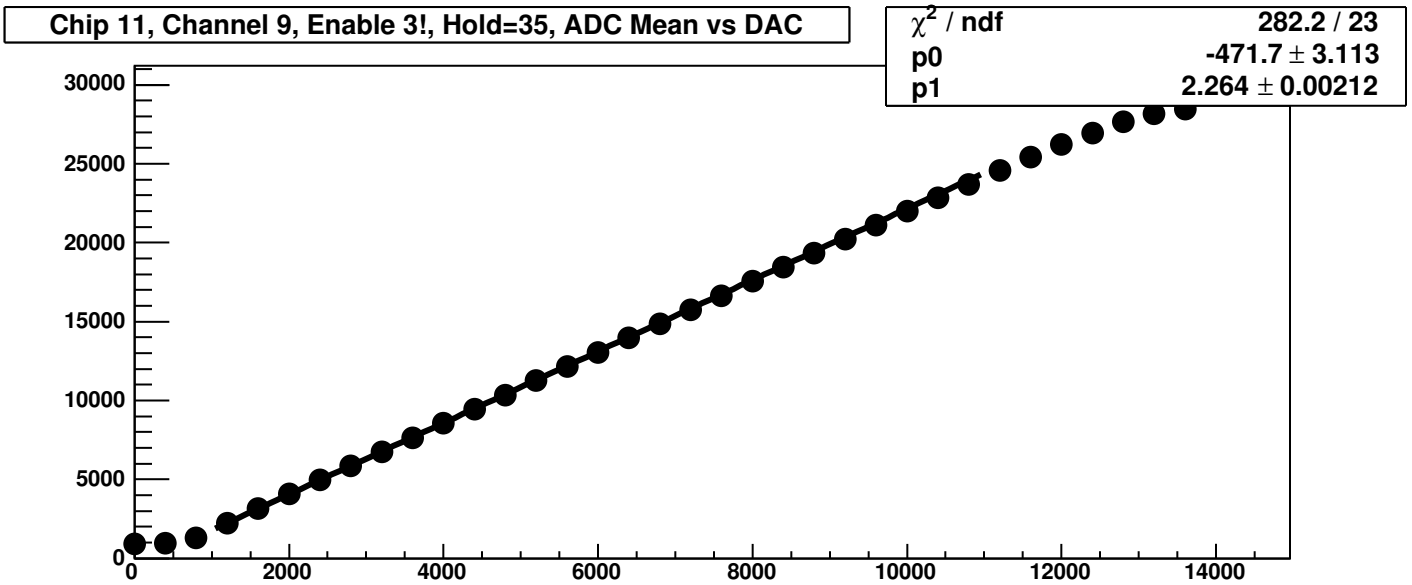

Chip 11, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

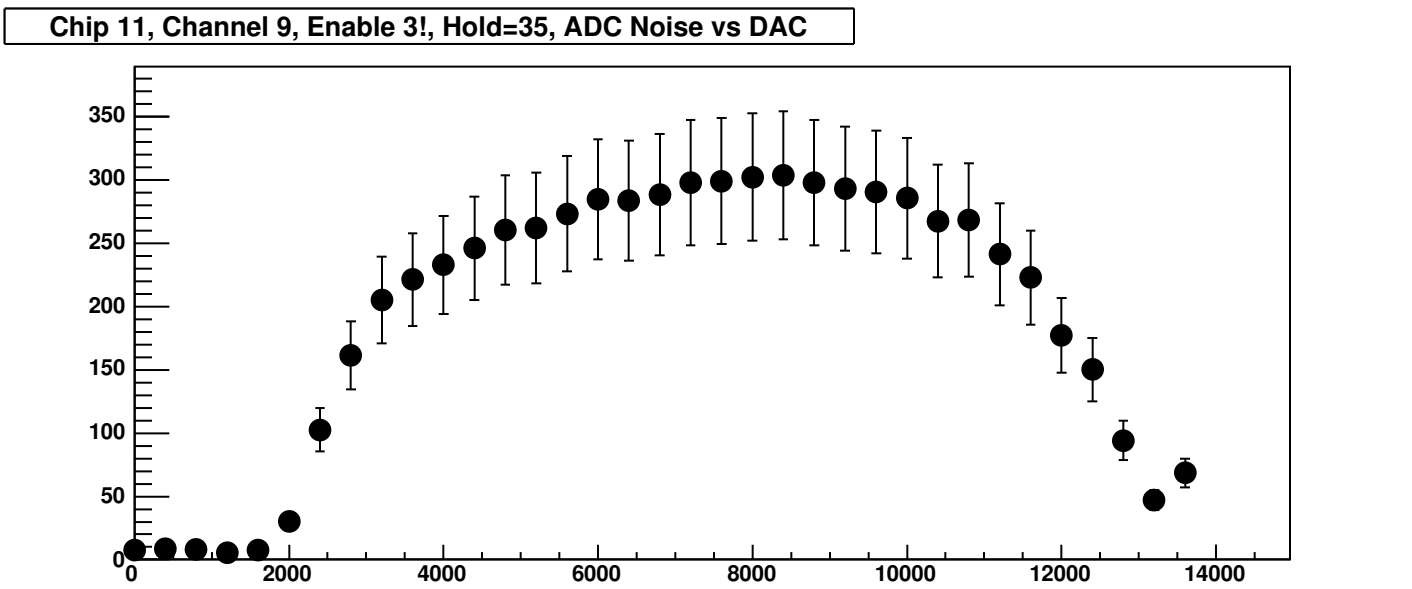

Chip 11, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

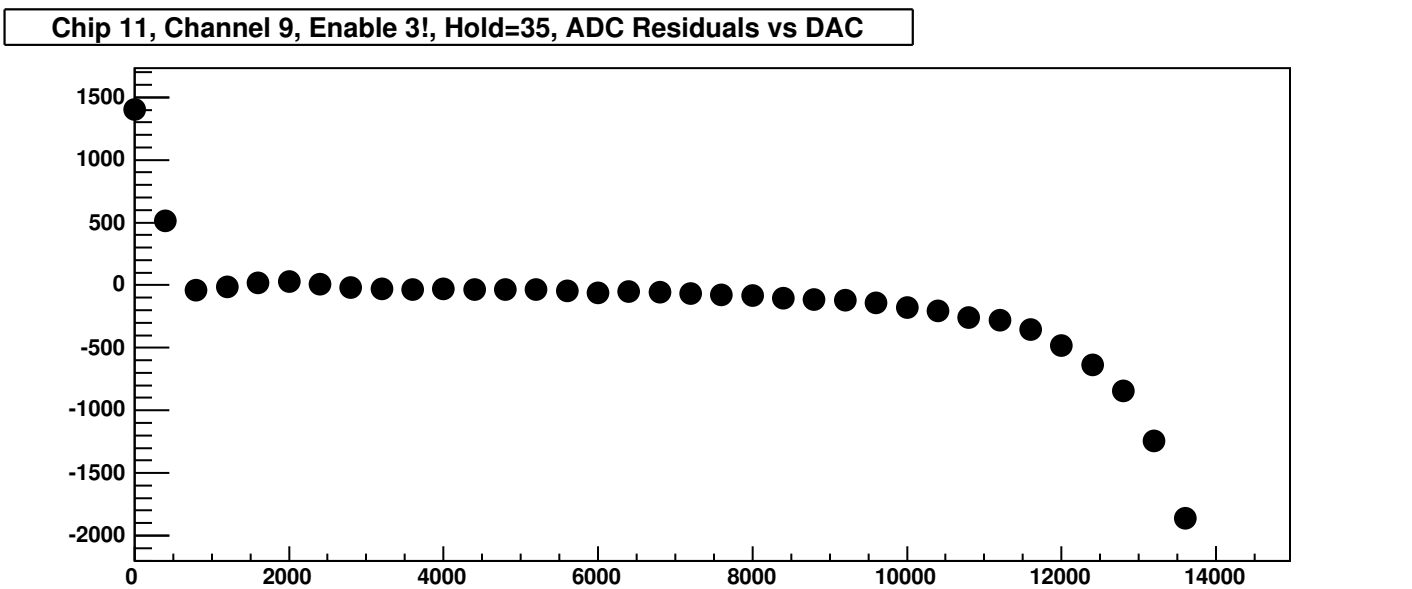

Chip 11, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 42.77 / 23
p0 -549.2 ± 0.706
p1 0.01832 ± 0.0001026
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

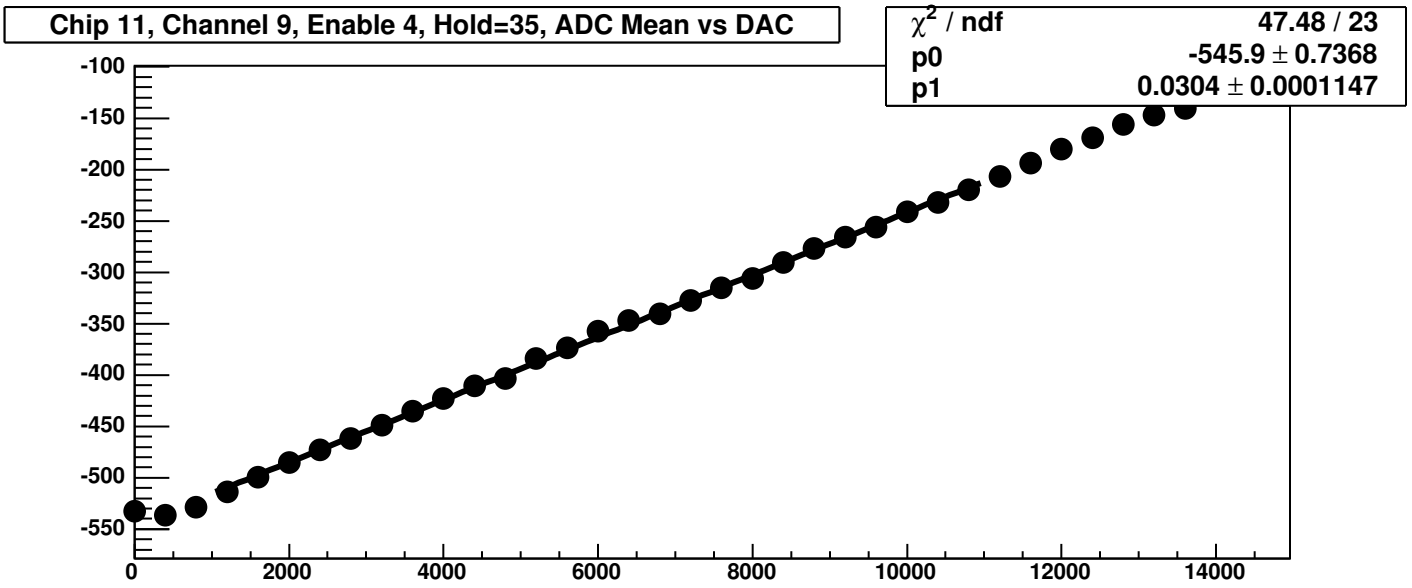

Chip 11, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

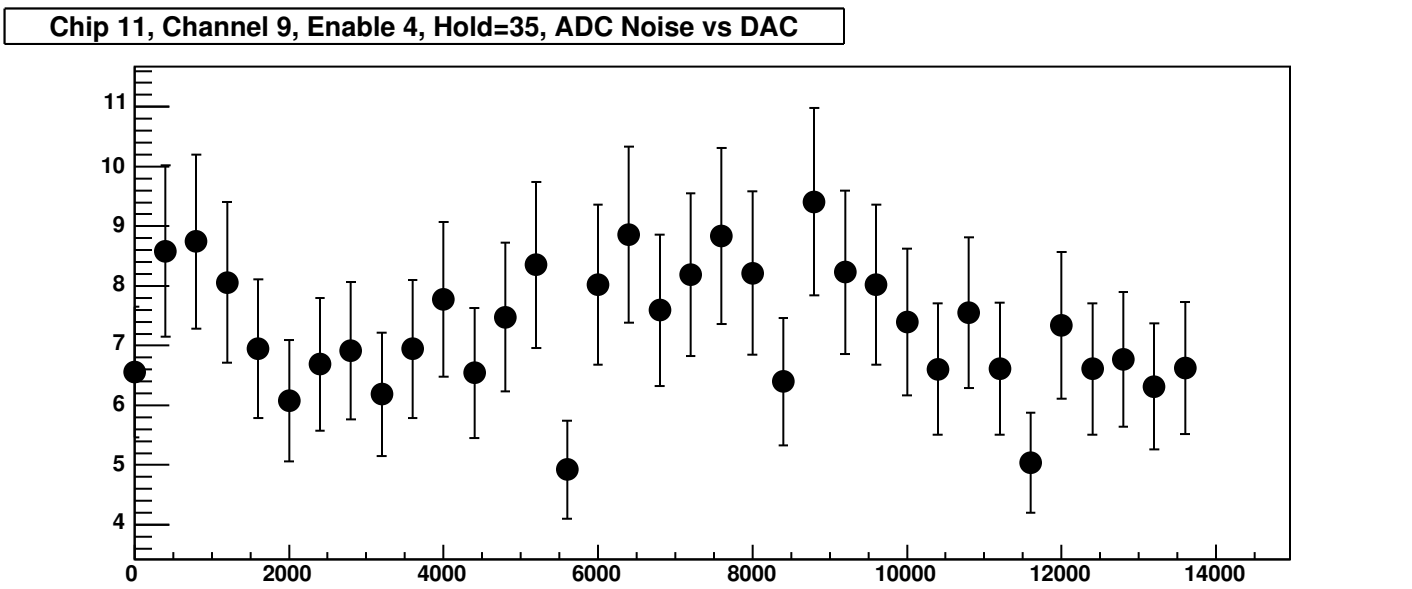

Chip 11, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
0
2000
4000
6000
8000
10000
12000
14000

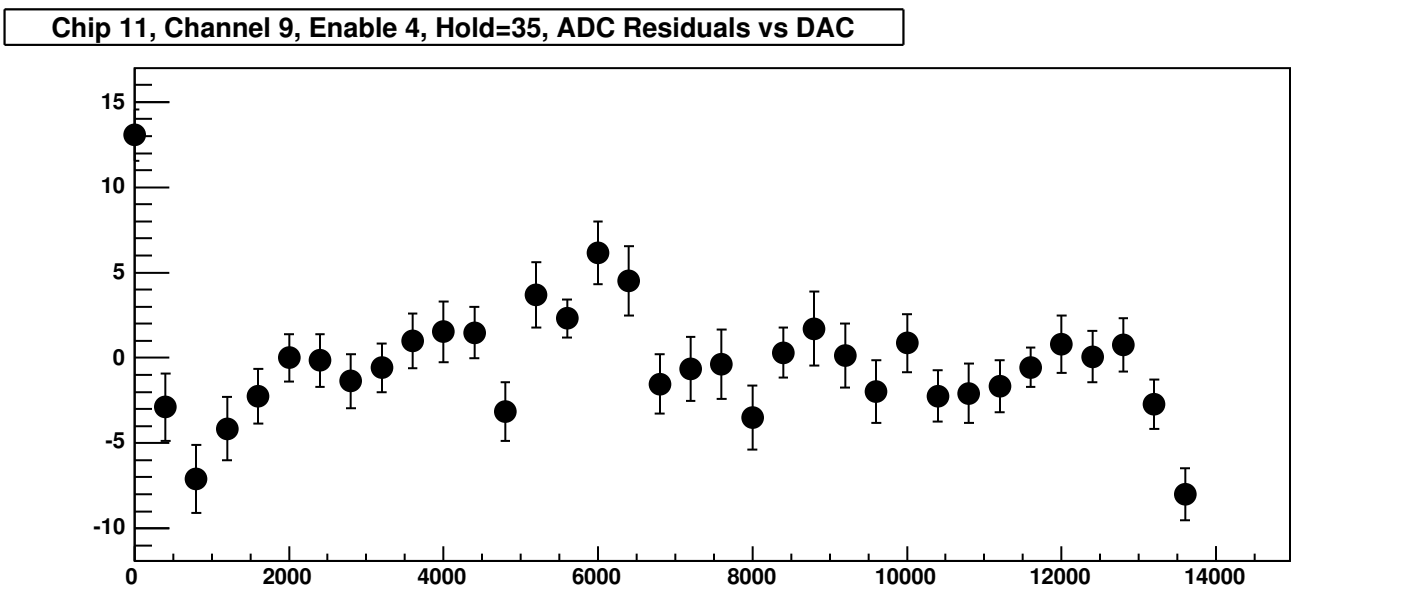

Chip 11, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 282.2 / 23
p0 -471.7 ± 3.113
p1 2.264 ± 0.00212
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

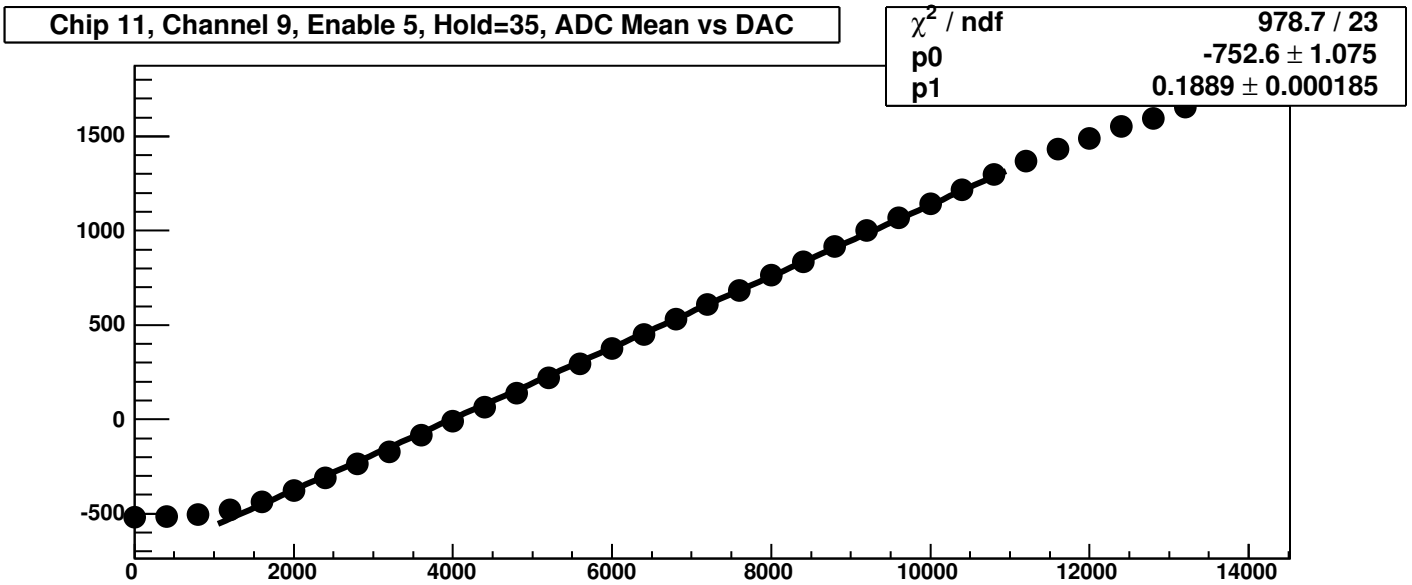

Chip 11, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

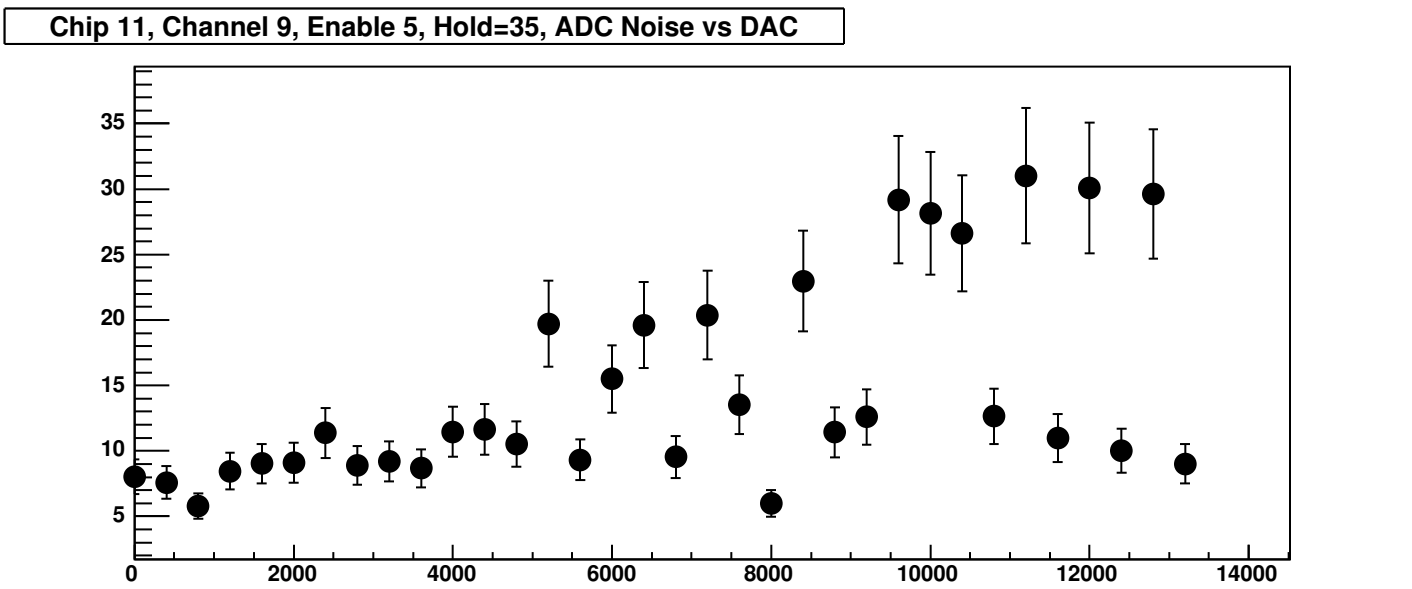

Chip 11, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

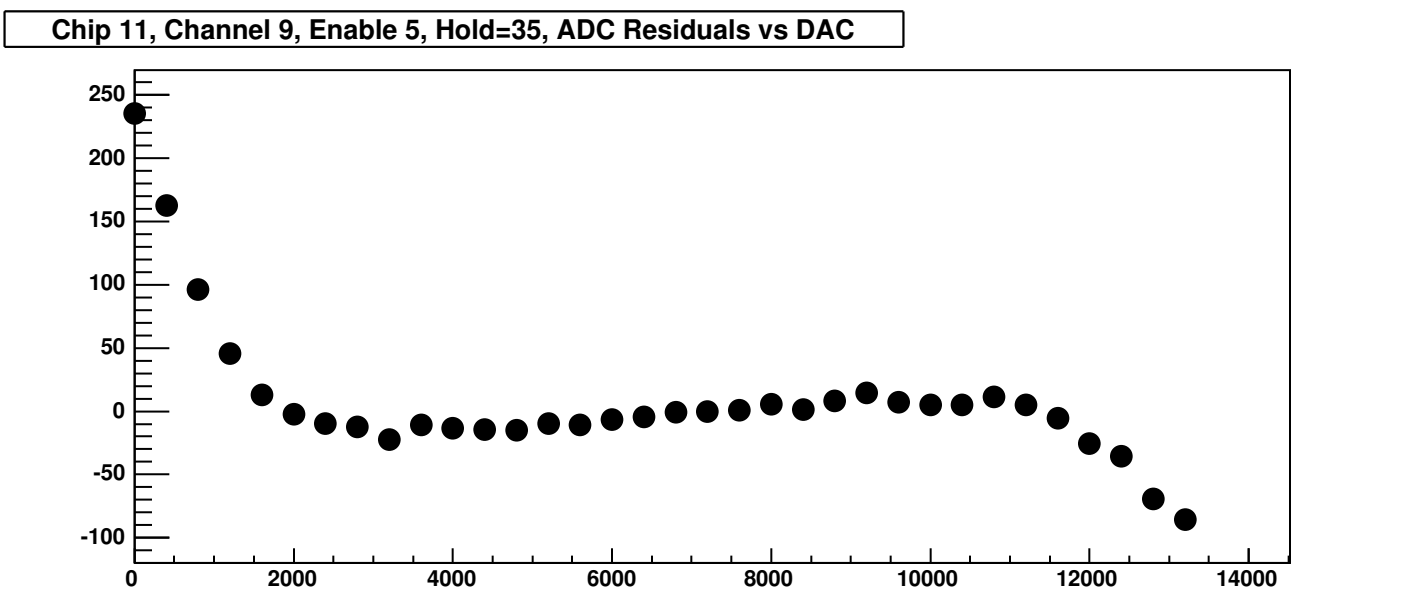

Chip 11, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 47.48 / 23
p0 -545.9 ± 0.7368
p1 0.0304 ± 0.0001147
-100
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

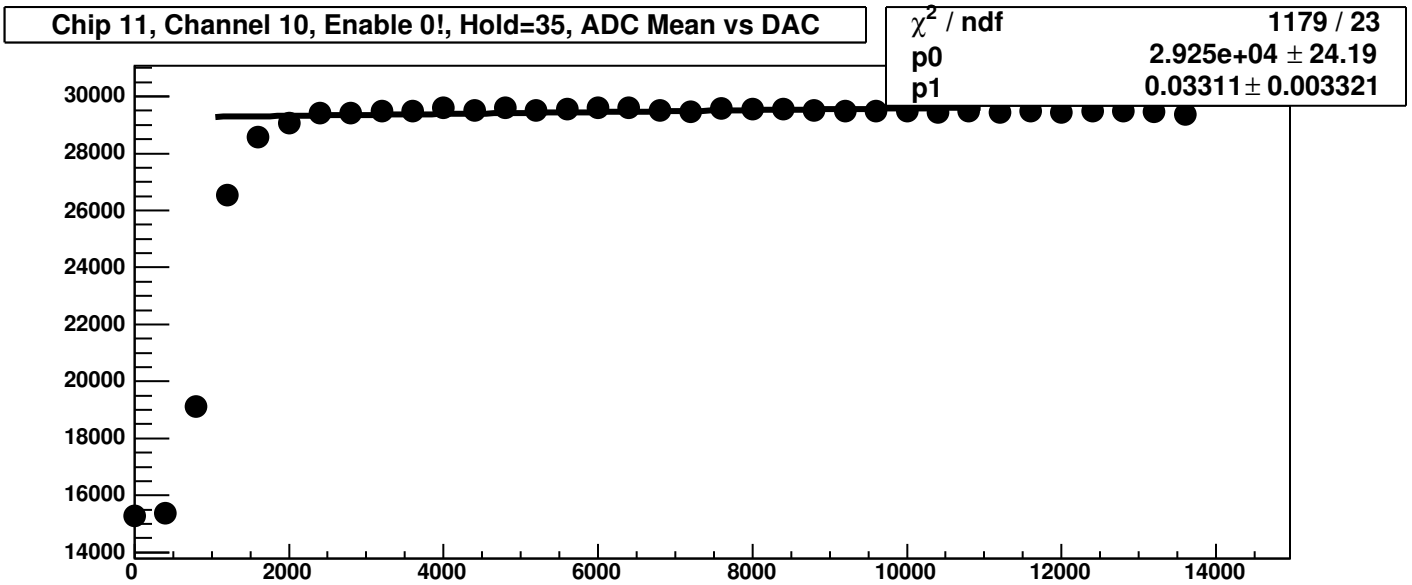

Chip 11, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

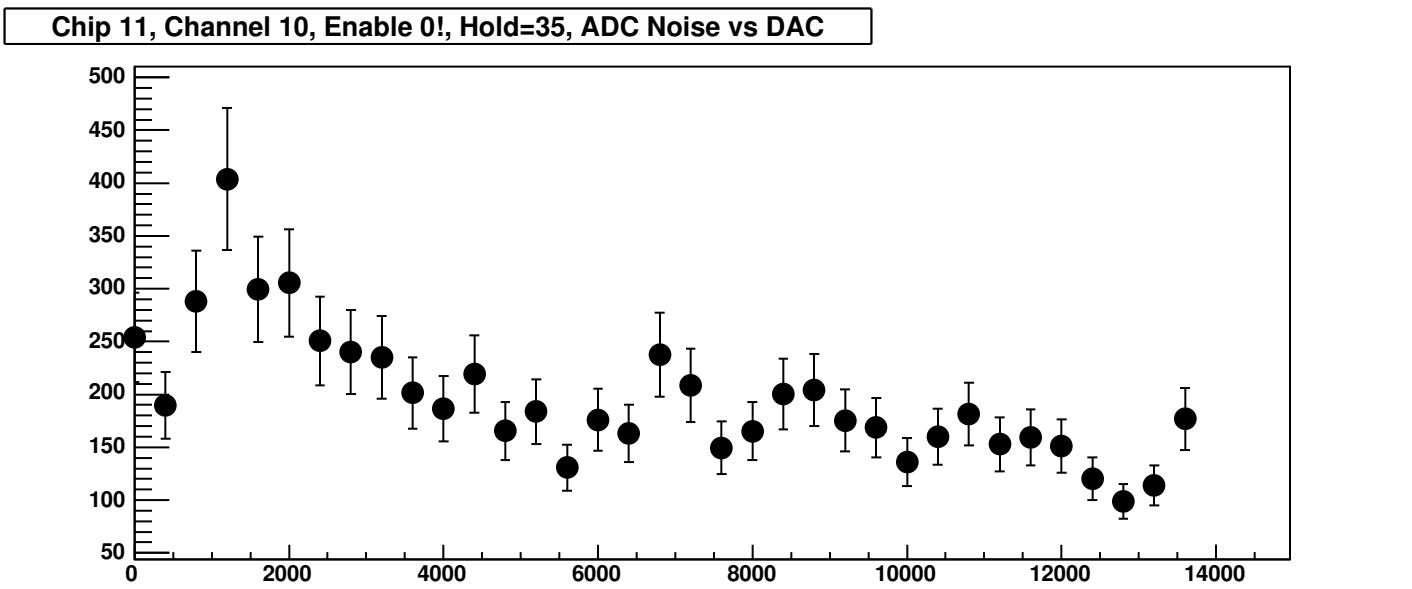

Chip 11, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

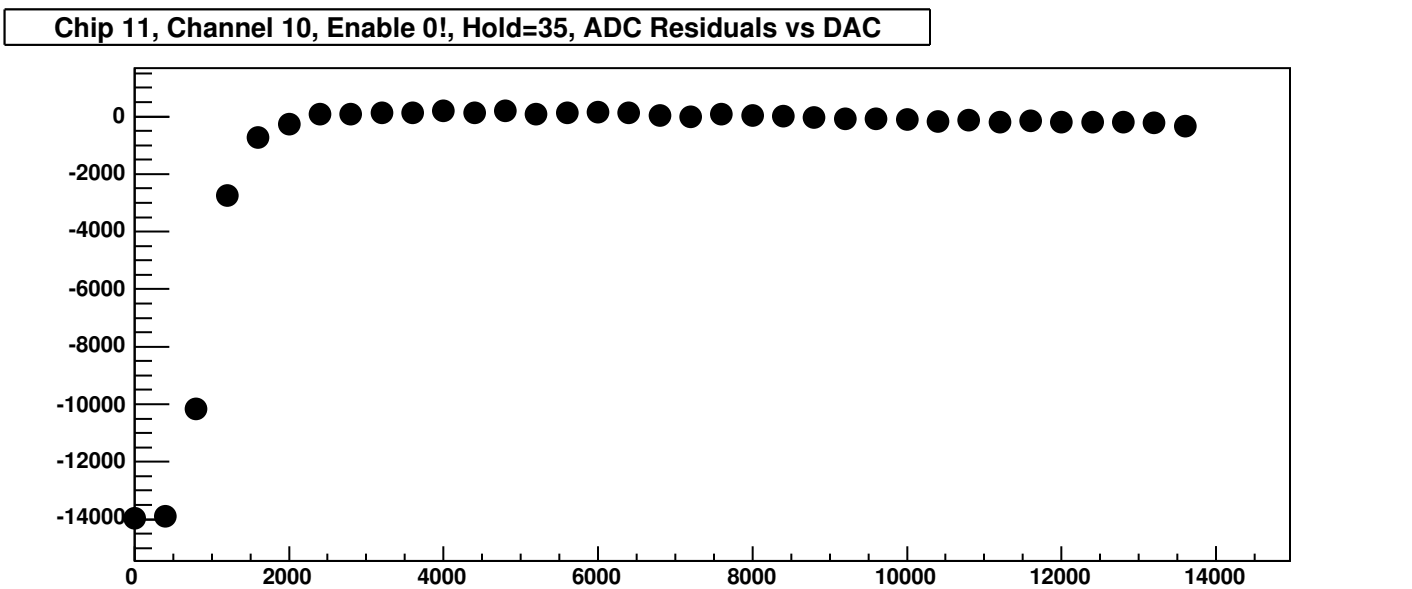

Chip 11, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 978.7 / 23
p0 -752.6 ± 1.075
p1 0.1889 ± 0.000185
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

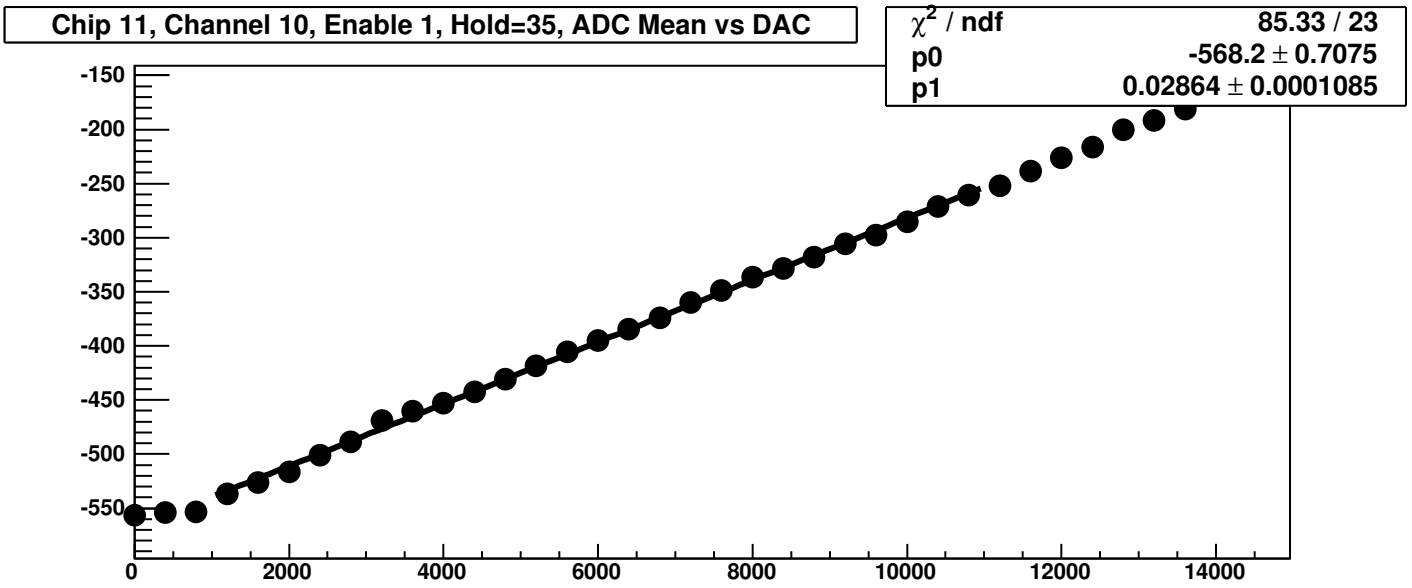

Chip 11, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

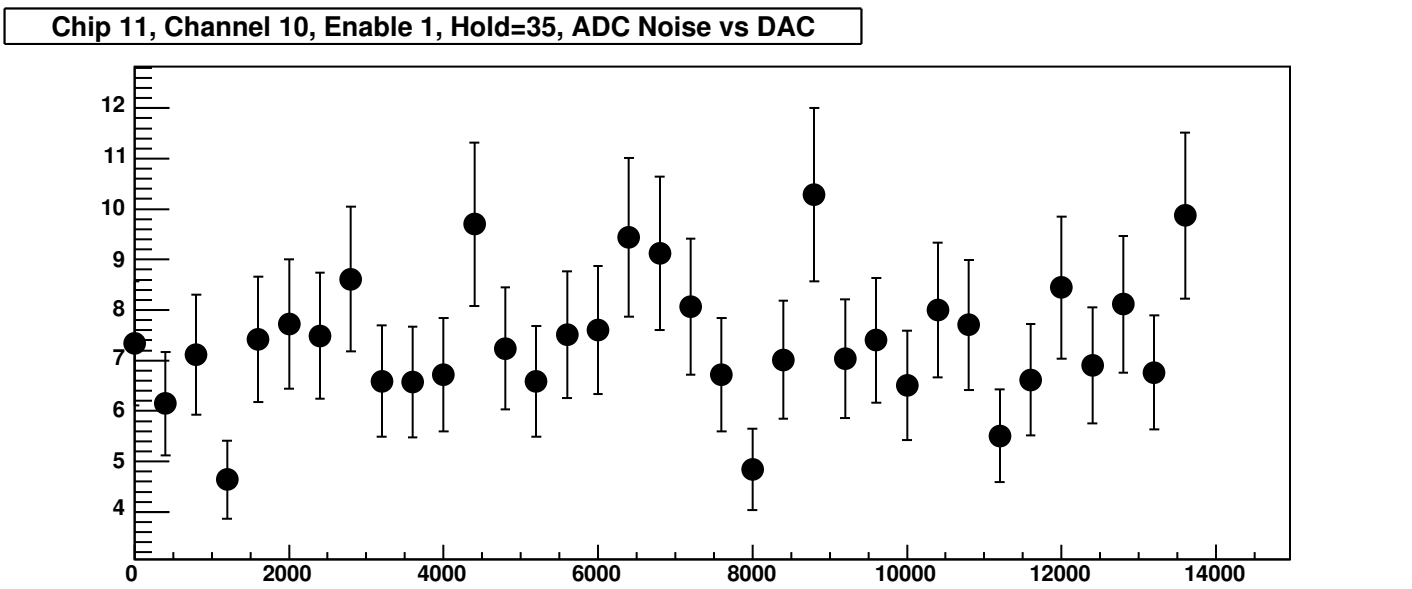

Chip 11, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

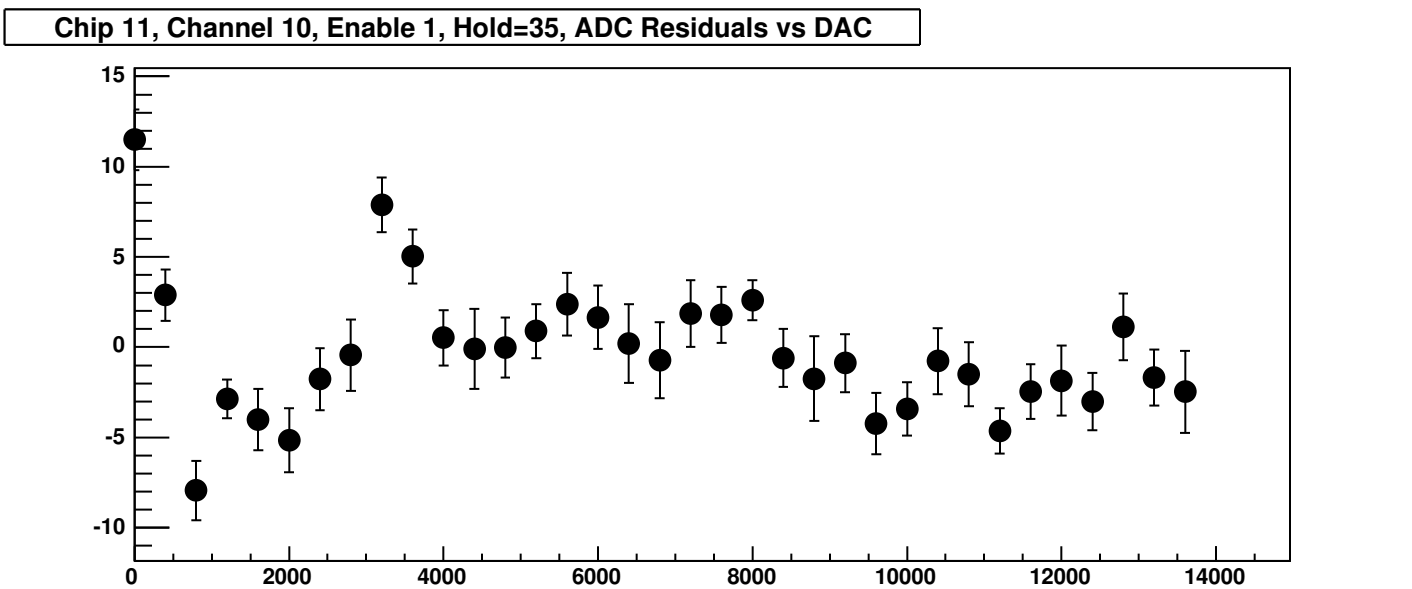

Chip 11, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1179 / 23
p0 2.925e+04 ± 24.19
p1 0.03311 ± 0.003321
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

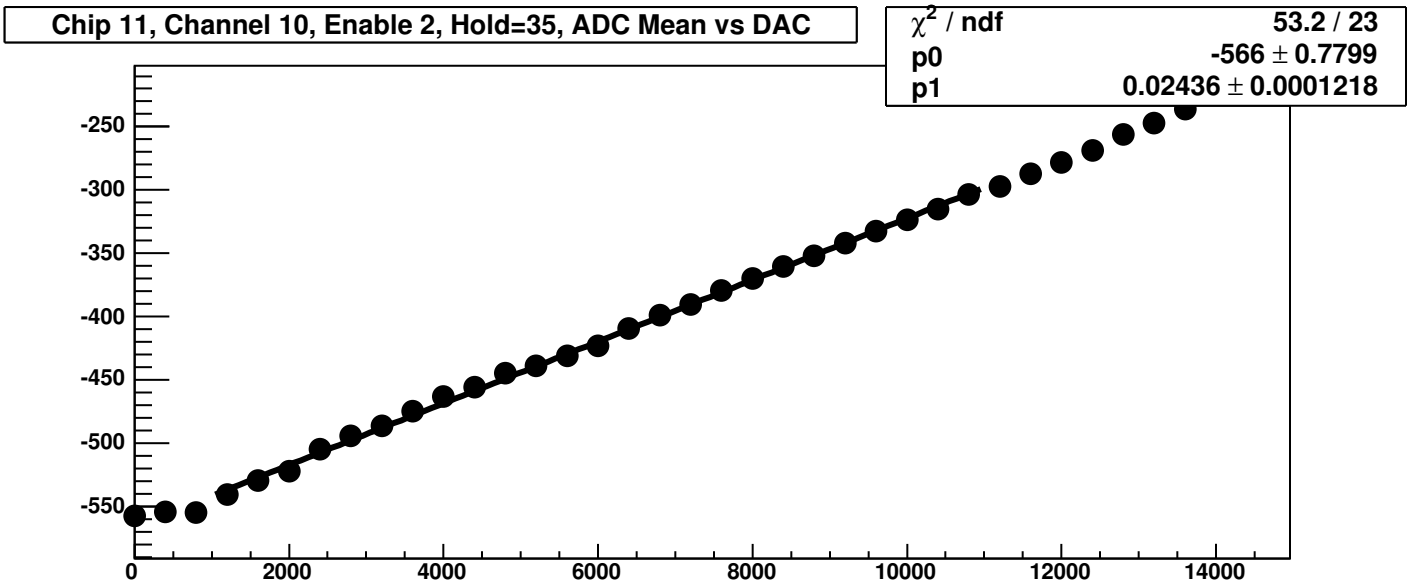

Chip 11, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
500
450
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

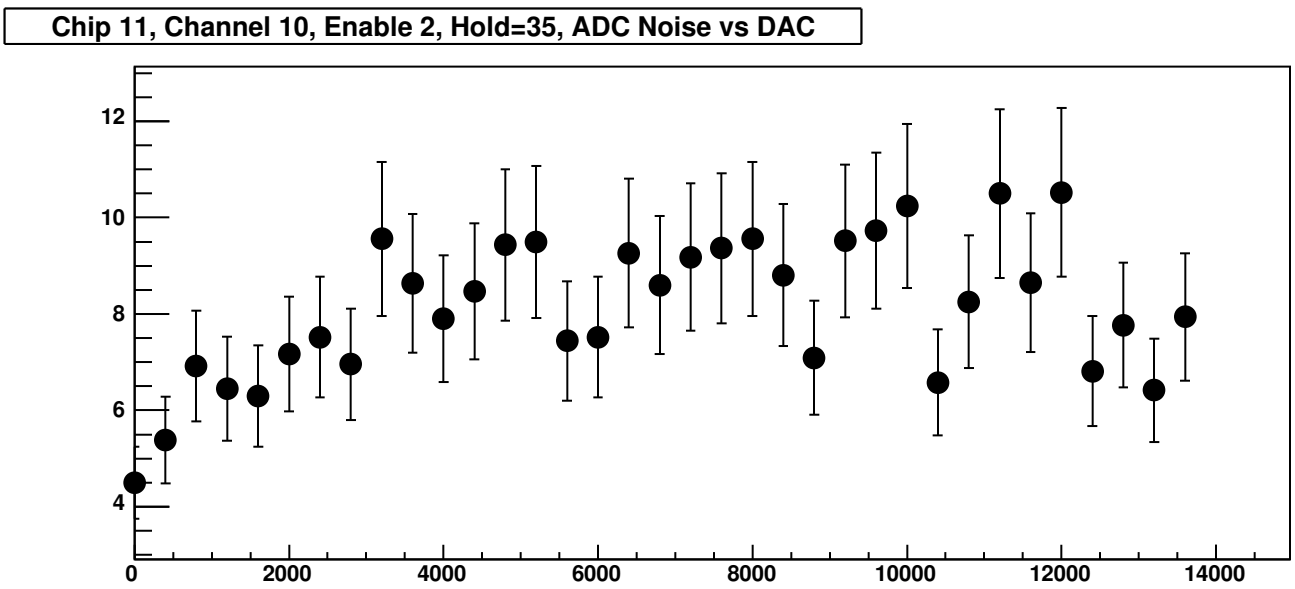

Chip 11, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

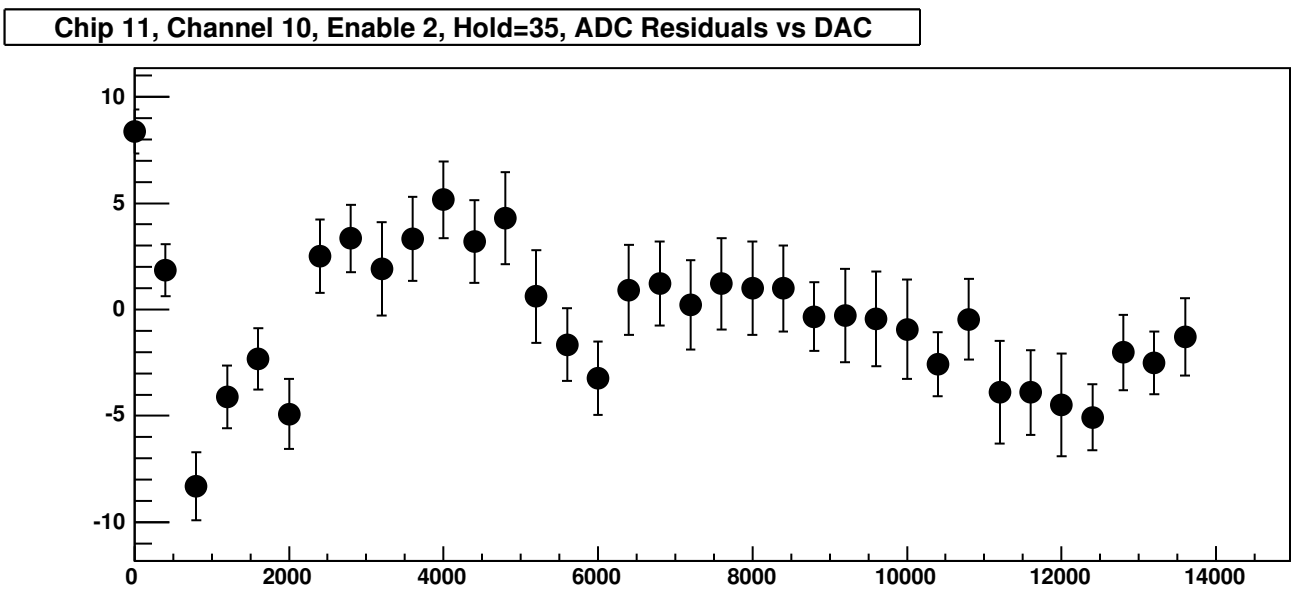

Chip 11, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 85.33 / 23
p0 -568.2 ± 0.7075
p1 0.02864 ± 0.0001085
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

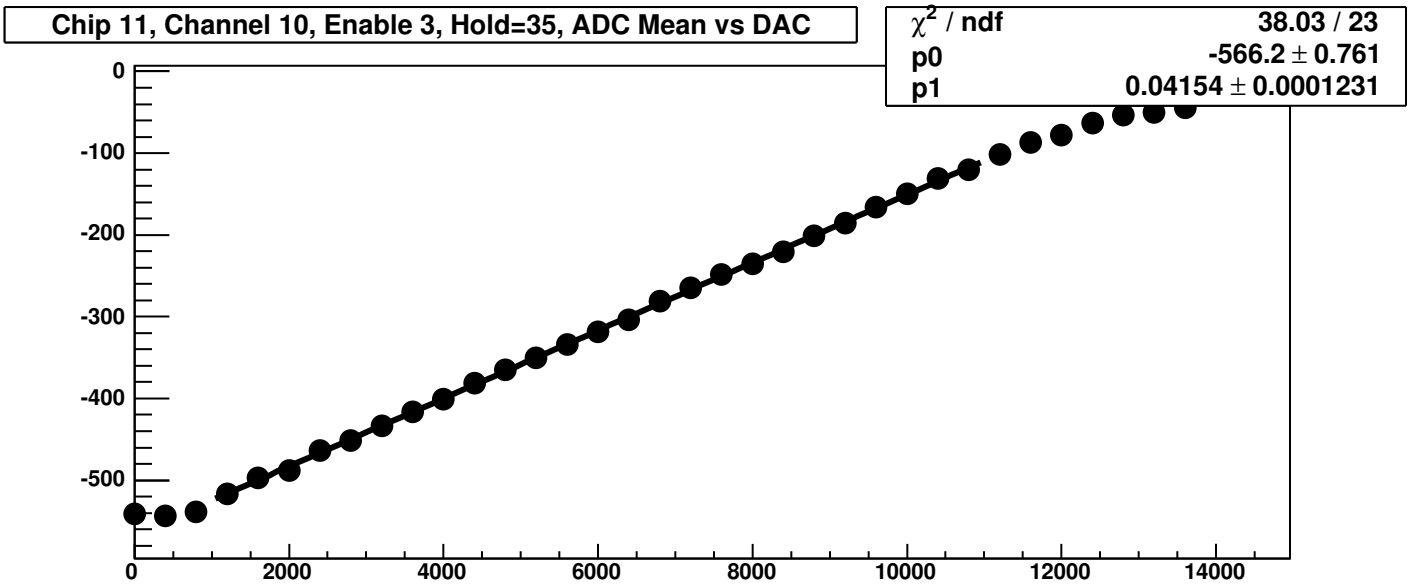

Chip 11, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

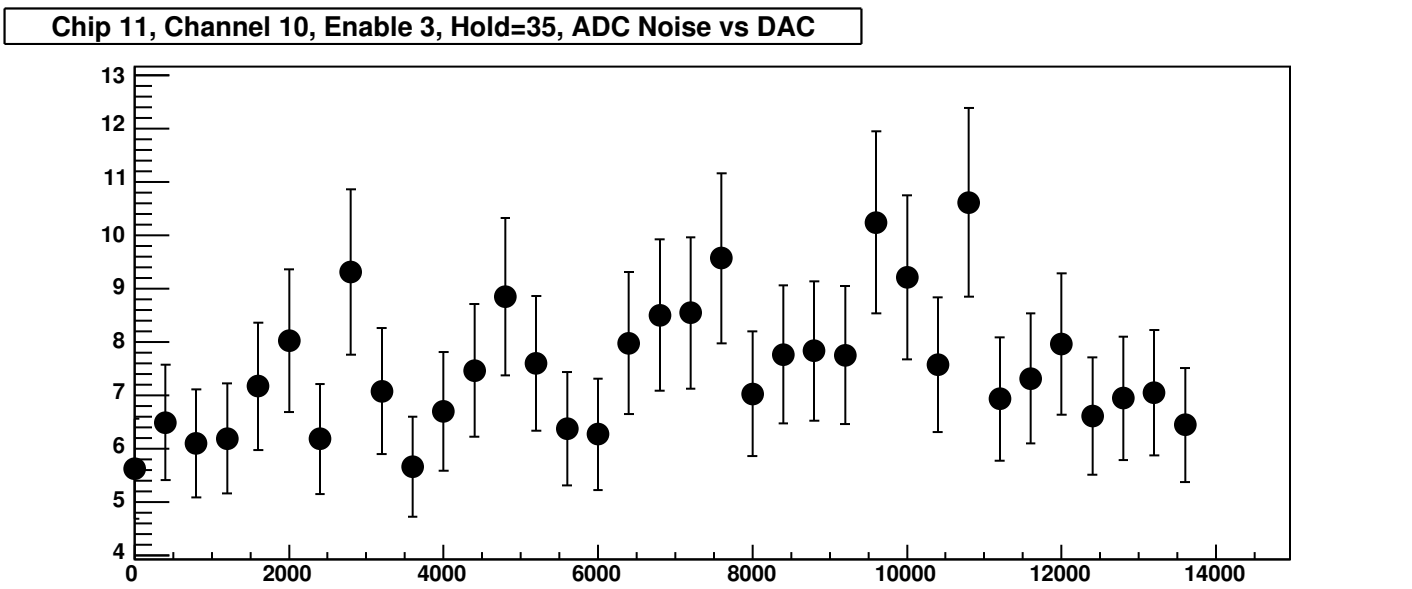

Chip 11, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

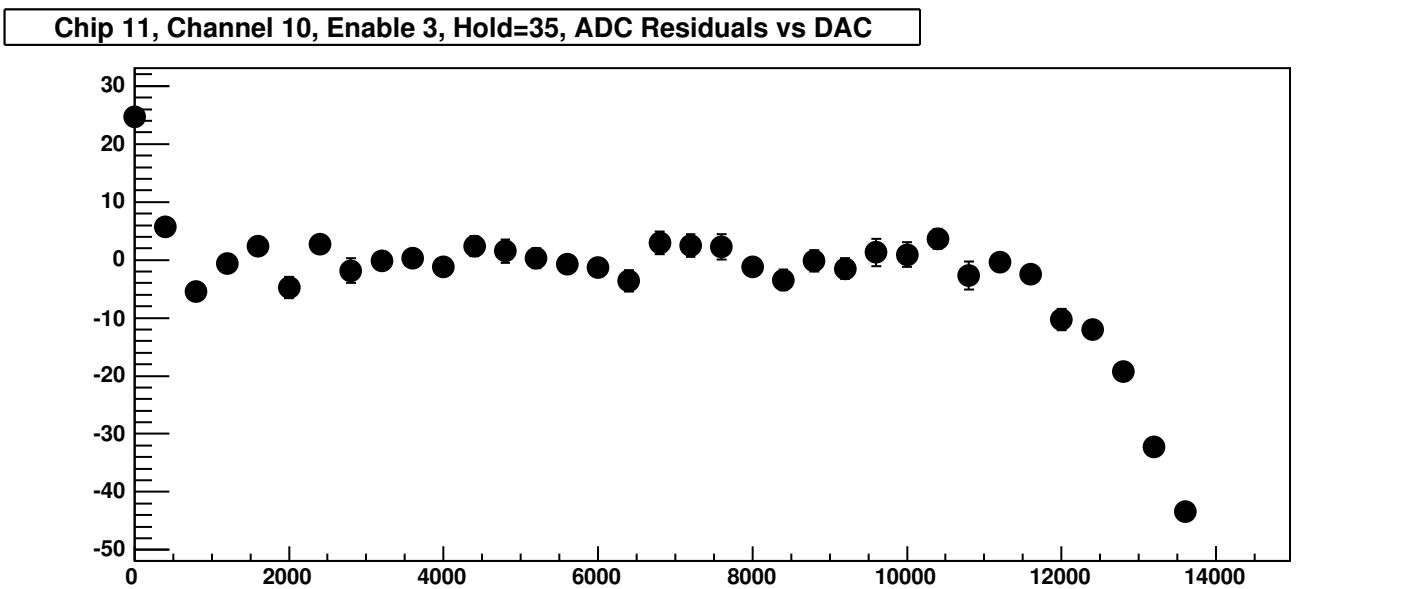

Chip 11, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 53.2 / 23
p0 -566 ± 0.7799
p1 0.02436 ± 0.0001218
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

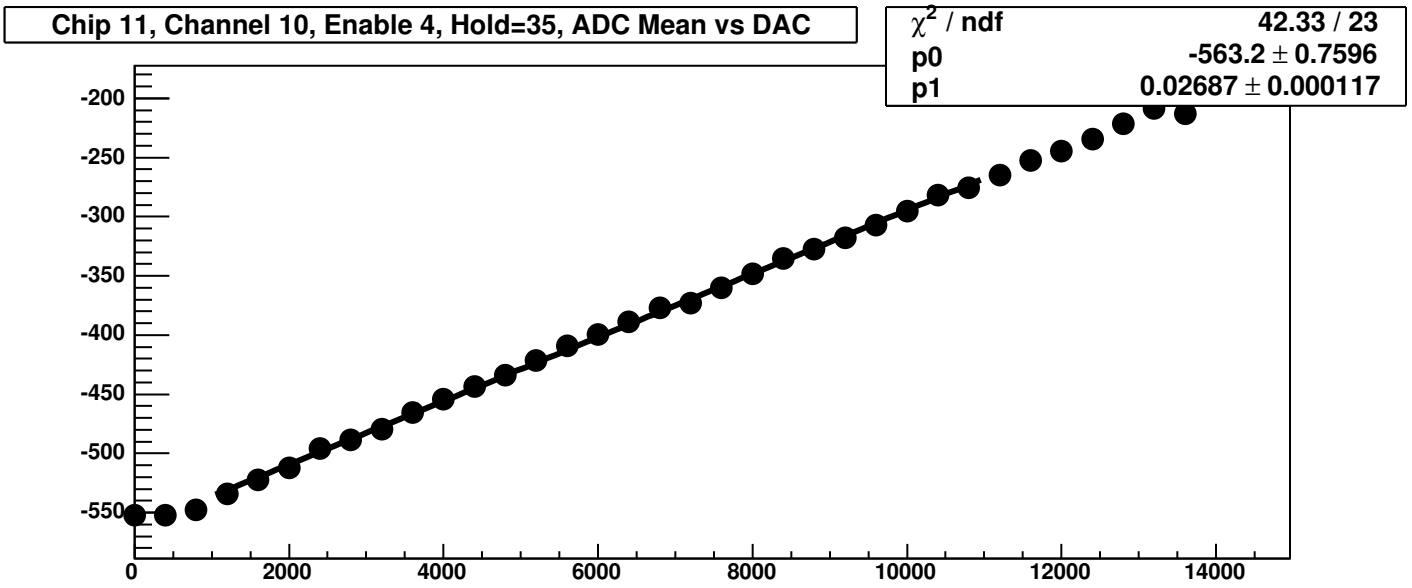

Chip 11, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

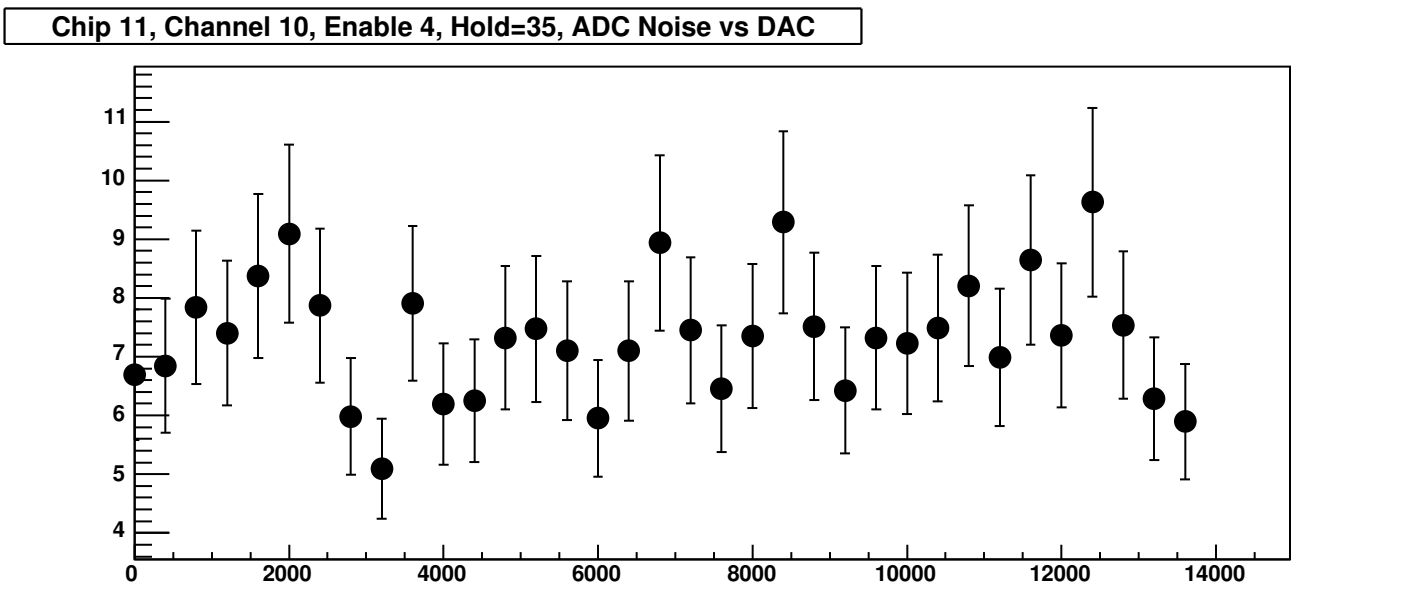

Chip 11, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

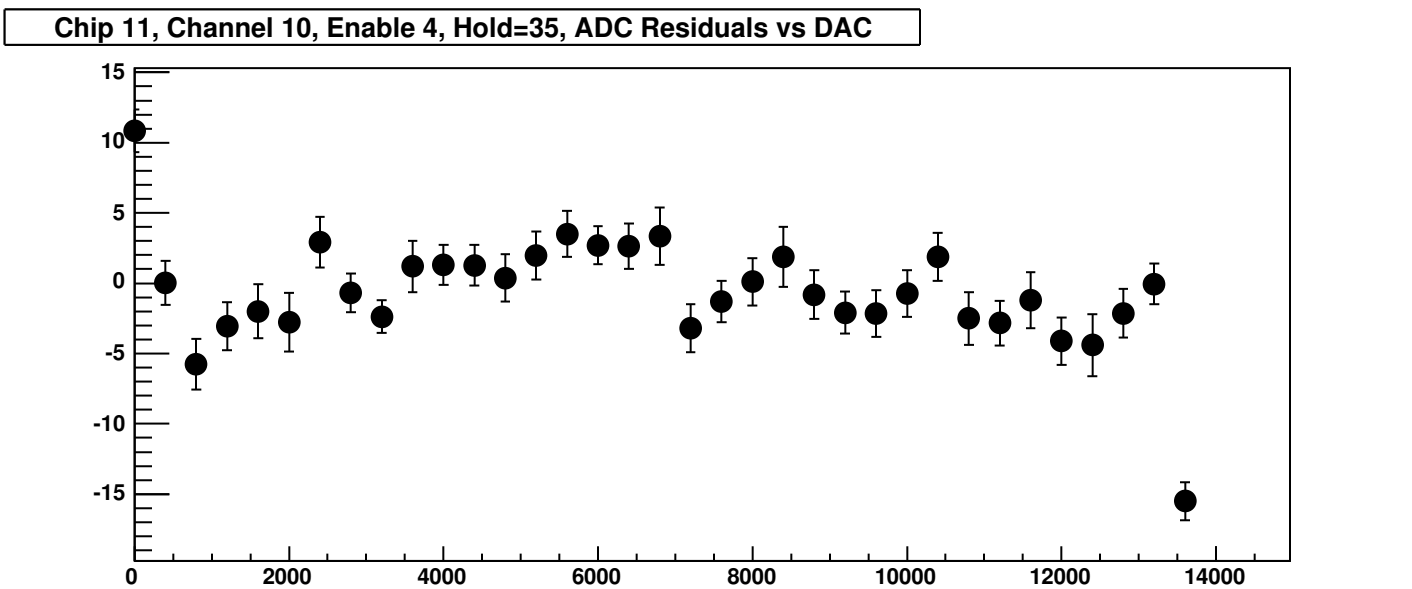

Chip 11, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.03 / 23
p0 -566.2 ± 0.761
p1 0.04154 ± 0.0001231
0
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000

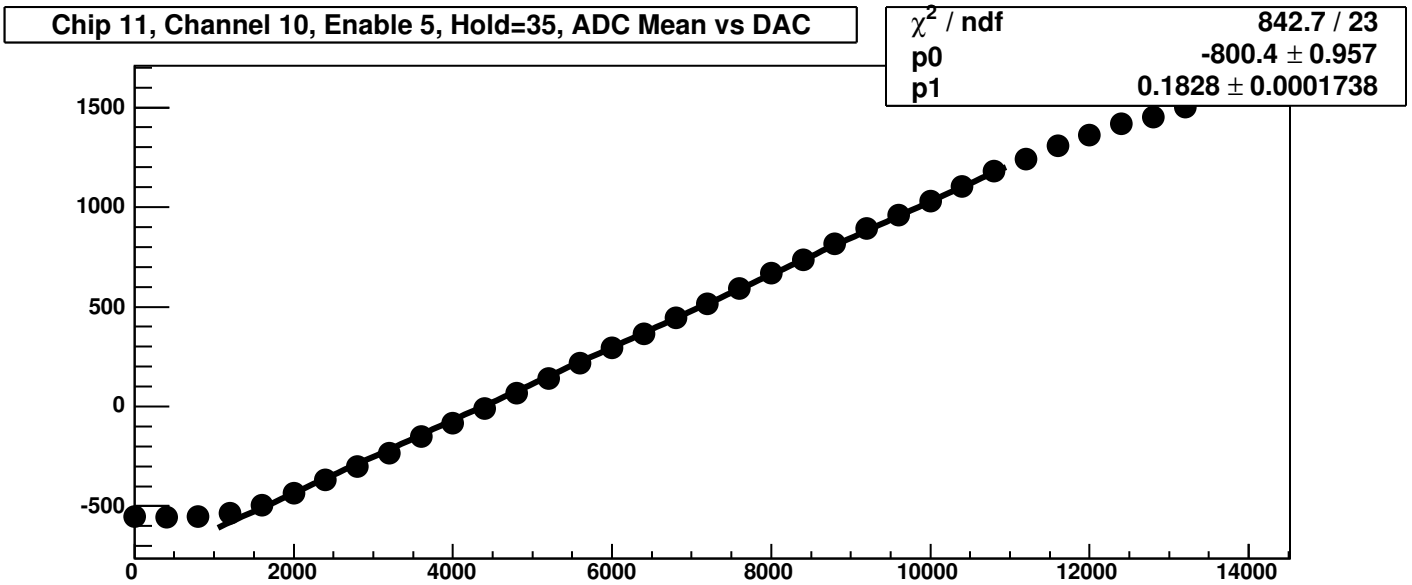

Chip 11, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

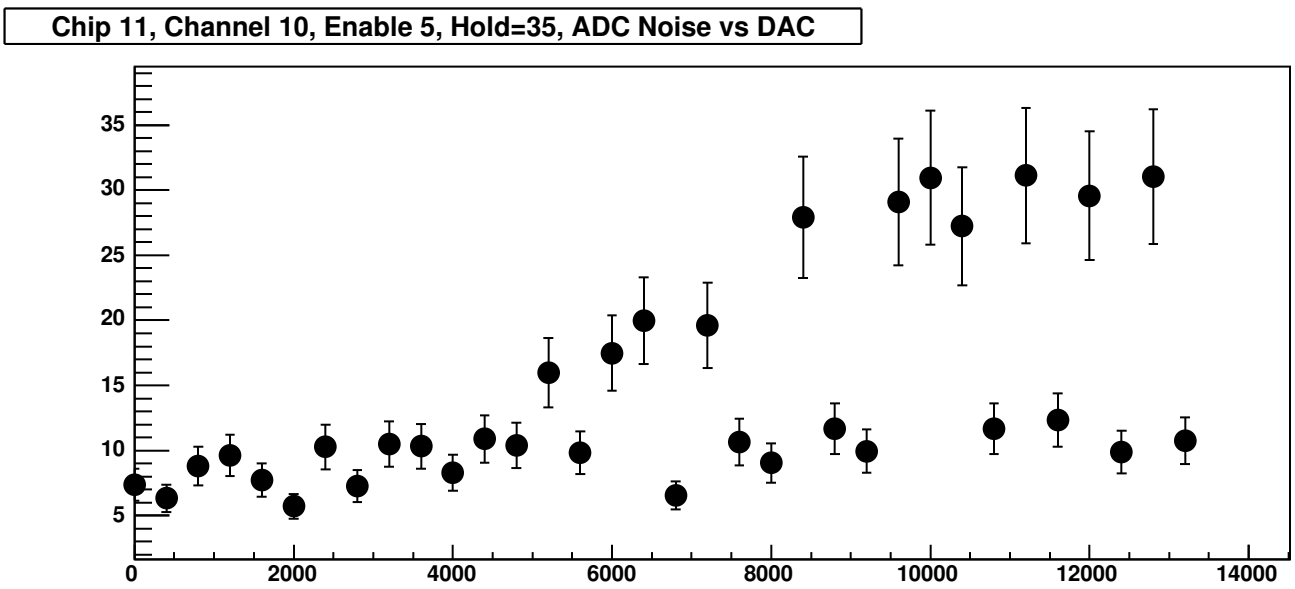

Chip 11, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

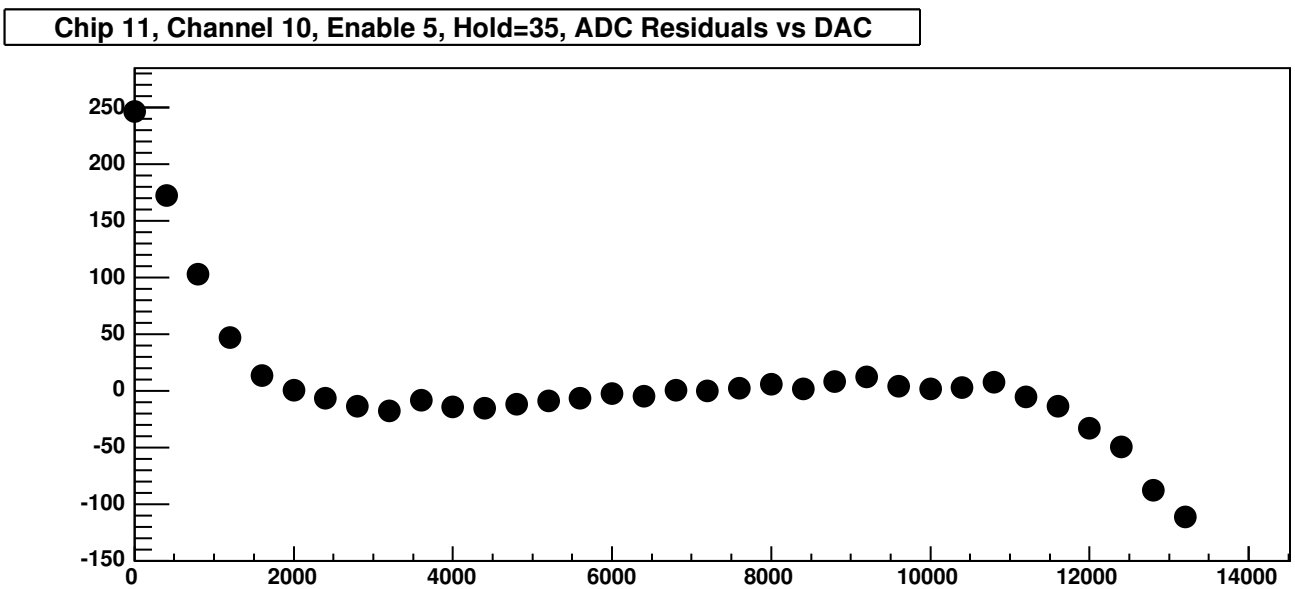

Chip 11, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 42.33 / 23
p0 -563.2 ± 0.7596
p1 0.02687 ± 0.000117
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

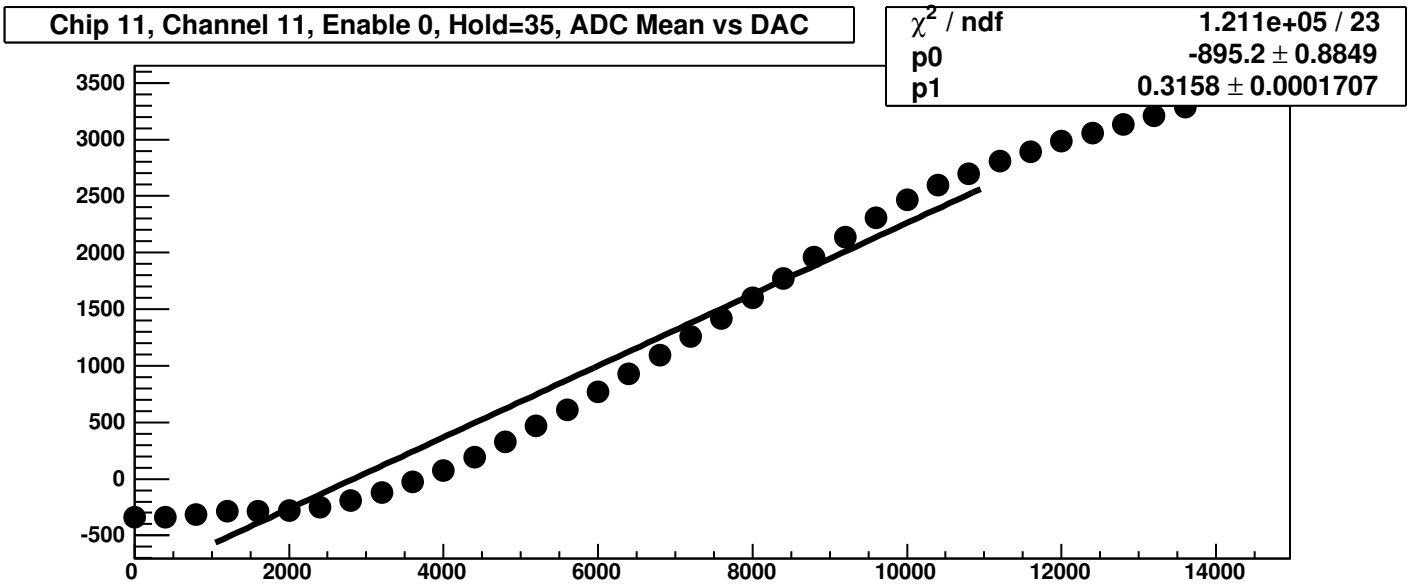

Chip 11, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

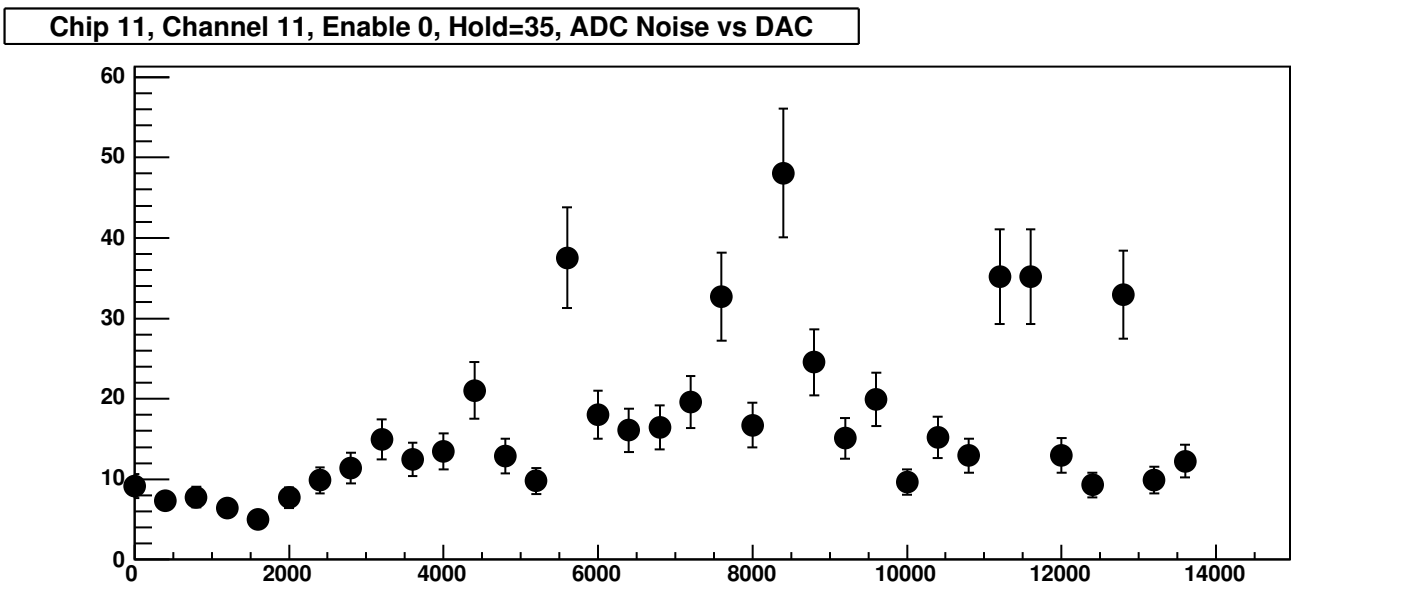

Chip 11, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

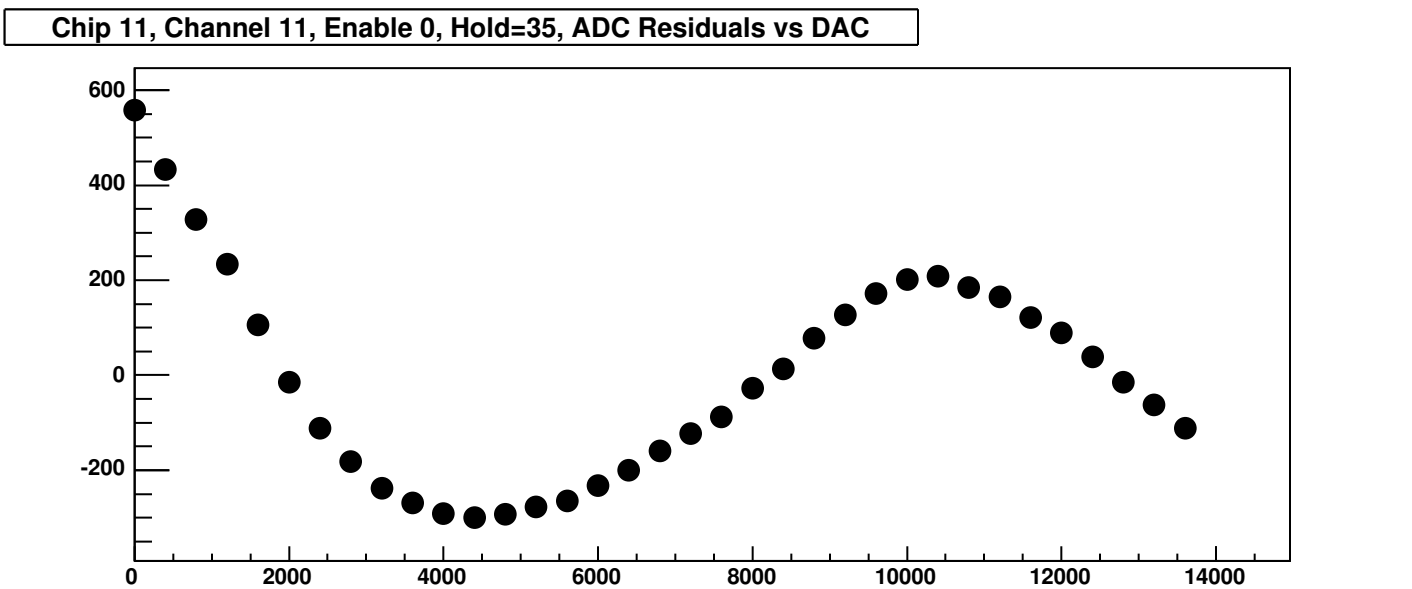

Chip 11, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 842.7 / 23
p0 -800.4 ± 0.957
p1 0.1828 ± 0.0001738
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

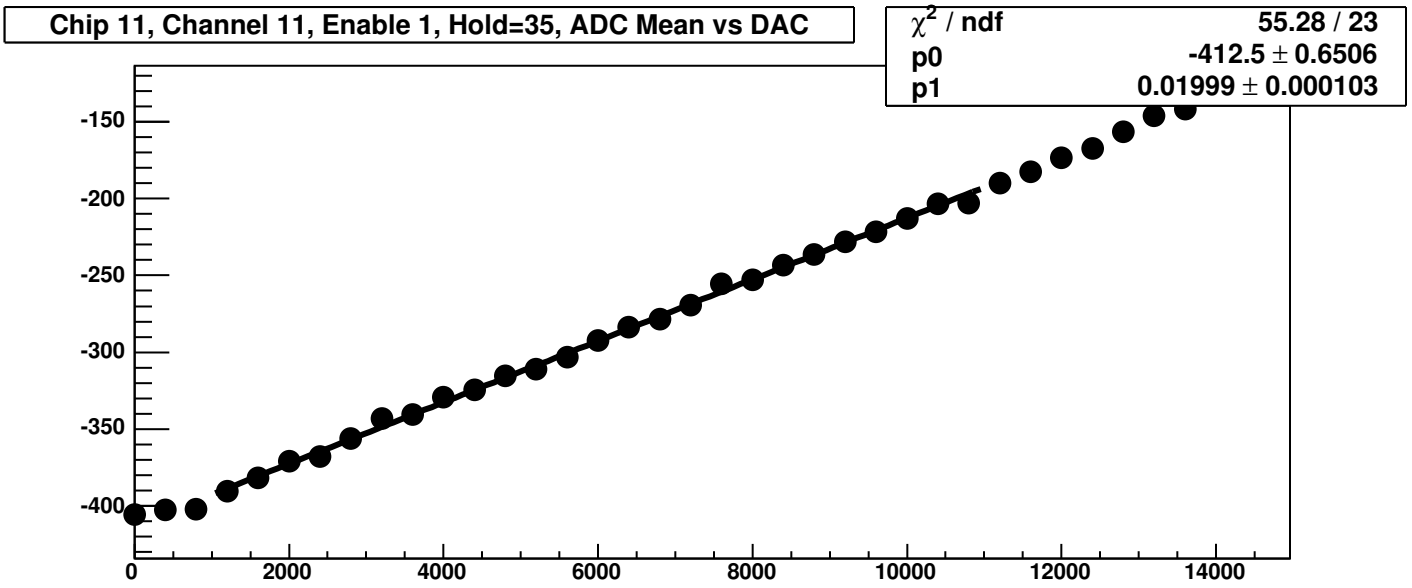

Chip 11, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

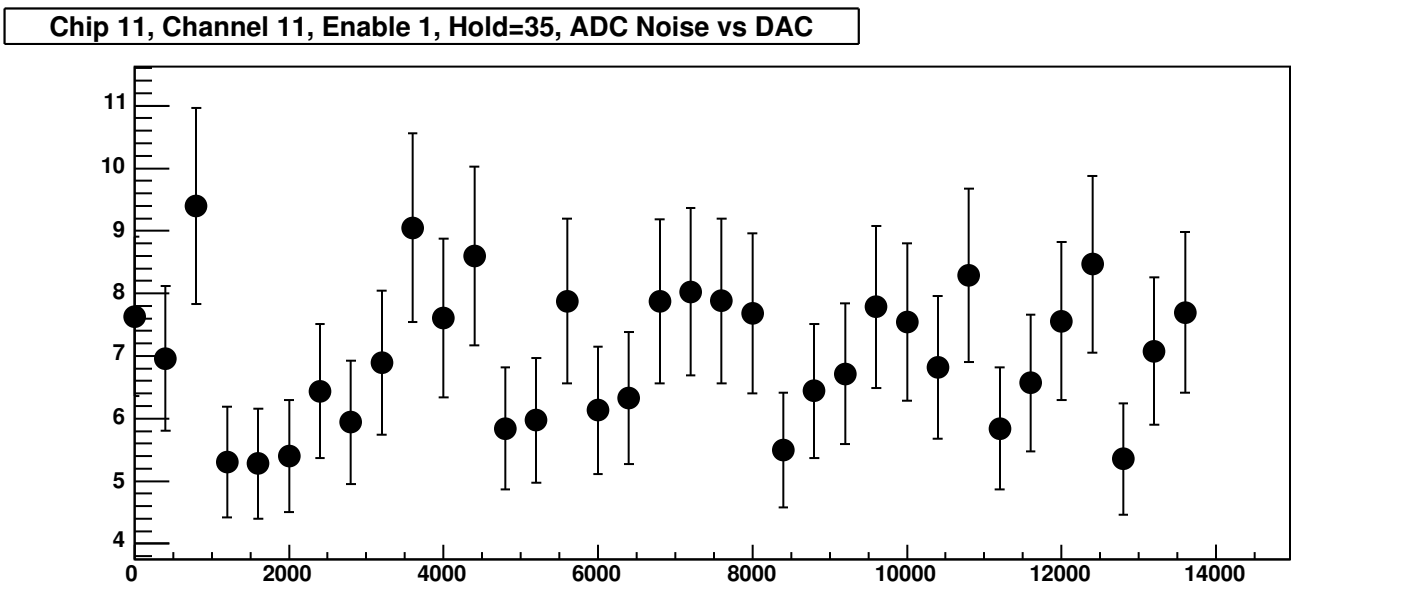

Chip 11, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

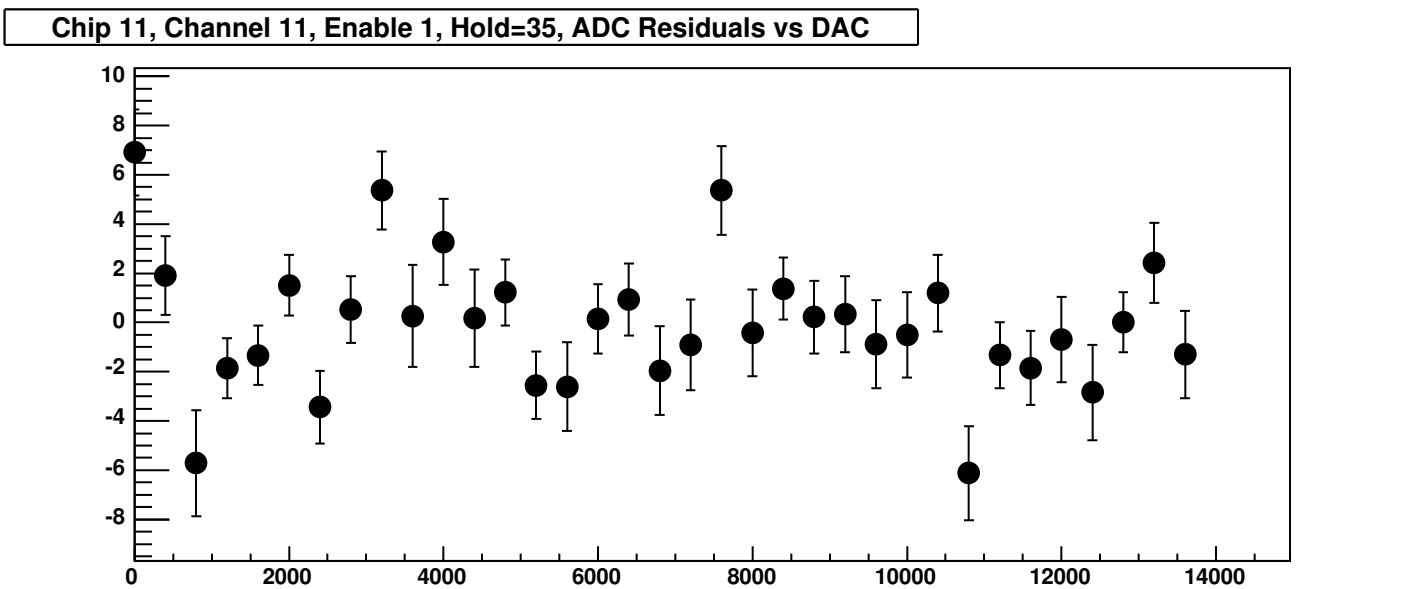

Chip 11, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.211e+05 / 23
p0 -895.2 ± 0.8849
p1 0.3158 ± 0.0001707
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

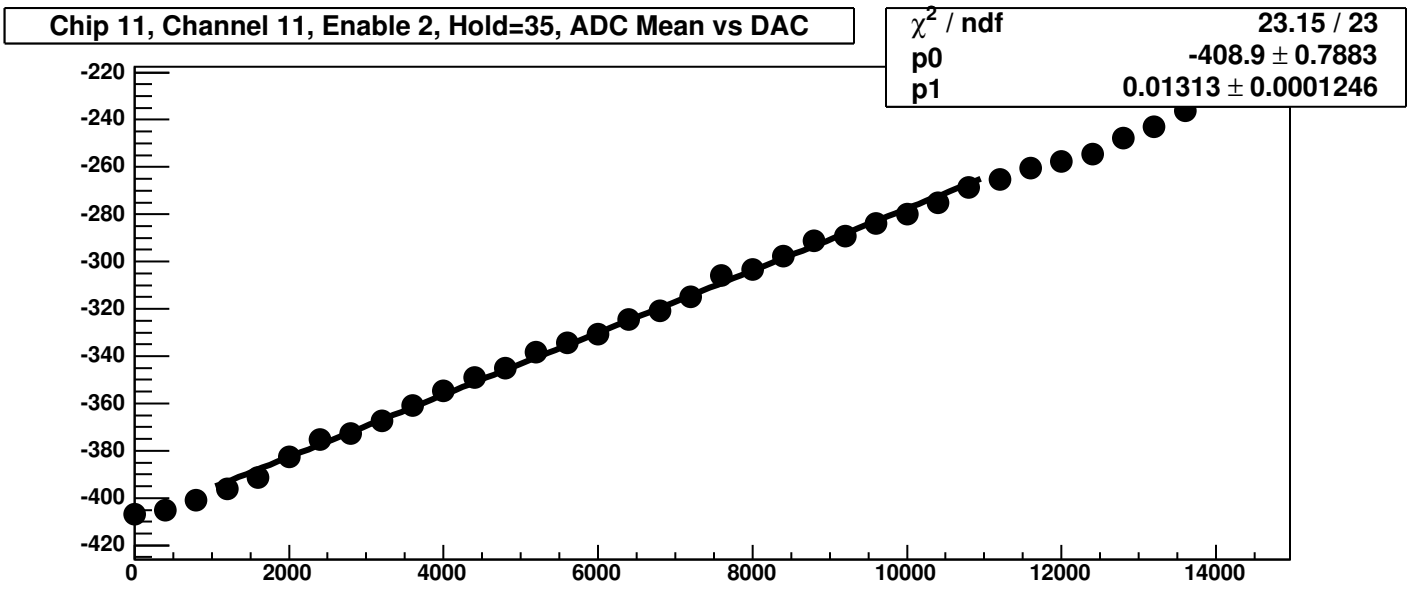

Chip 11, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

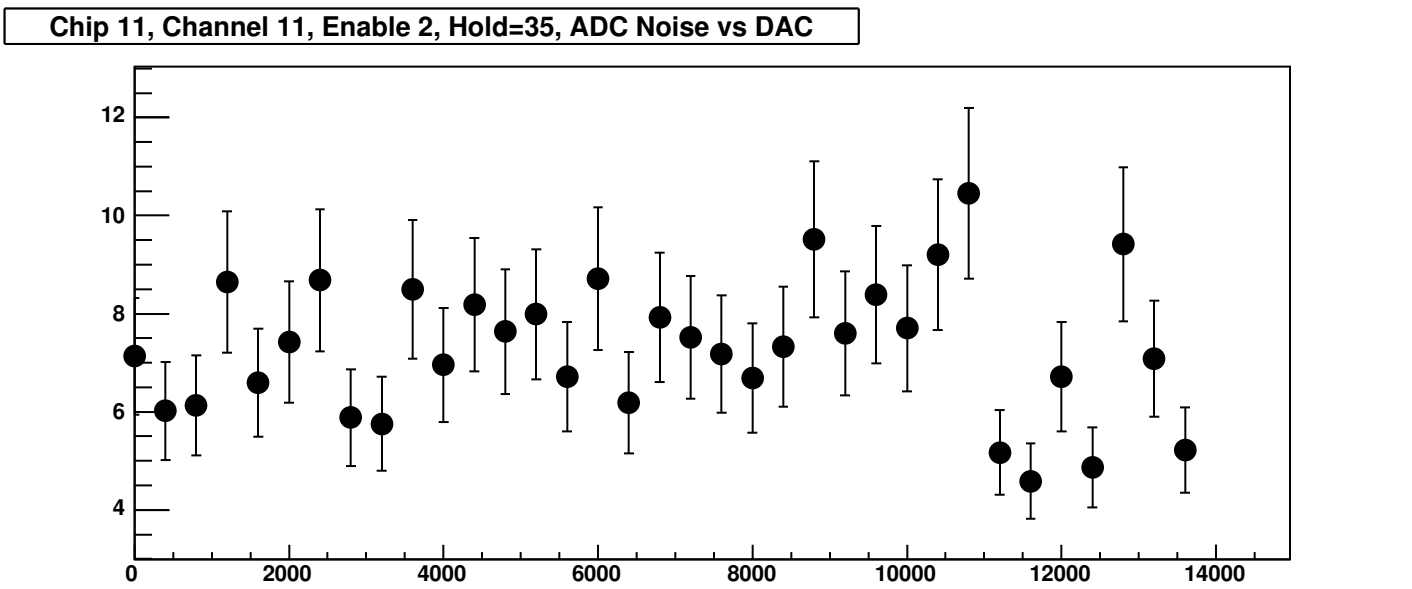

Chip 11, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

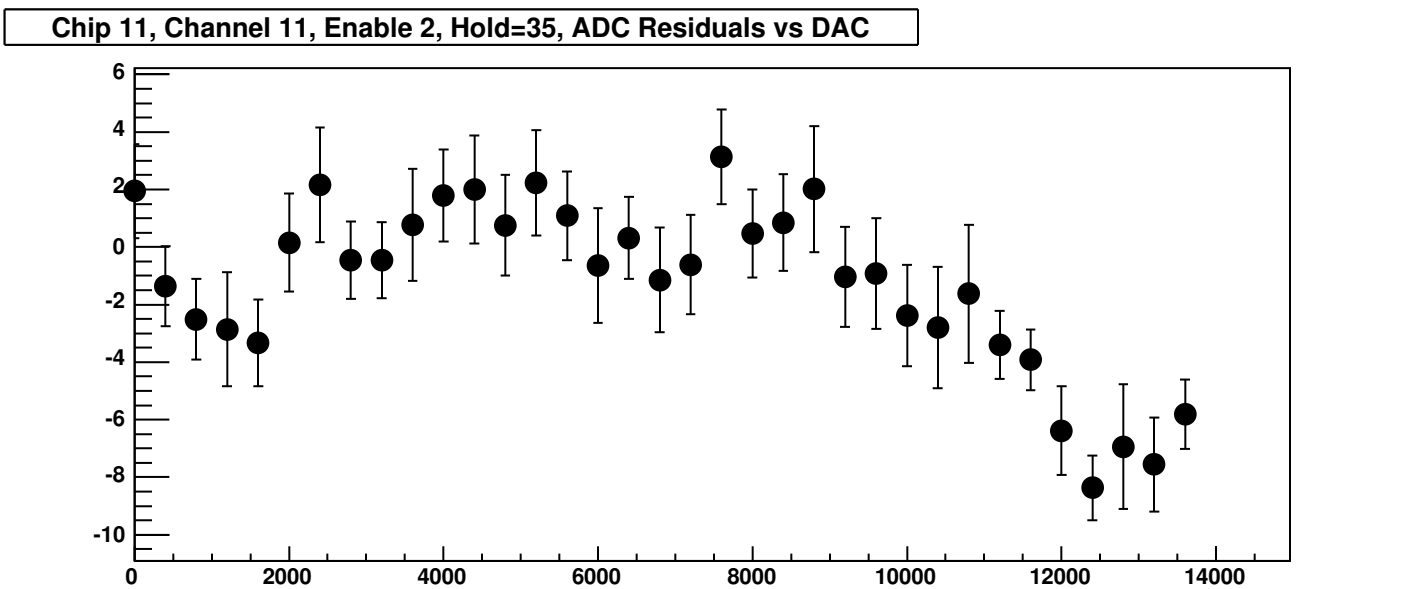

Chip 11, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 55.28 / 23
p0 -412.5 ± 0.6506
p1 0.01999 ± 0.000103
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

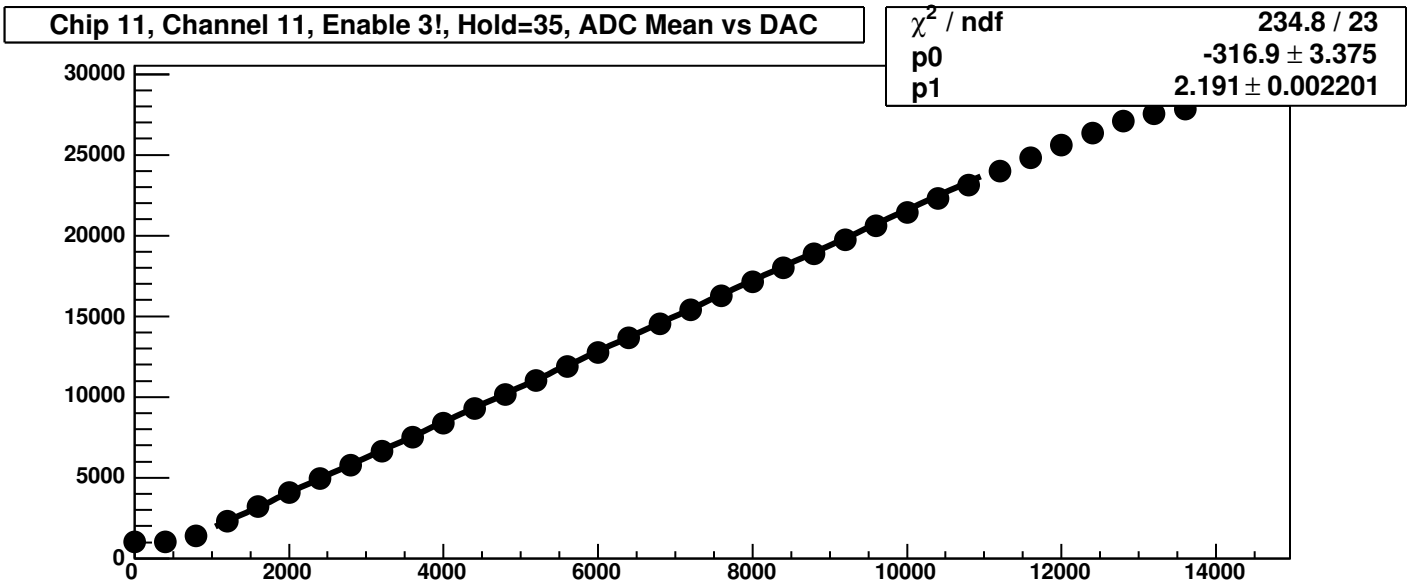

Chip 11, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

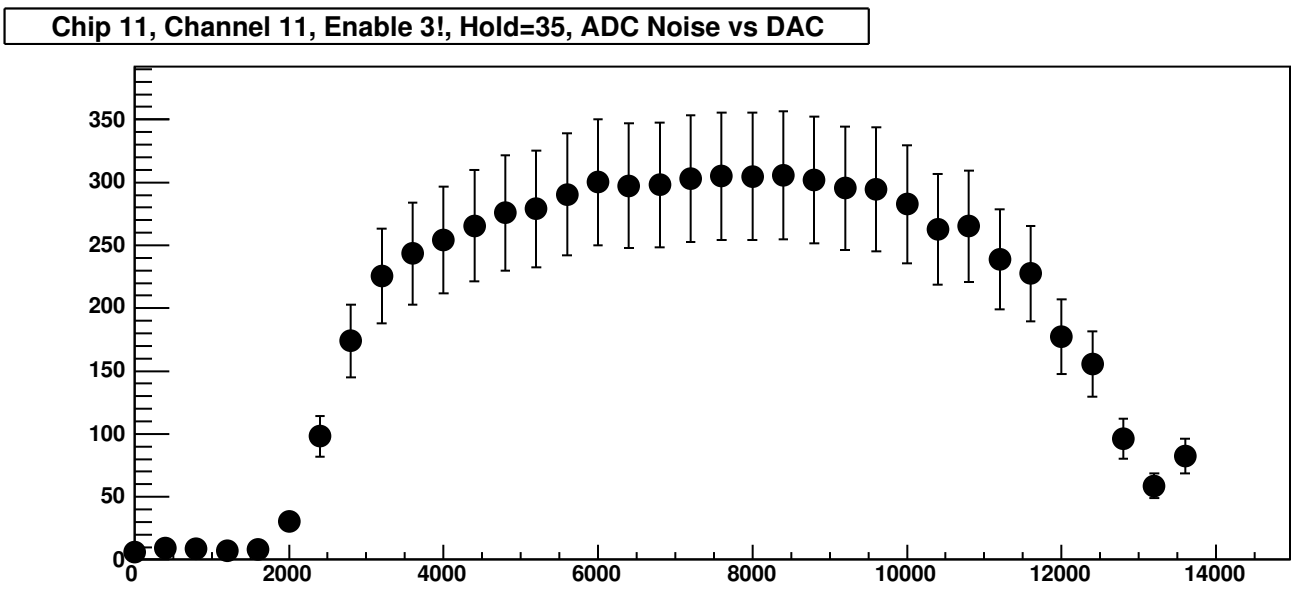

Chip 11, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

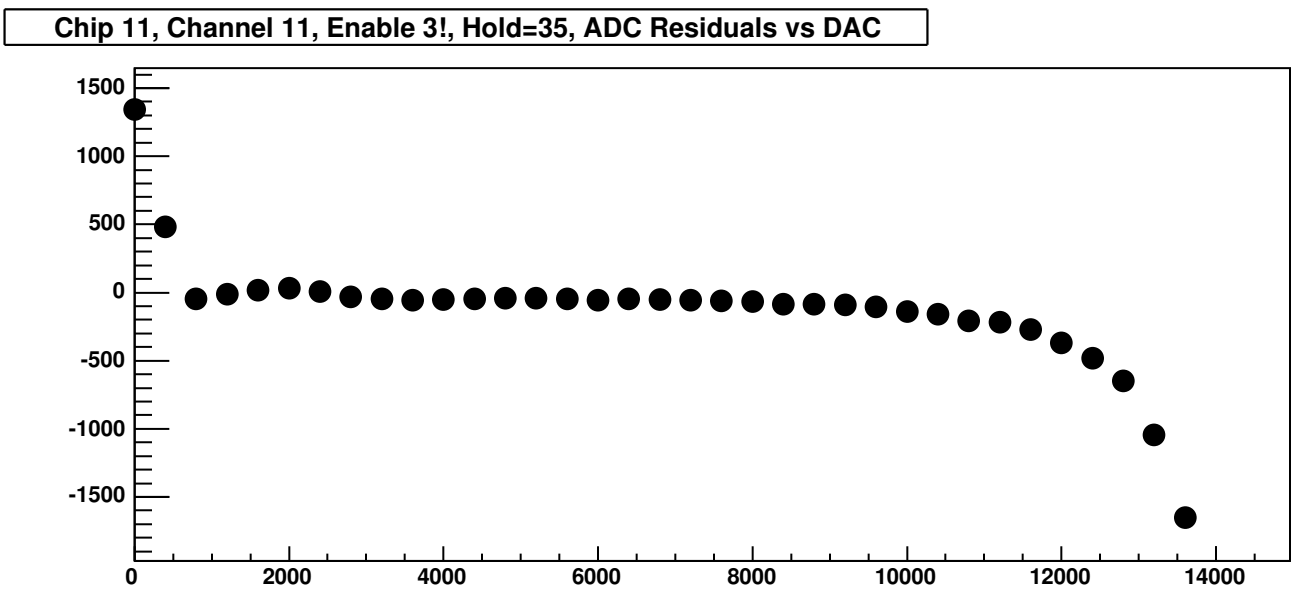

Chip 11, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 23.15 / 23
p0 -408.9 ± 0.7883
p1 0.01313 ± 0.0001246
-220
-240
-260
-280
-300
-320
-340
-360
-380
-400
-420
0
2000
4000
6000
8000
10000
12000
14000

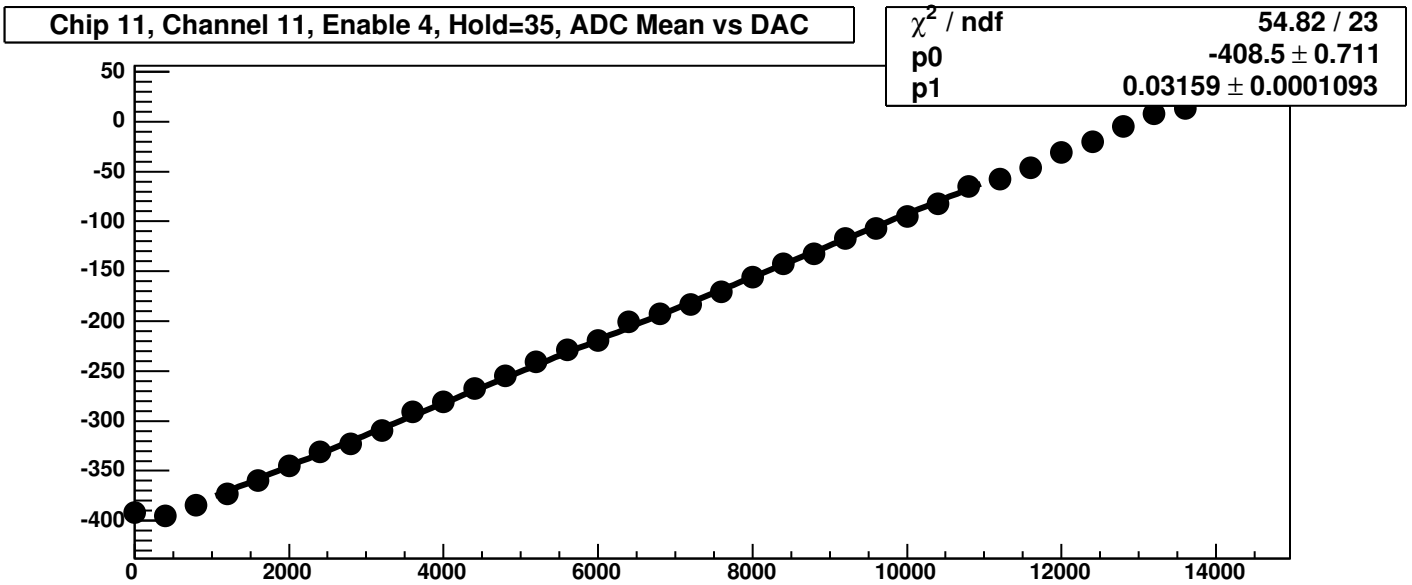

Chip 11, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

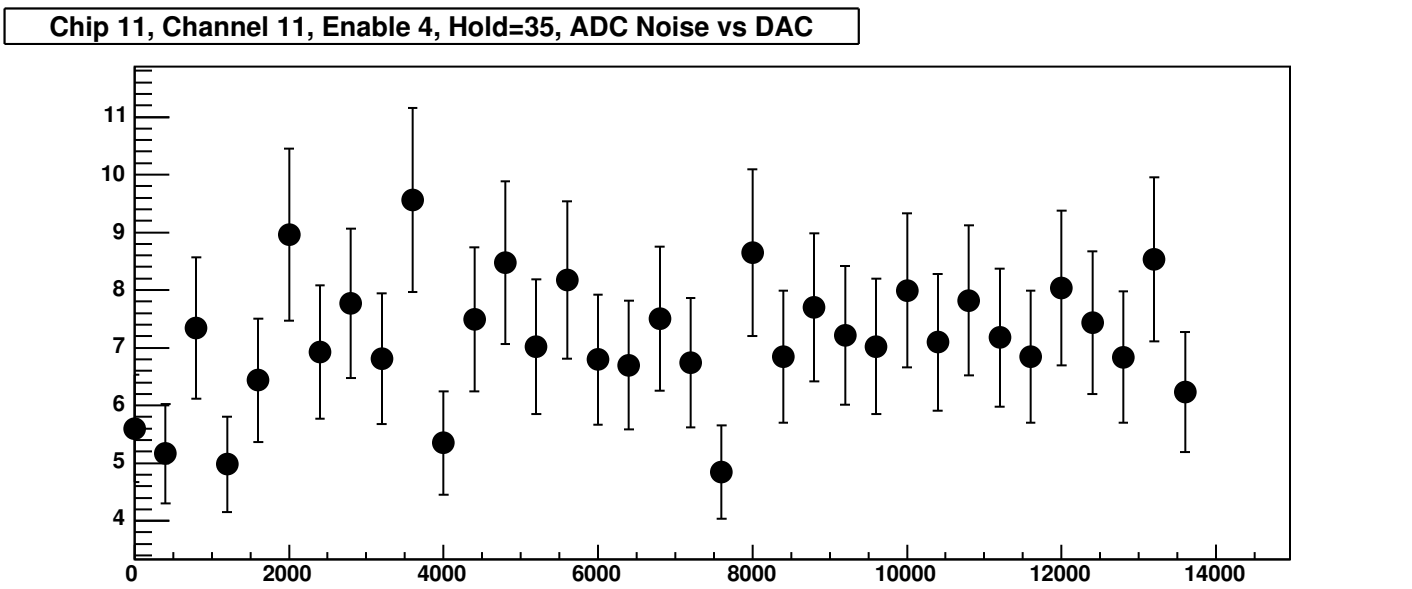

Chip 11, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
0
2000
4000
6000
8000
10000
12000
14000

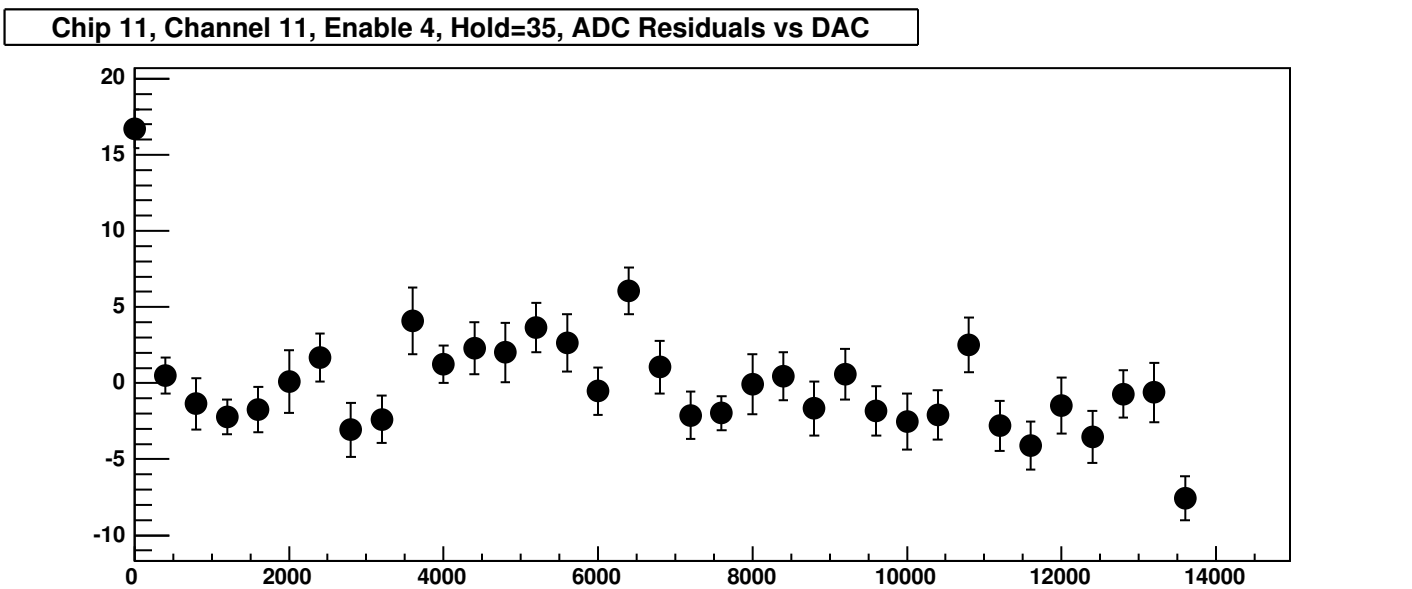

Chip 11, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 234.8 / 23
p0 -316.9 ± 3.375
p1 2.191 ± 0.002201
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

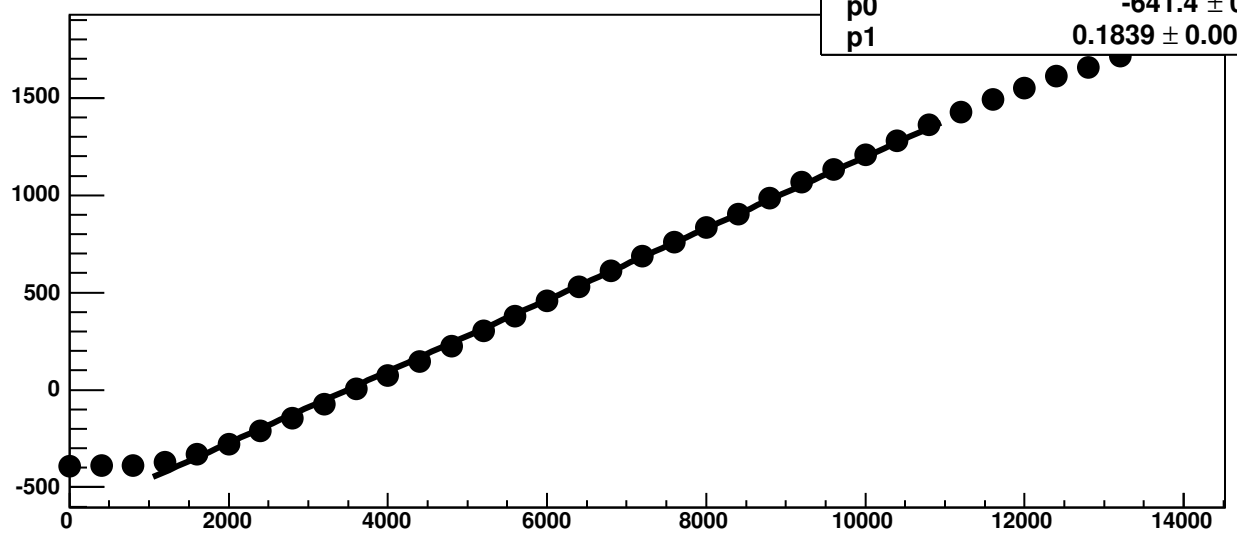

Chip 11, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 54.82 / 23
p0 -408.5 ± 0.711
p1 0.03159 ± 0.0001093
50
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

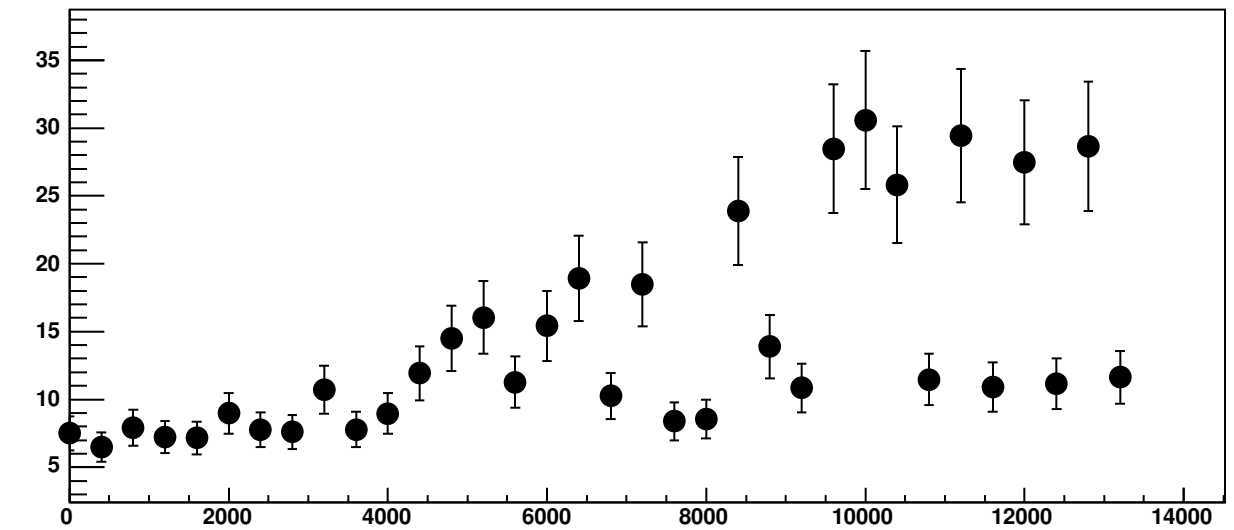

Chip 11, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

Chip 11, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

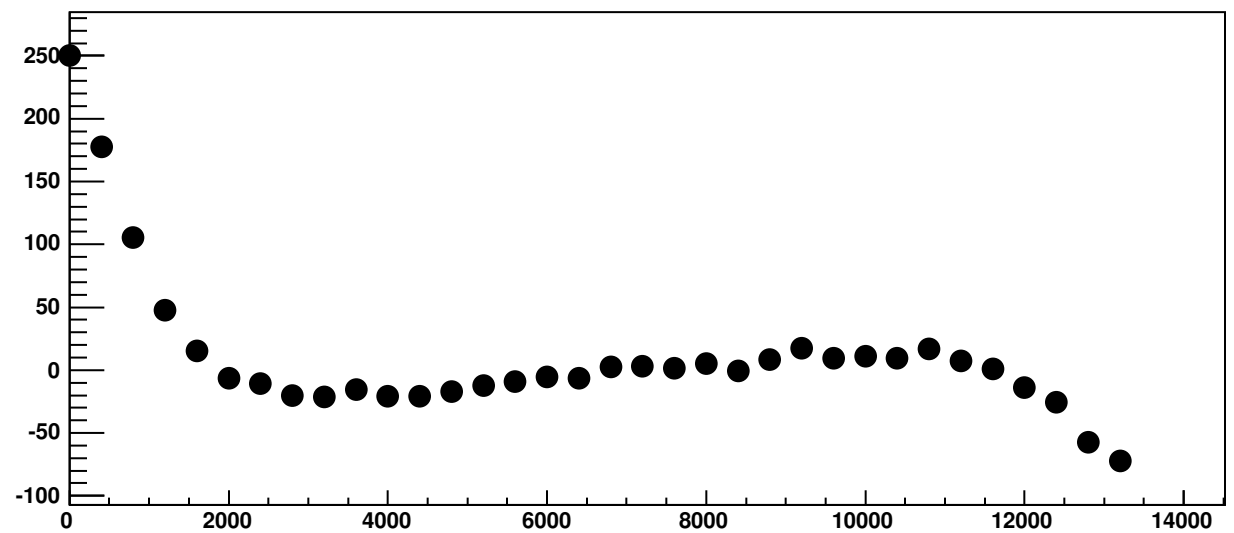
χ^2 / ndf	1570 / 23
p0	-641.4 ± 0.9476
p1	0.1839 ± 0.0001742

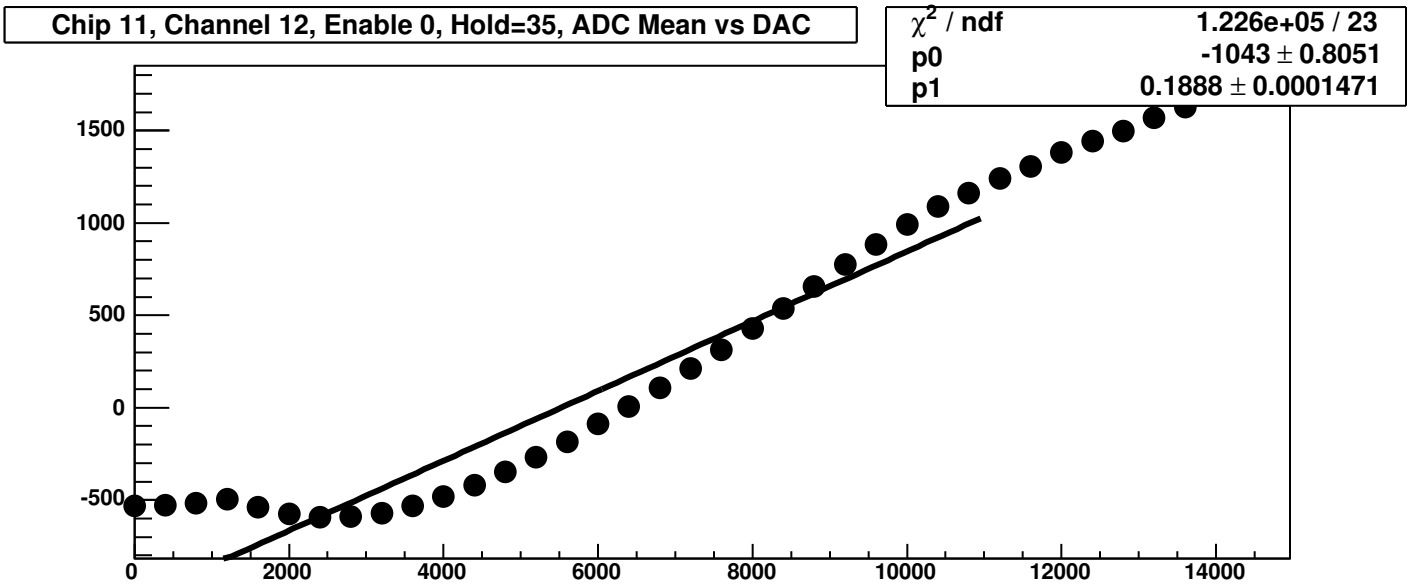


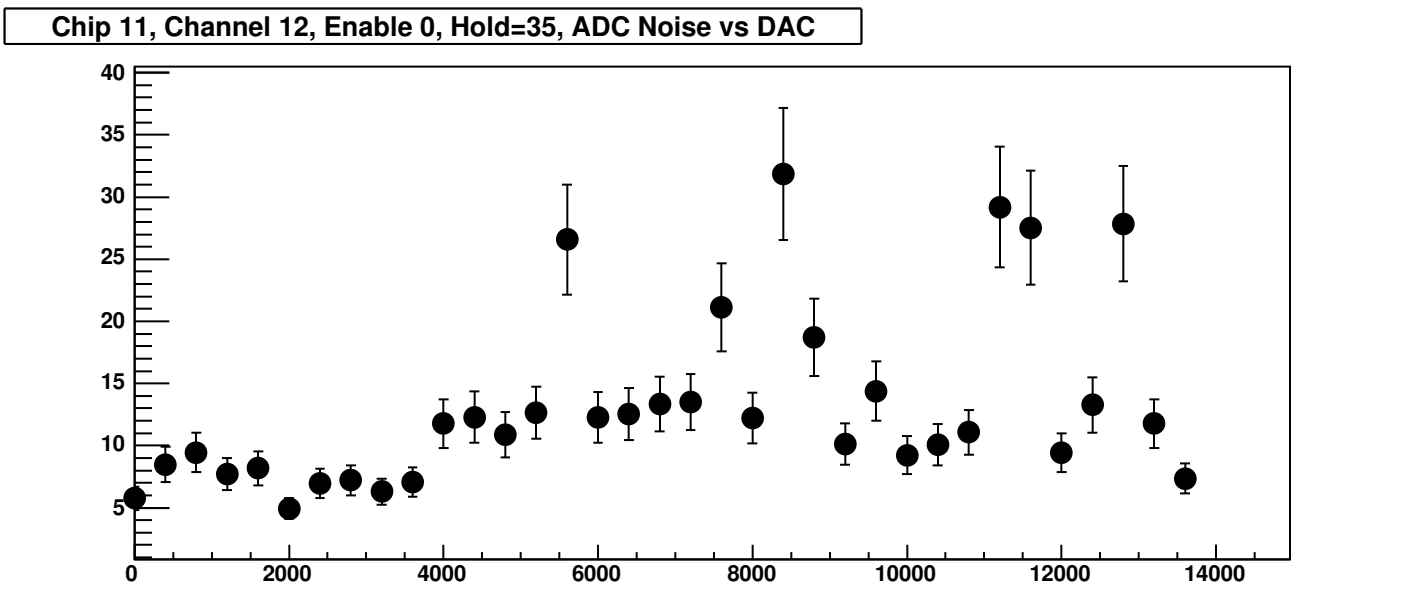
Chip 11, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

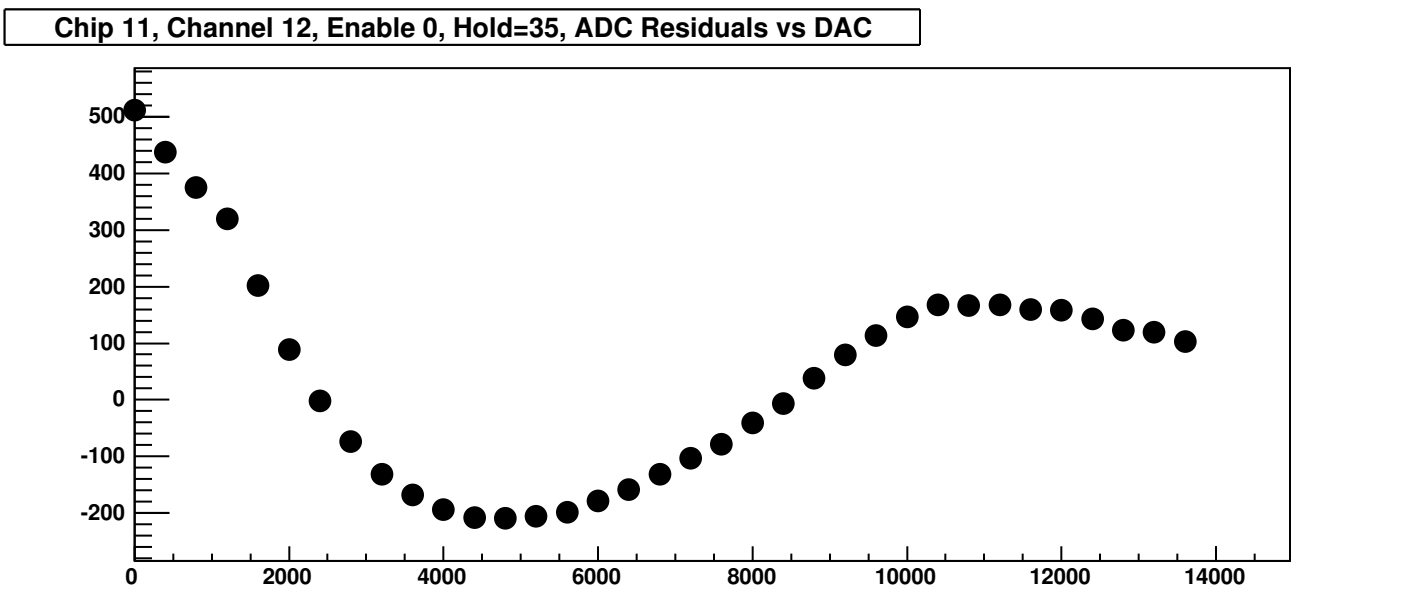


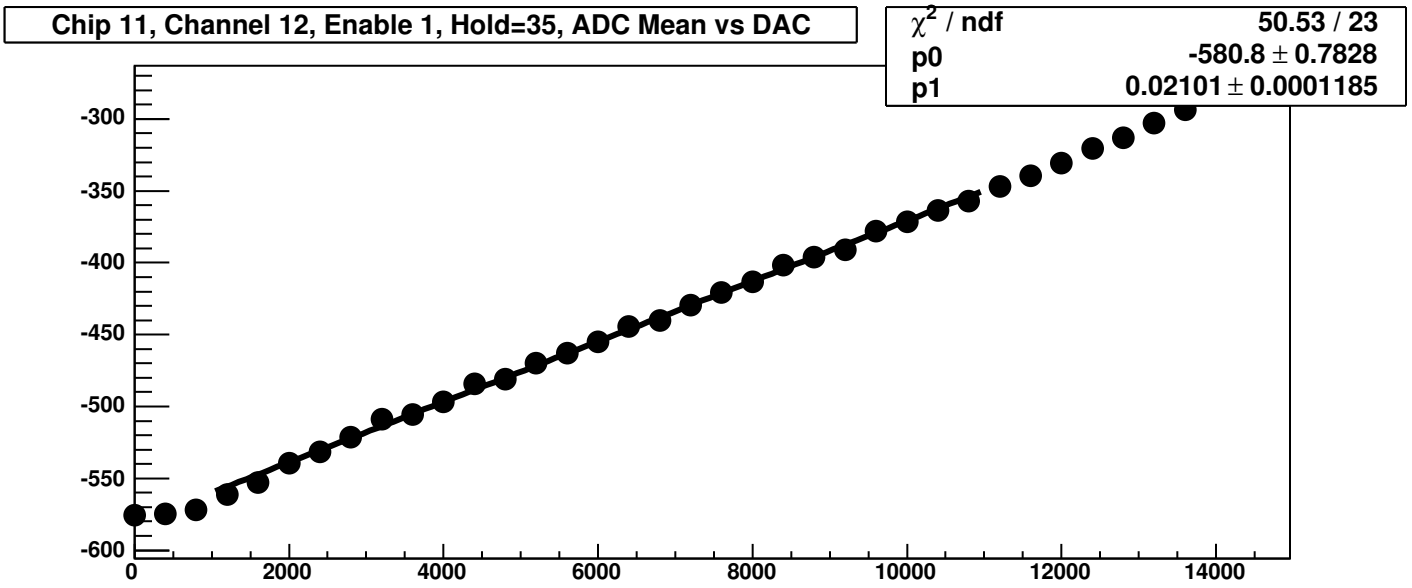
Chip 11, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

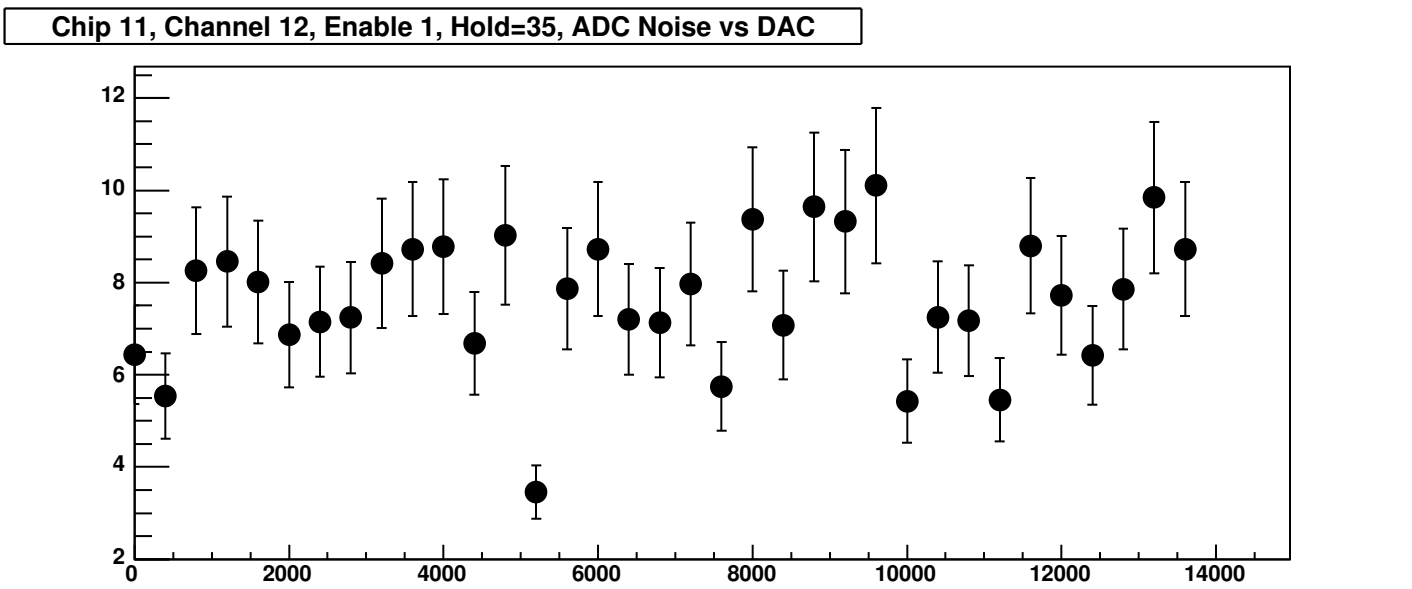


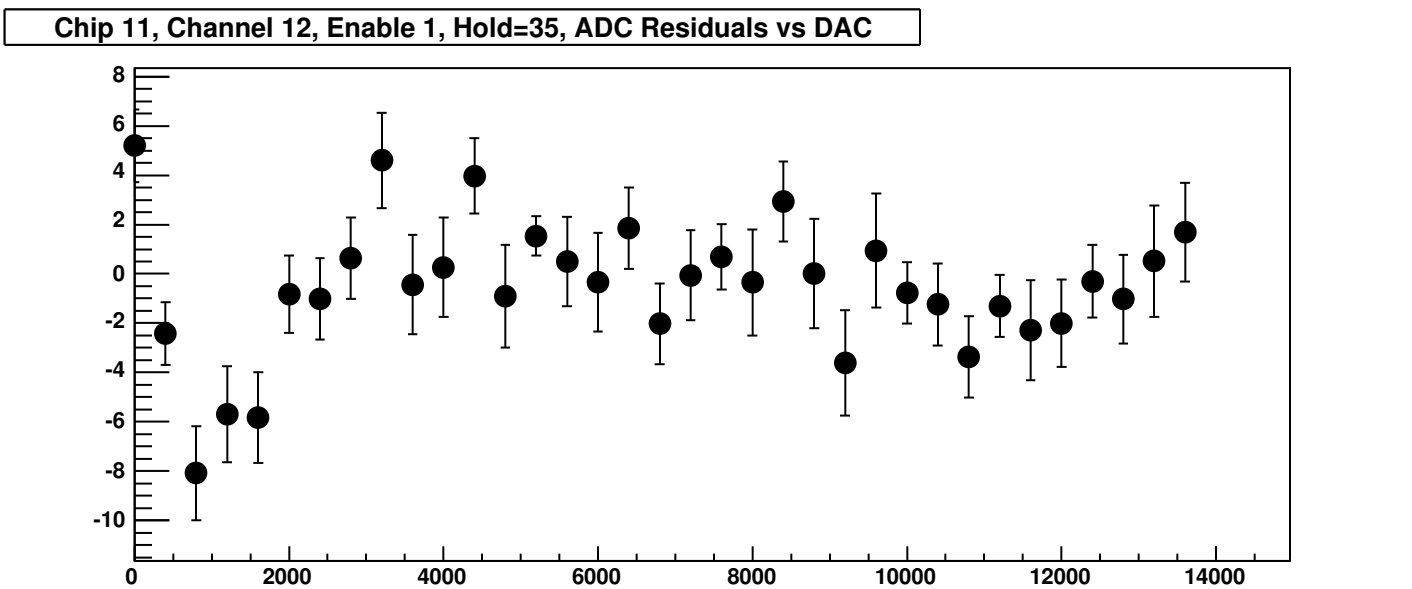

Chip 11, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.226e+05 / 23
p0 -1043 ± 0.8051
p1 0.1888 ± 0.0001471
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

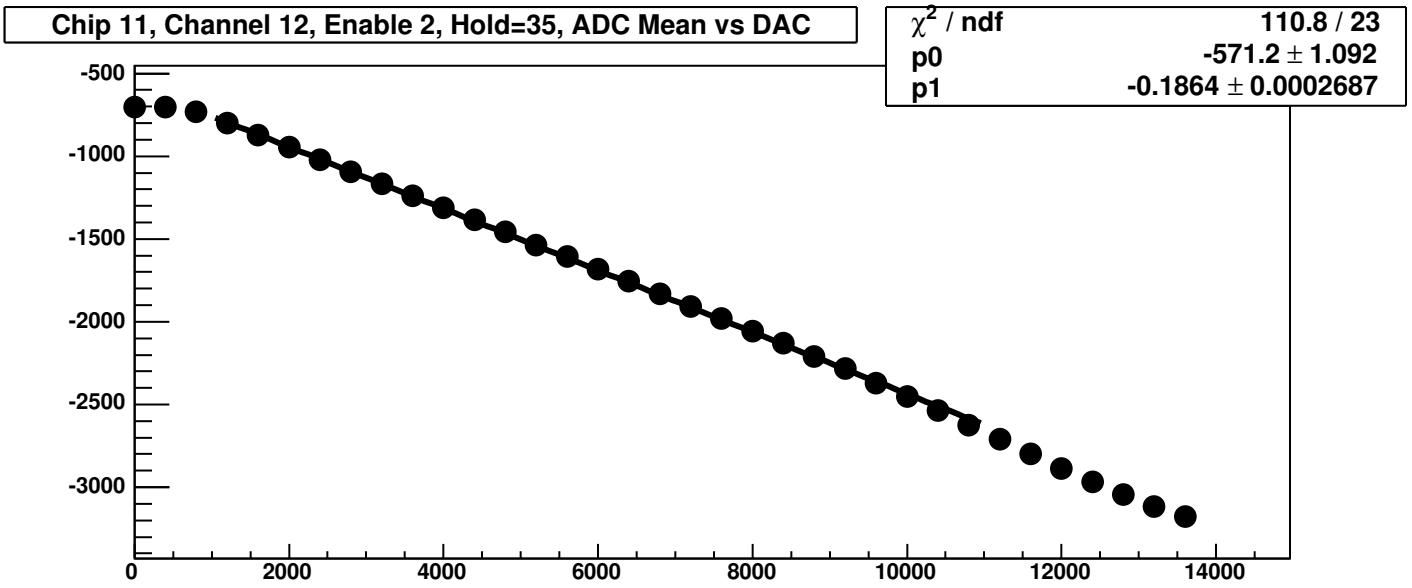

Chip 11, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

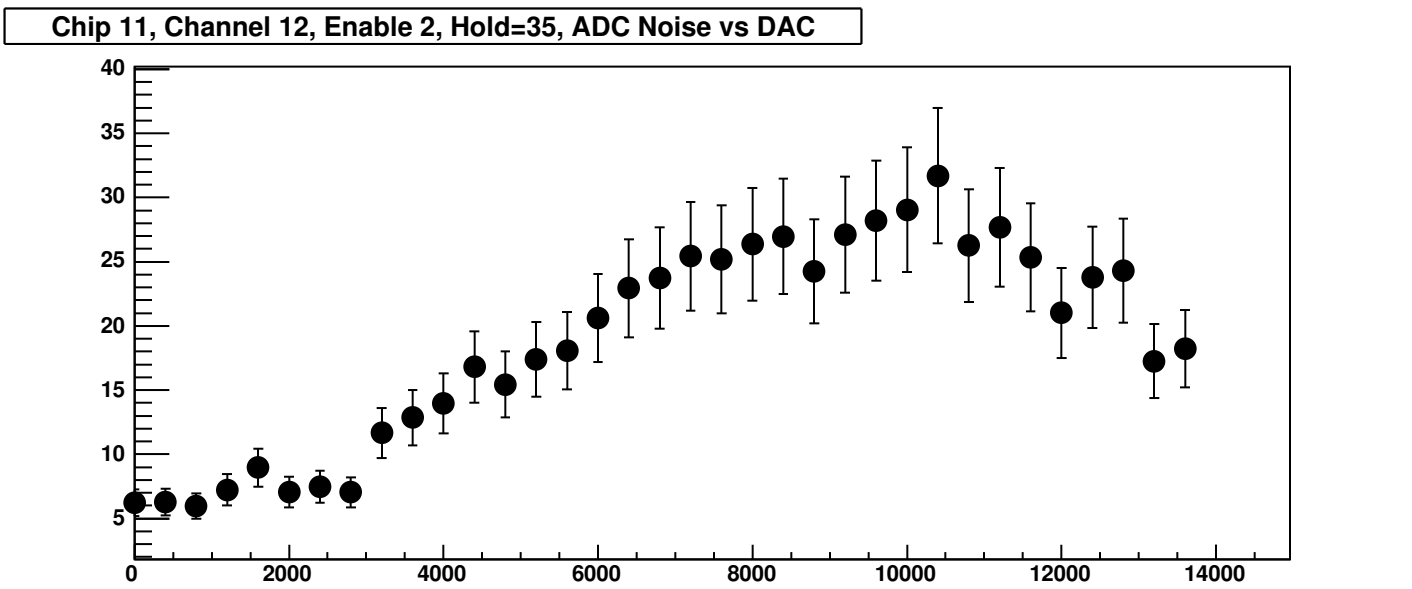

Chip 11, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

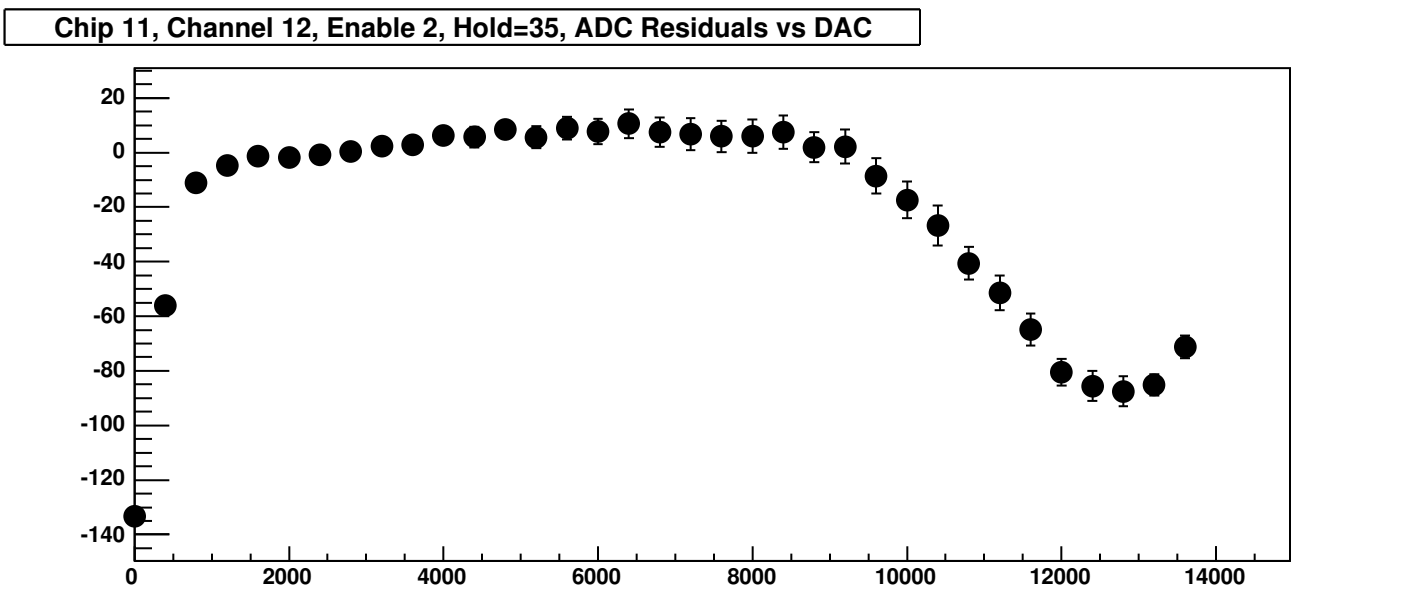

Chip 11, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 50.53 / 23
p0 -580.8 ± 0.7828
p1 0.02101 ± 0.0001185
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

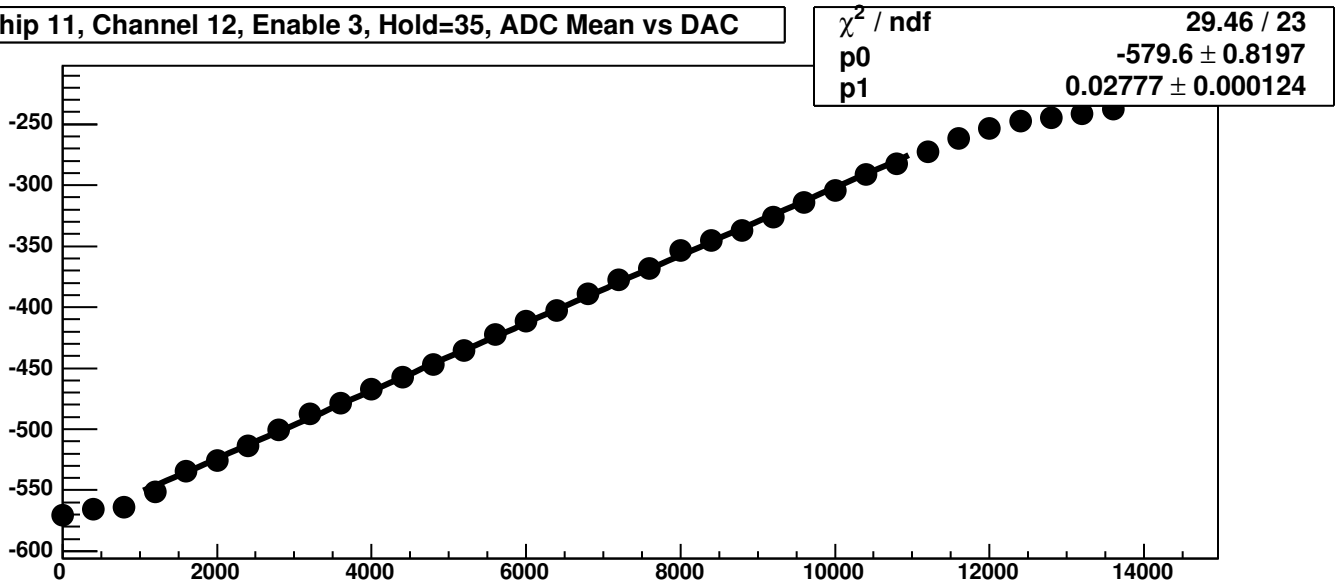

Chip 11, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 110.8 / 23
p0 -571.2 ± 1.092
p1 -0.1864 ± 0.0002687
-500
-1000
-1500
-2000
-2500
-3000
0
2000
4000
6000
8000
10000
12000
14000

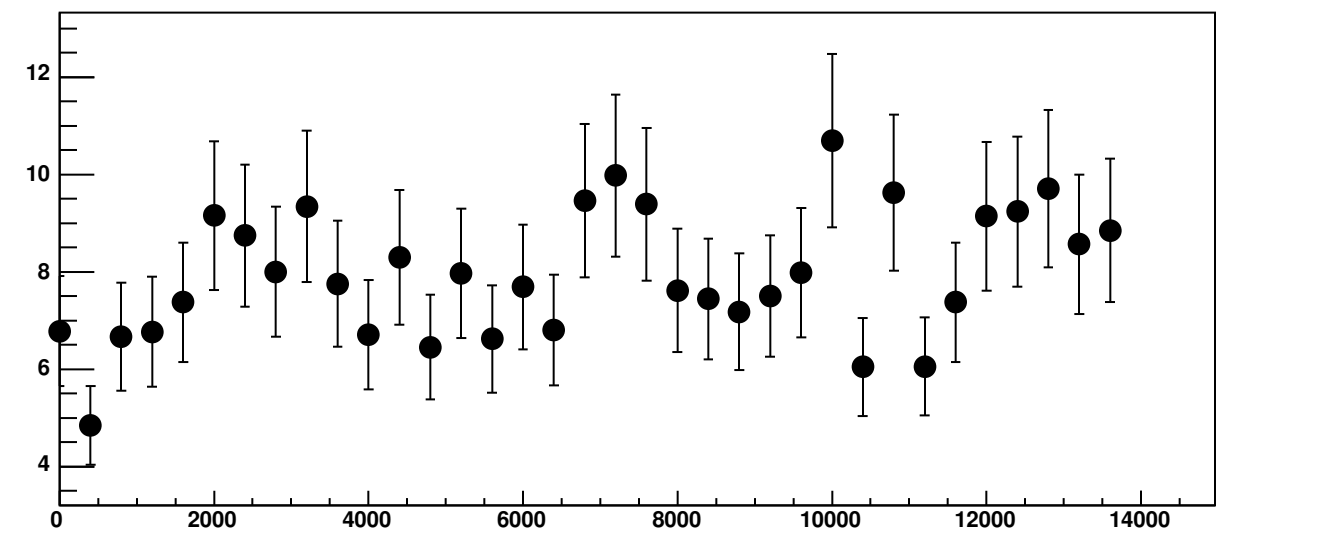

Chip 11, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
-100
-120
-140
0
2000
4000
6000
8000
10000
12000
14000

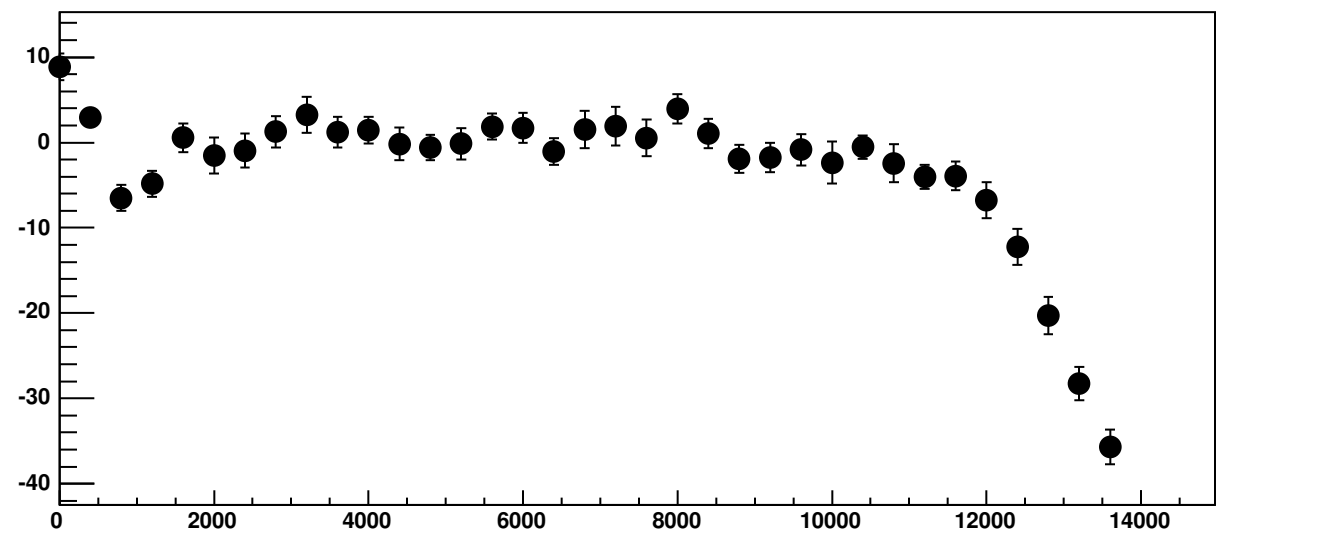
Chip 11, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

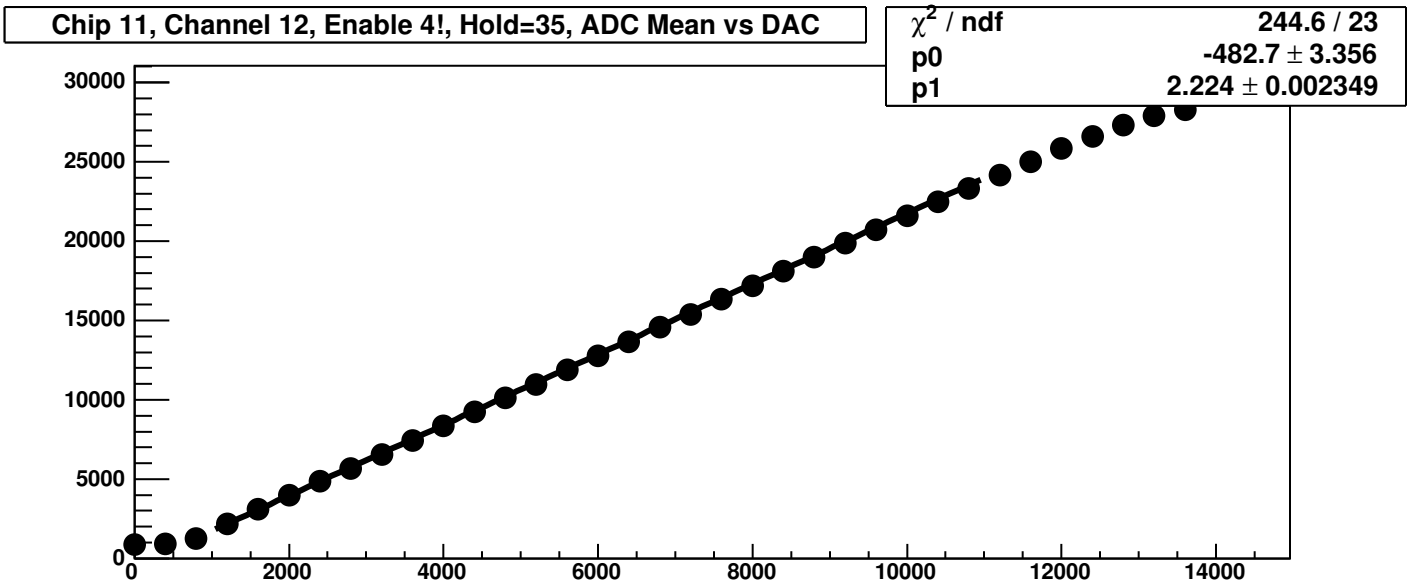


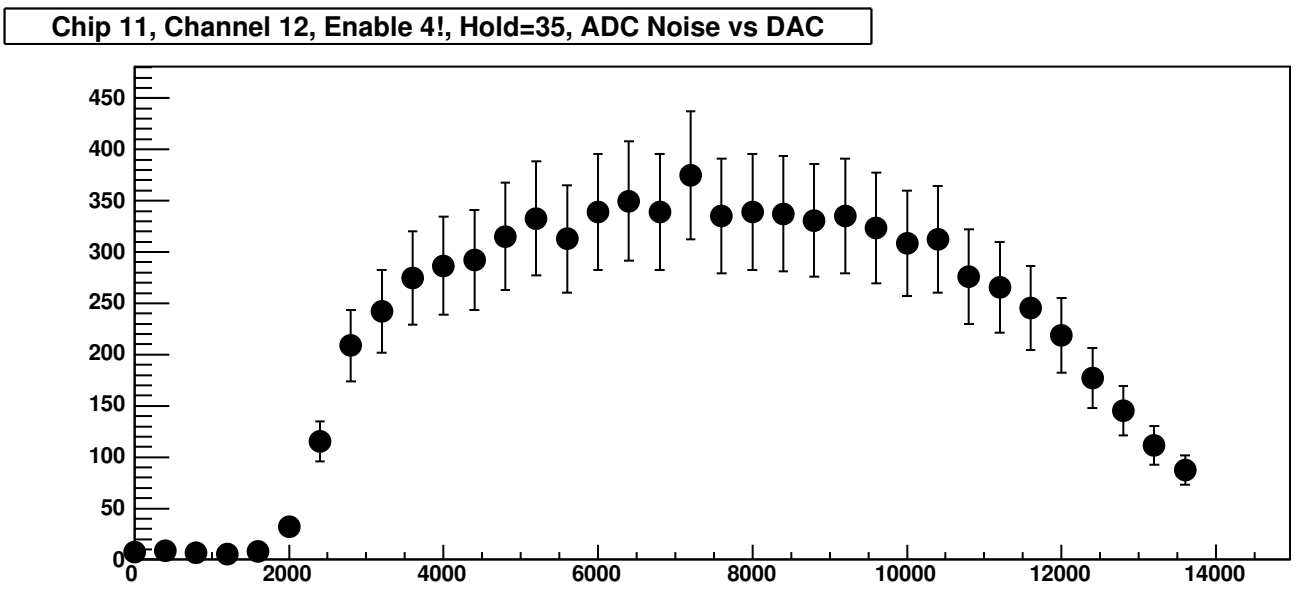
Chip 11, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC

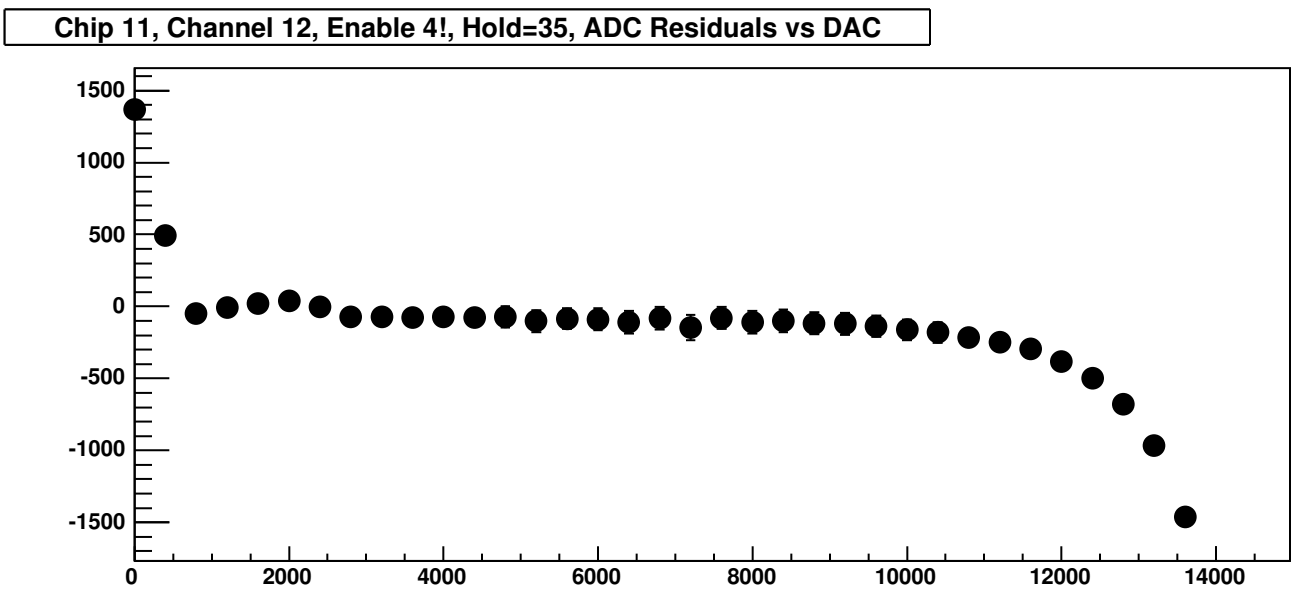


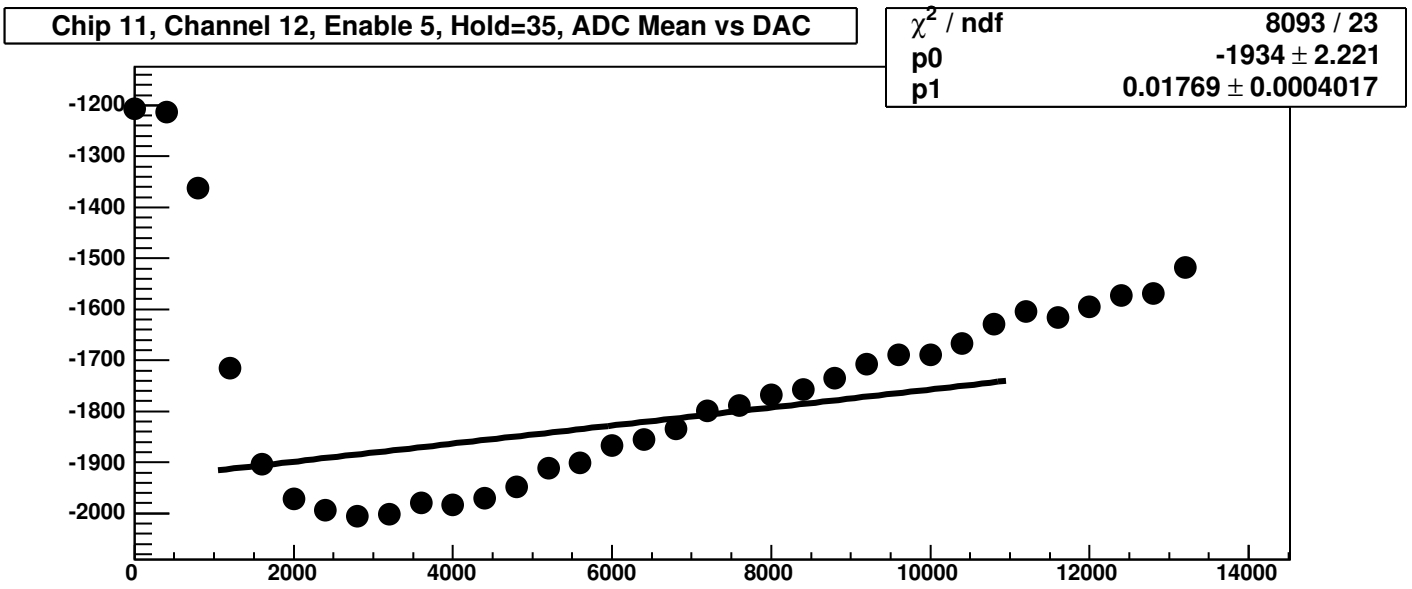
Chip 11, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

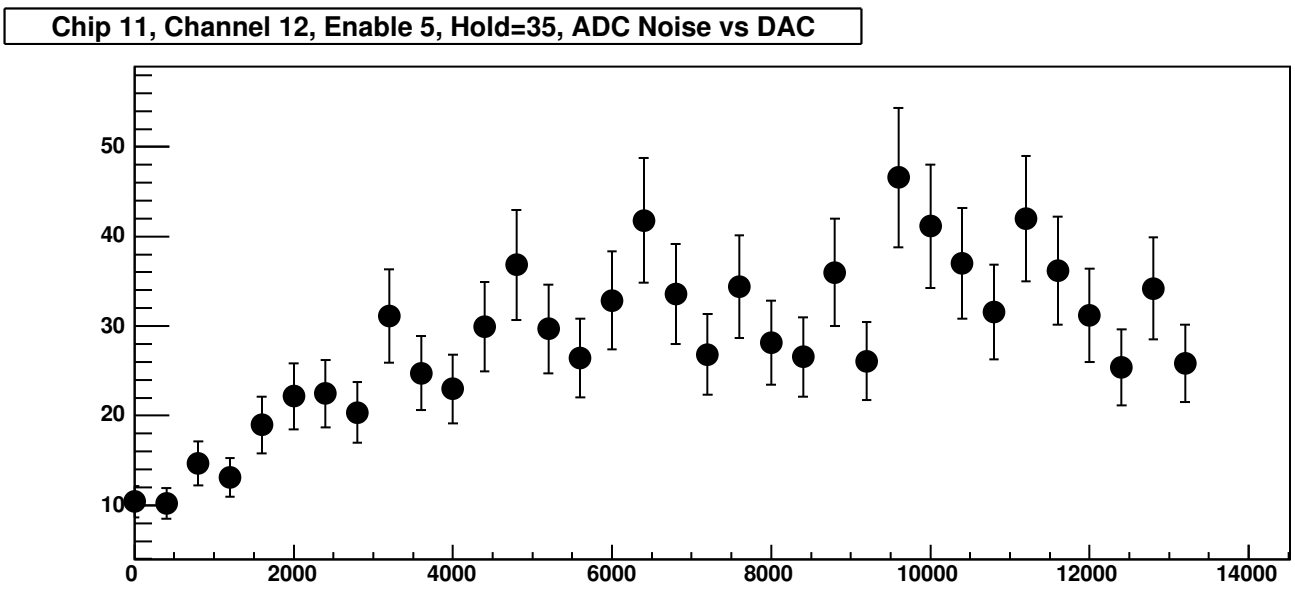


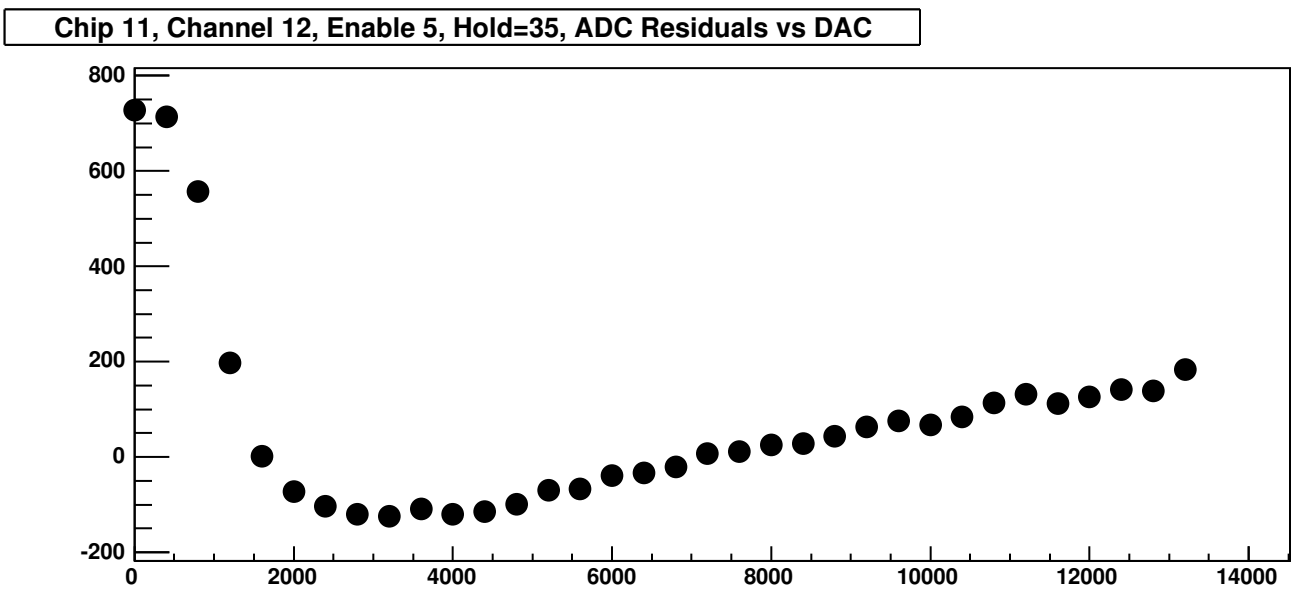

Chip 11, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 244.6 / 23
p0 -482.7 ± 3.356
p1 2.224 ± 0.002349
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

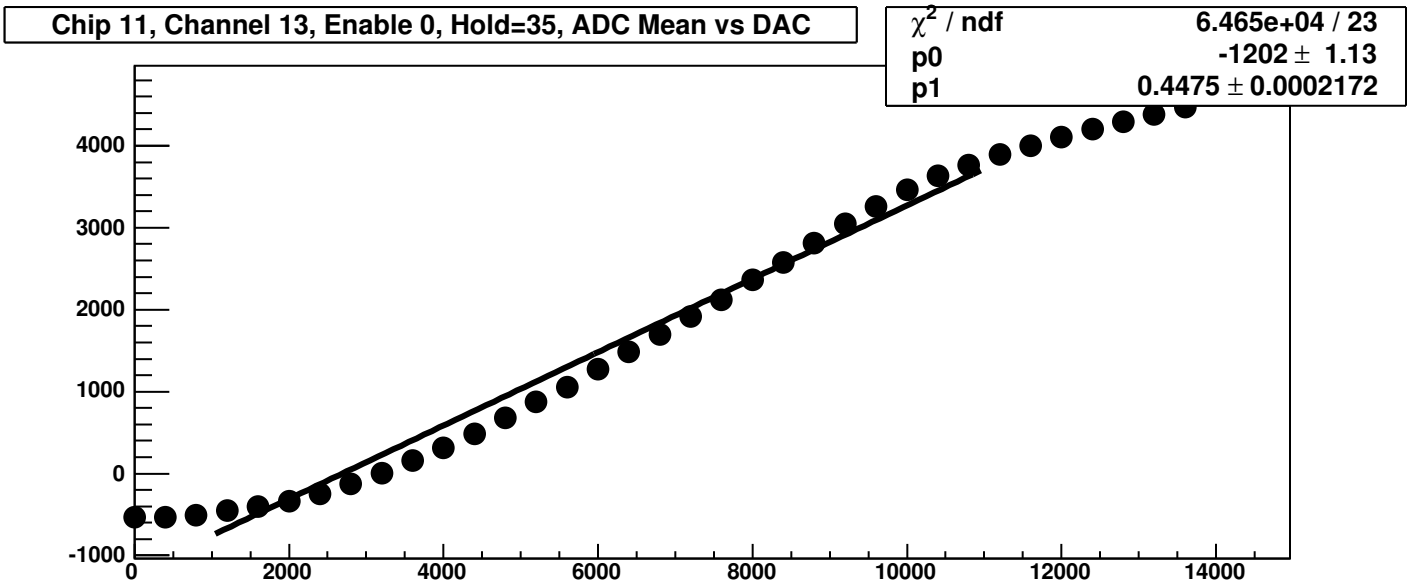

Chip 11, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

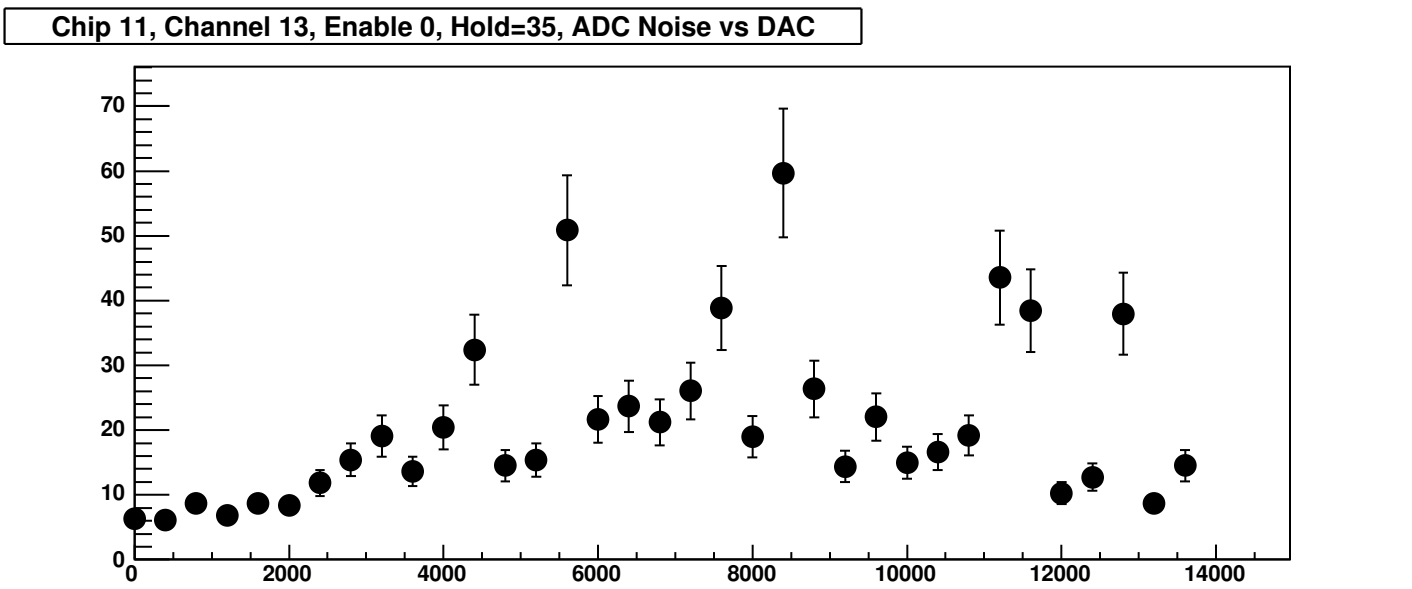

Chip 11, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

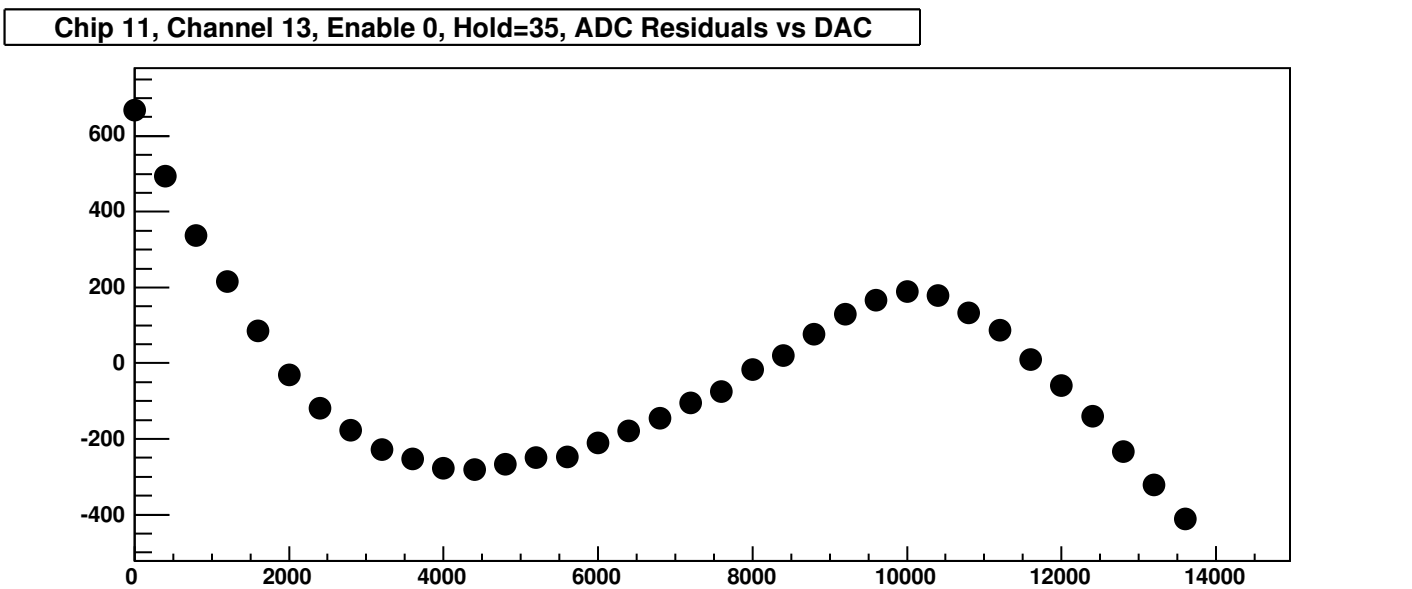

Chip 11, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 8093 / 23
p0 -1934 ± 2.221
p1 0.01769 ± 0.0004017
-1200
-1300
-1400
-1500
-1600
-1700
-1800
-1900
-2000
0
2000
4000
6000
8000
10000
12000
14000

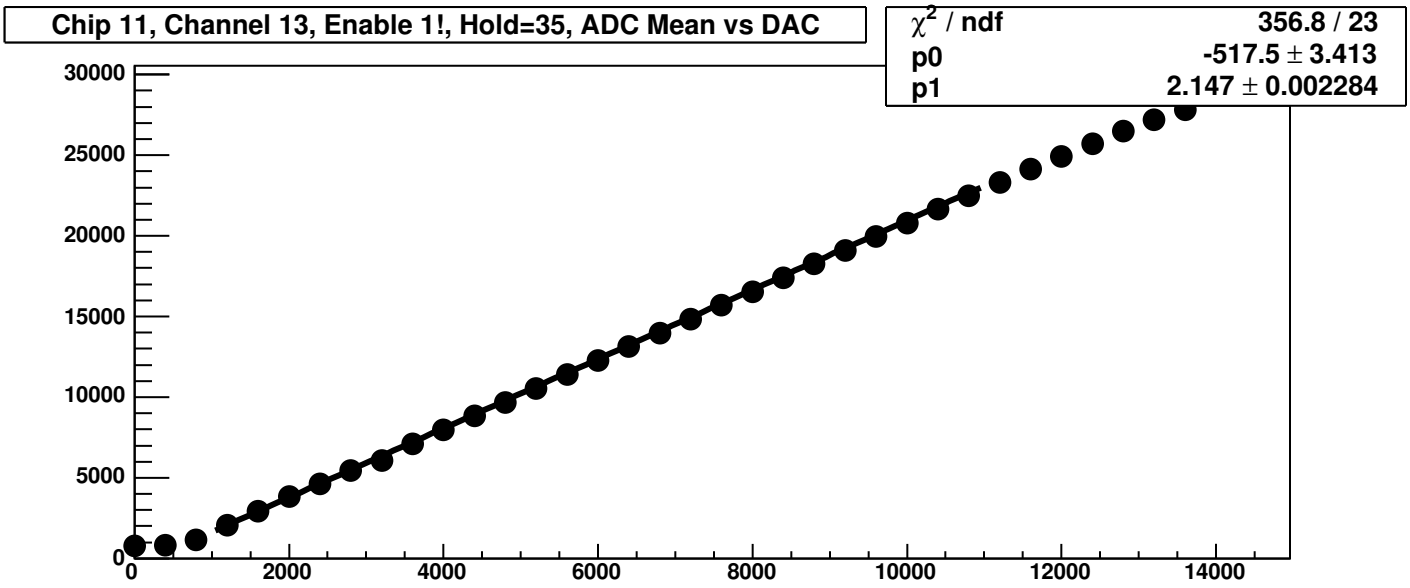

Chip 11, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

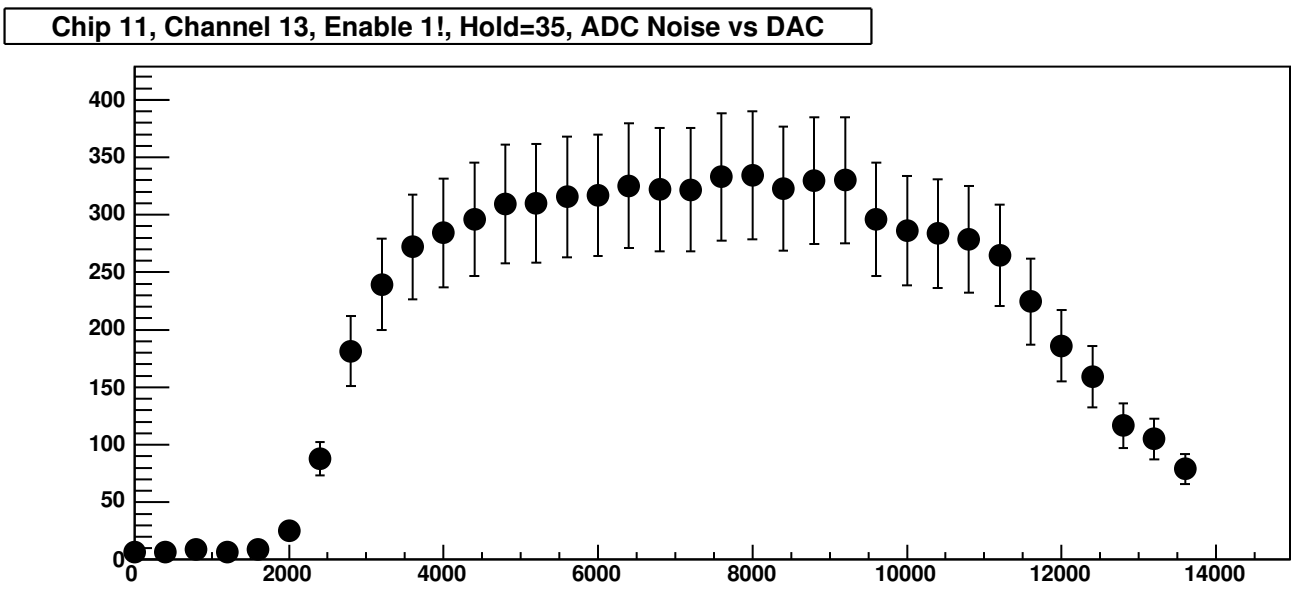

Chip 11, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

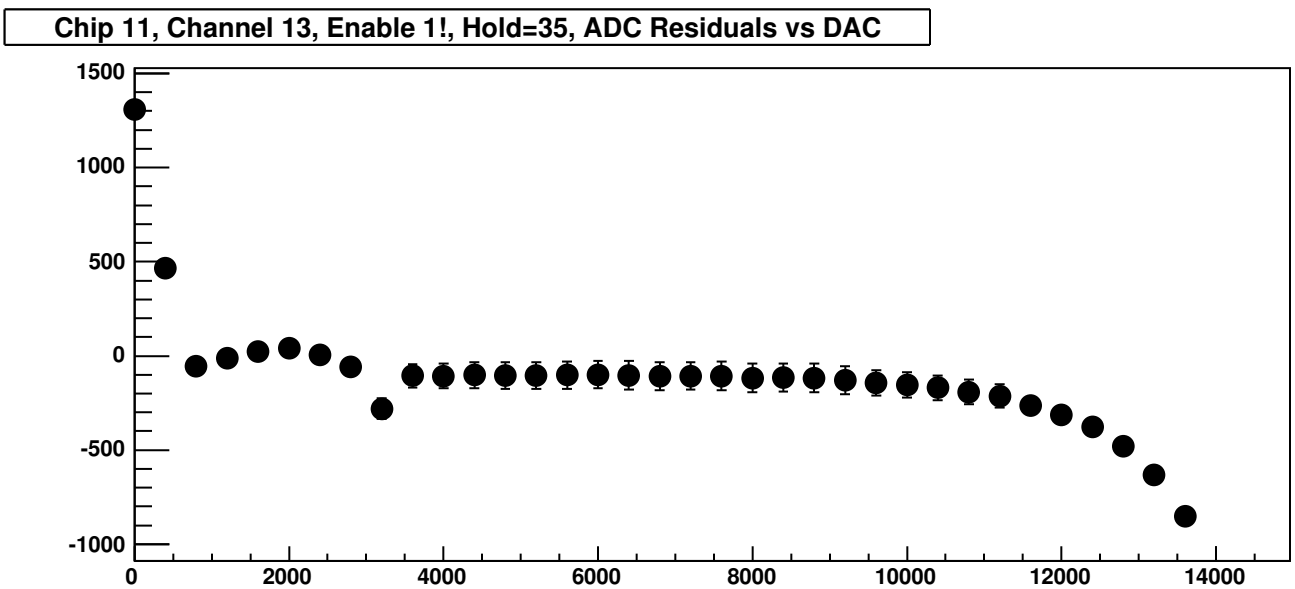

Chip 11, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 6.465e+04 / 23
p0 -1202 ± 1.13
p1 0.4475 ± 0.0002172
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

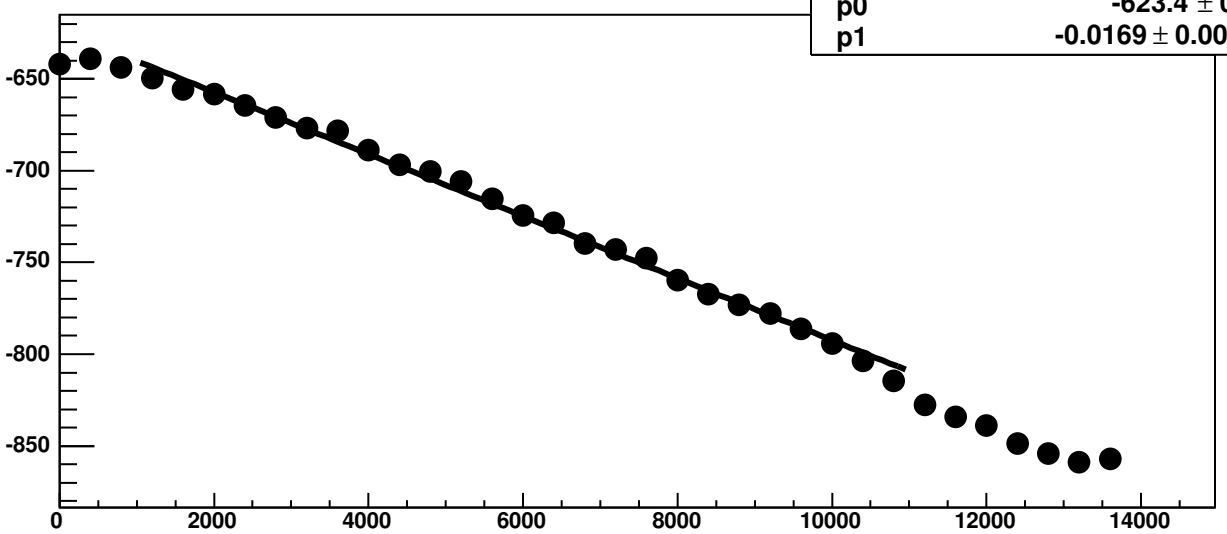

Chip 11, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 356.8 / 23
p0 -517.5 ± 3.413
p1 2.147 ± 0.002284
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

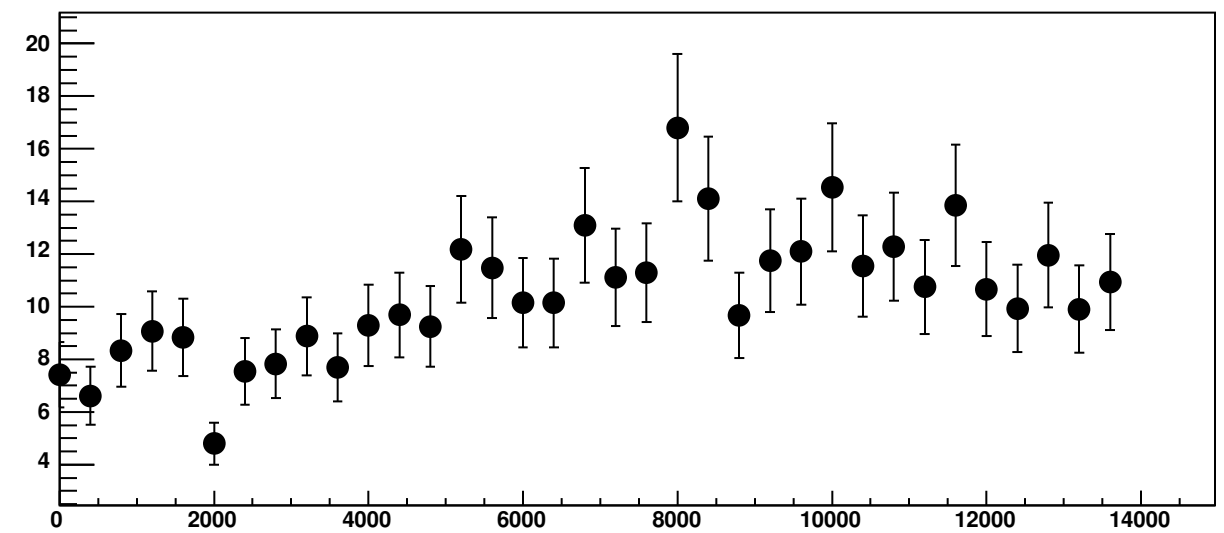

Chip 11, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

Chip 11, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

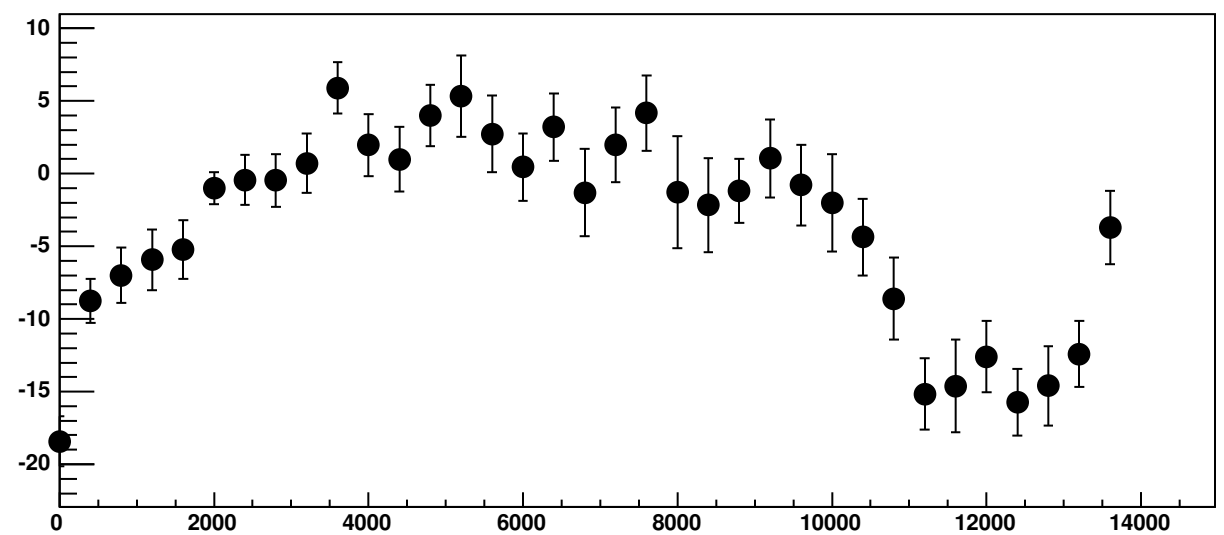
χ^2 / ndf	55.08 / 23
p0	-623.4 ± 0.8595
p1	-0.0169 ± 0.0001543

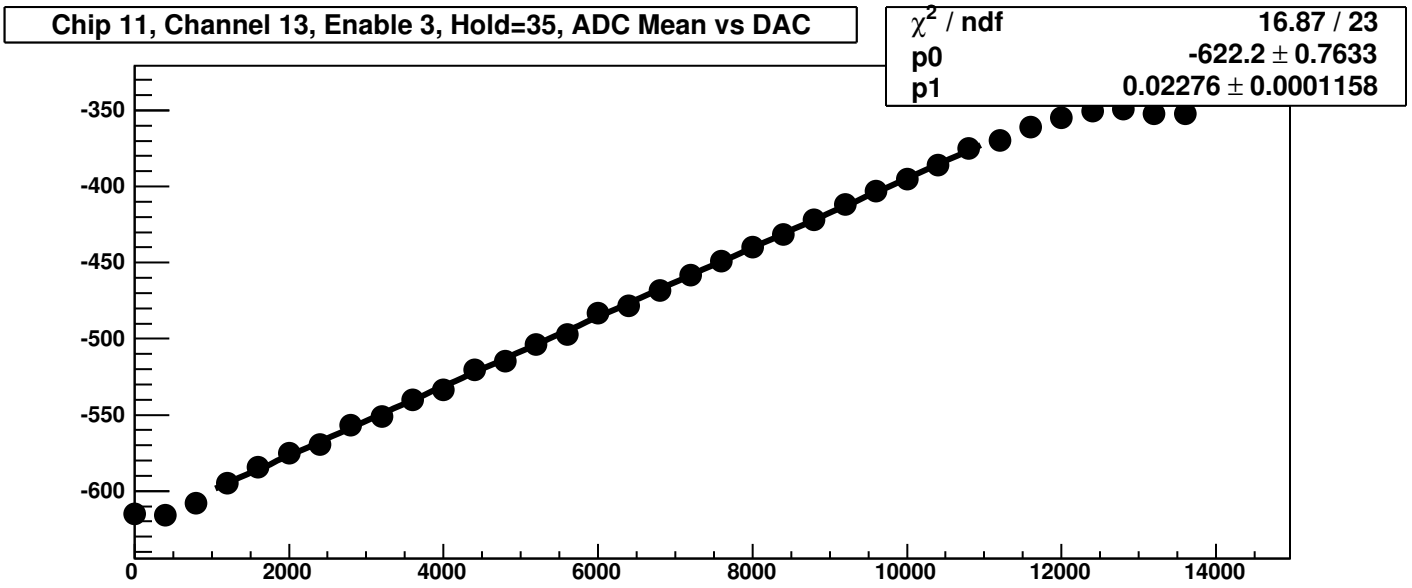


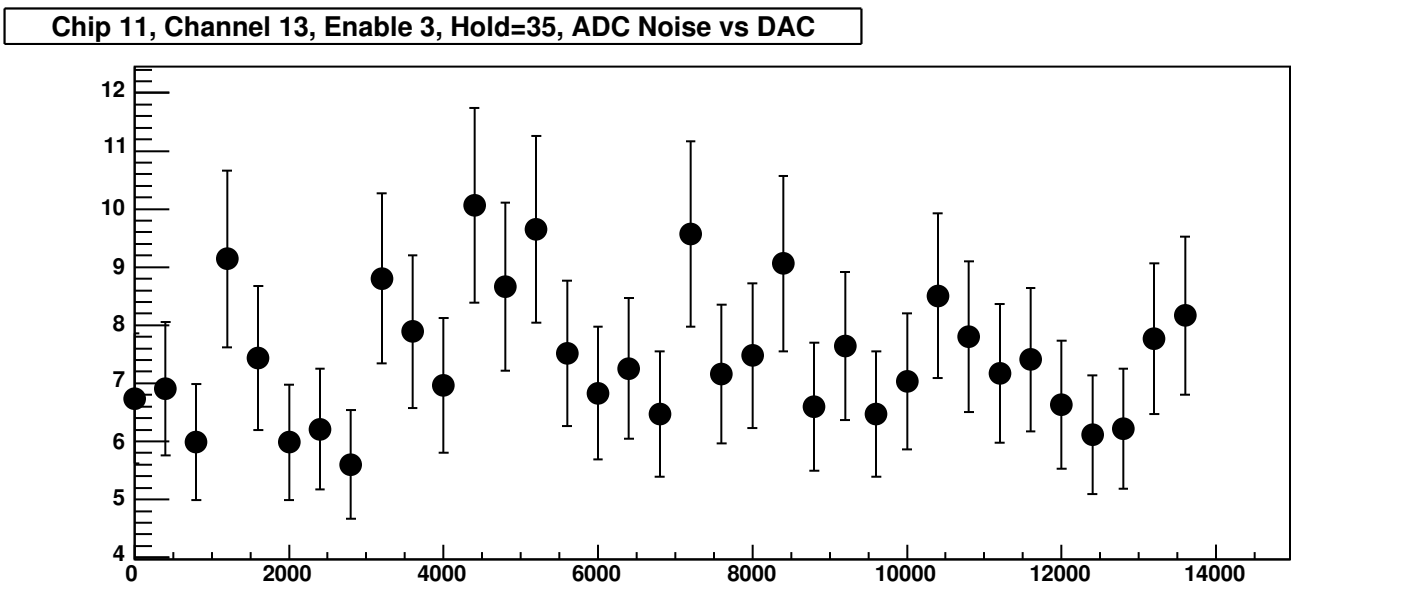
Chip 11, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC

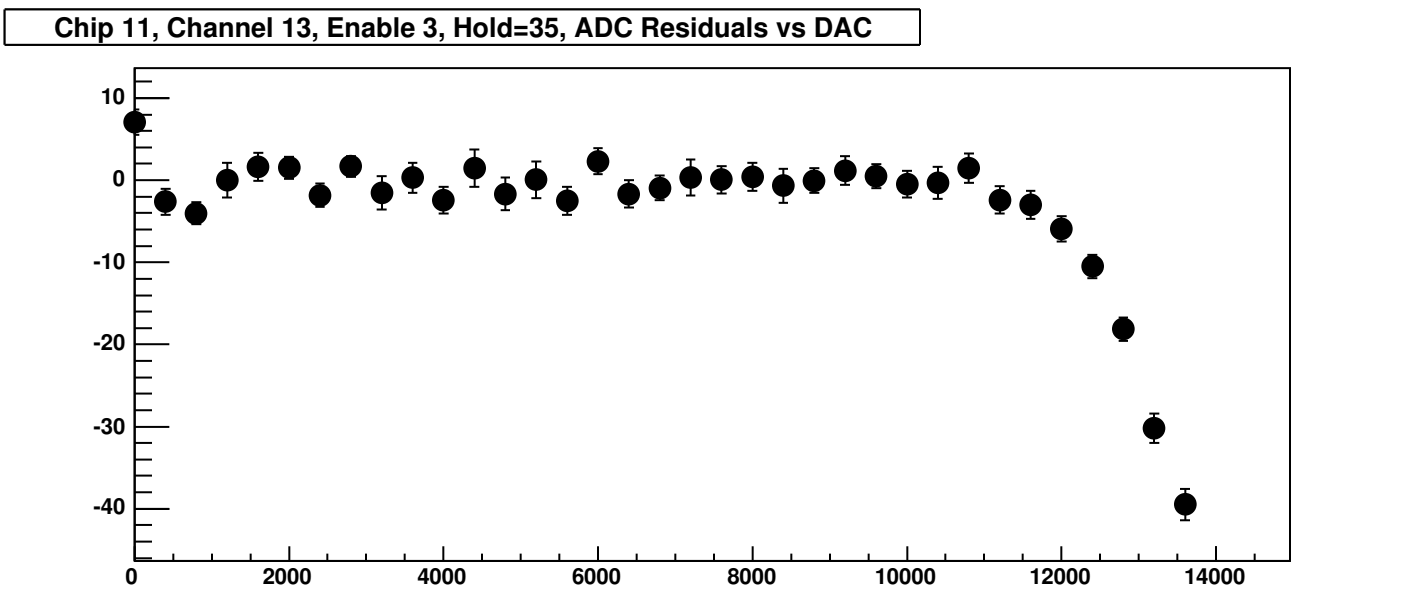


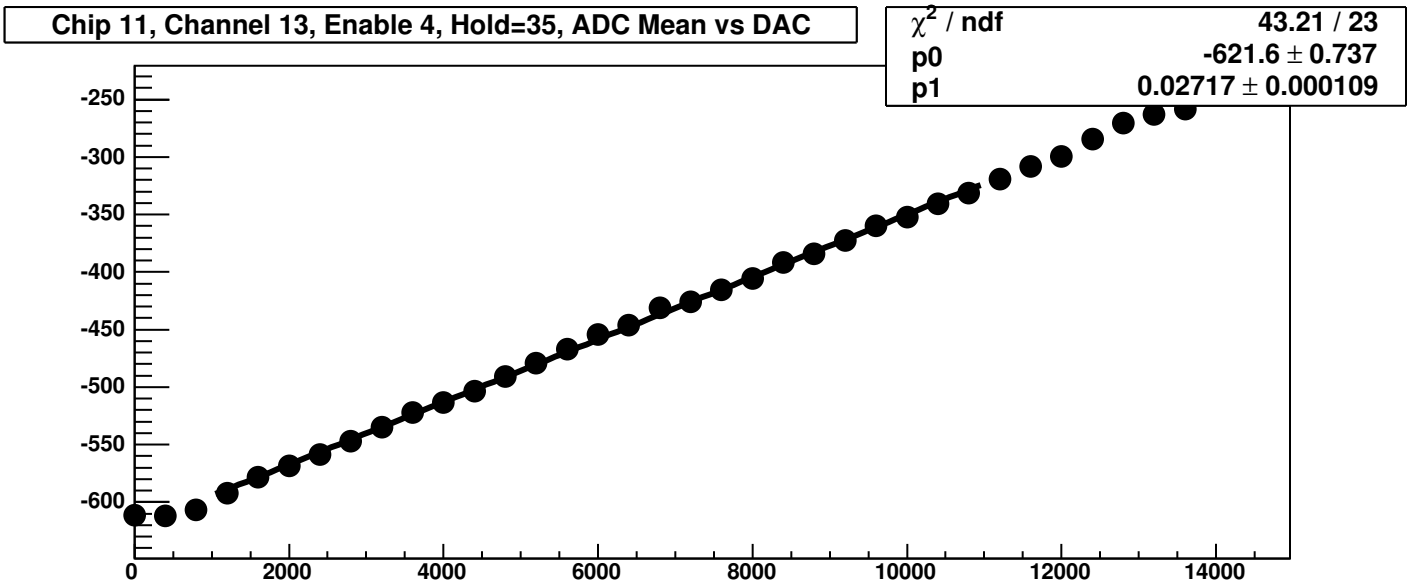
Chip 11, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

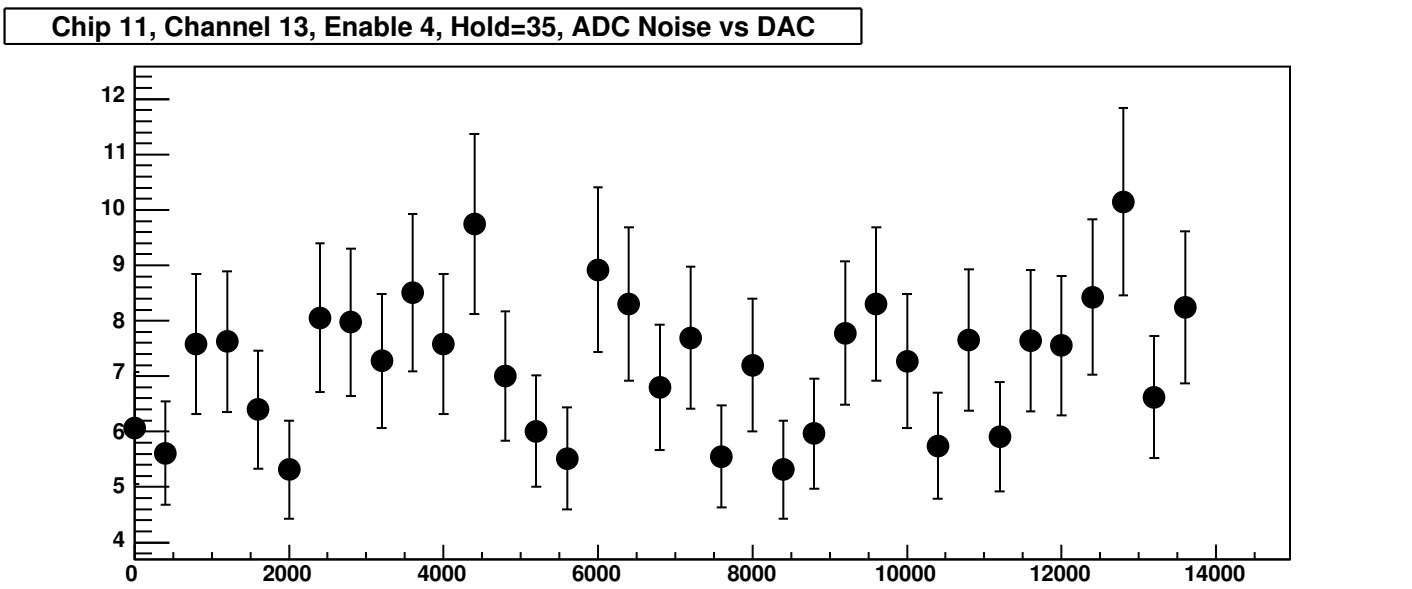


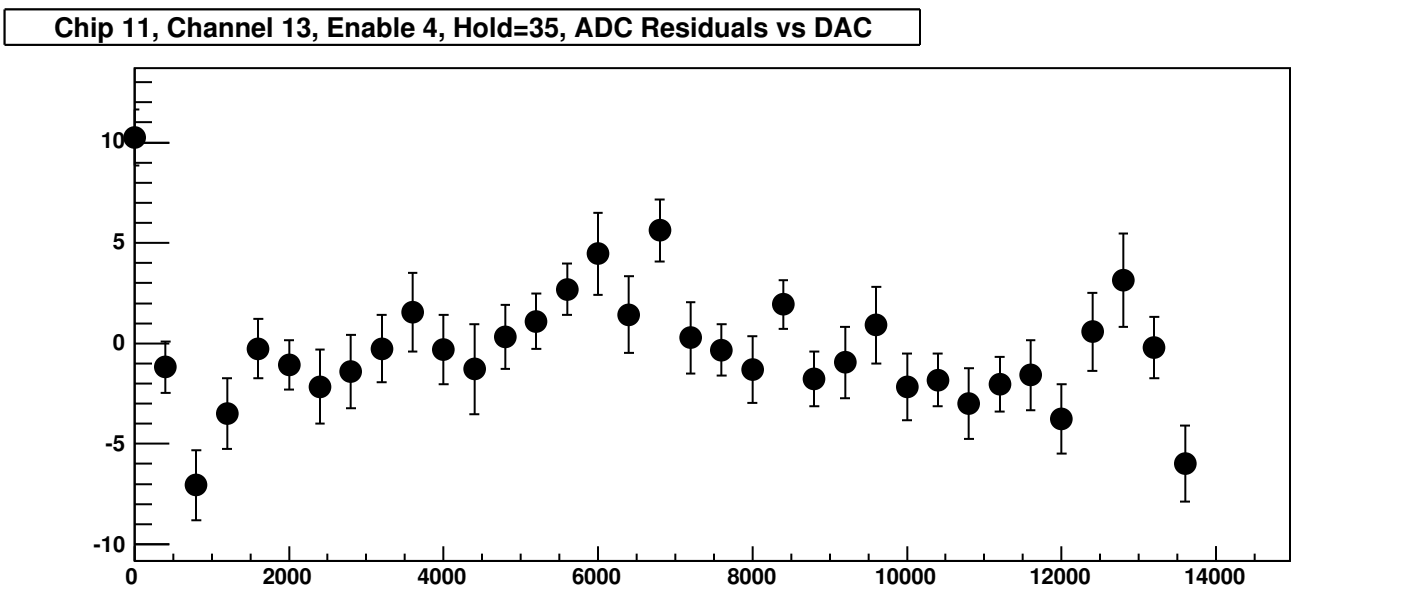

Chip 11, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 16.87 / 23
p0 -622.2 ± 0.7633
p1 0.02276 ± 0.0001158
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

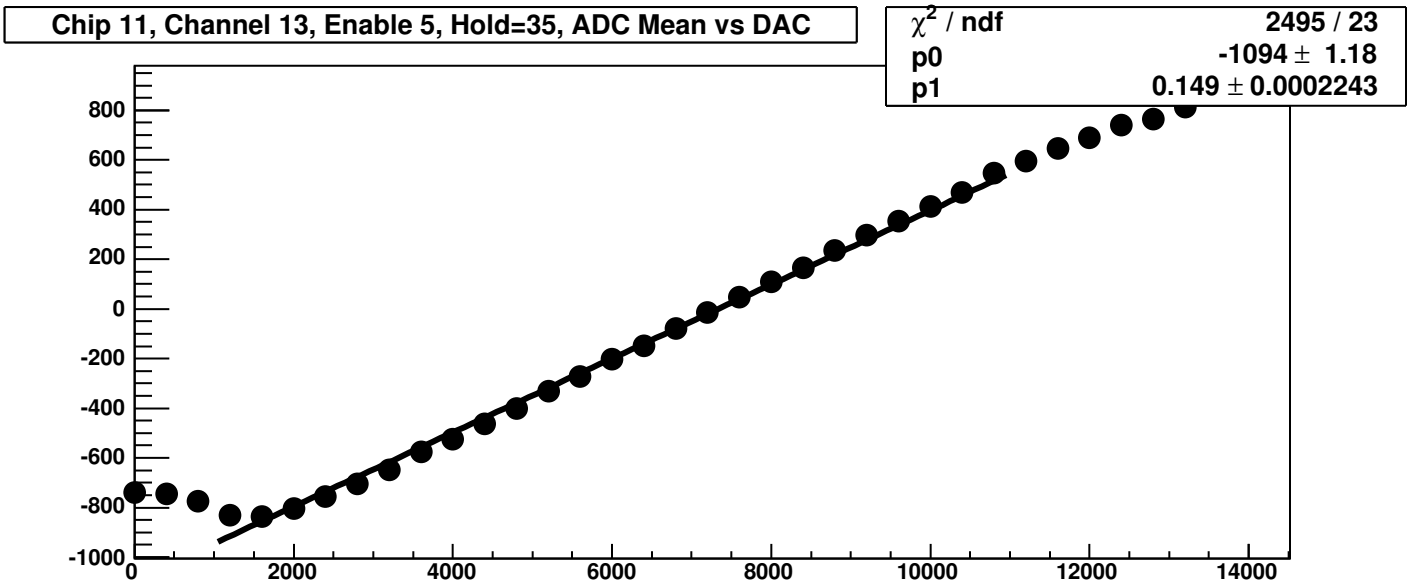

Chip 11, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

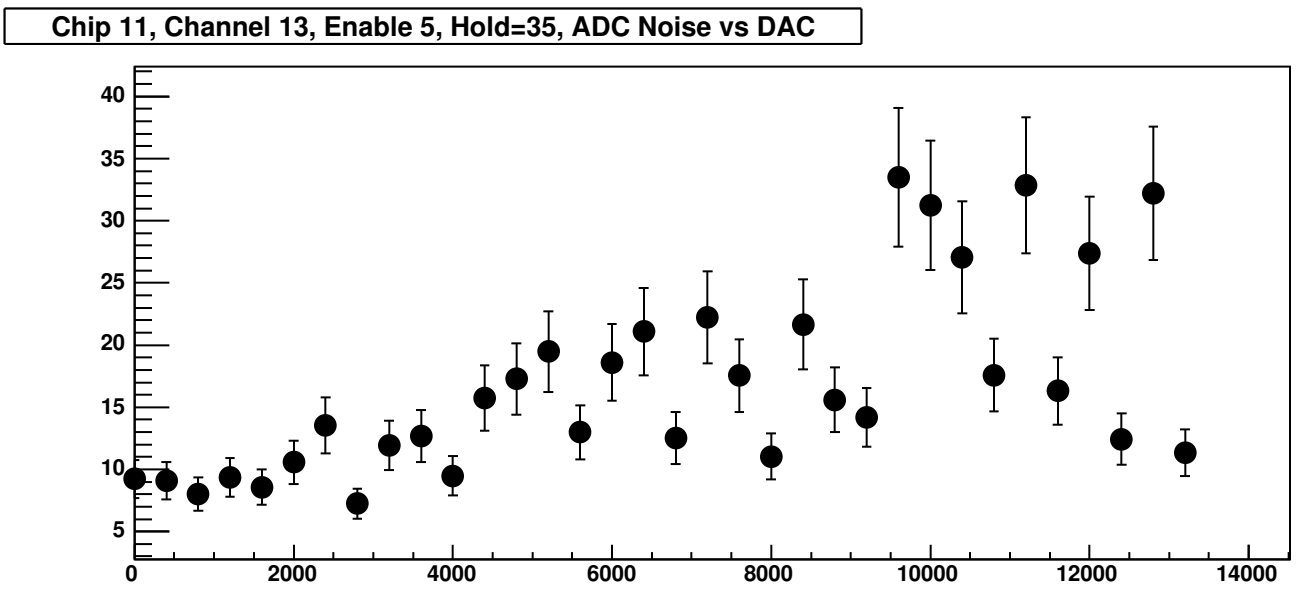

Chip 11, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

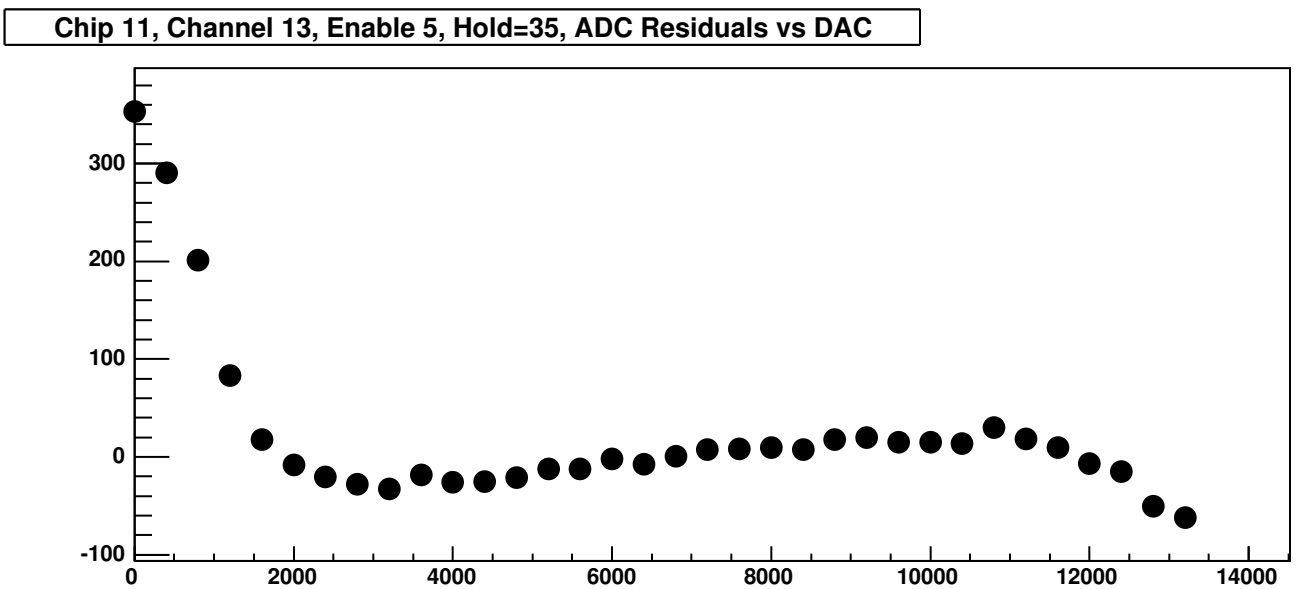

Chip 11, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 43.21 / 23
p0 -621.6 ± 0.737
p1 0.02717 ± 0.000109
-250
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

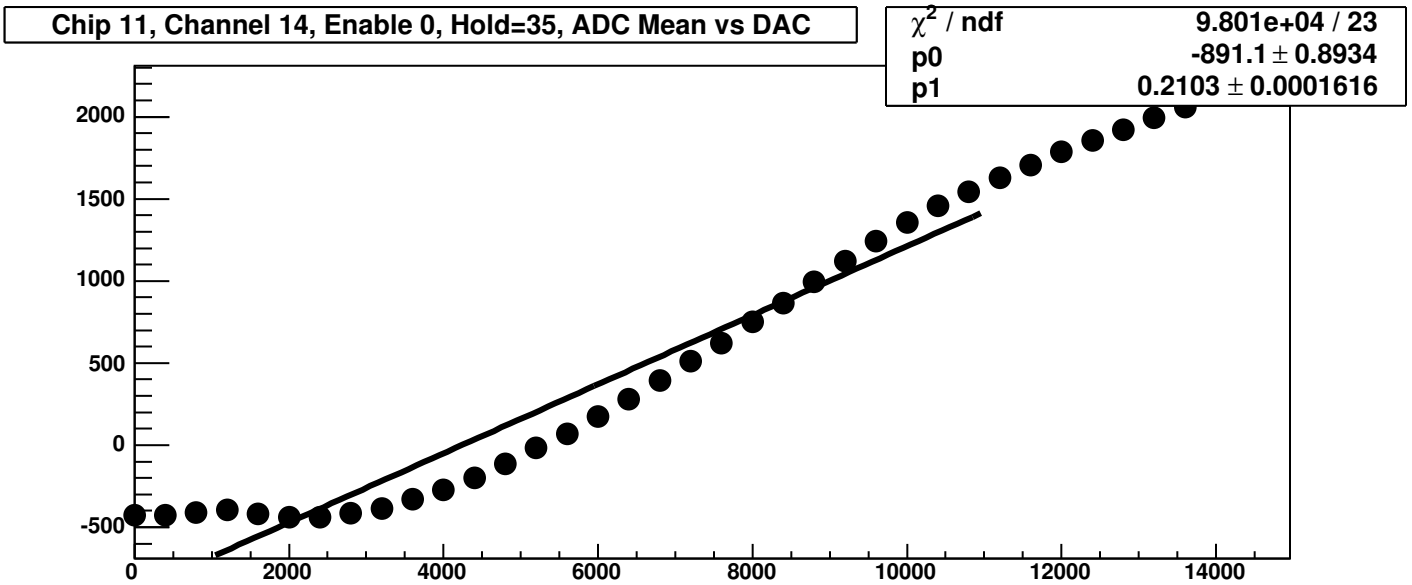

Chip 11, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

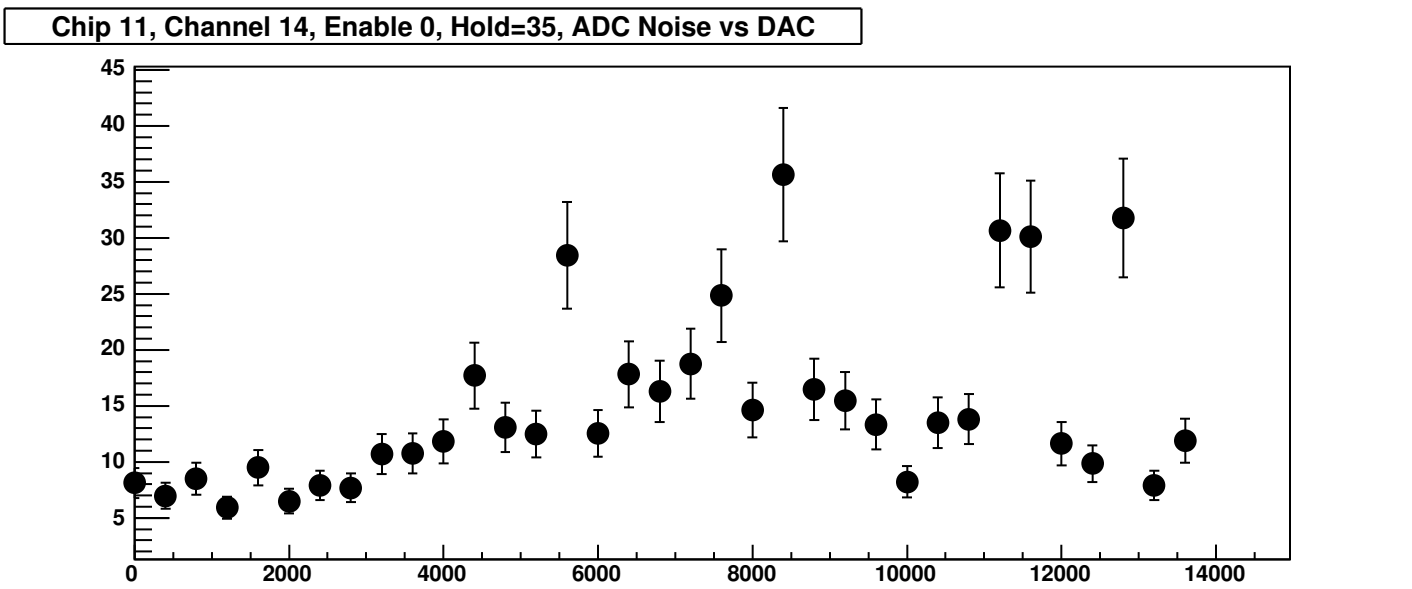

Chip 11, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

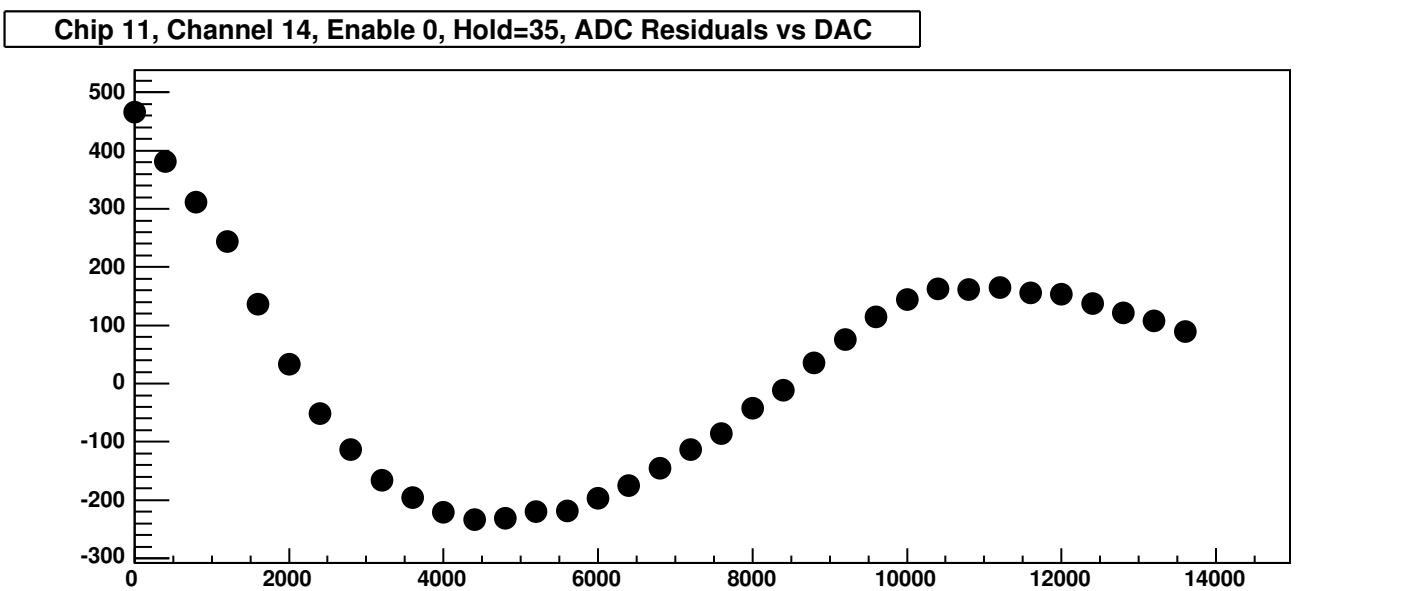

Chip 11, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2495 / 23
p0 -1094 ± 1.18
p1 0.149 ± 0.0002243
800
600
400
200
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

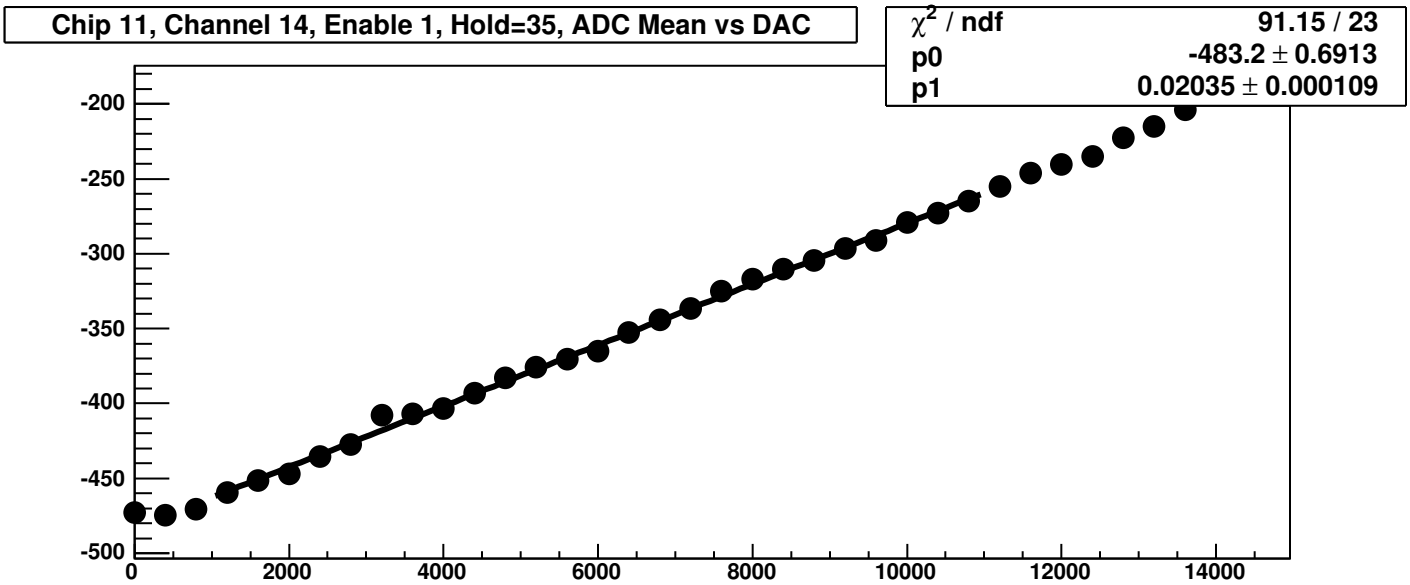

Chip 11, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

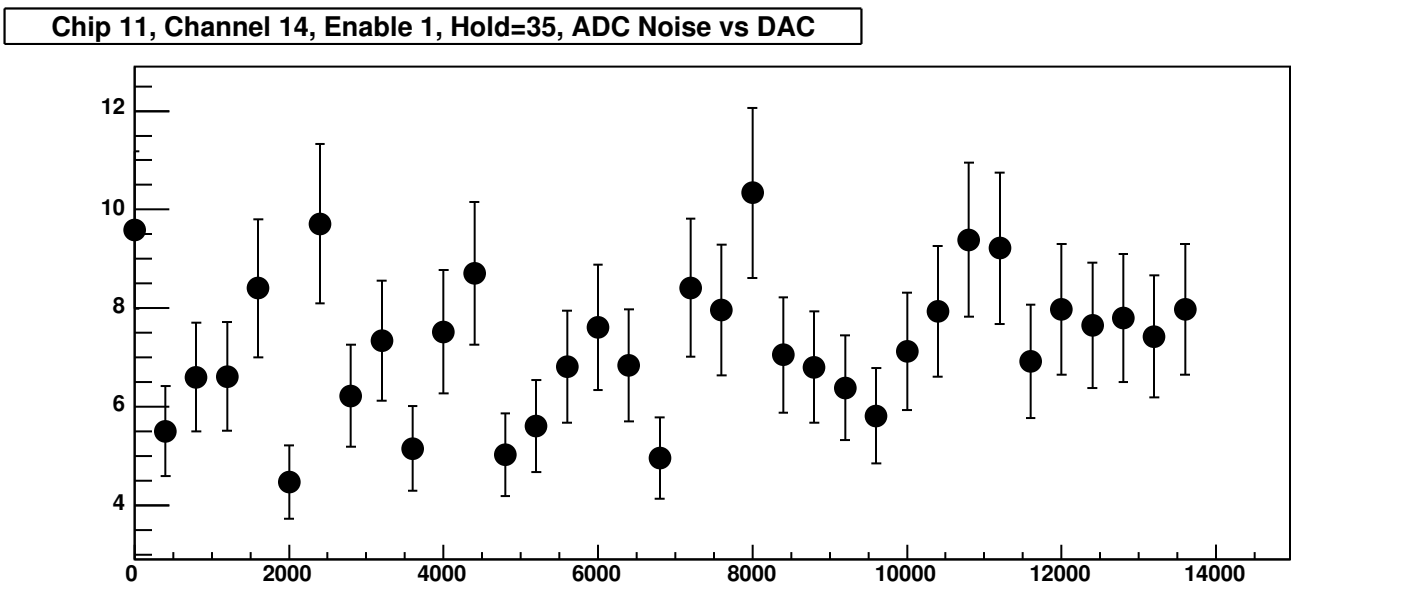

Chip 11, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

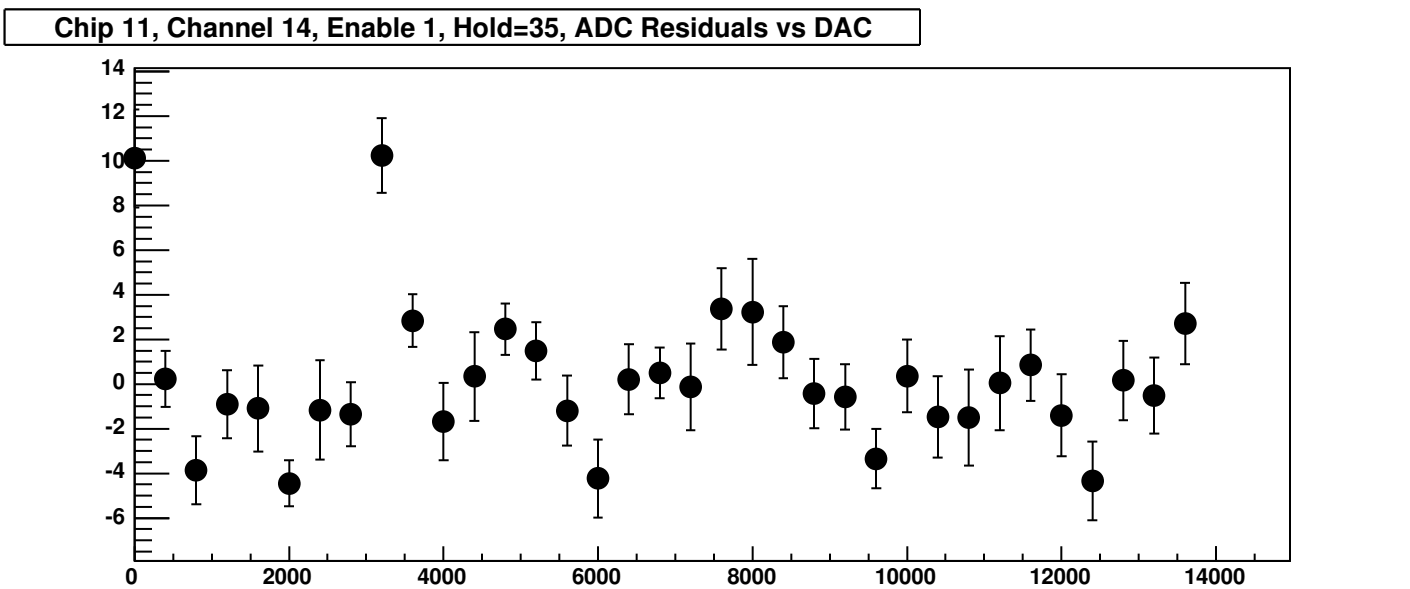

Chip 11, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 9.801e+04 / 23
p0 -891.1 ± 0.8934
p1 0.2103 ± 0.0001616
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

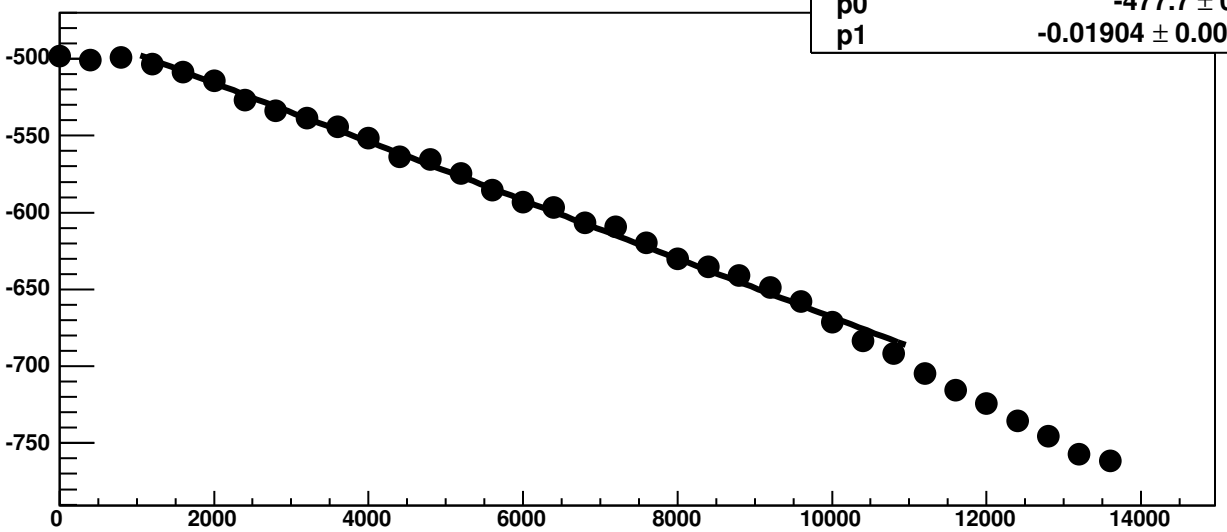

Chip 11, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 91.15 / 23
p0 -483.2 ± 0.6913
p1 0.02035 ± 0.000109
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

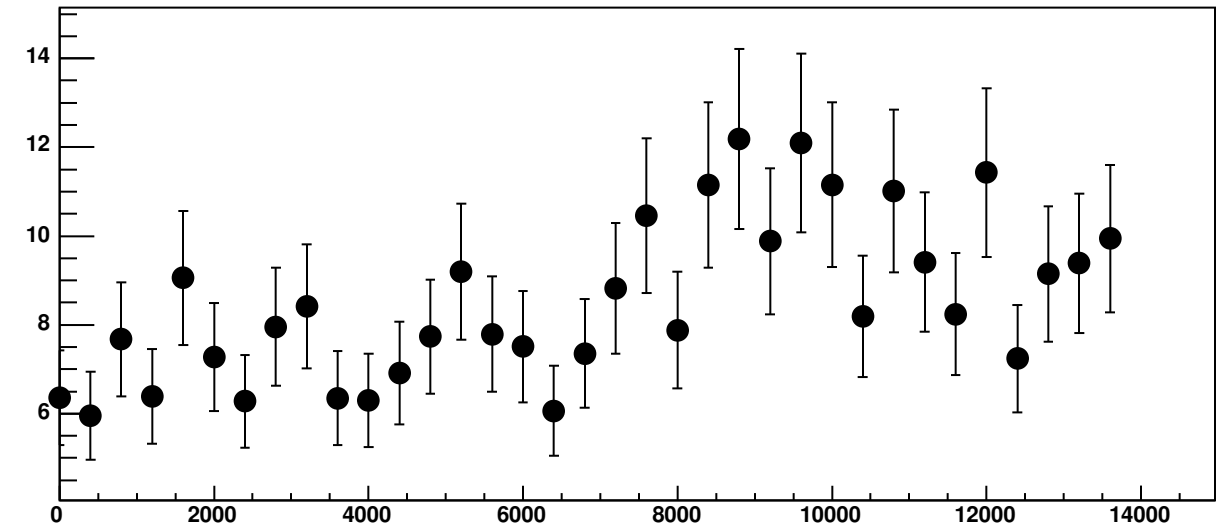

Chip 11, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000

Chip 11, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

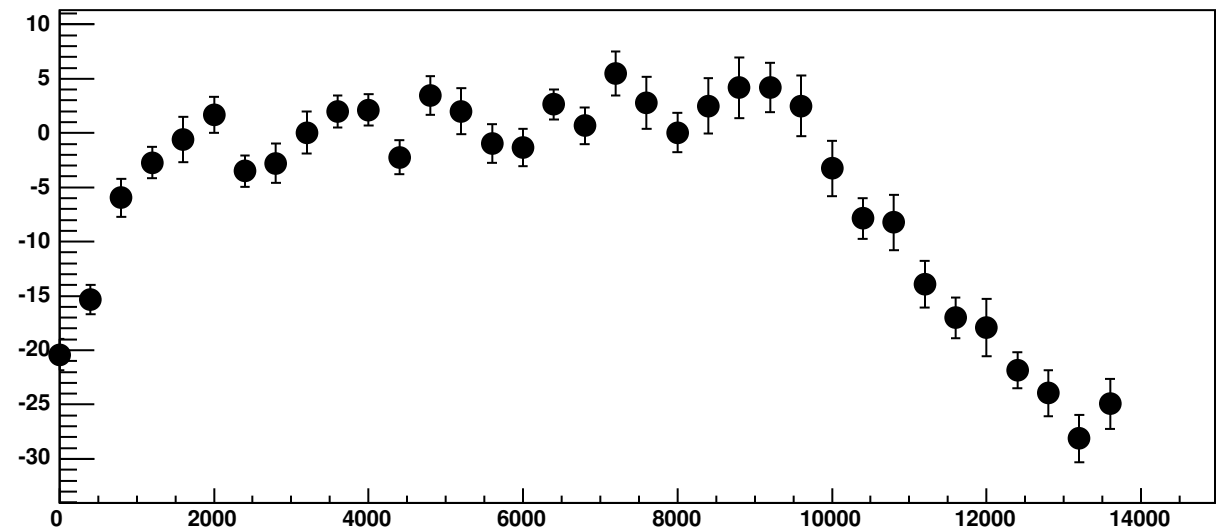
χ^2 / ndf	73.88 / 23
p0	-477.7 ± 0.8043
p1	-0.01904 ± 0.0001359

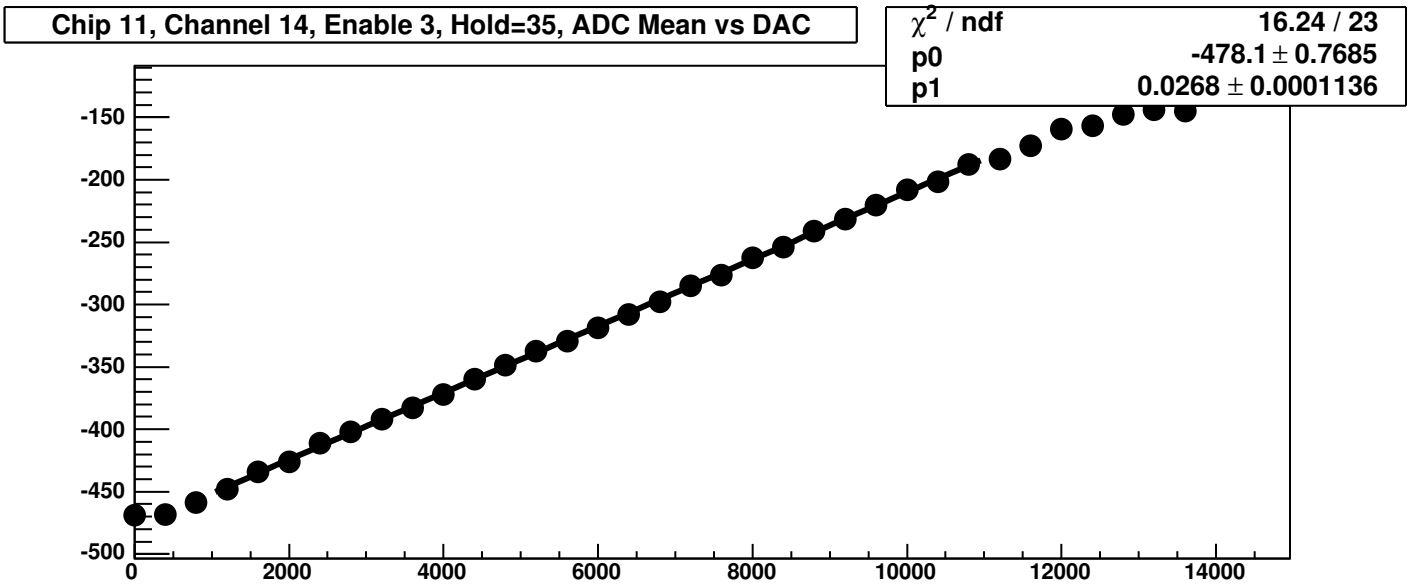


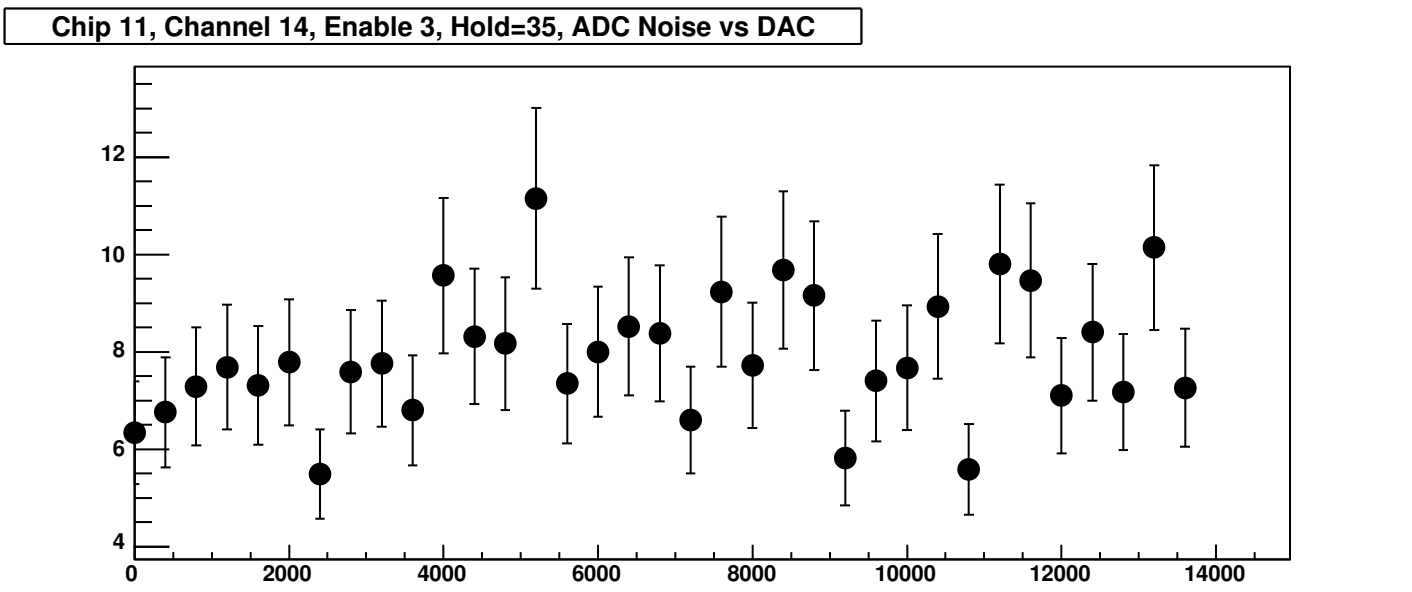
Chip 11, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC

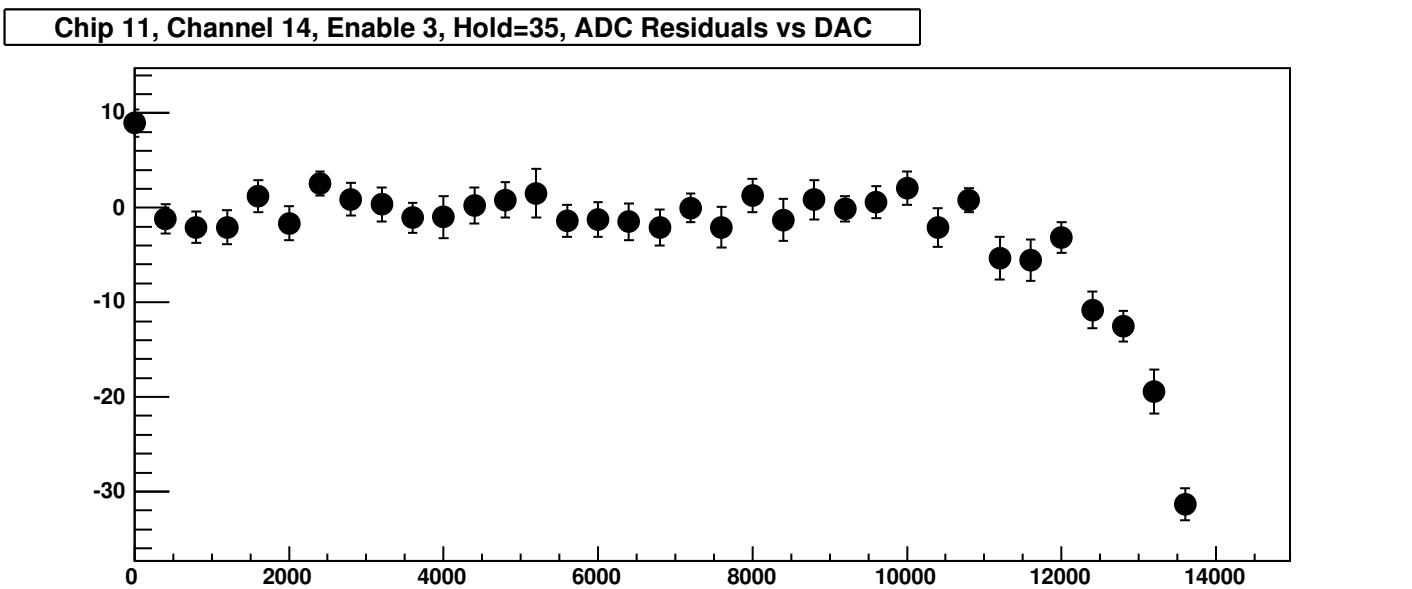


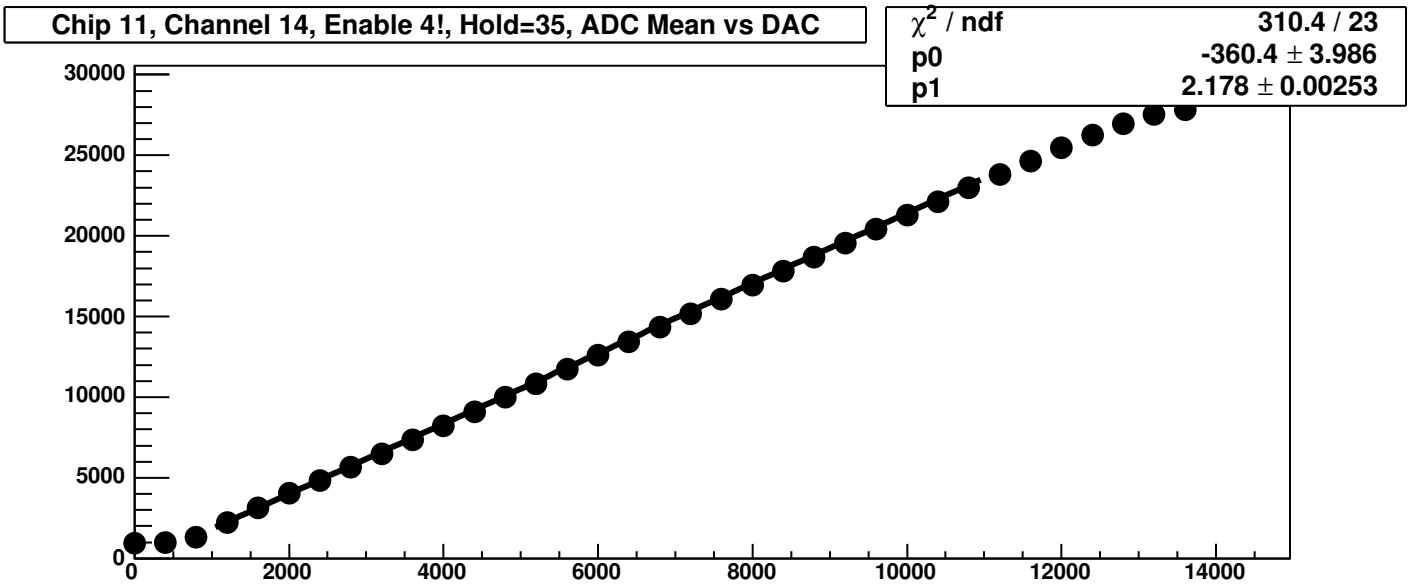
Chip 11, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

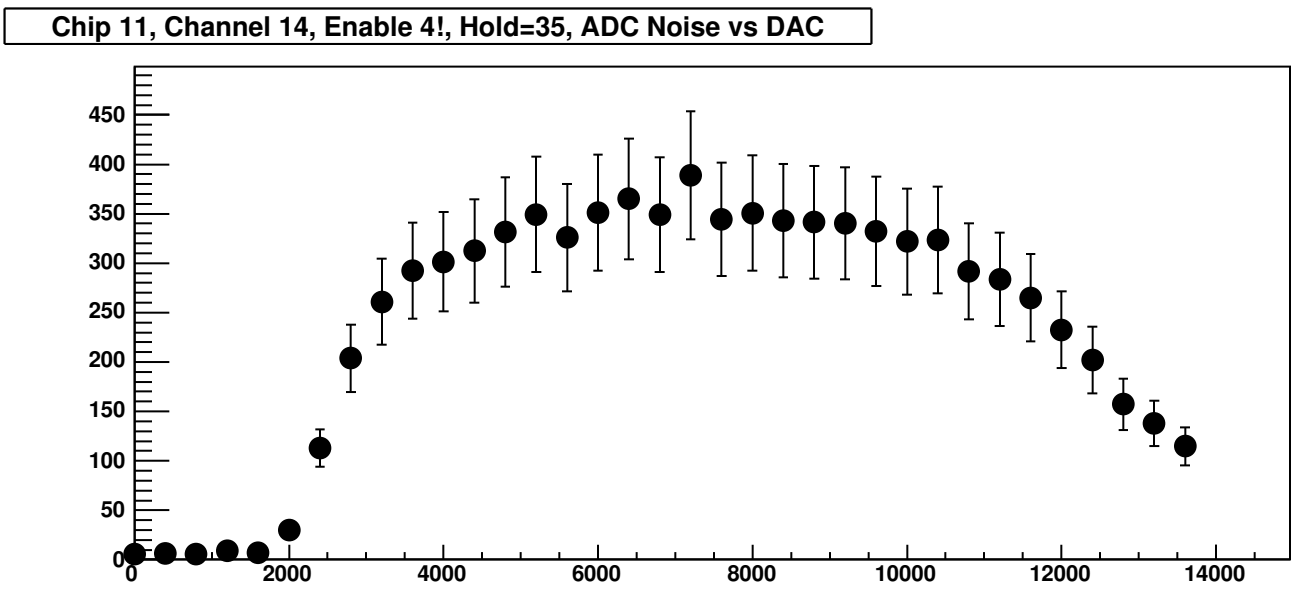


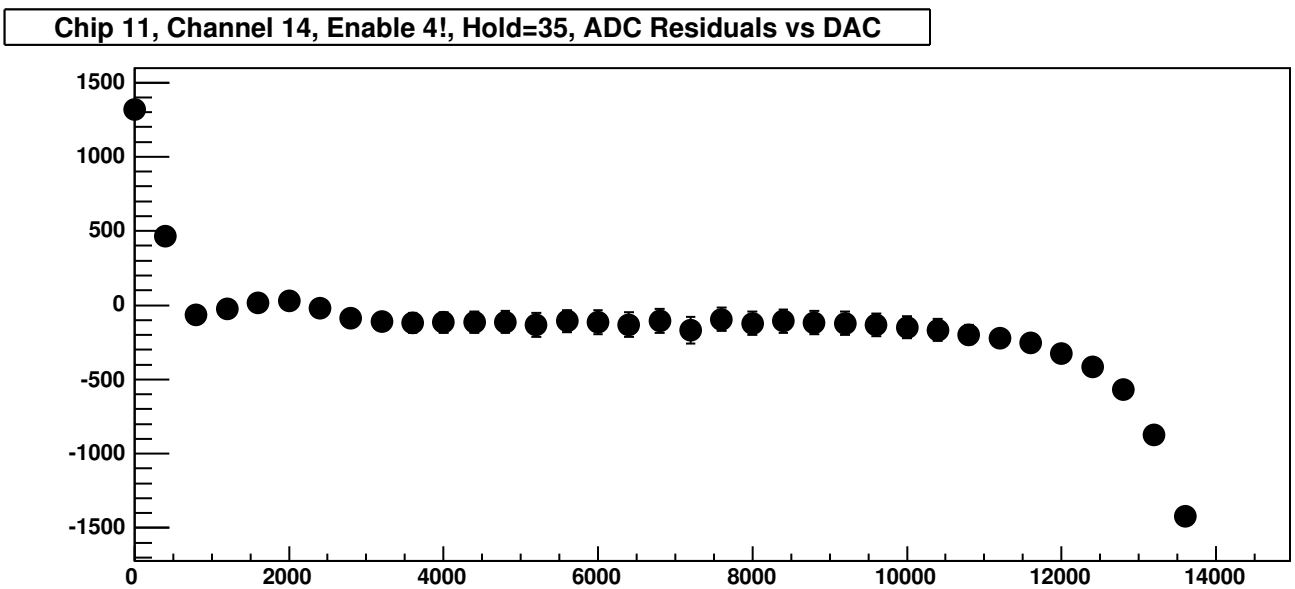

Chip 11, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 16.24 / 23
p0 -478.1 ± 0.7685
p1 0.0268 ± 0.0001136
-150
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

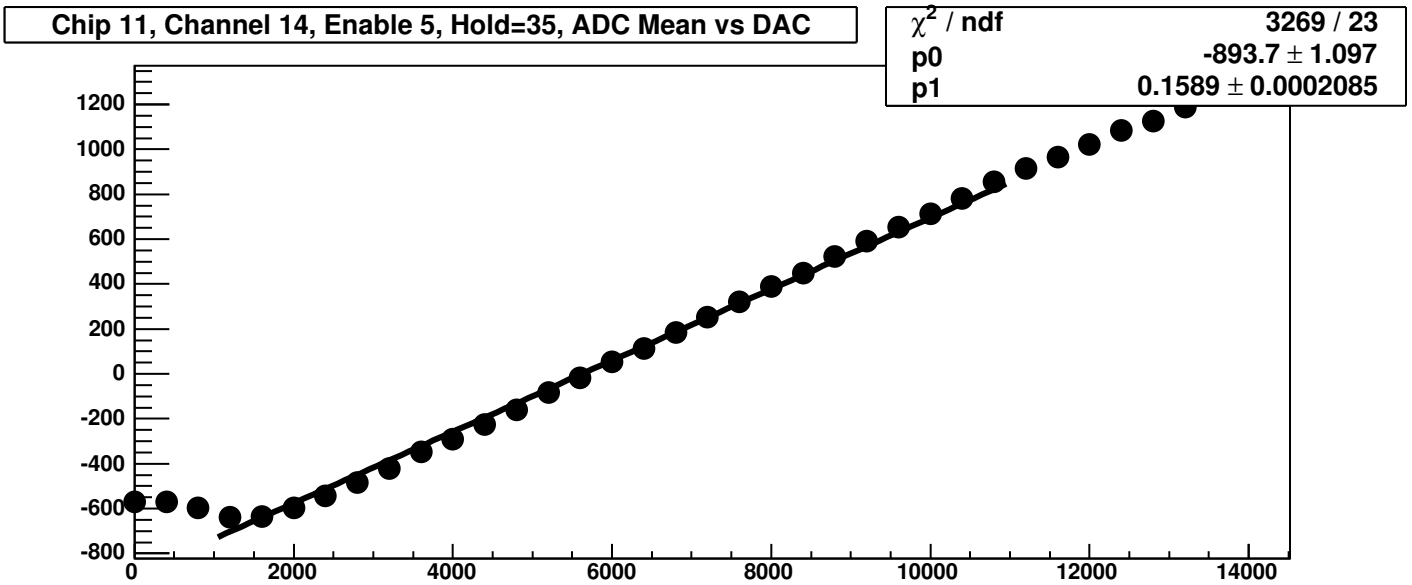

Chip 11, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

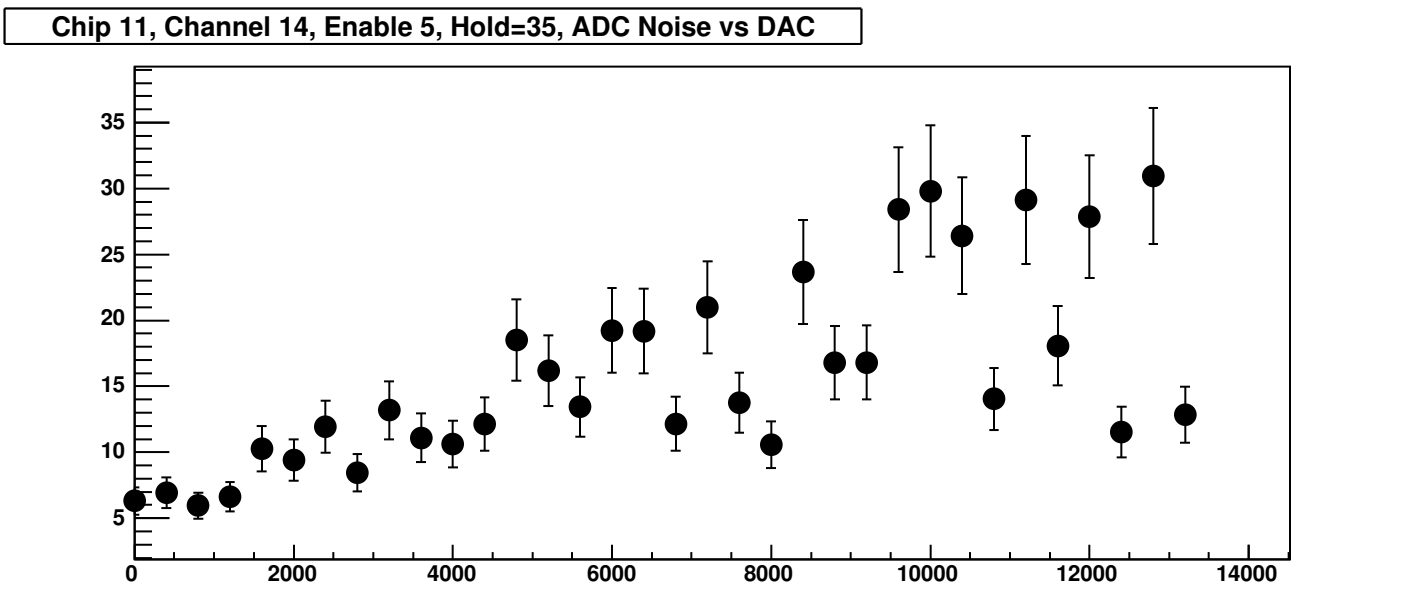

Chip 11, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

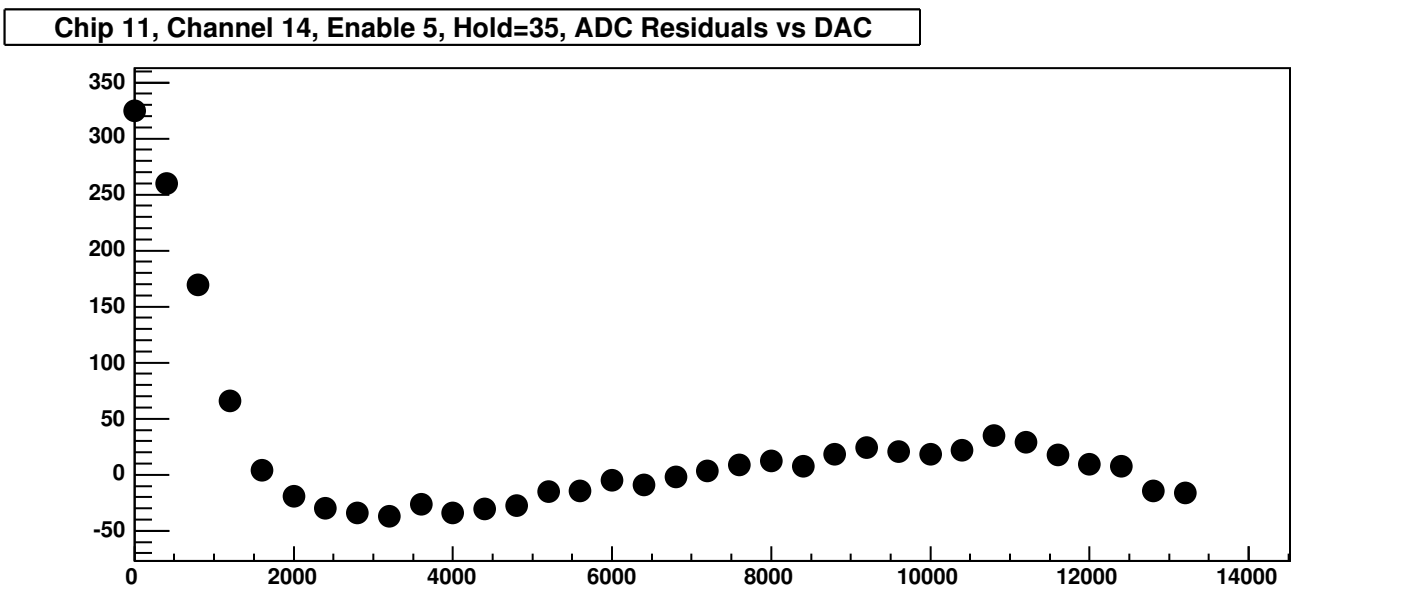

Chip 11, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 310.4 / 23
p0 -360.4 ± 3.986
p1 2.178 ± 0.00253
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

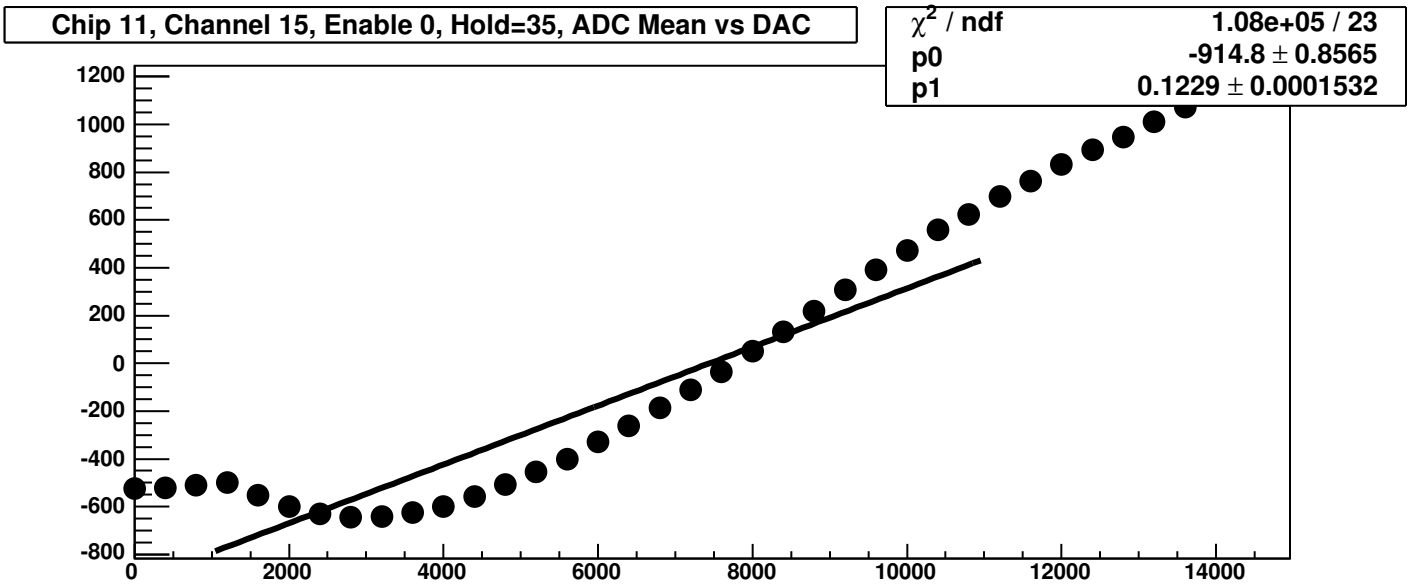

Chip 11, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

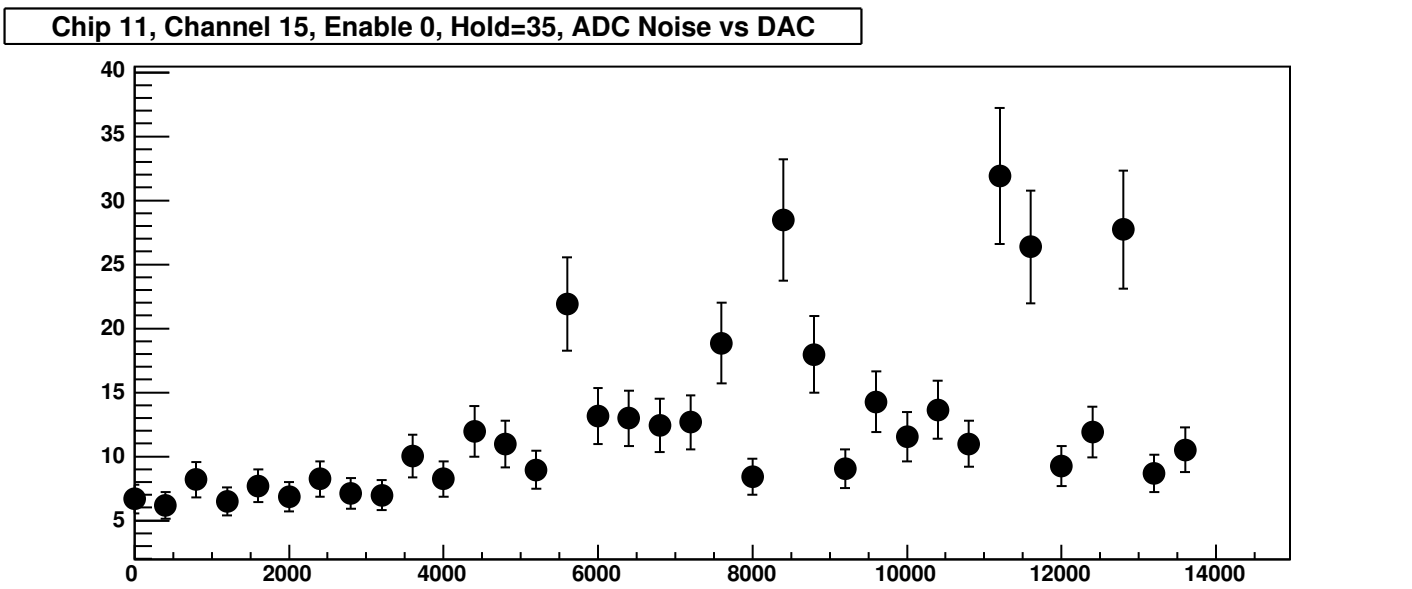

Chip 11, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

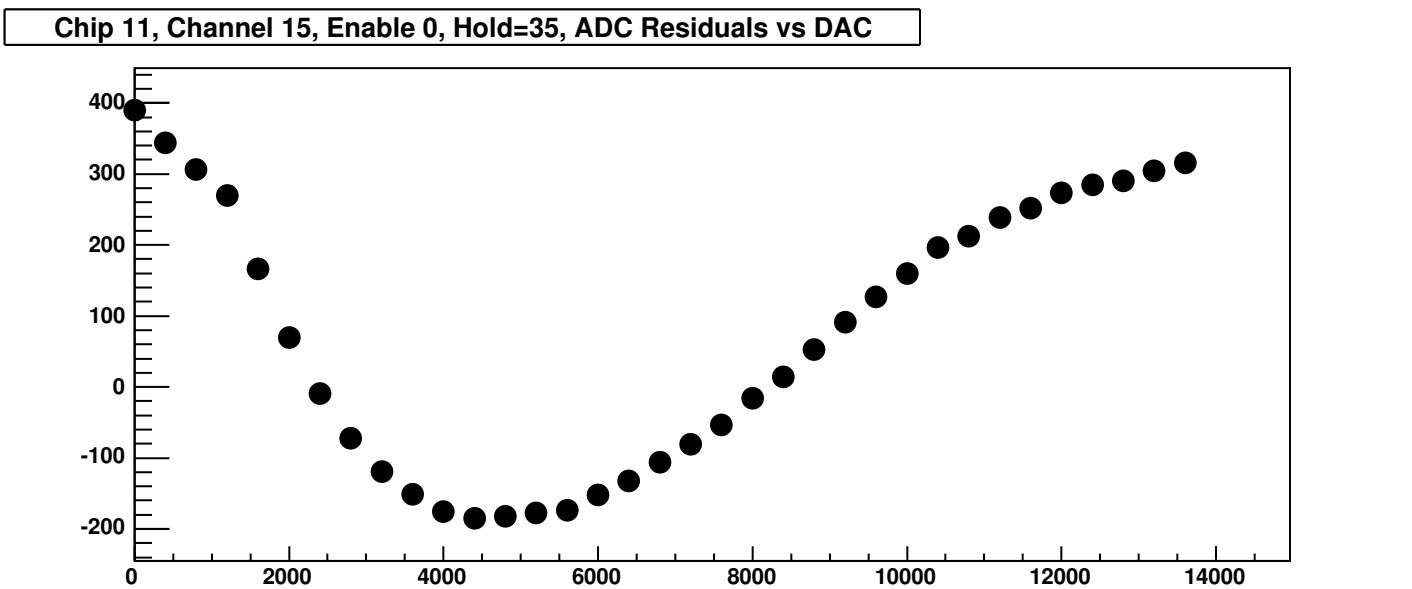

Chip 11, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3269 / 23
p0 -893.7 ± 1.097
p1 0.1589 ± 0.0002085
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

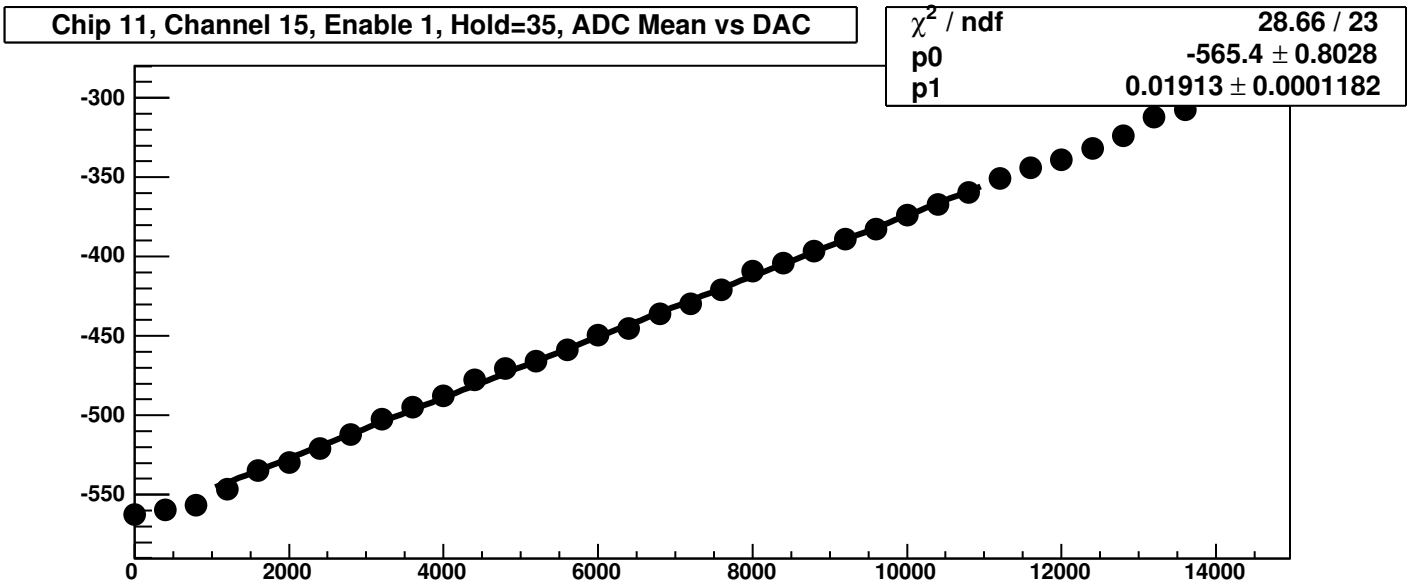

Chip 11, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

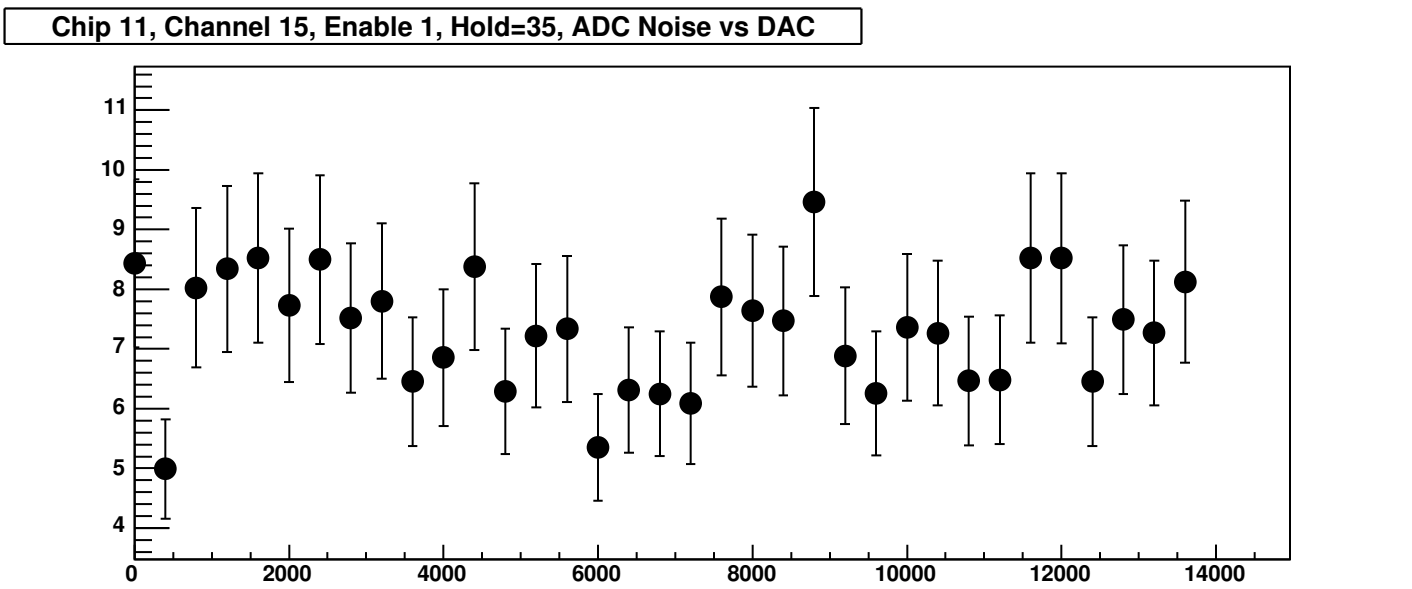

Chip 11, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
350
300
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

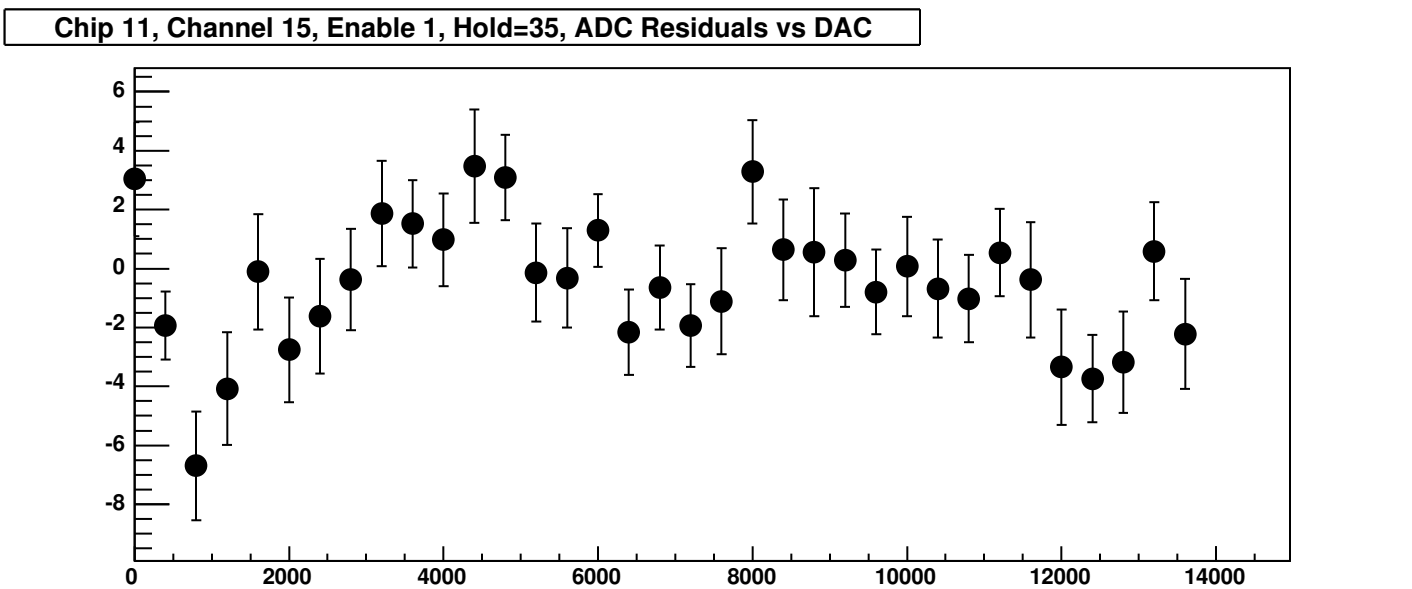

Chip 11, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.08e+05 / 23
p0 -914.8 ± 0.8565
p1 0.1229 ± 0.0001532
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

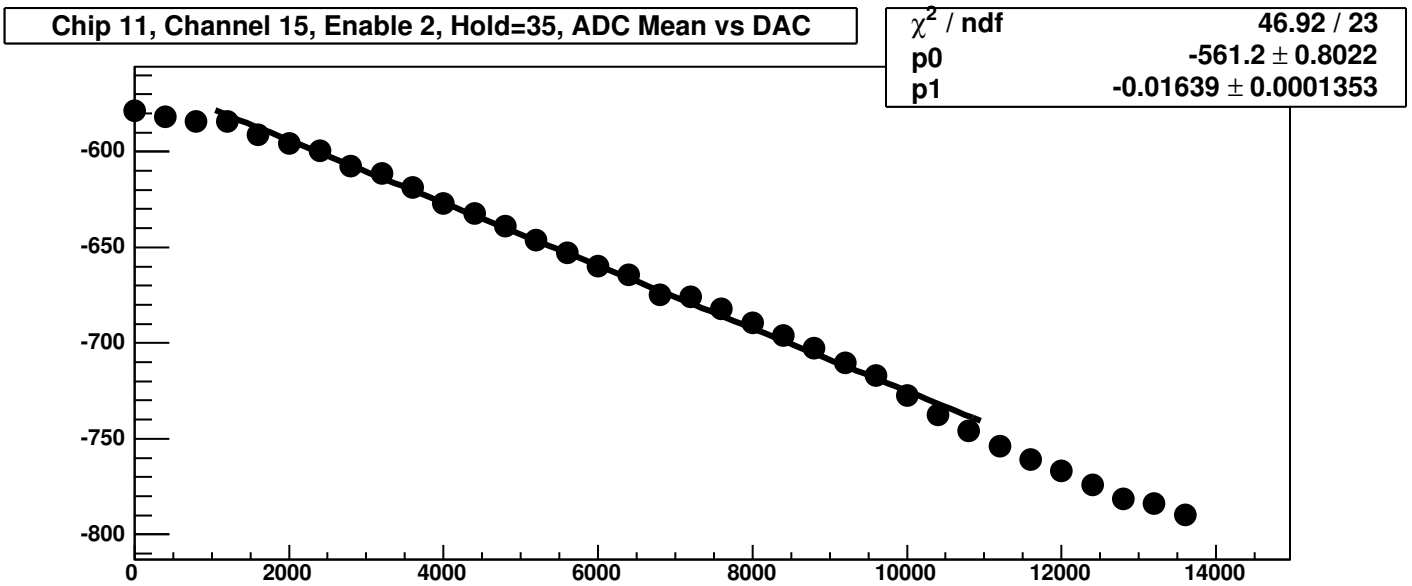

Chip 11, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

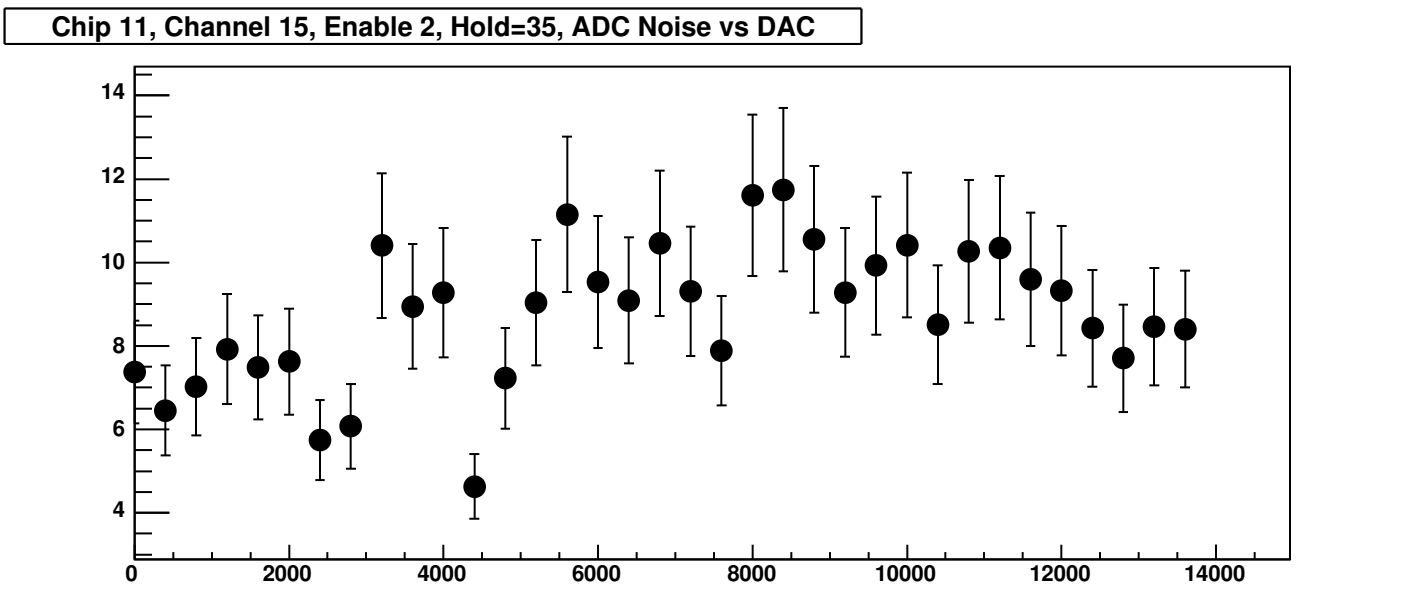

Chip 11, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

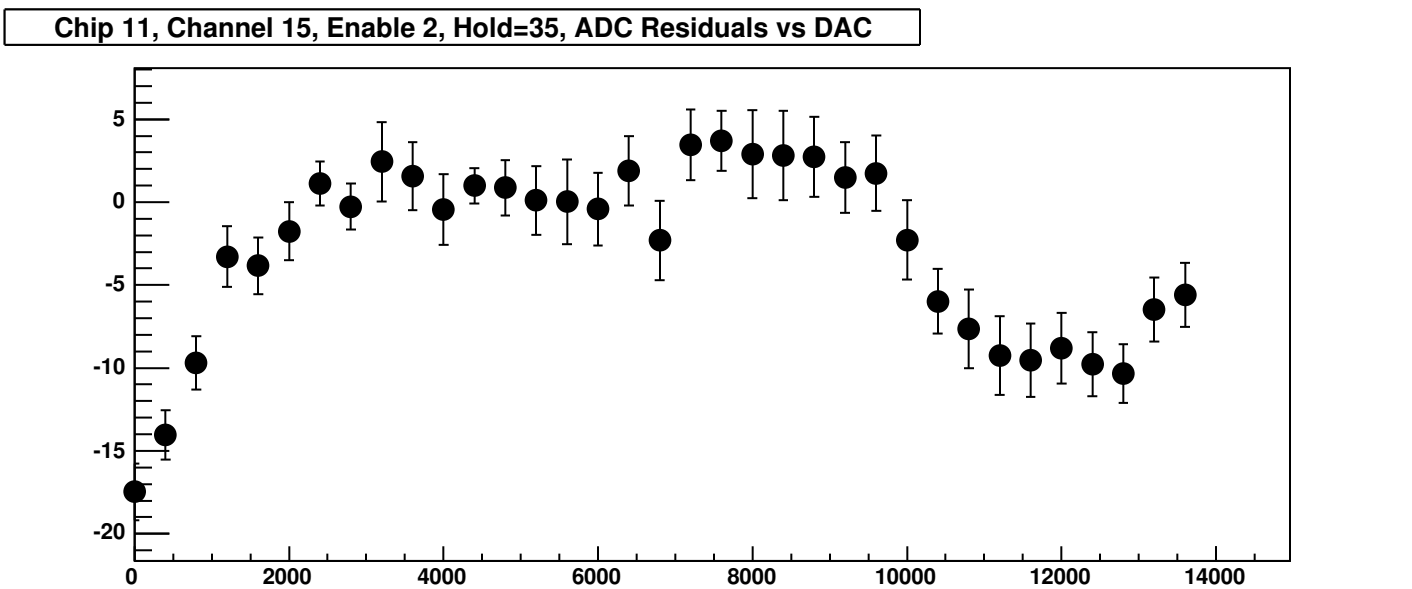

Chip 11, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 28.66 / 23
p0 -565.4 ± 0.8028
p1 0.01913 ± 0.0001182
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

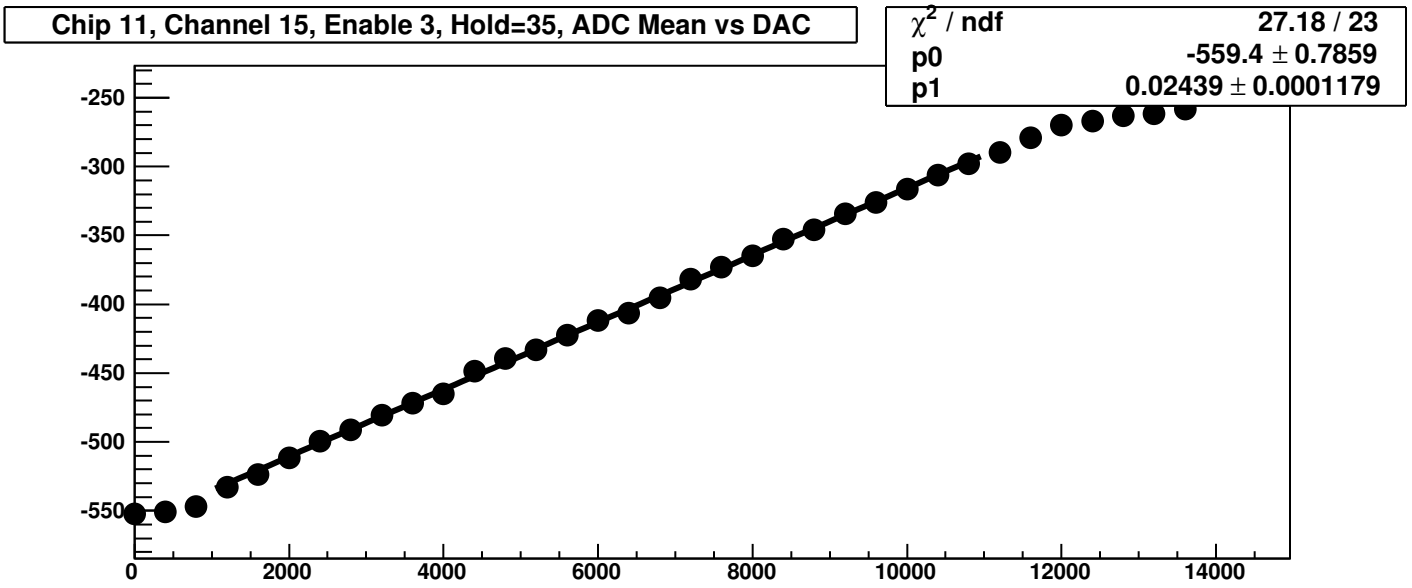

Chip 11, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

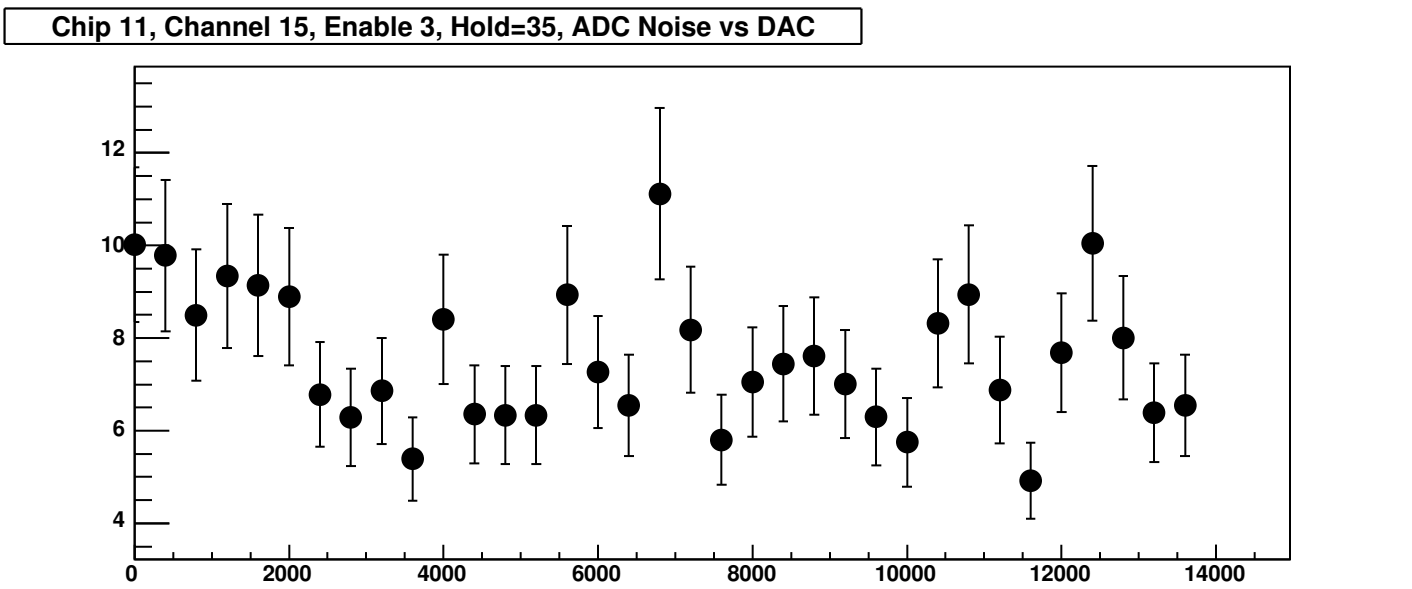

Chip 11, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

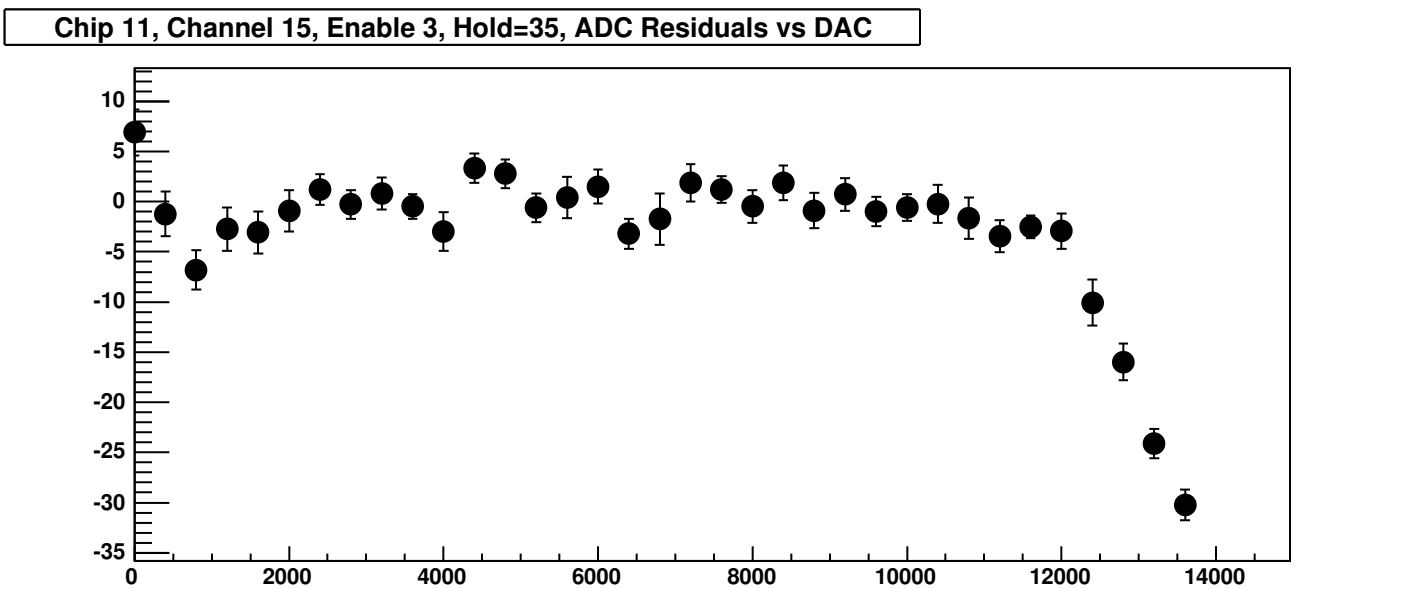

Chip 11, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 46.92 / 23
p0 -561.2 ± 0.8022
p1 -0.01639 ± 0.0001353
-600
-650
-700
-750
-800
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

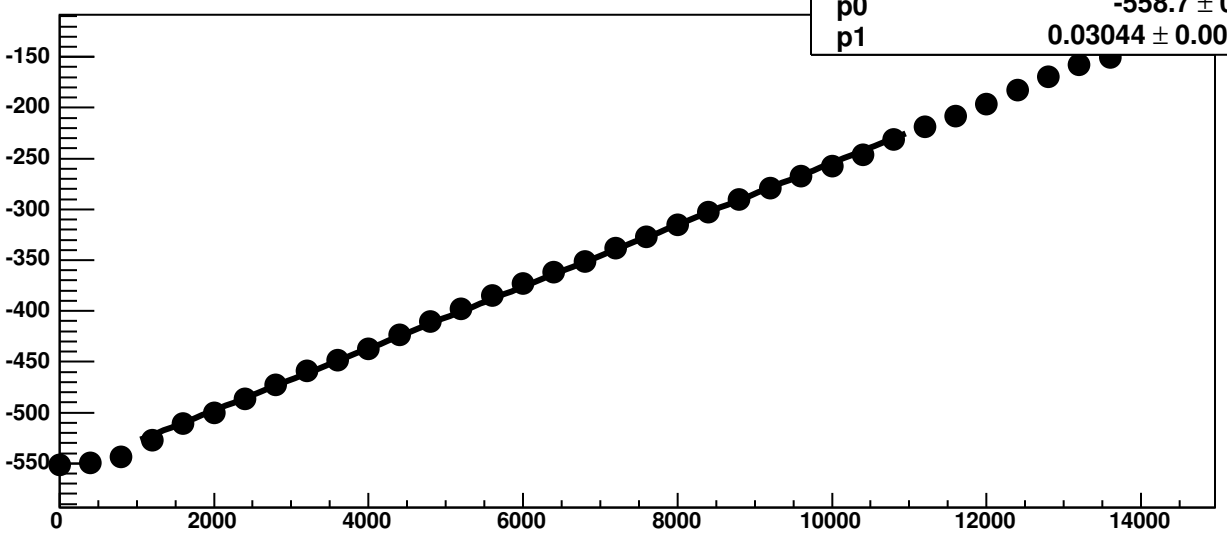

Chip 11, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 27.18 / 23
p0 -559.4 ± 0.7859
p1 0.02439 ± 0.0001179
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

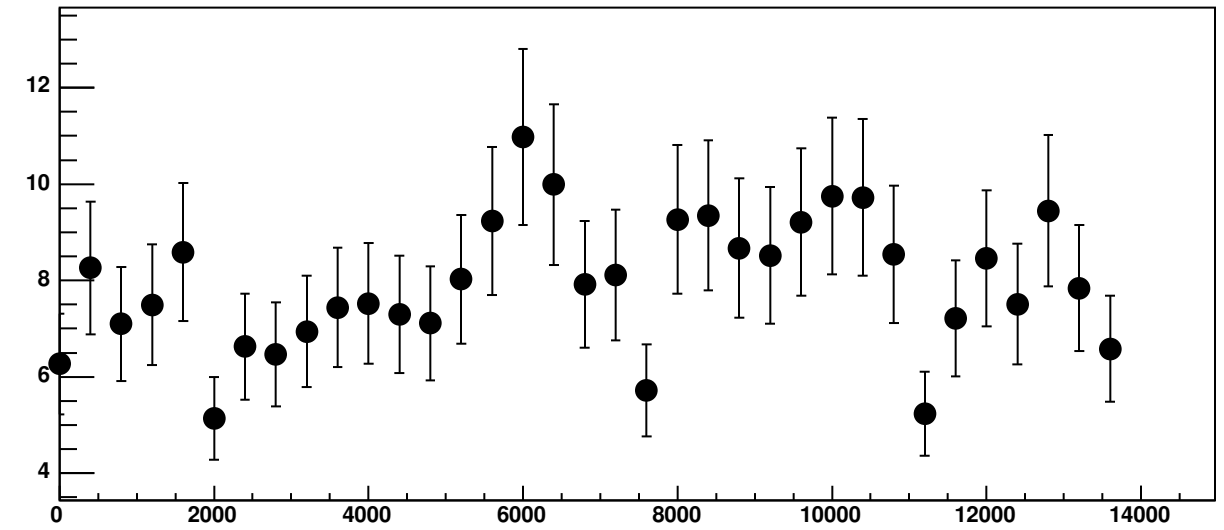

Chip 11, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
-35
0
2000
4000
6000
8000
10000
12000
14000

Chip 11, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC

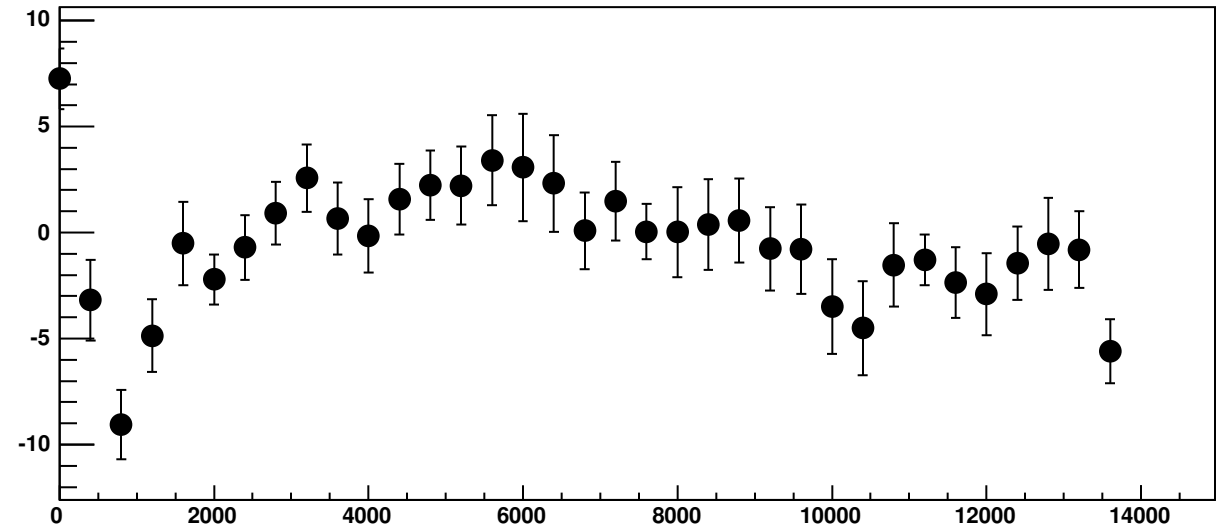
χ^2 / ndf	32.39 / 23
p0	-558.7 ± 0.7596
p1	0.03044 ± 0.0001243

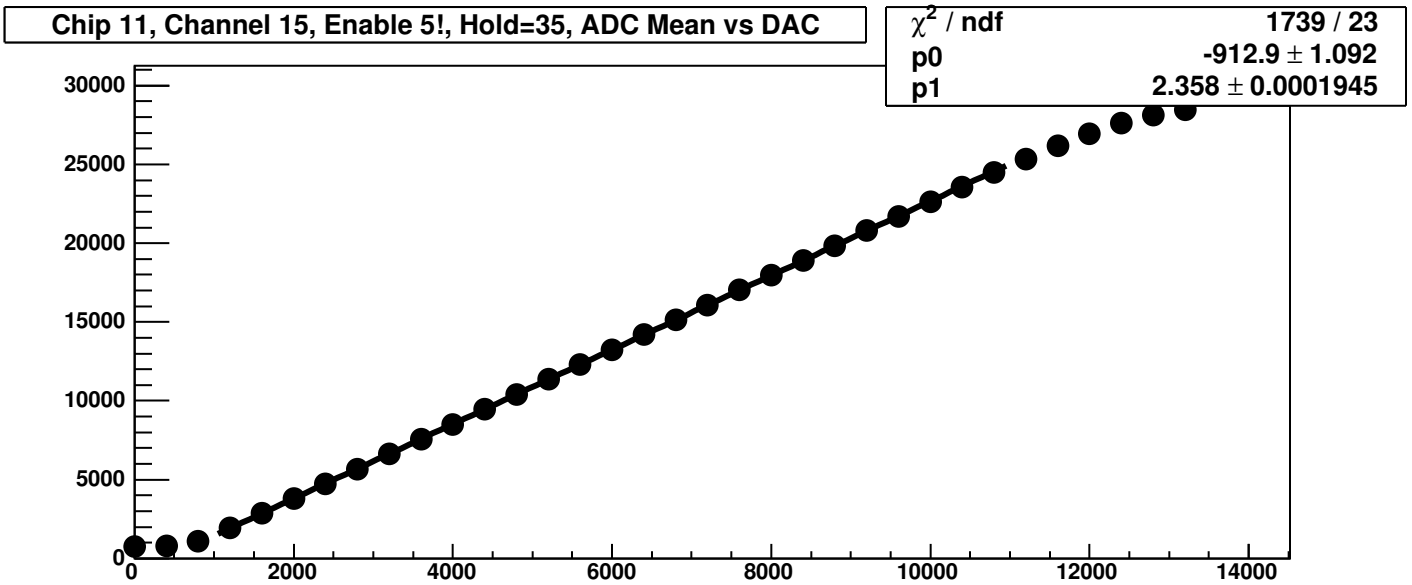


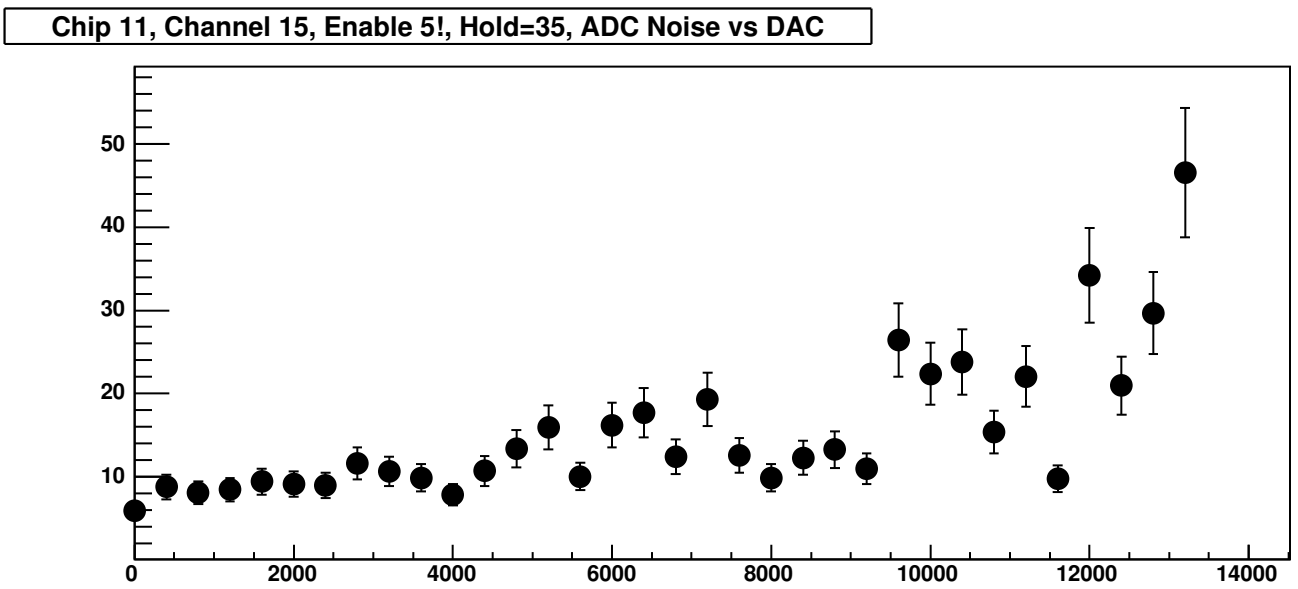
Chip 11, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC

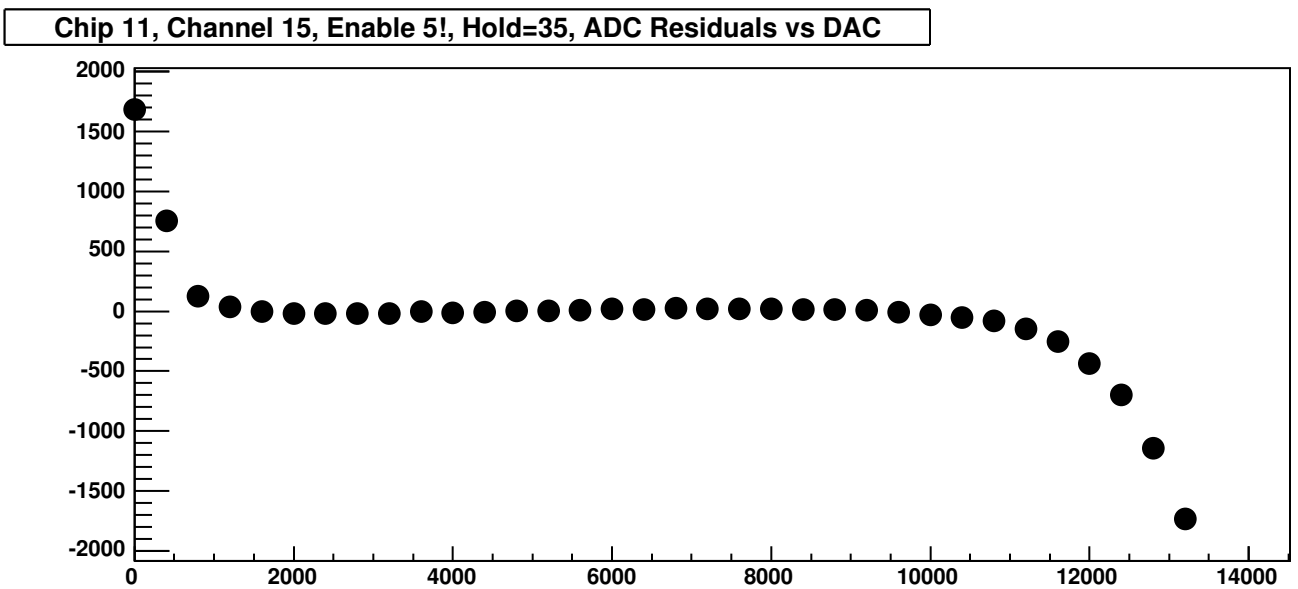


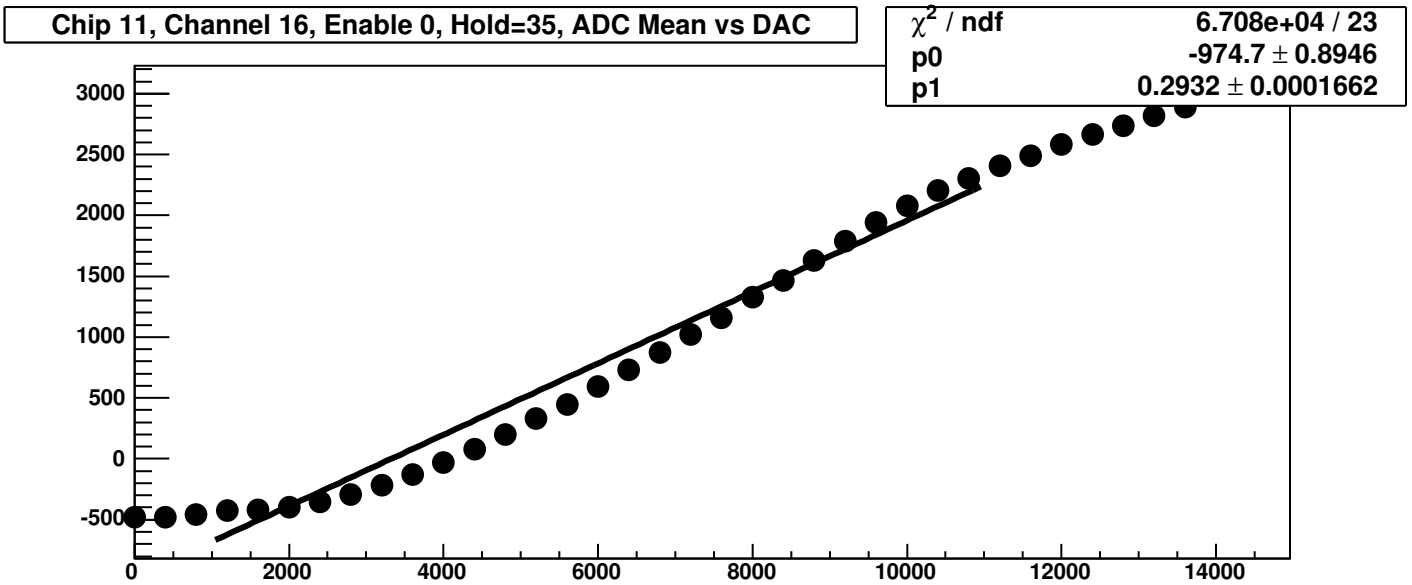
Chip 11, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC

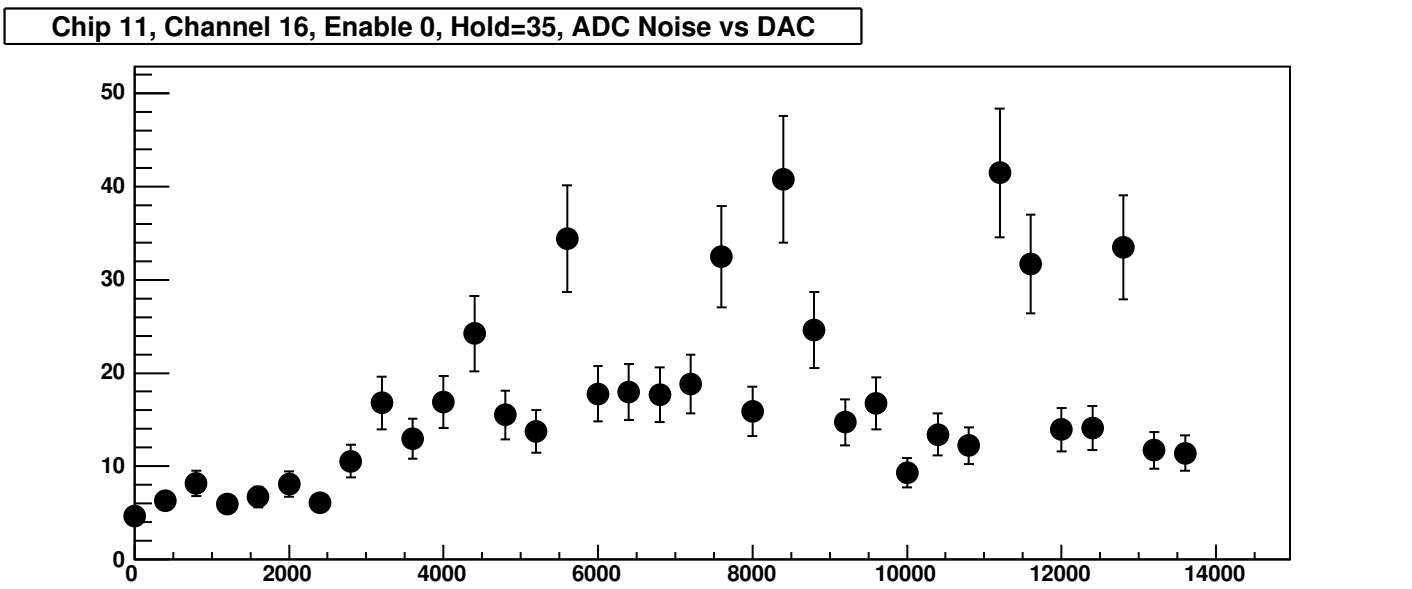


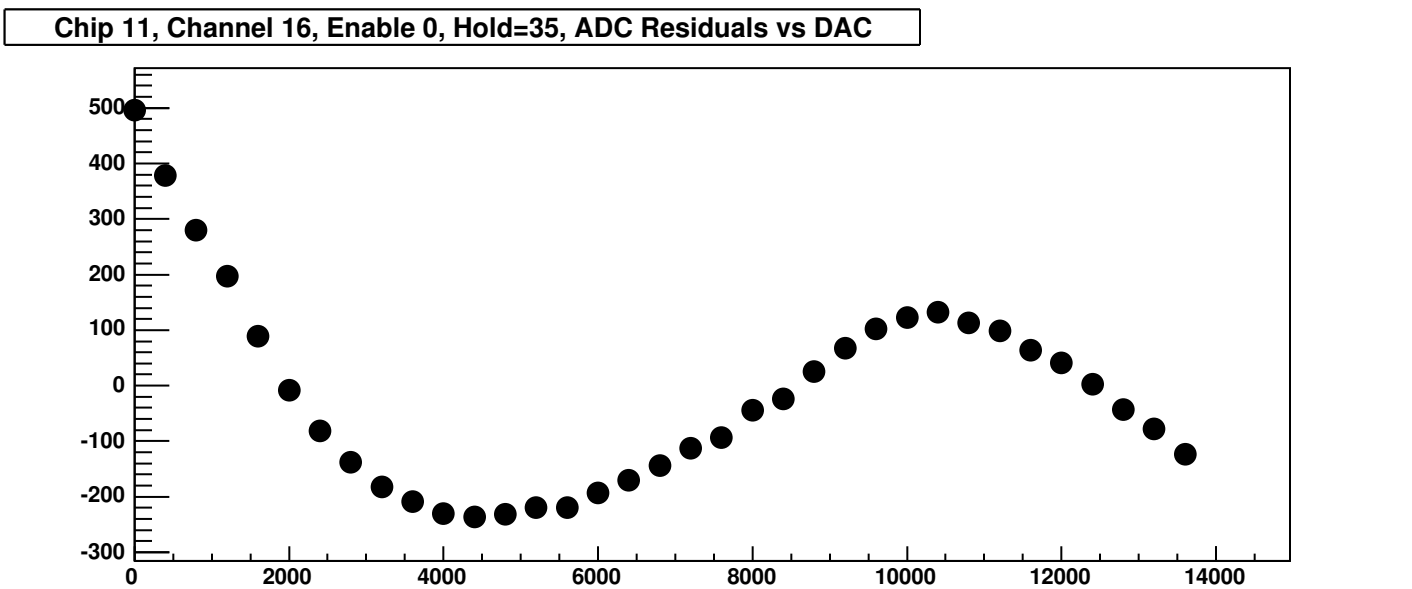

Chip 11, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1739 / 23
p0 -912.9 ± 1.092
p1 2.358 ± 0.0001945
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

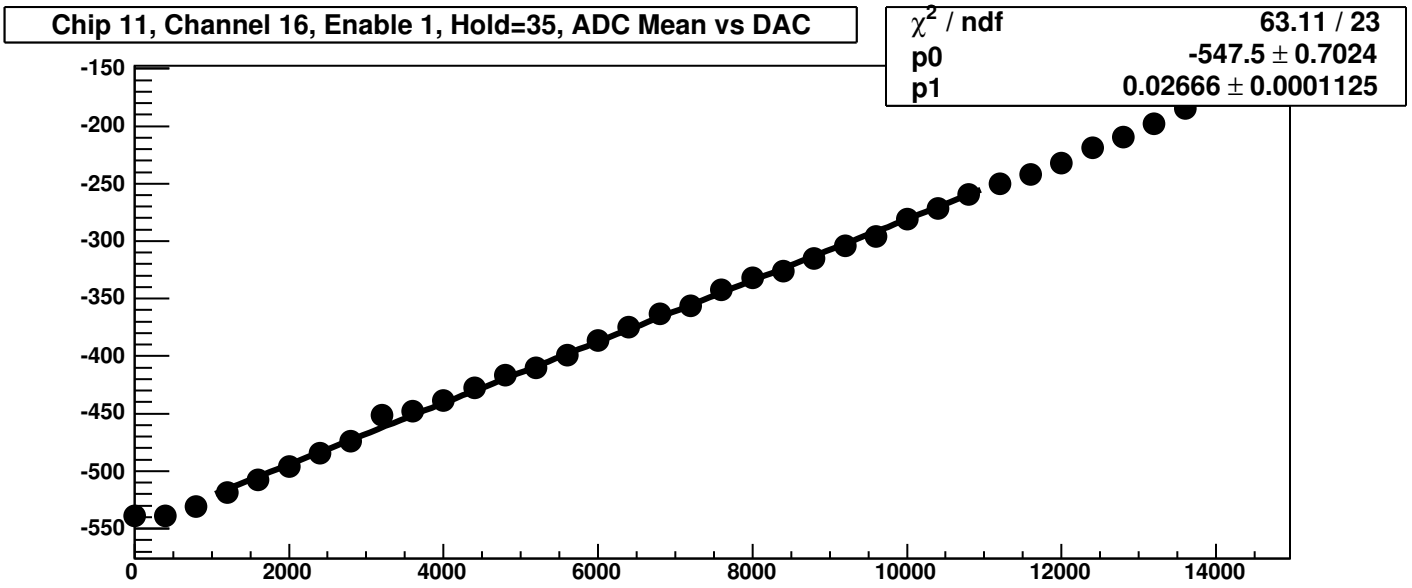

Chip 11, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

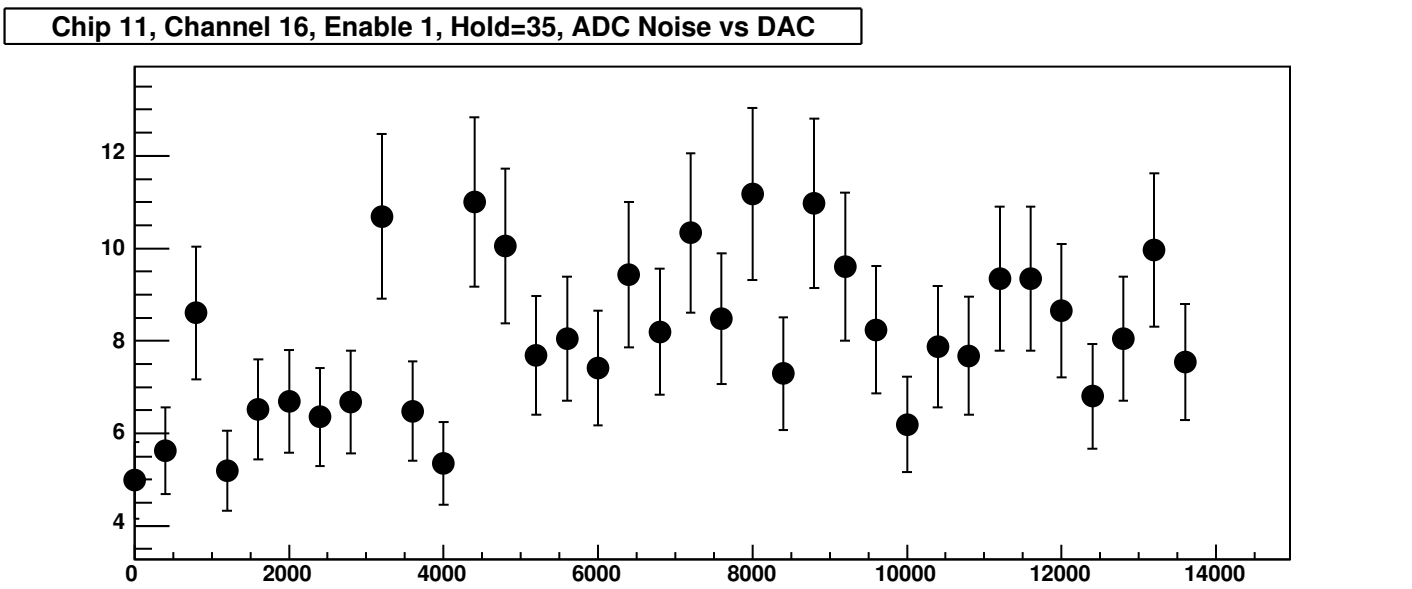

Chip 11, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

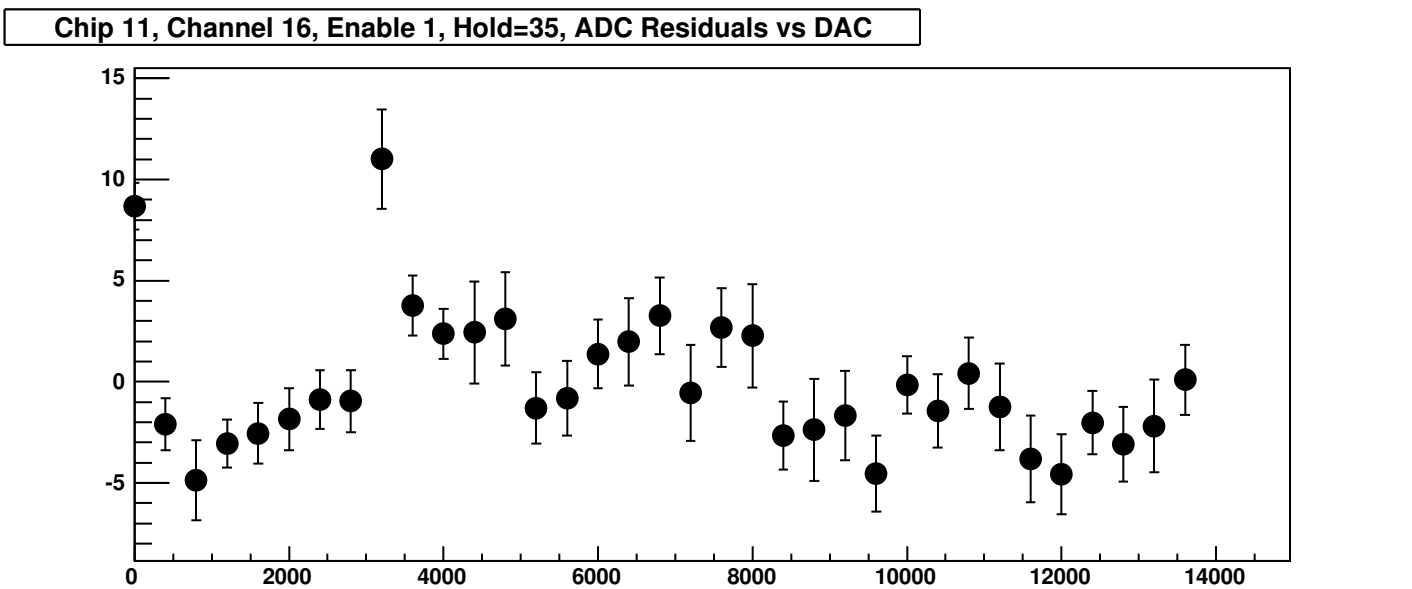

Chip 11, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 6.708e+04 / 23
p0 -974.7 ± 0.8946
p1 0.2932 ± 0.0001662
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

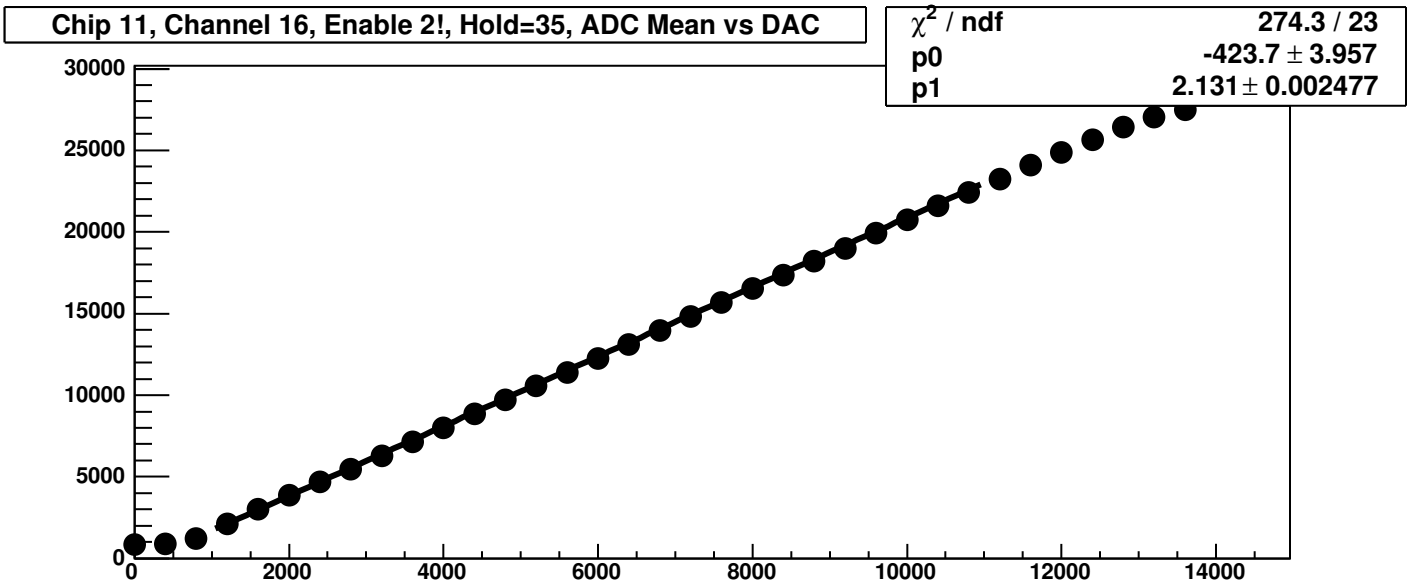

Chip 11, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

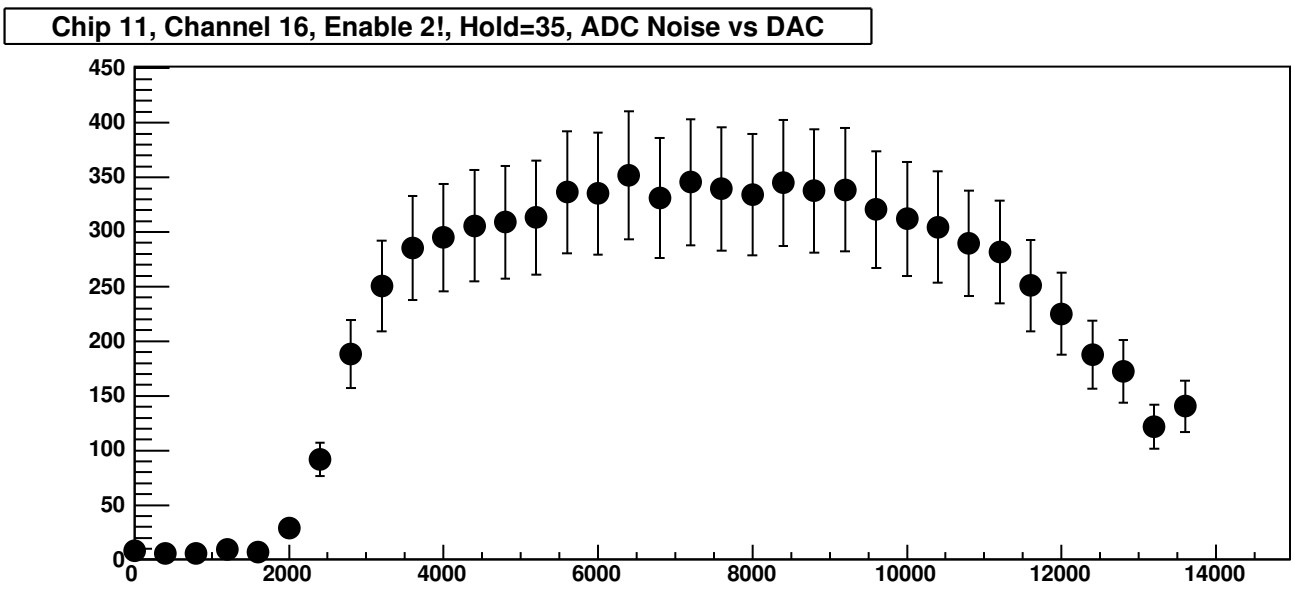

Chip 11, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

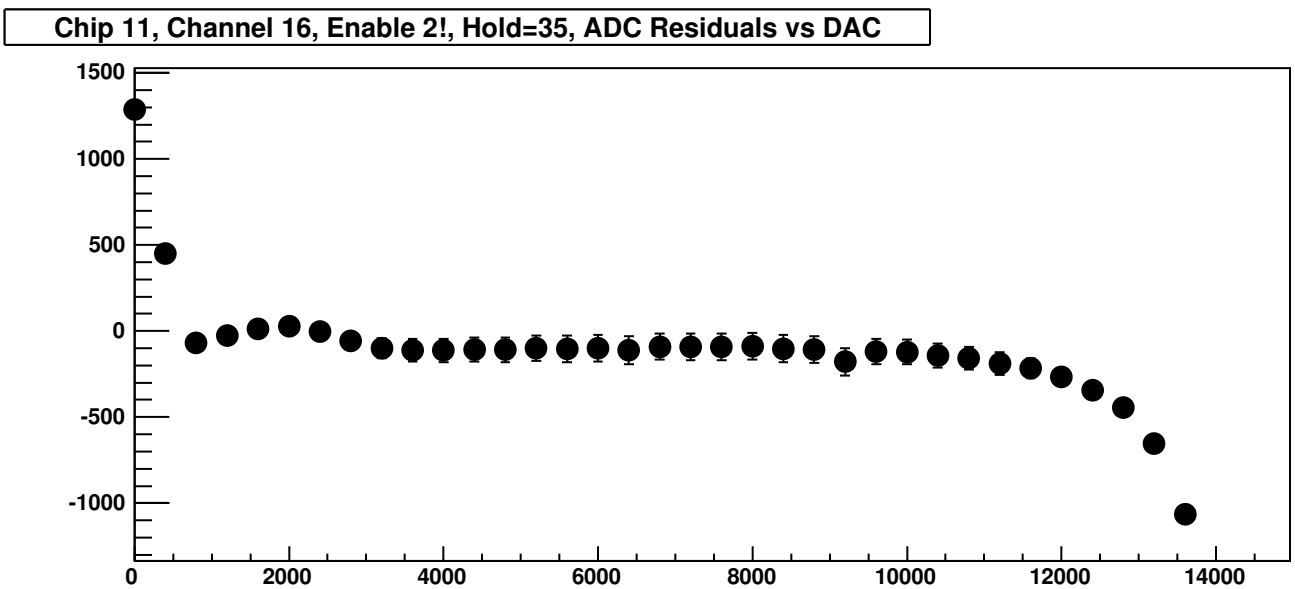

Chip 11, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 63.11 / 23
p0 -547.5 ± 0.7024
p1 0.02666 ± 0.0001125
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

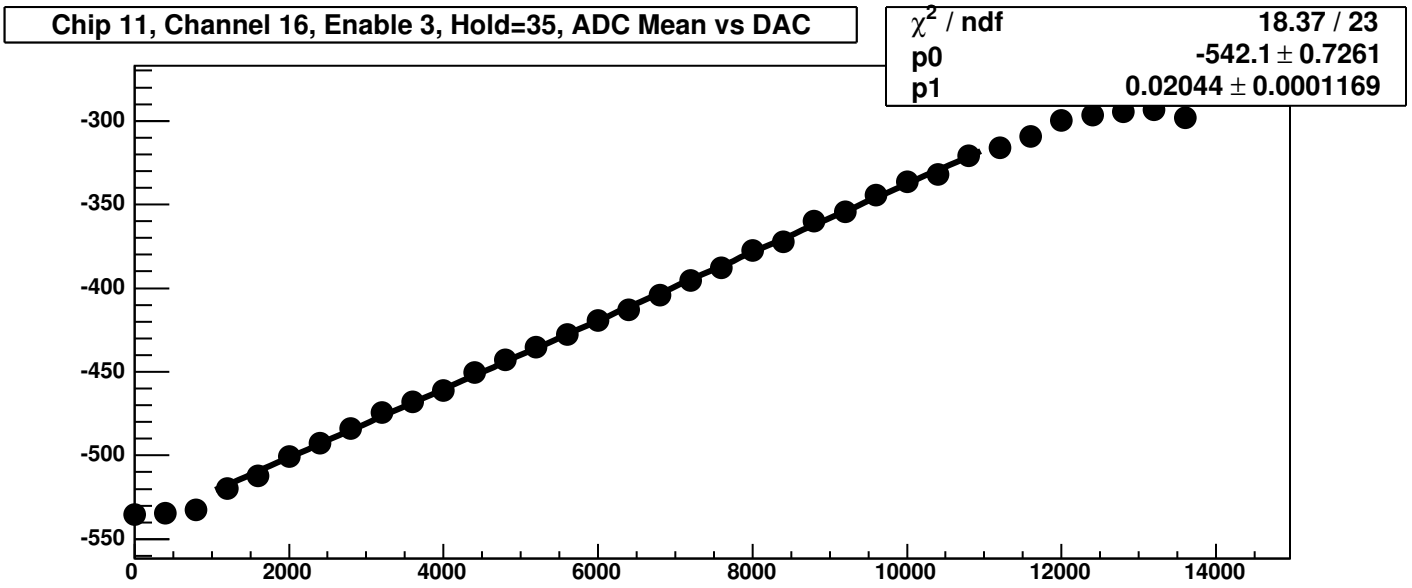

Chip 11, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

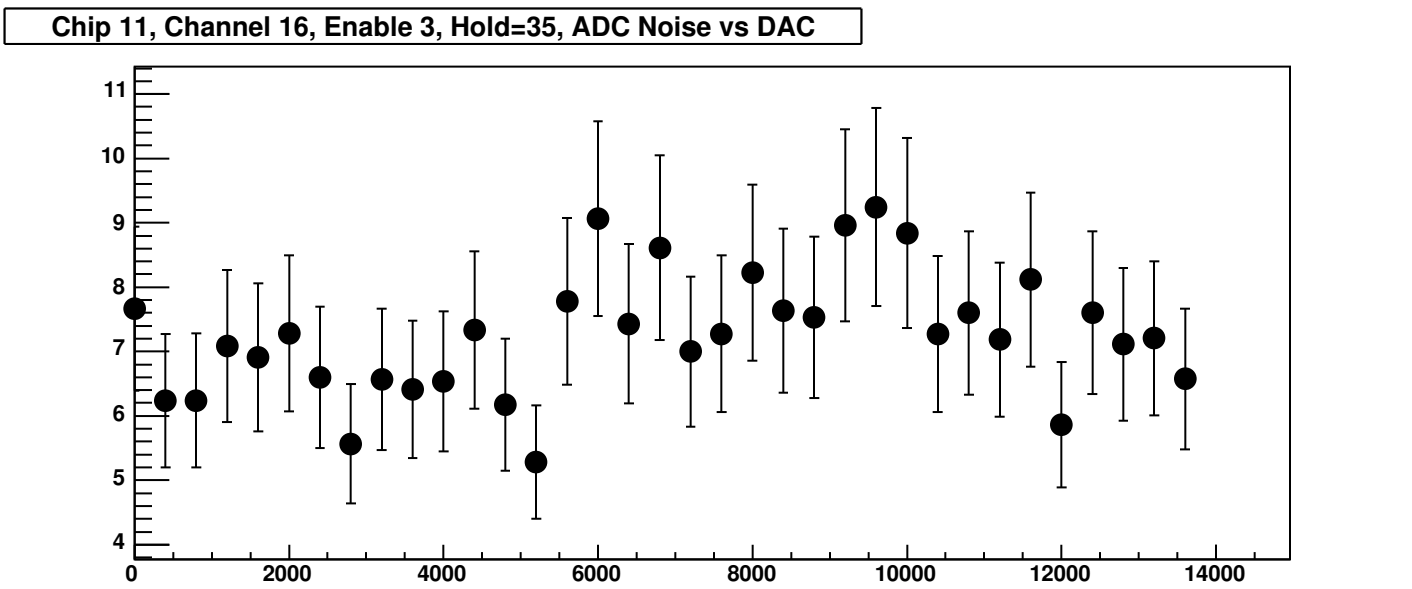

Chip 11, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

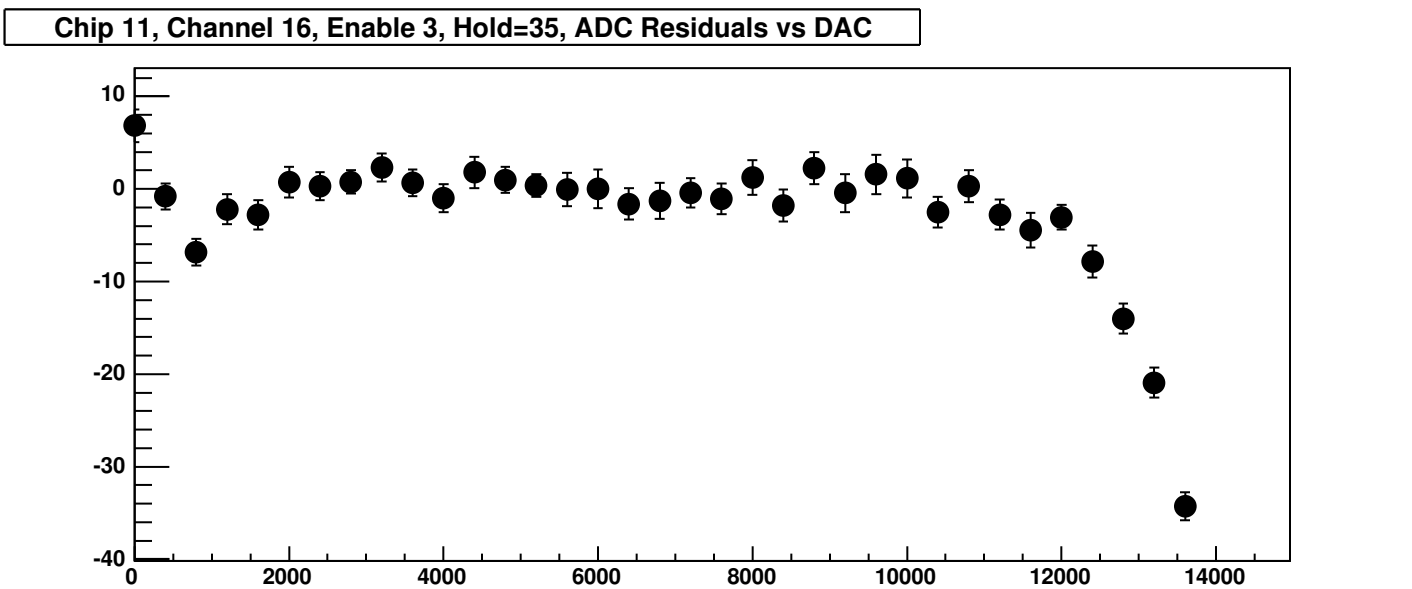

Chip 11, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 274.3 / 23
p0 -423.7 ± 3.957
p1 2.131 ± 0.002477
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

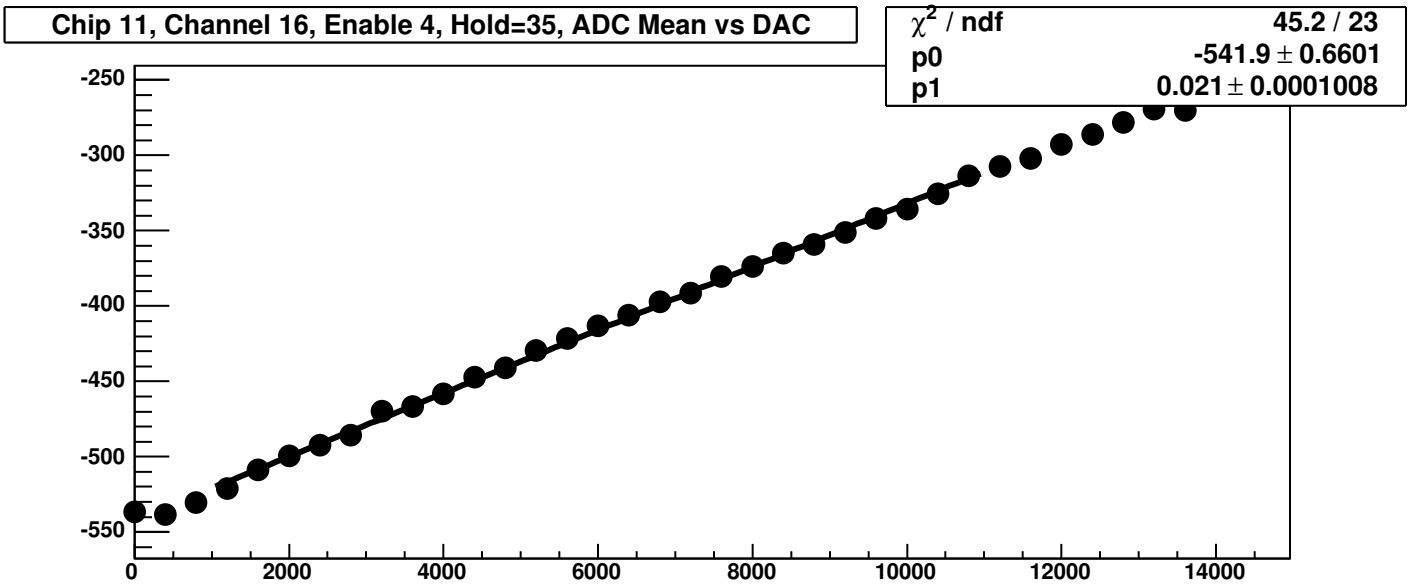

Chip 11, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

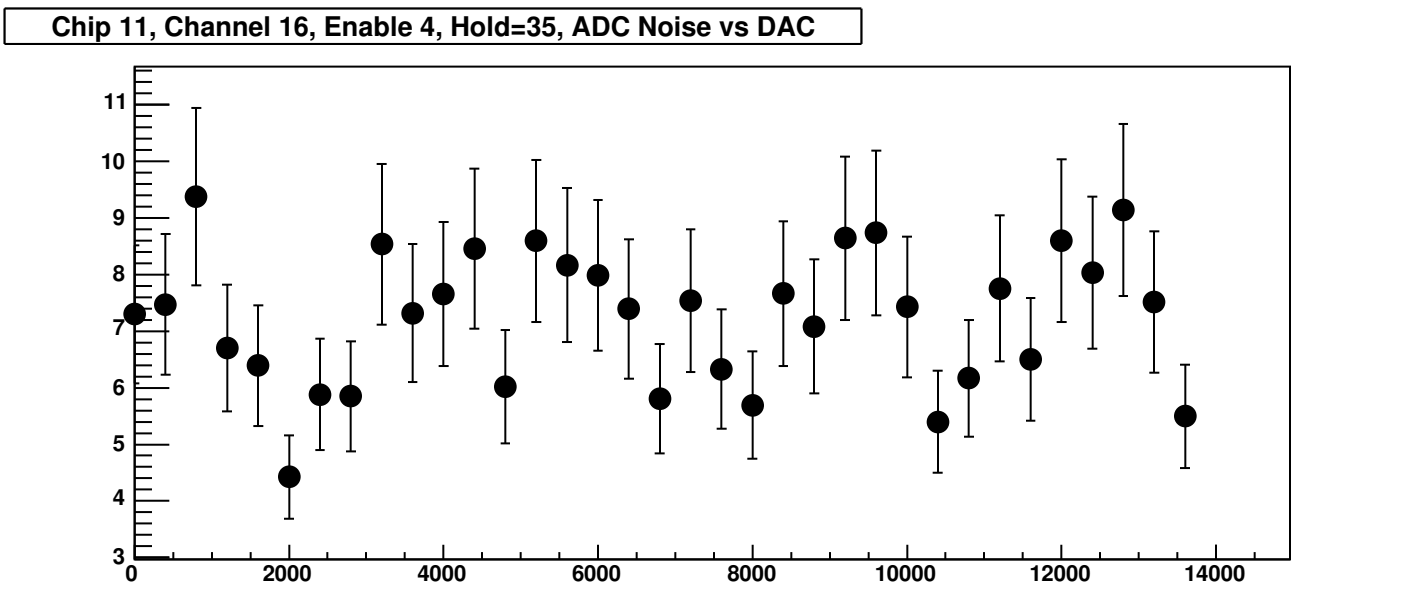

Chip 11, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

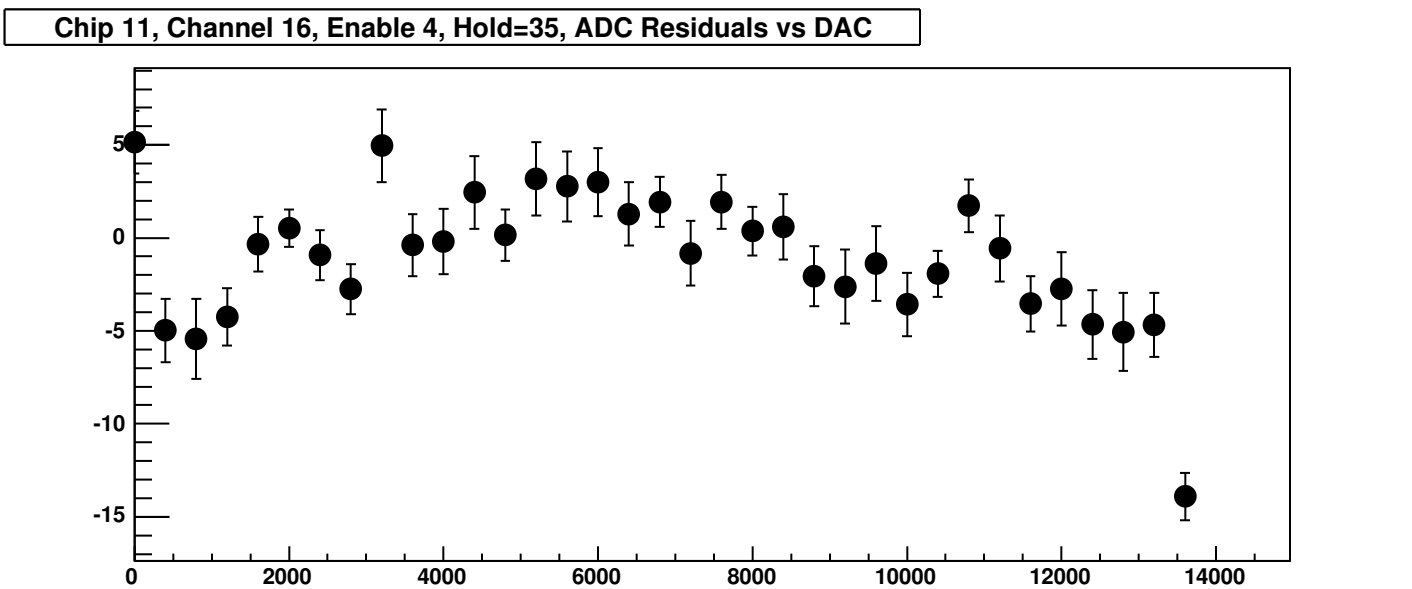

Chip 11, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 18.37 / 23
p0 -542.1 ± 0.7261
p1 0.02044 ± 0.0001169
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

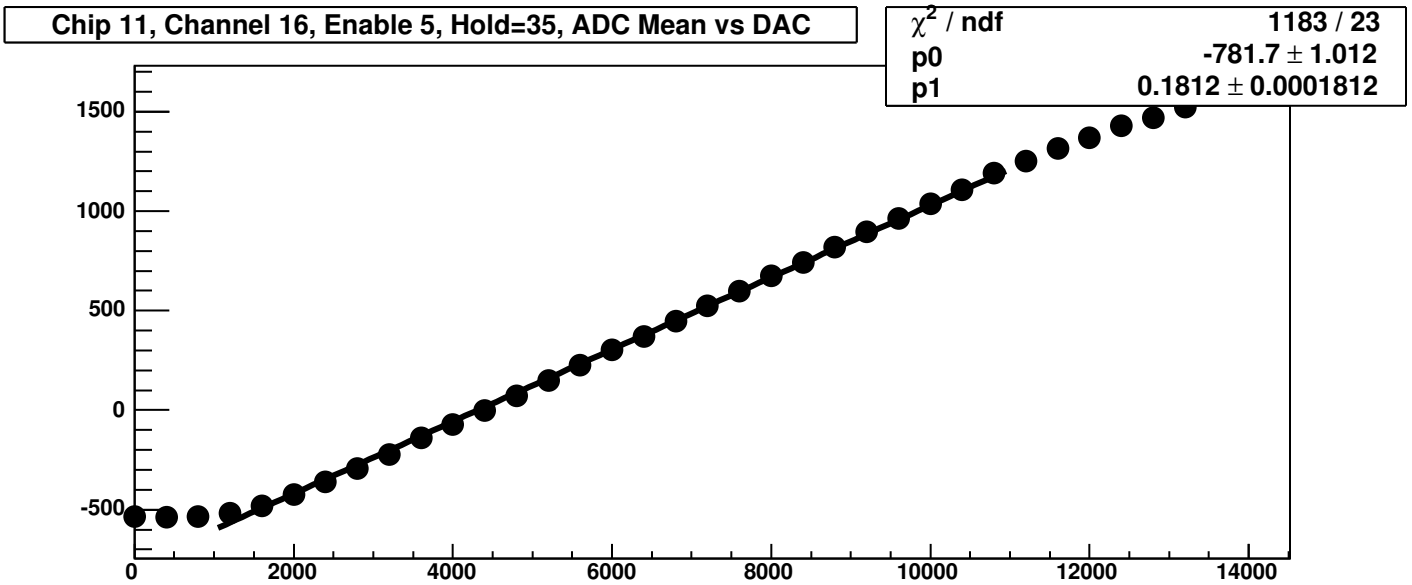

Chip 11, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

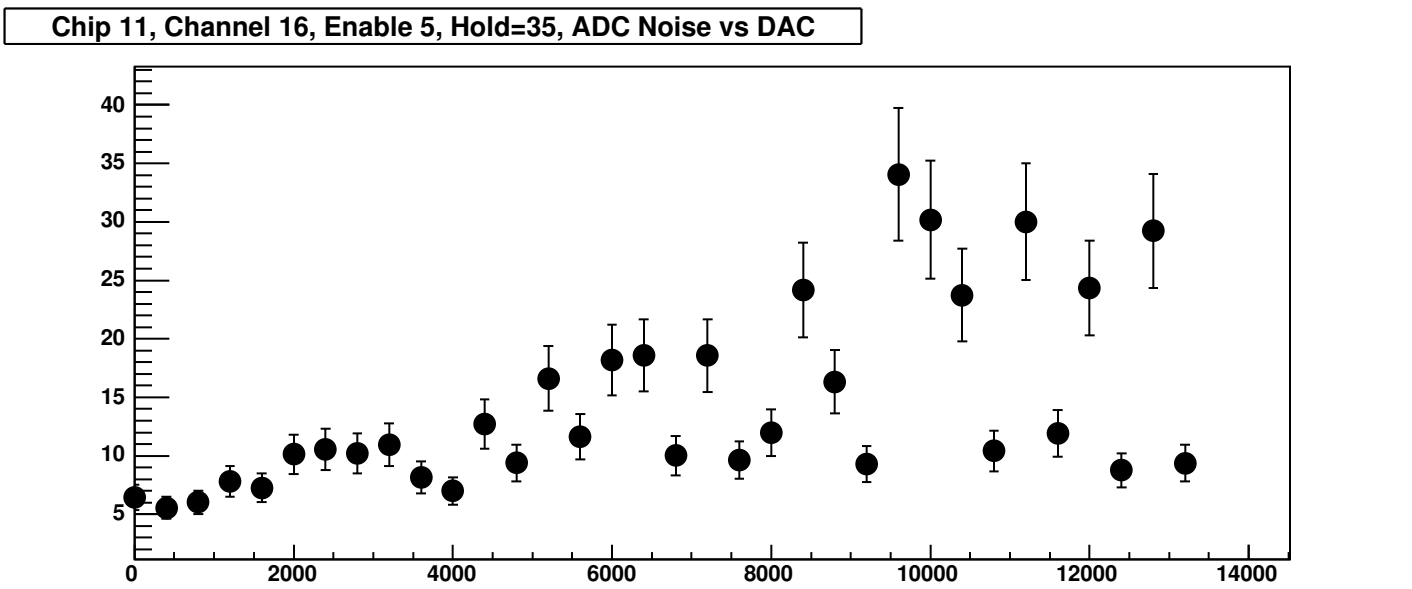

Chip 11, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

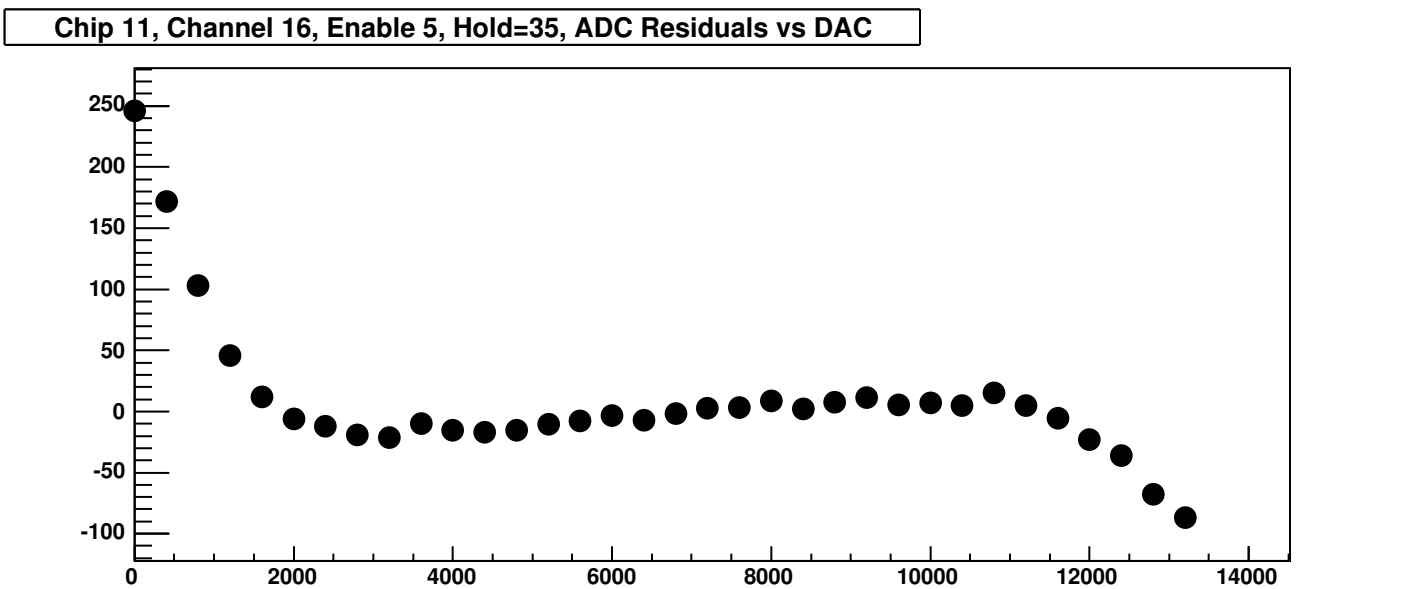

Chip 11, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 45.2 / 23
p0 -541.9 ± 0.6601
p1 0.021 ± 0.0001008
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

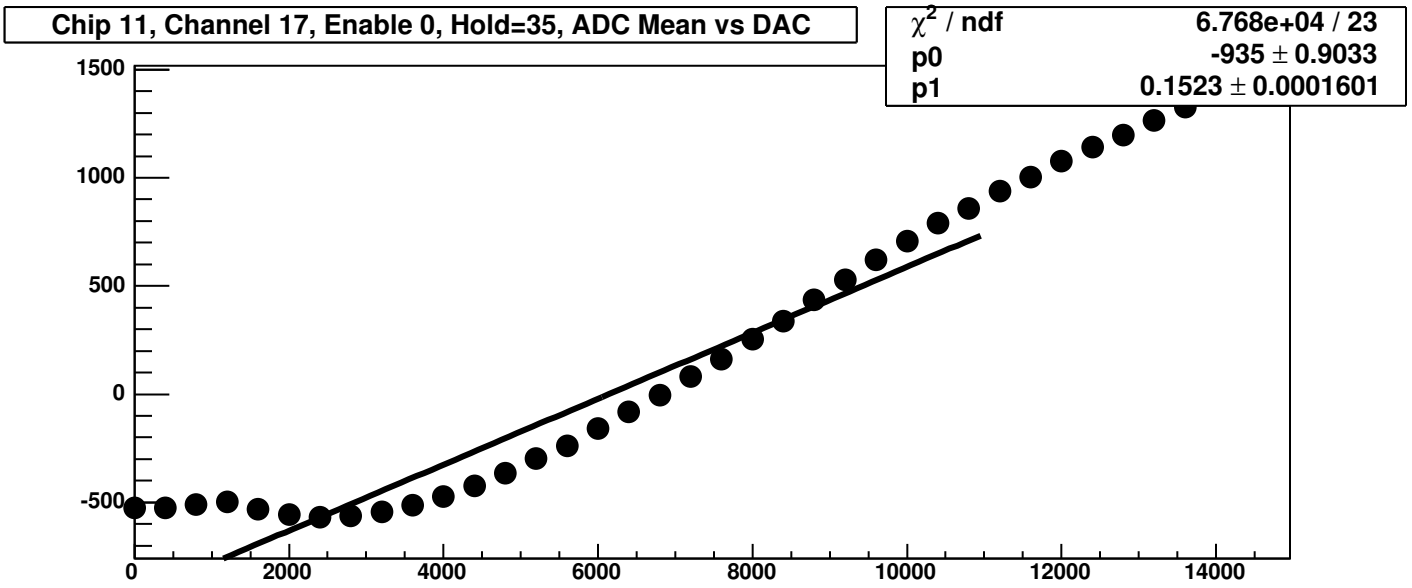

Chip 11, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

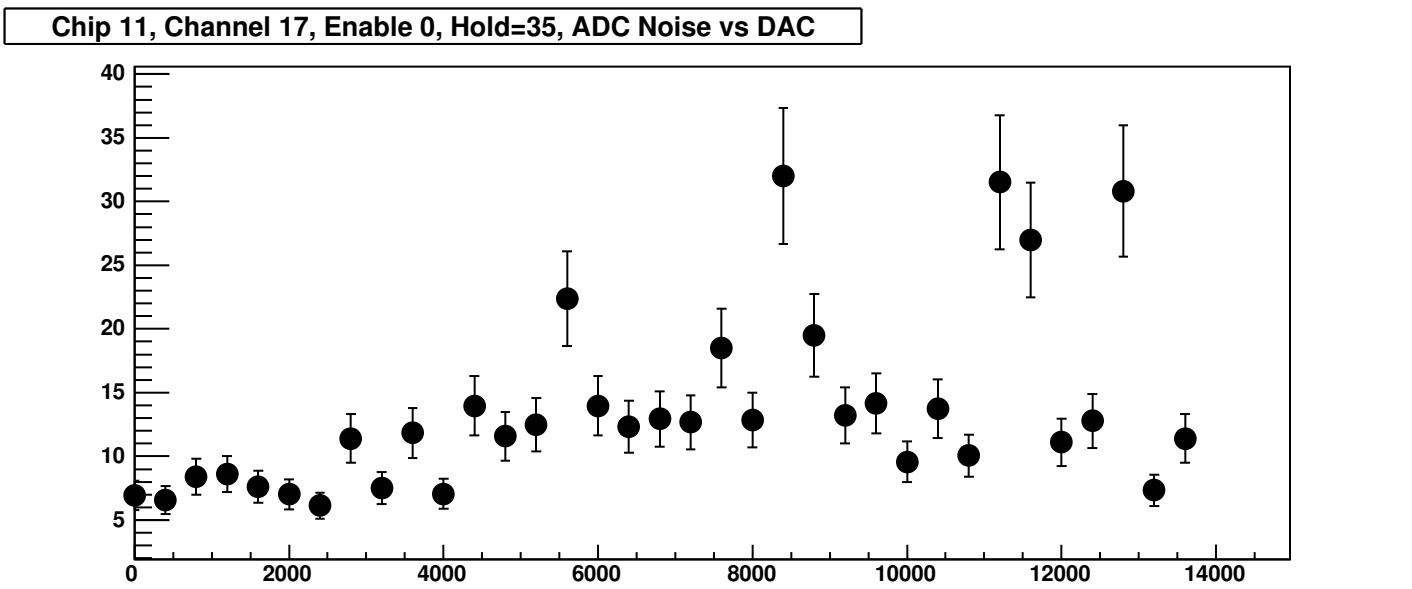

Chip 11, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

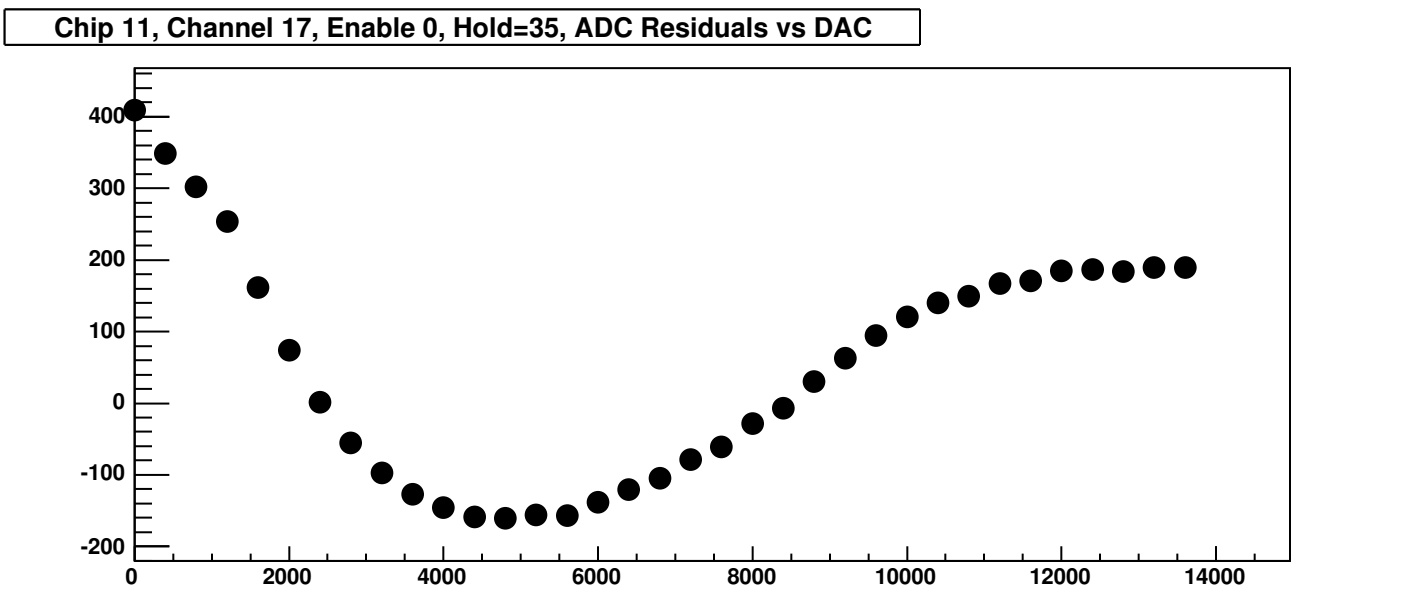

Chip 11, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1183 / 23
p0 -781.7 ± 1.012
p1 0.1812 ± 0.0001812
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

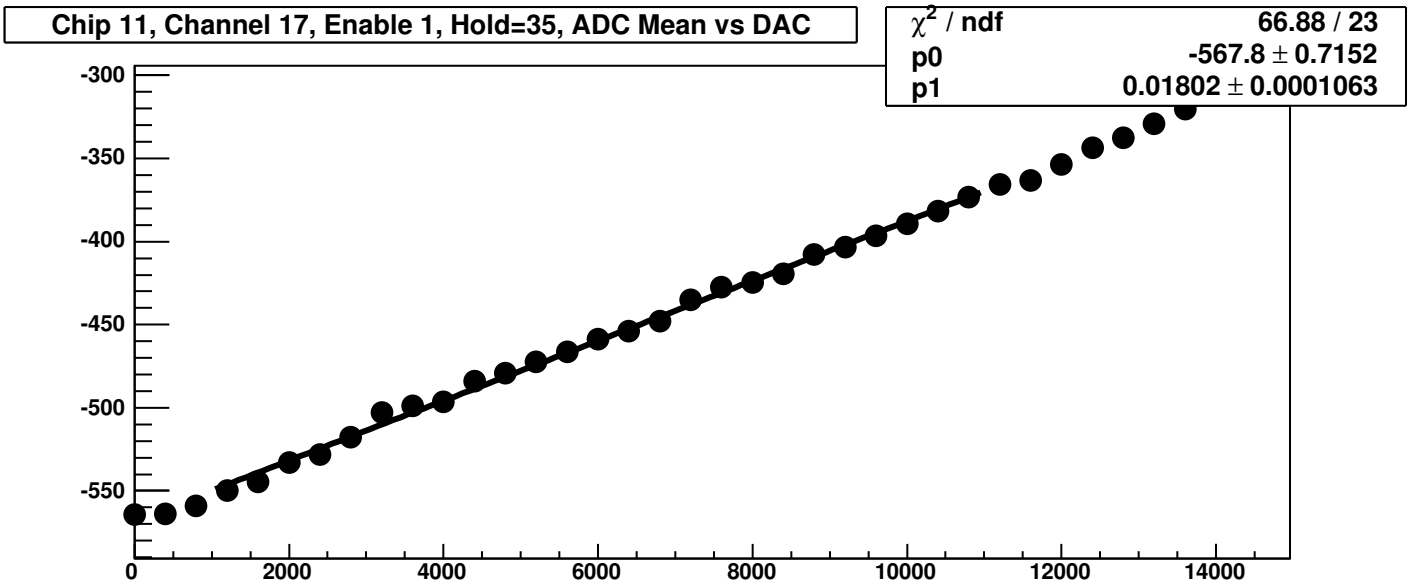

Chip 11, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

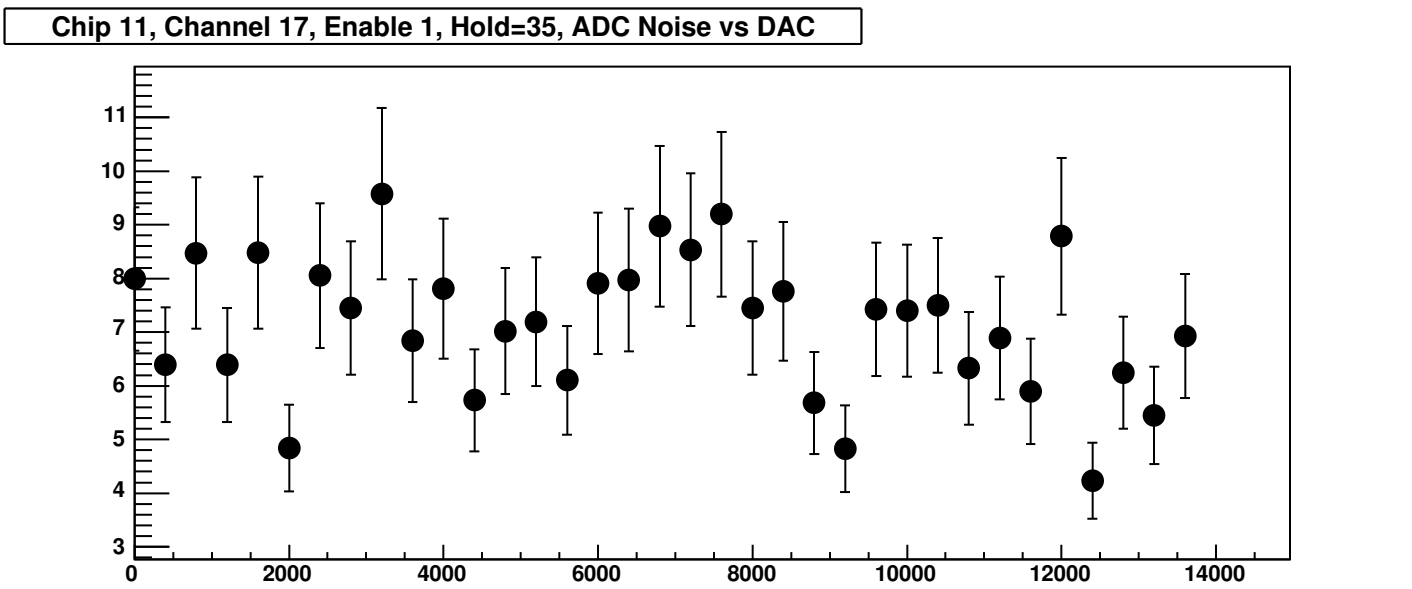

Chip 11, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

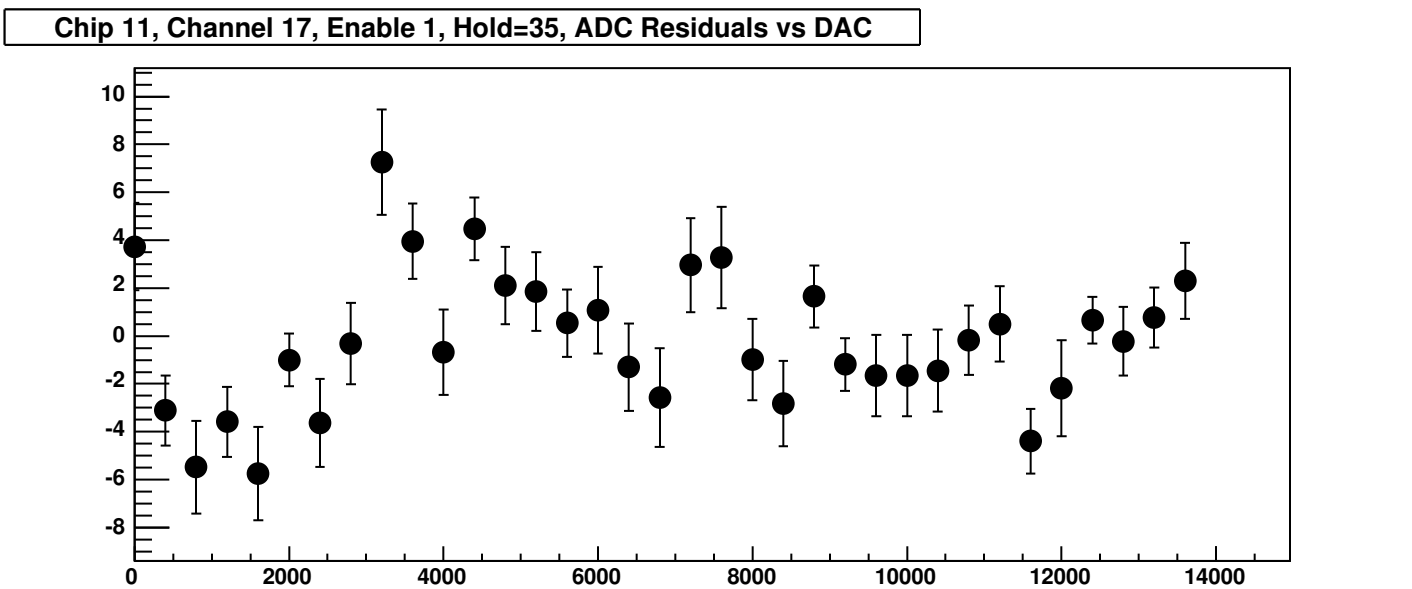

Chip 11, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 6.768e+04 / 23
p0 -935 ± 0.9033
p1 0.1523 ± 0.0001601
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

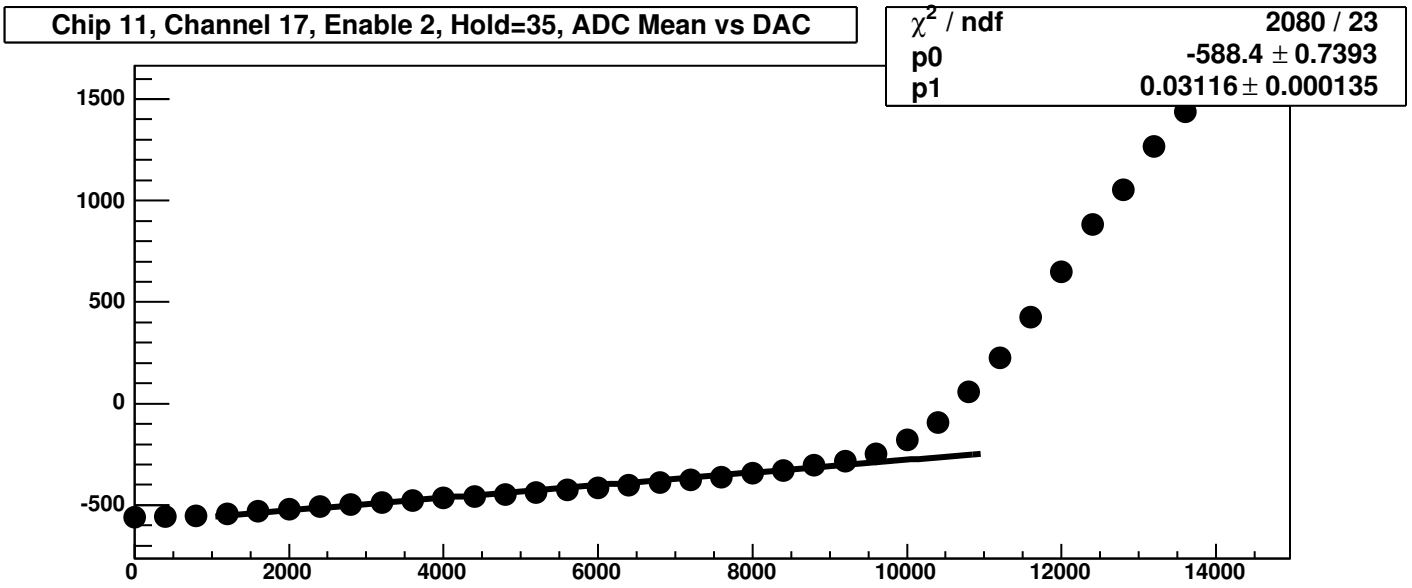

Chip 11, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

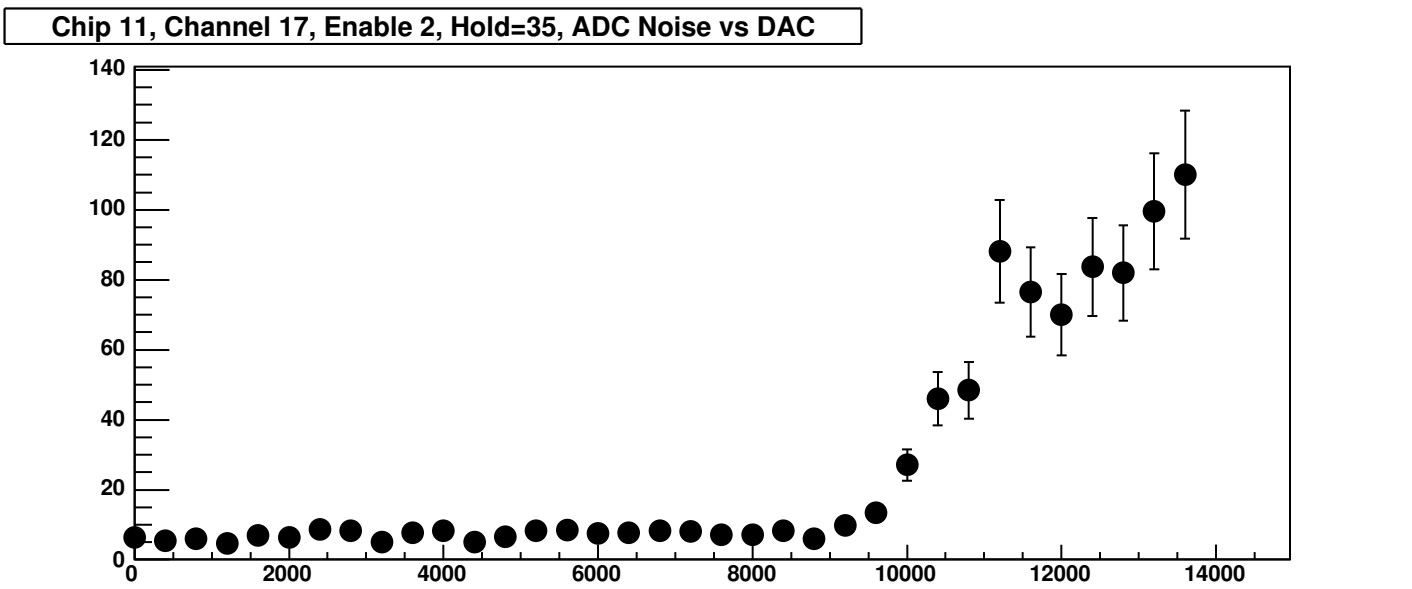

Chip 11, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

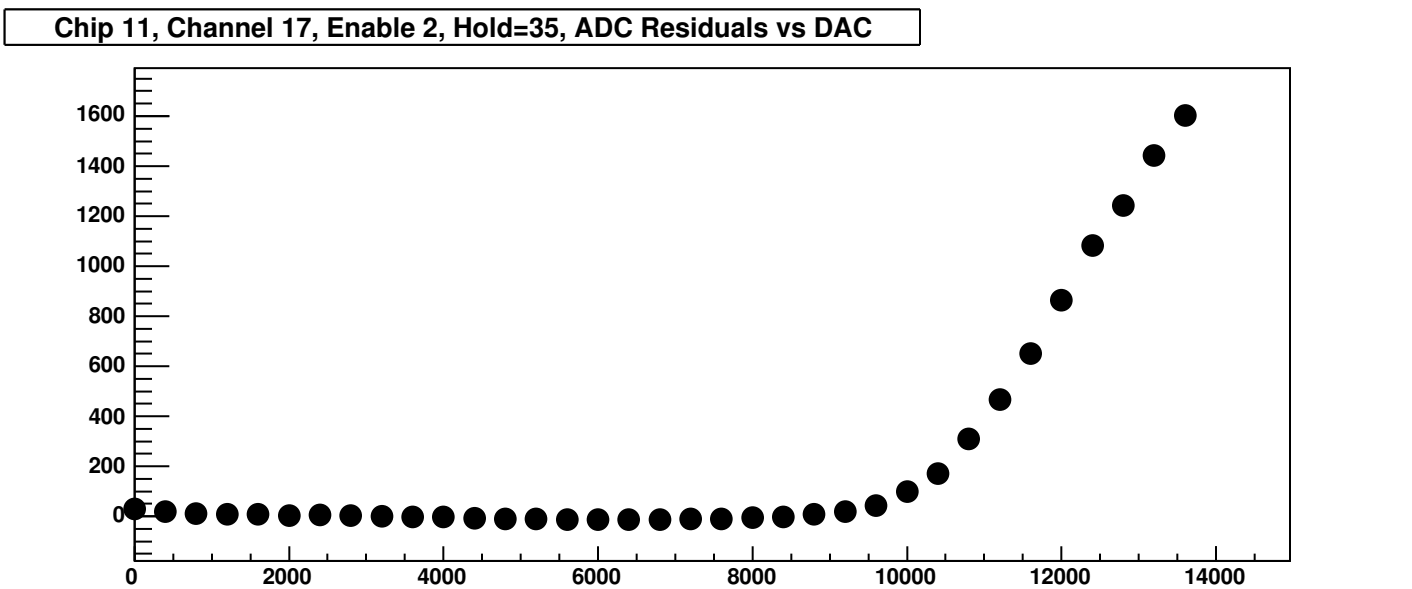

Chip 11, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 66.88 / 23
p0 -567.8 ± 0.7152
p1 0.01802 ± 0.0001063
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

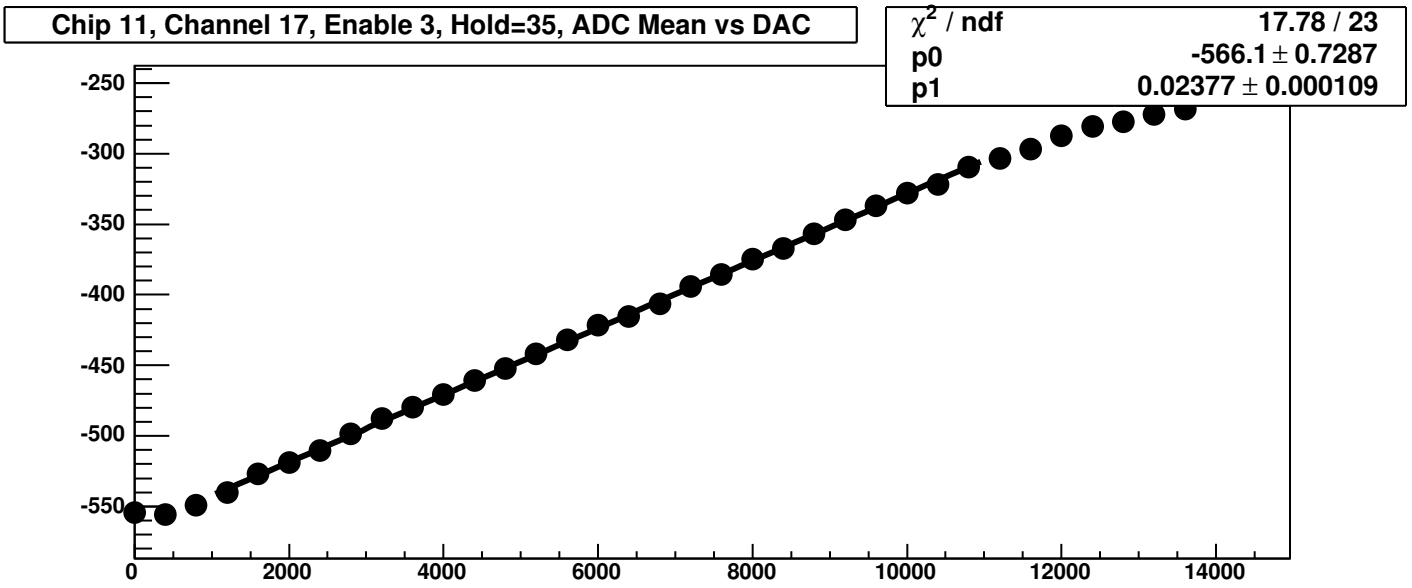

Chip 11, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

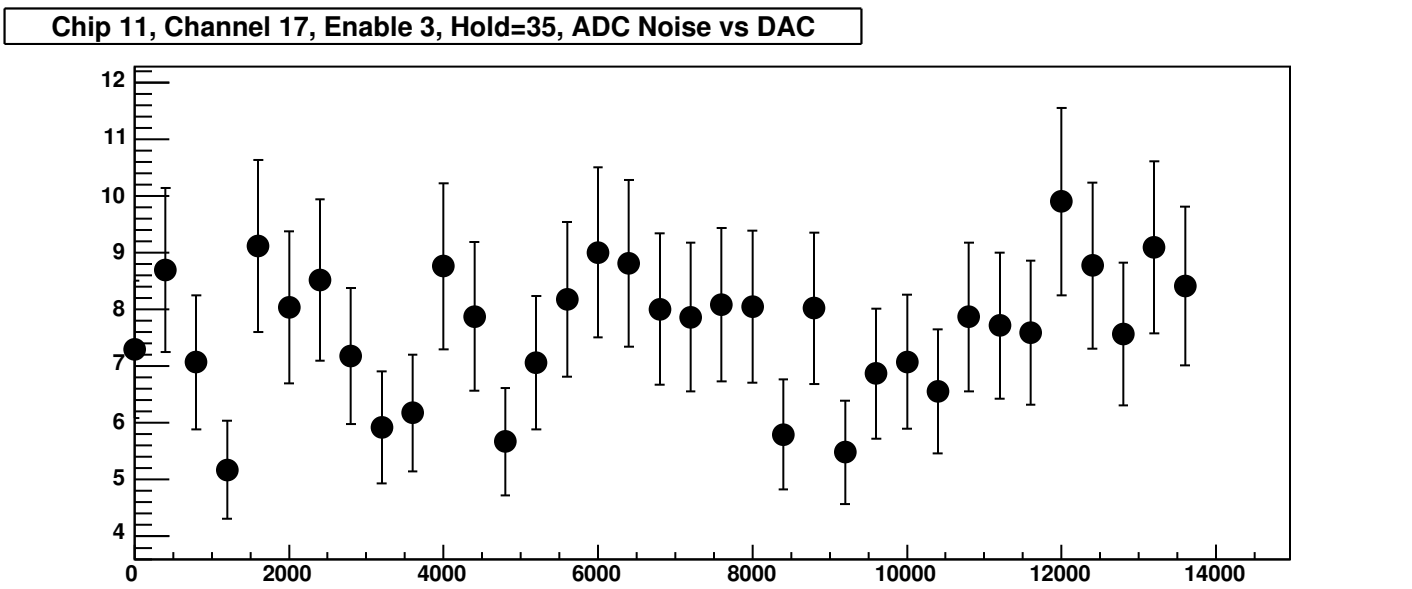

Chip 11, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

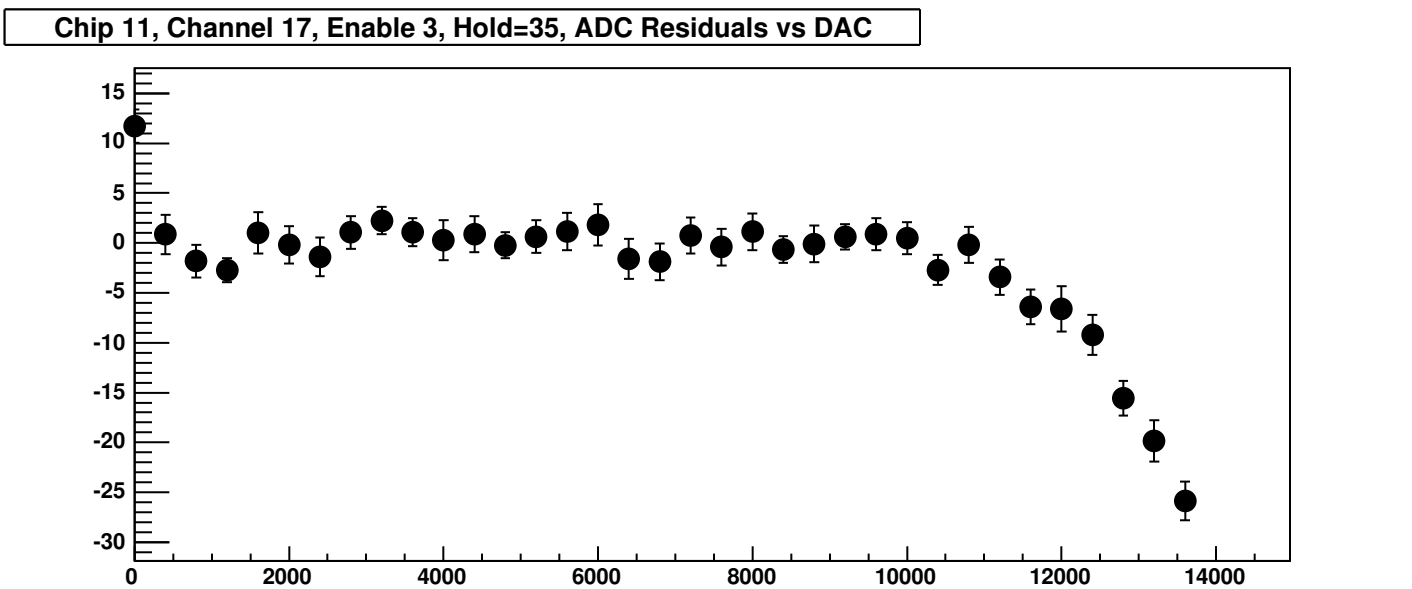

Chip 11, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 2080 / 23
p0 -588.4 ± 0.7393
p1 0.03116 ± 0.000135
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

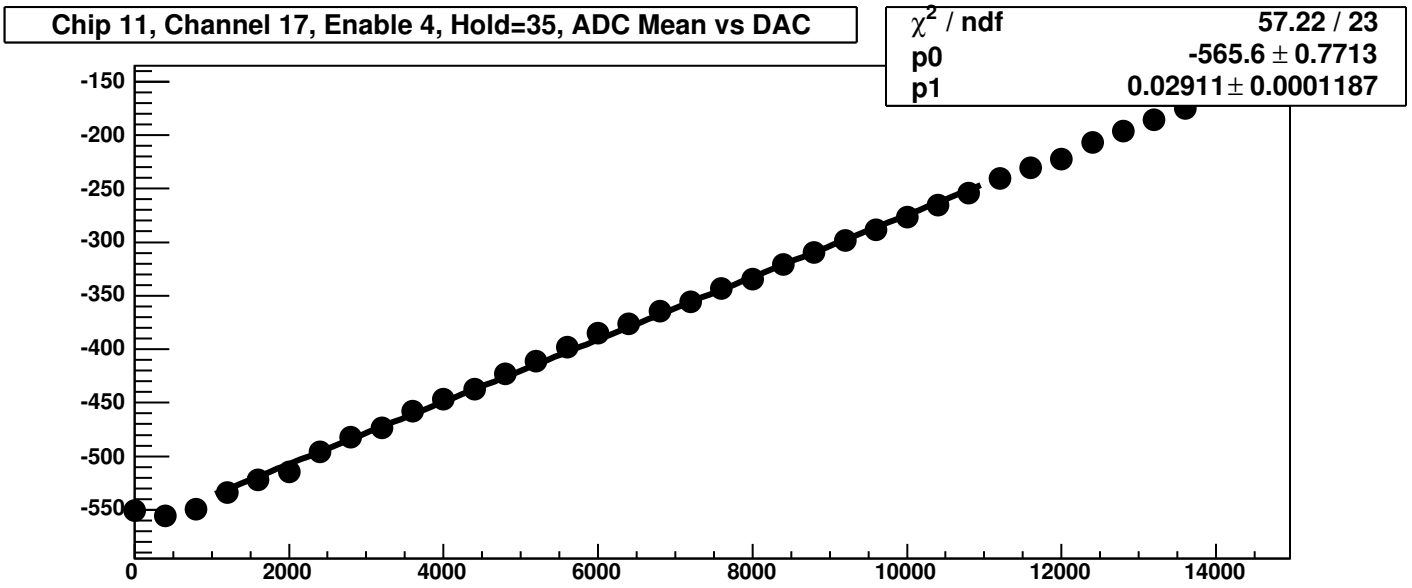

Chip 11, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

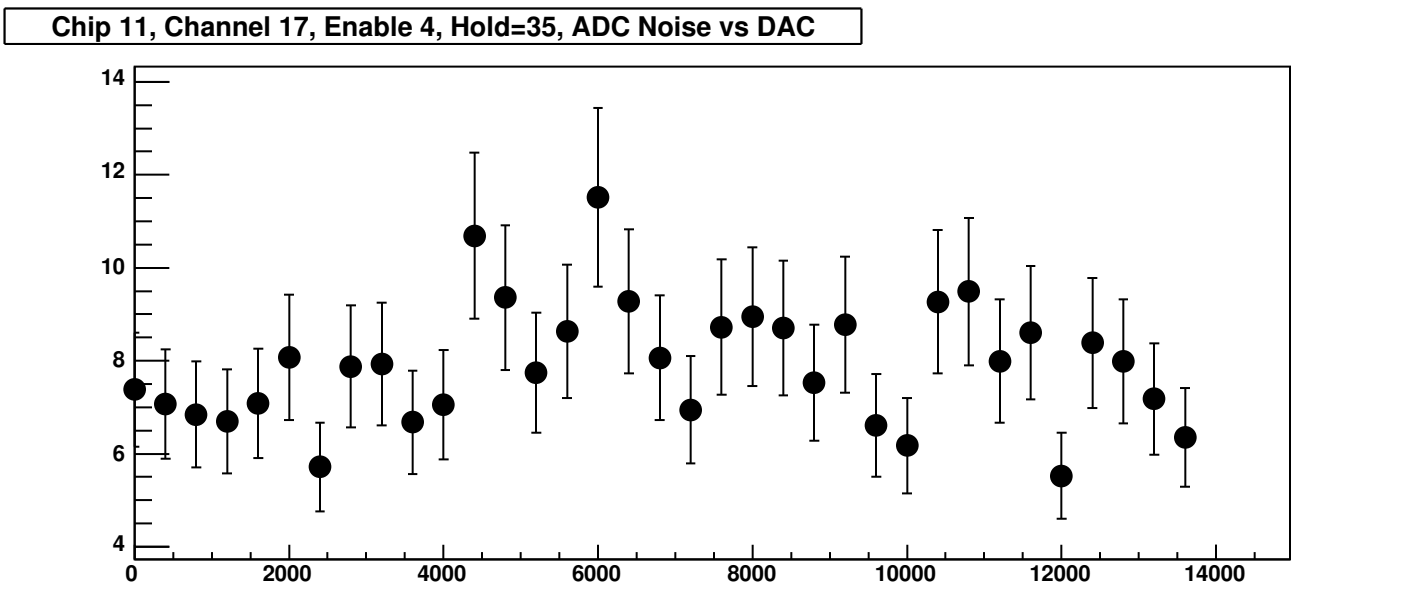

Chip 11, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

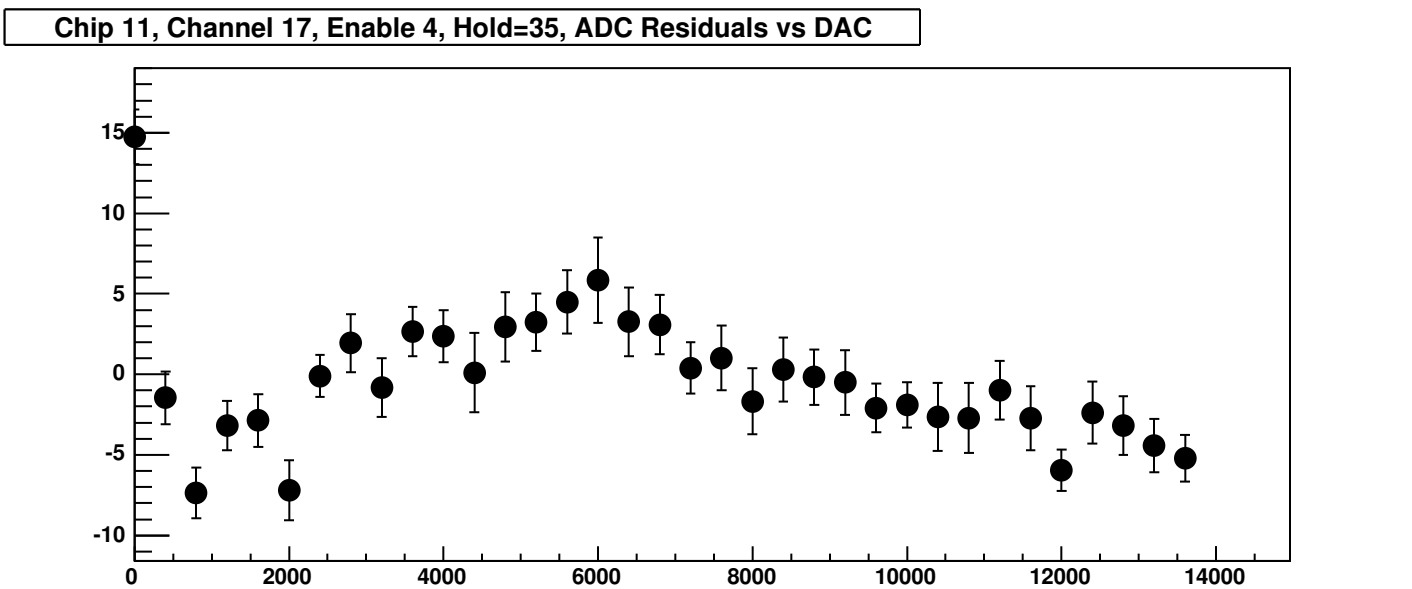

Chip 11, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 17.78 / 23
p0 -566.1 ± 0.7287
p1 0.02377 ± 0.000109
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
-25
-30
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 57.22 / 23
p0 -565.6 ± 0.7713
p1 0.02911 ± 0.0001187
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000