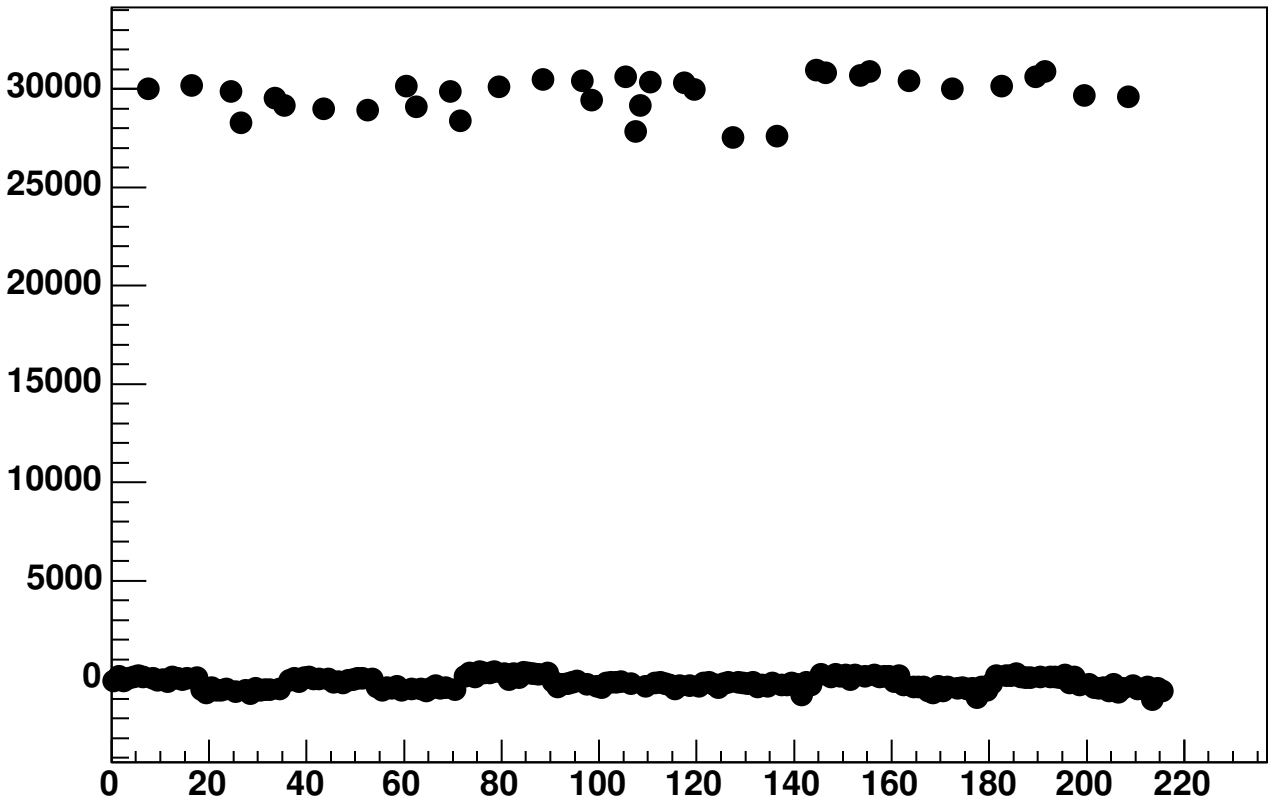
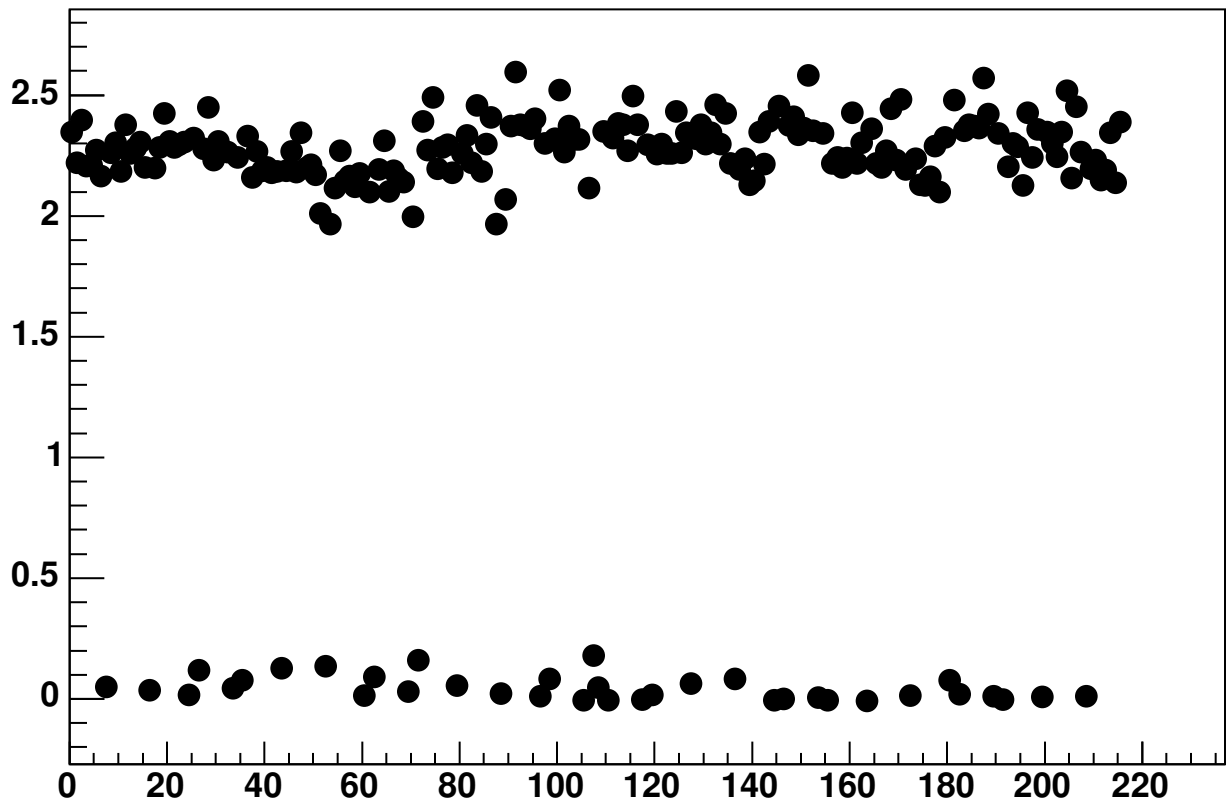
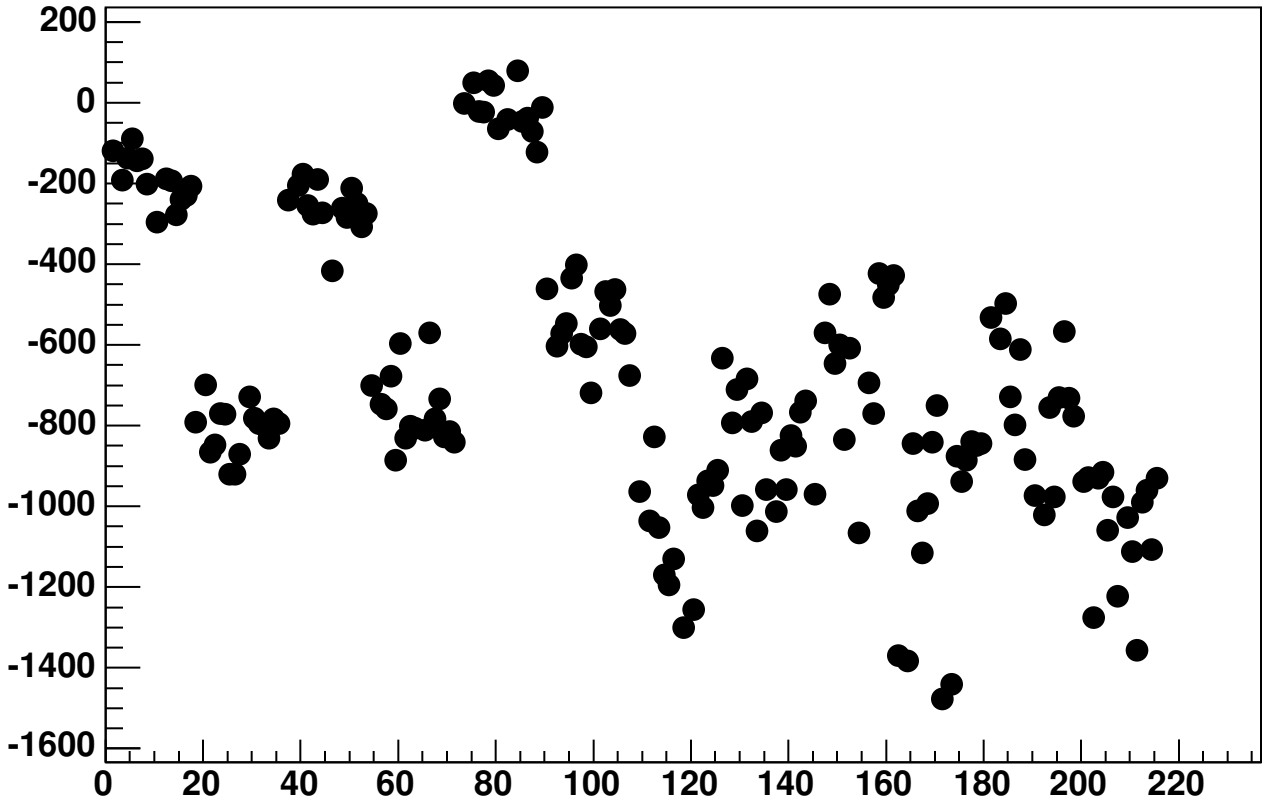
Channel Enabled, Hold=35, Fit Intercept vs 18*Chip+Chan



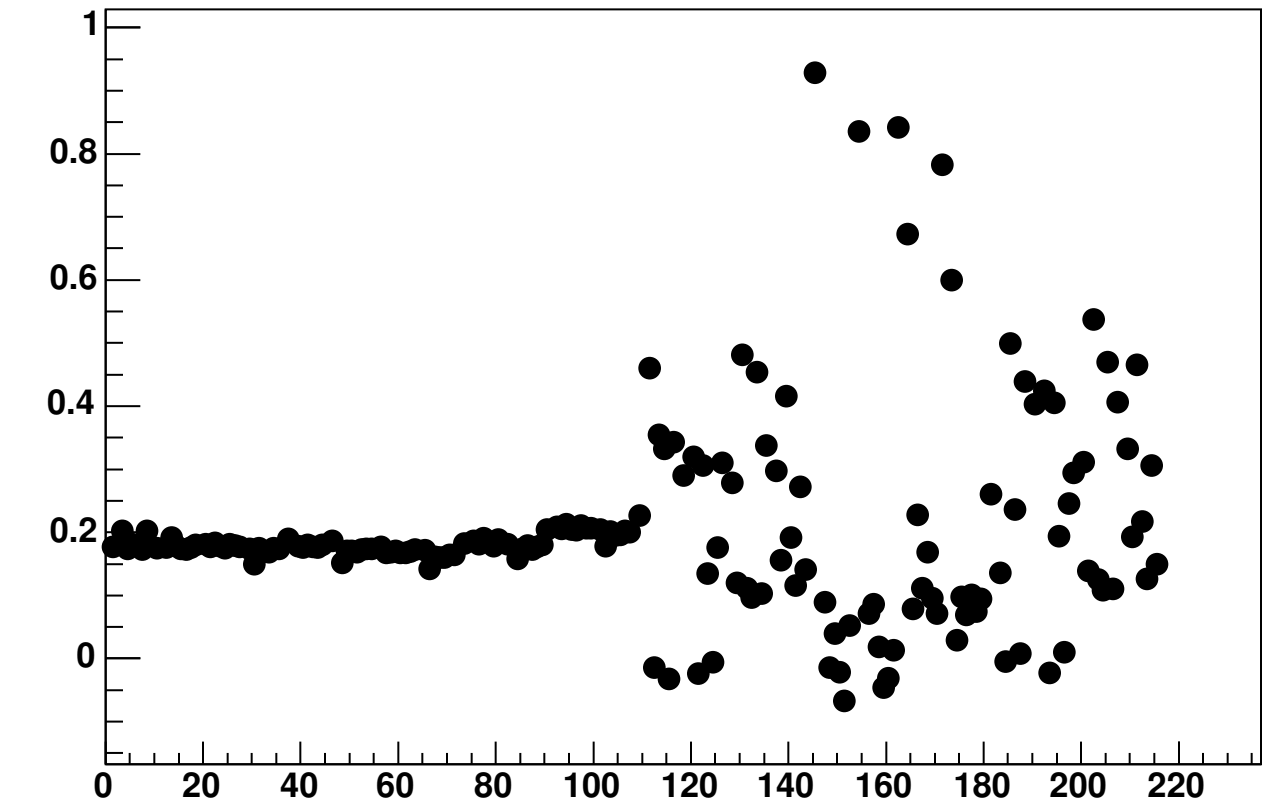
Channel Enabled, Hold=35, Fit Slope vs 18*Chip+Chan



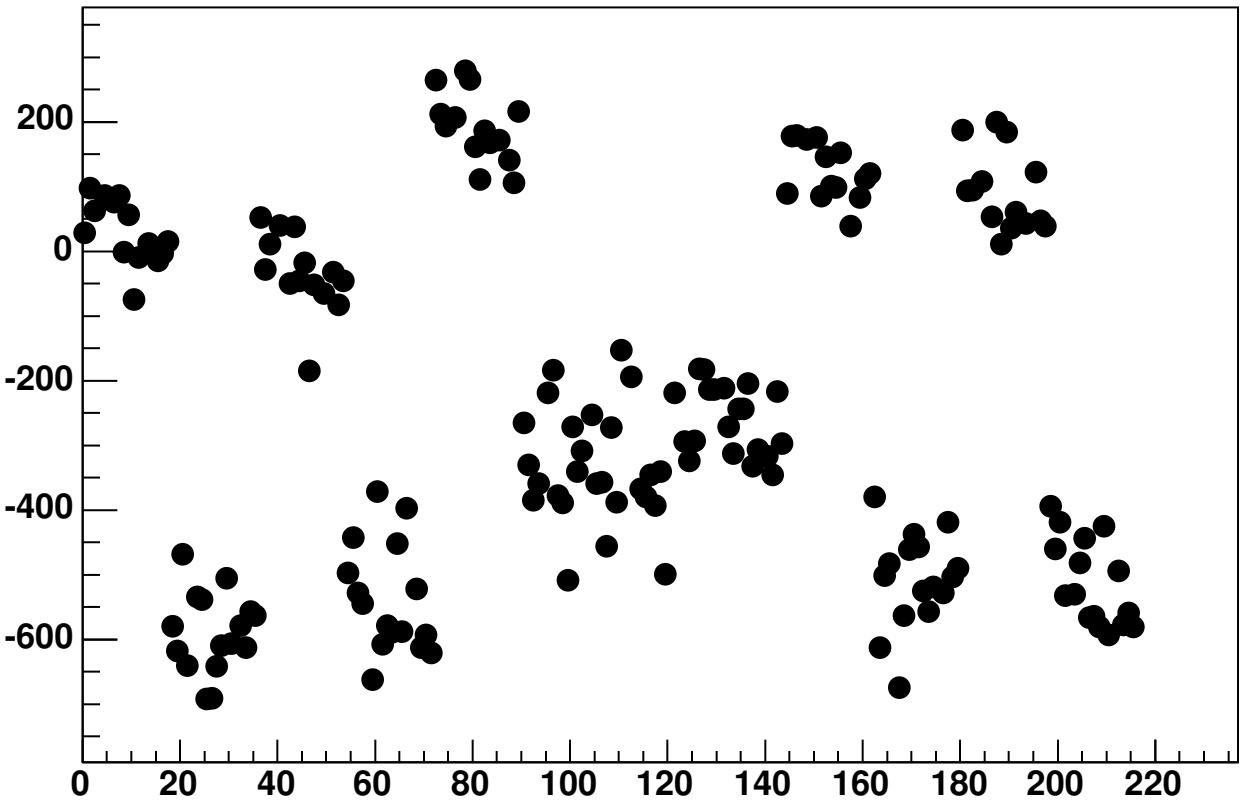
Disabled 0, Hold=35, Fit Intercept vs 18*Chip+Chan



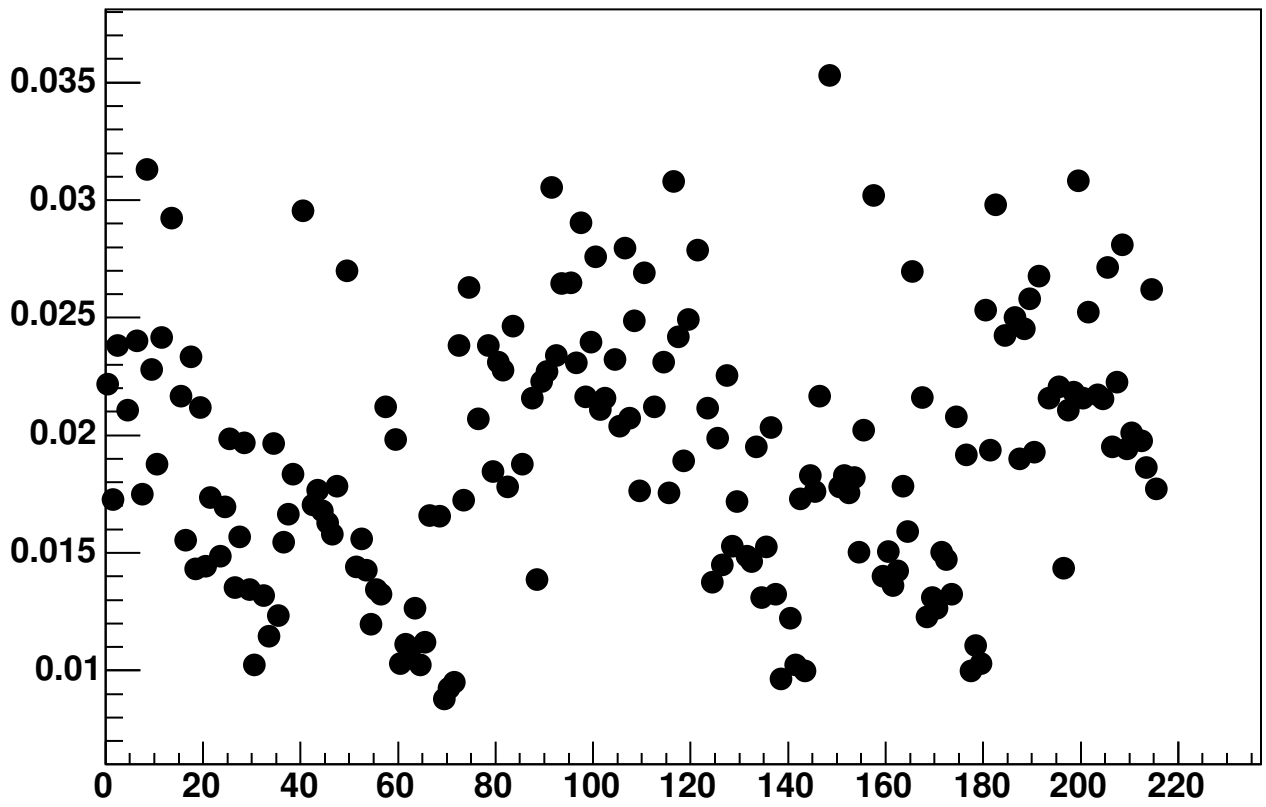
Disabled 0, Hold=35, Fit Slope vs 18*Chip+Chan



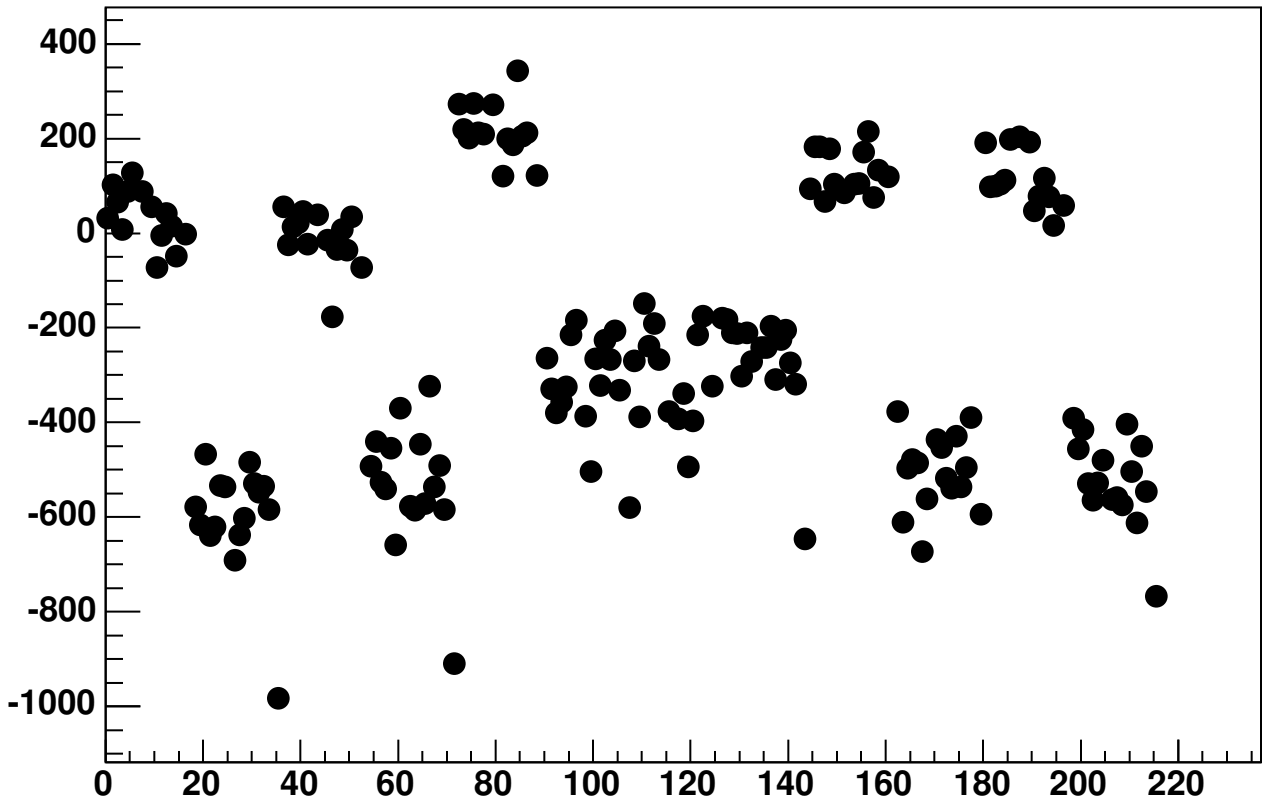
Disabled 1, Hold=35, Fit Intercept vs 18*Chip+Chan



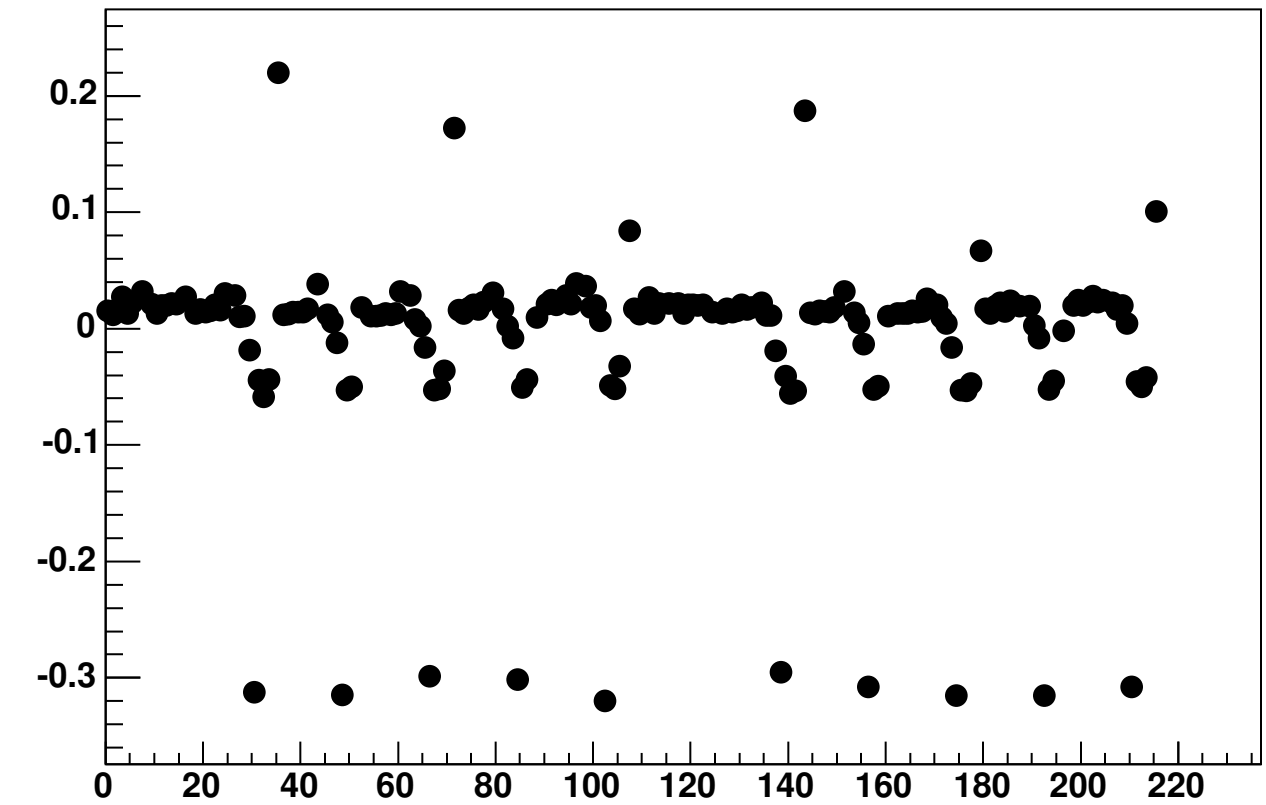
Disabled 1, Hold=35, Fit Slope vs 18*Chip+Chan



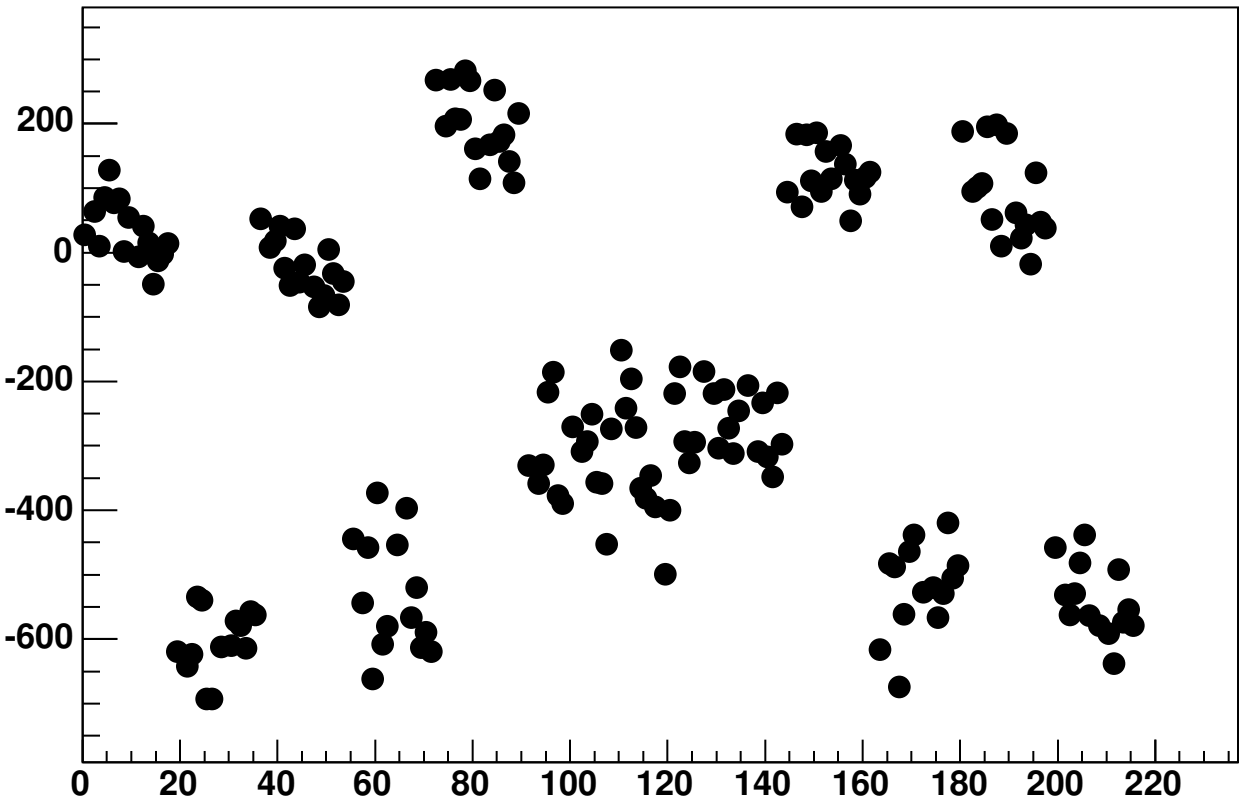
Disabled 2, Hold=35, Fit Intercept vs 18*Chip+Chan



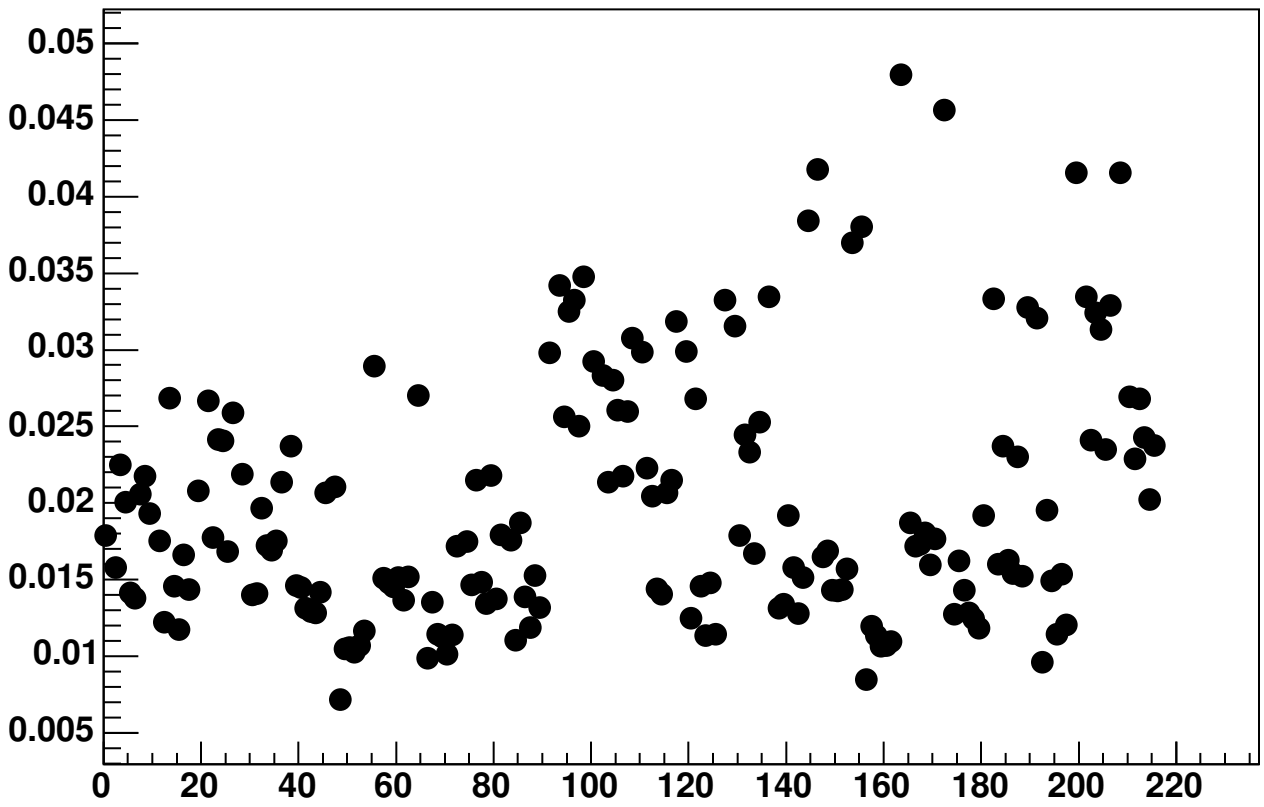
Disabled 2, Hold=35, Fit Slope vs 18*Chip+Chan



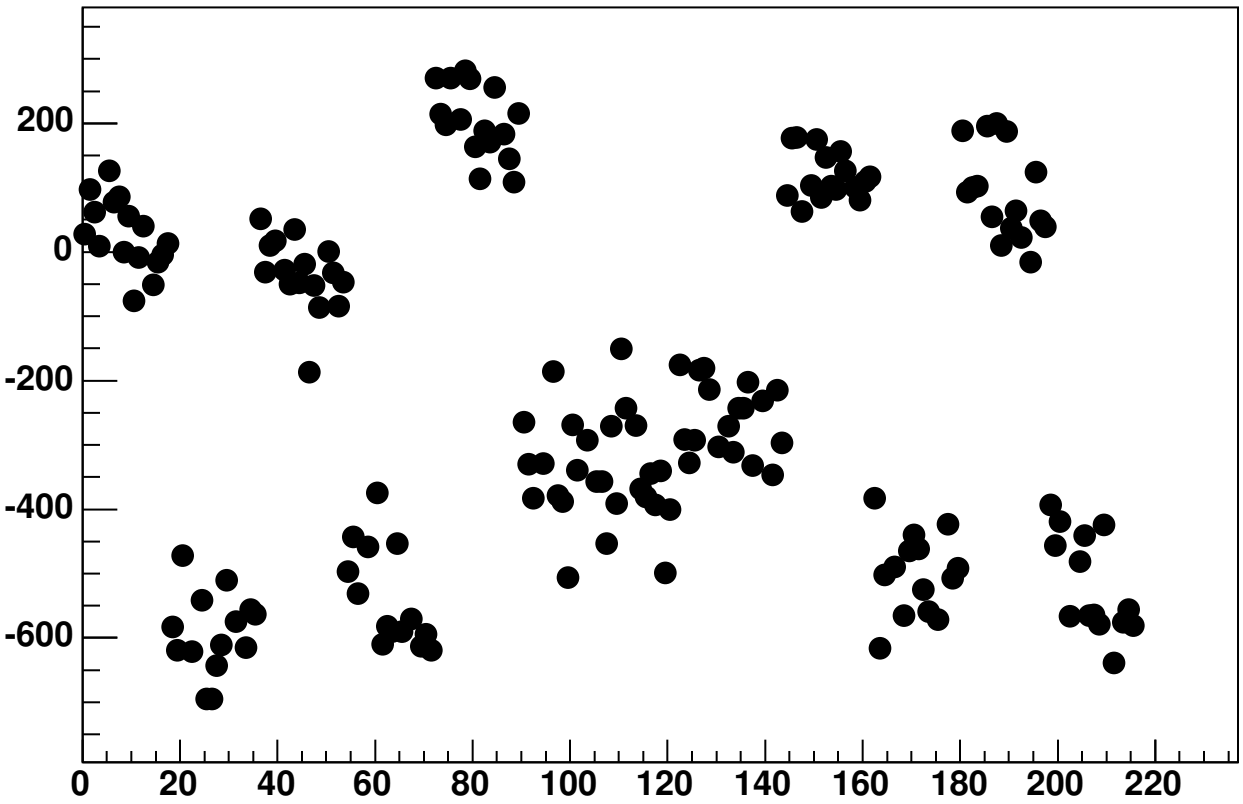
Disabled 3, Hold=35, Fit Intercept vs 18*Chip+Chan



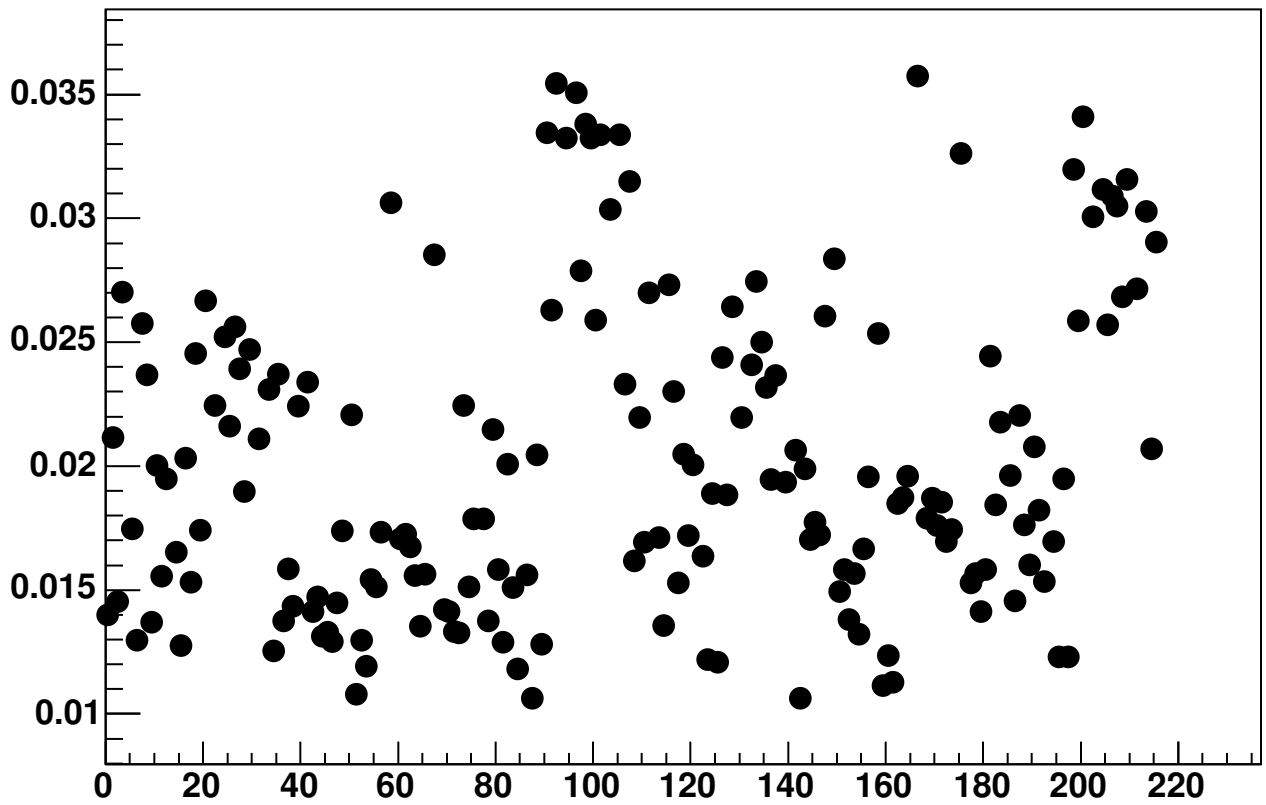
Disabled 3, Hold=35, Fit Slope vs 18*Chip+Chan



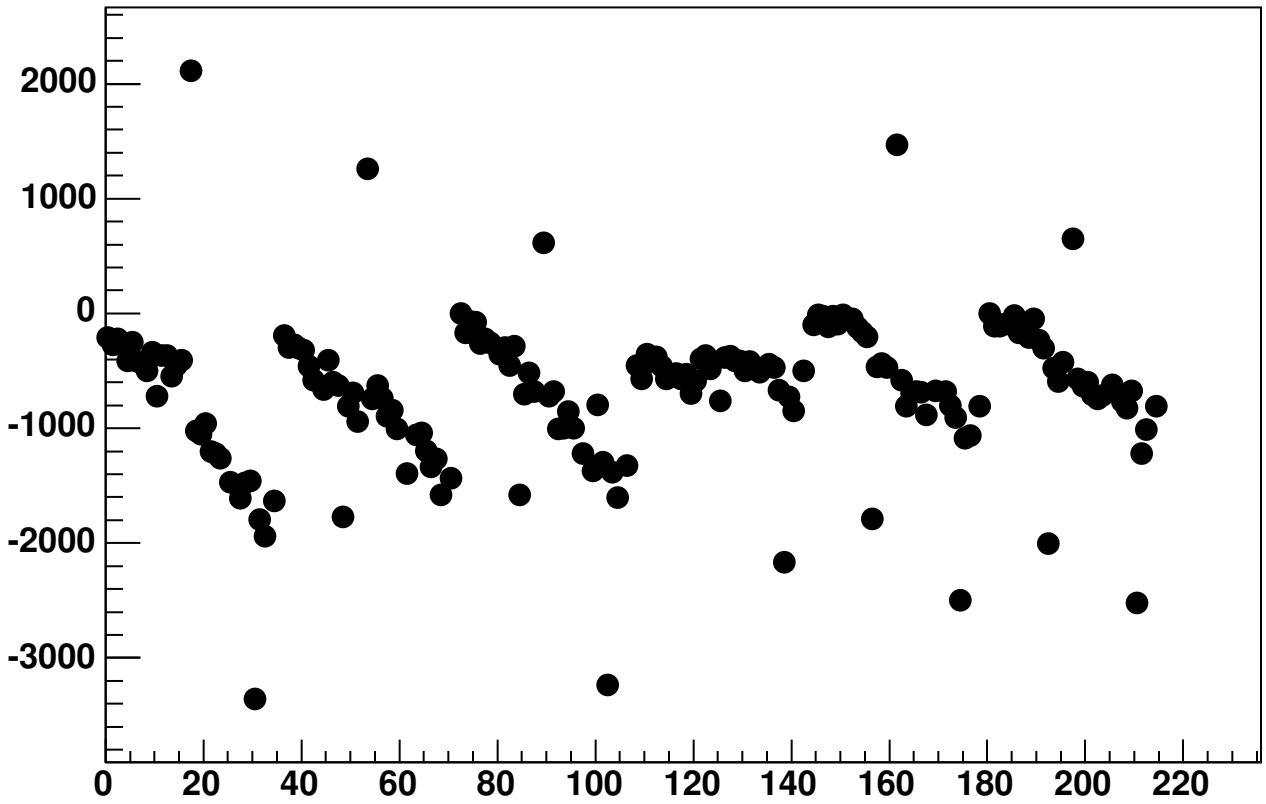
Disabled 4, Hold=35, Fit Intercept vs 18*Chip+Chan



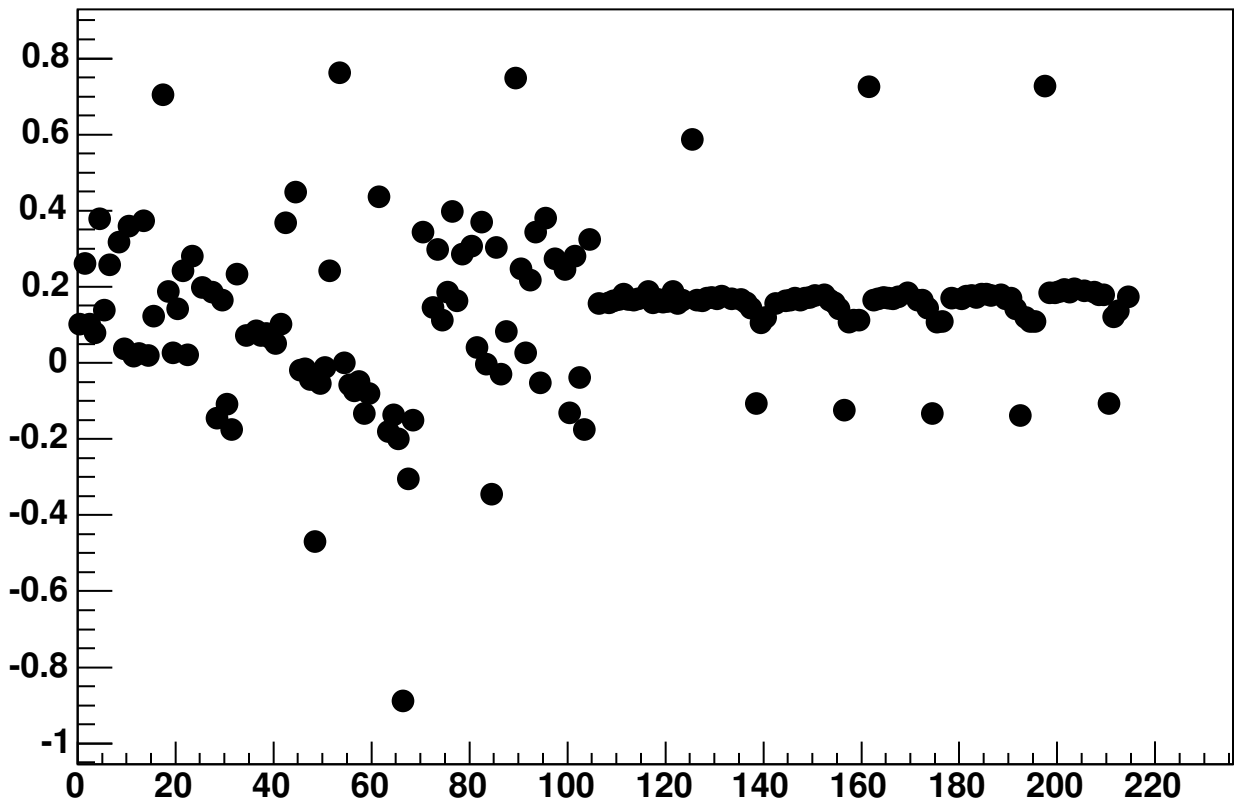
Disabled 4, Hold=35, Fit Slope vs 18*Chip+Chan



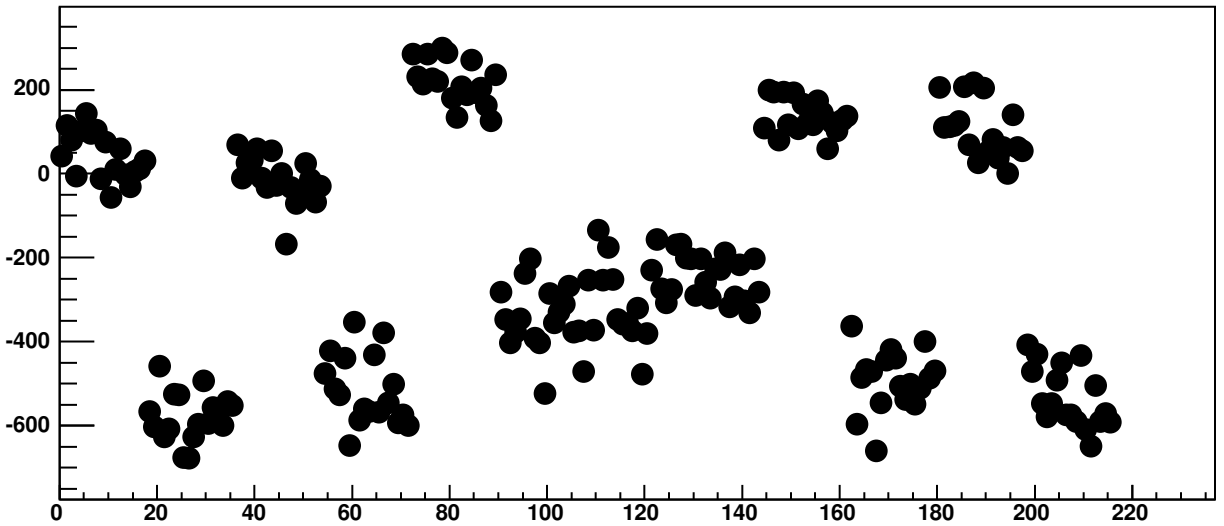
Disabled 5, Hold=35, Fit Intercept vs 18*Chip+Chan



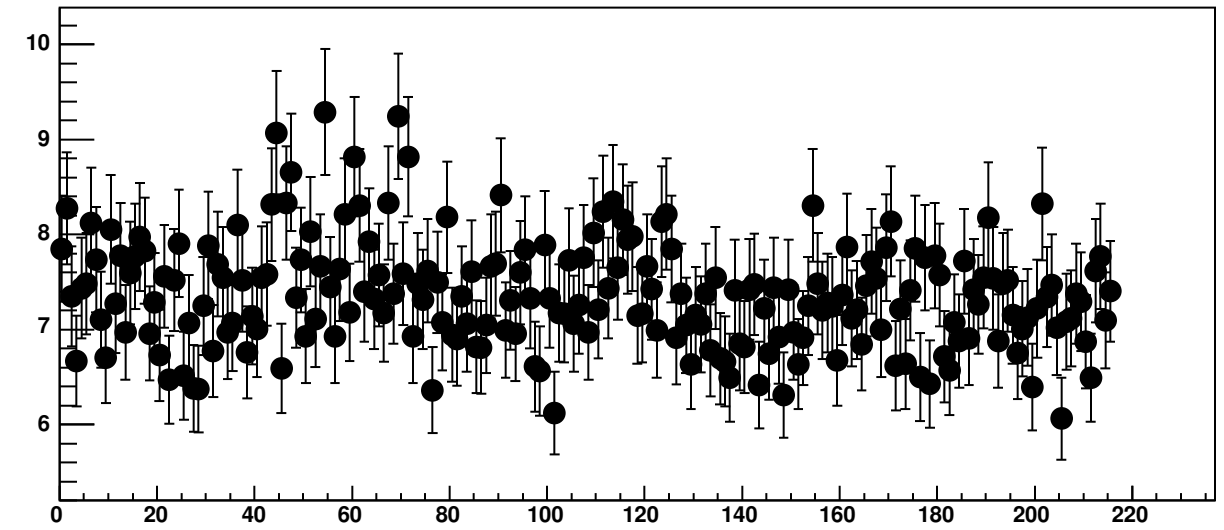
Disabled 5, Hold=35, Fit Slope vs 18*Chip+Chan



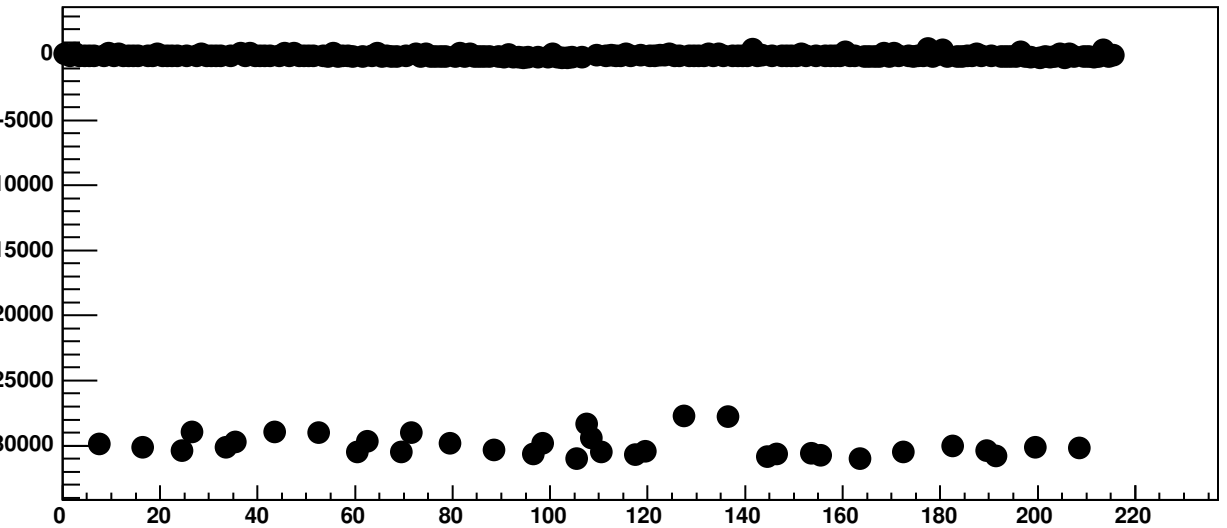
Enables off, ADC Mean vs 18*Chip+Chan



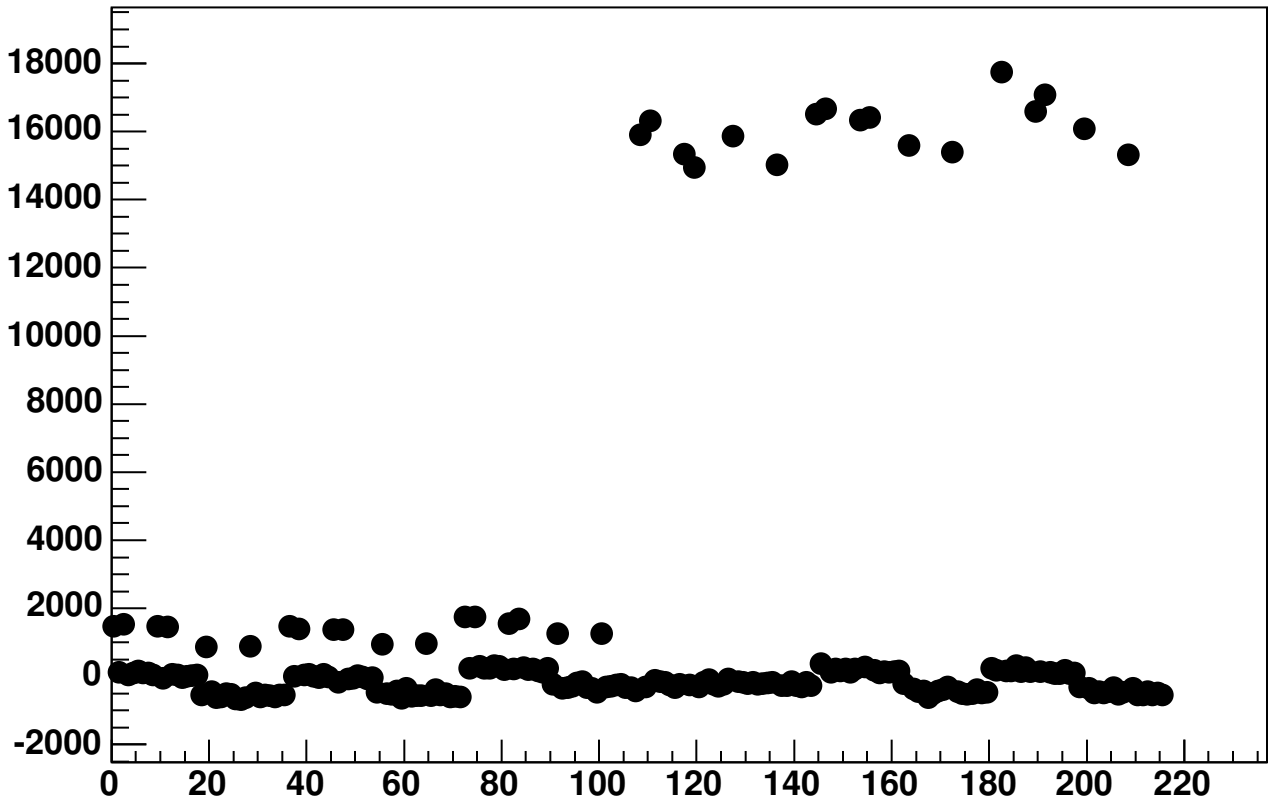
Enables off, ADC Noise vs 18*Chip+Chan



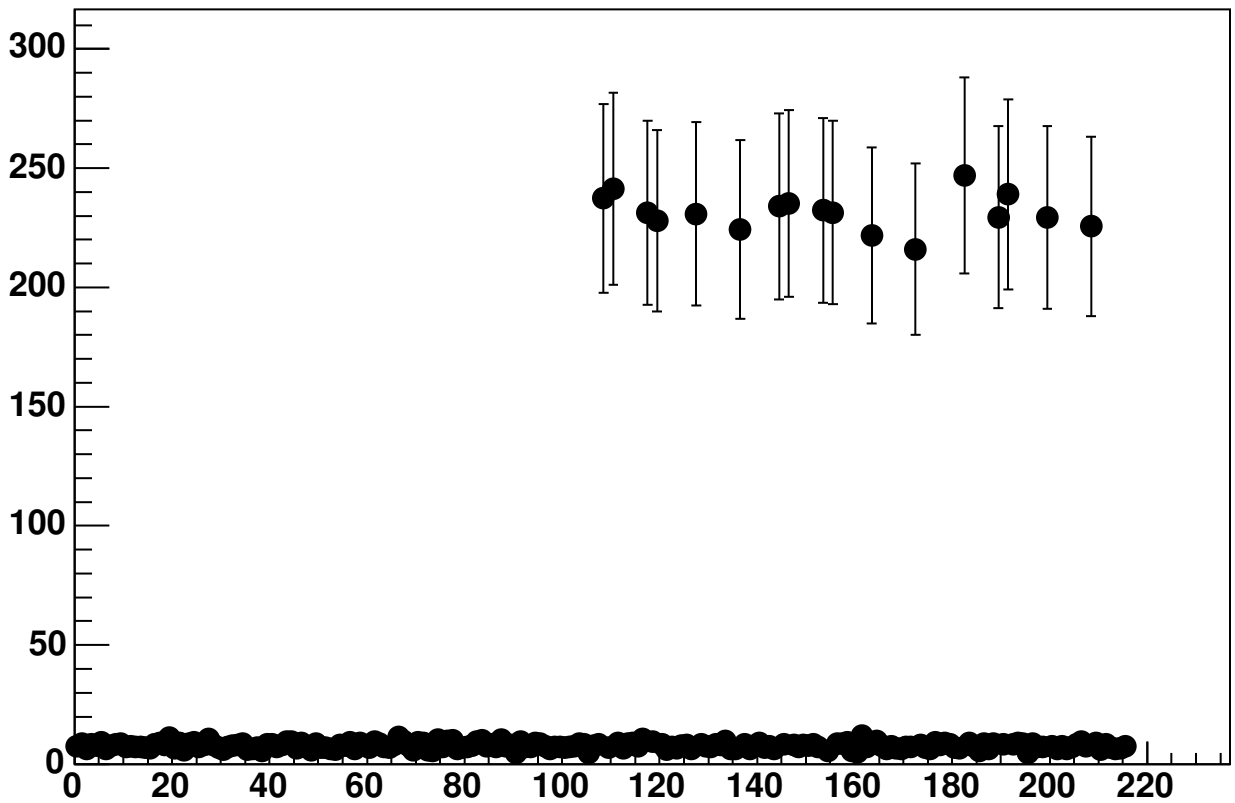
Enables off, ADC Residuals vs 18*Chip+Chan



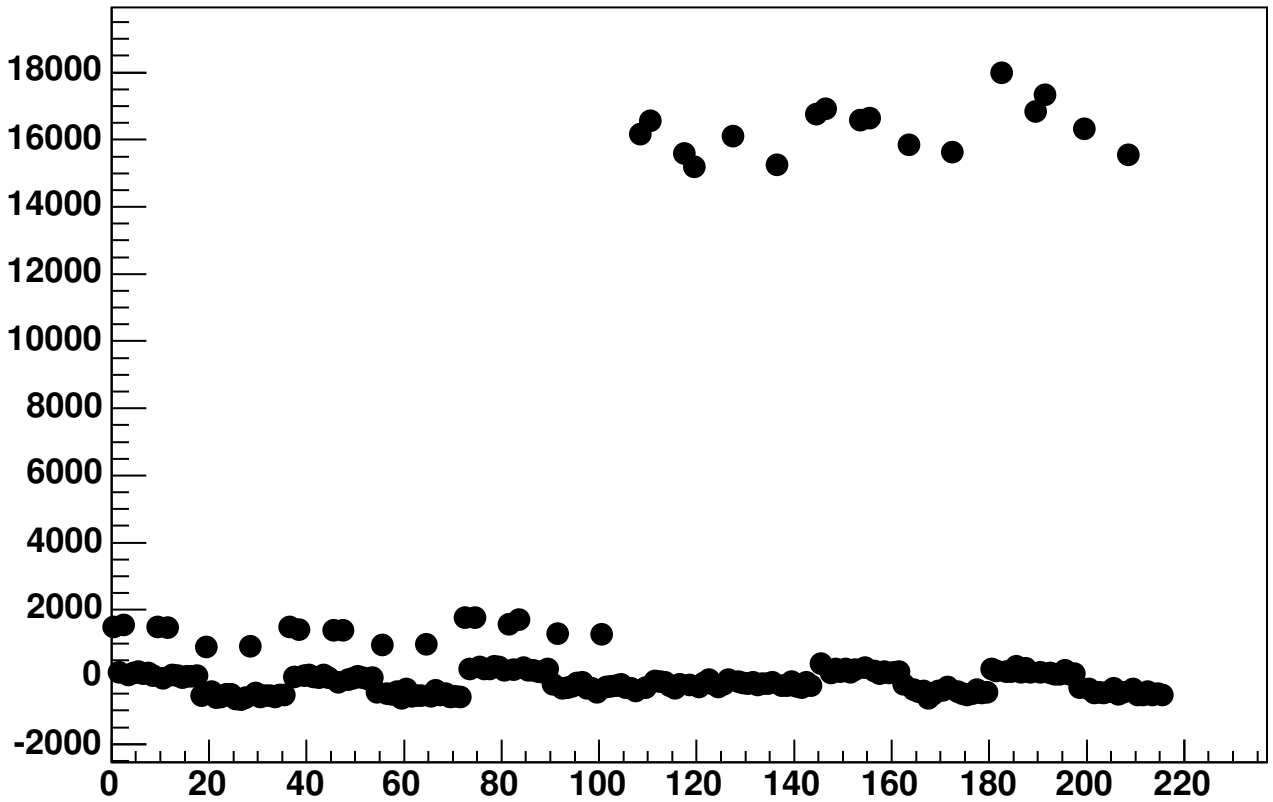
Enable 0, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



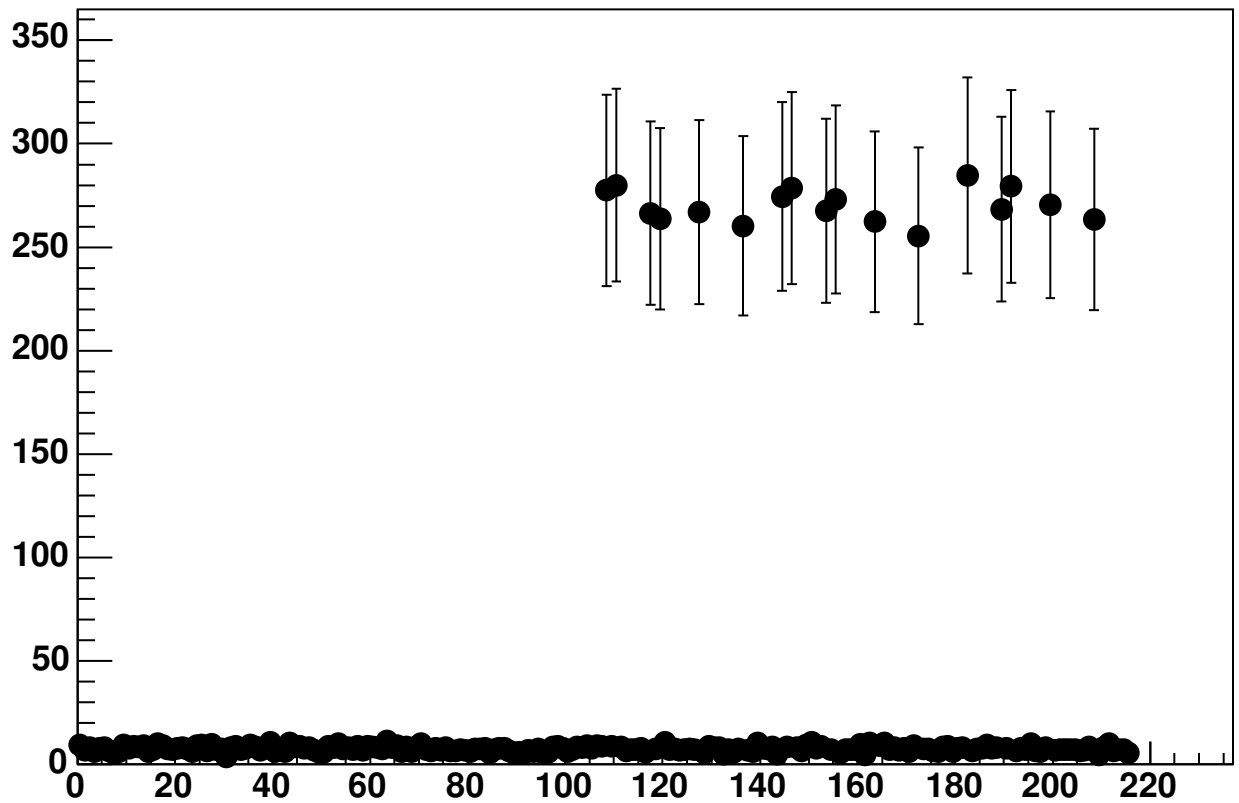
Enable 0, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



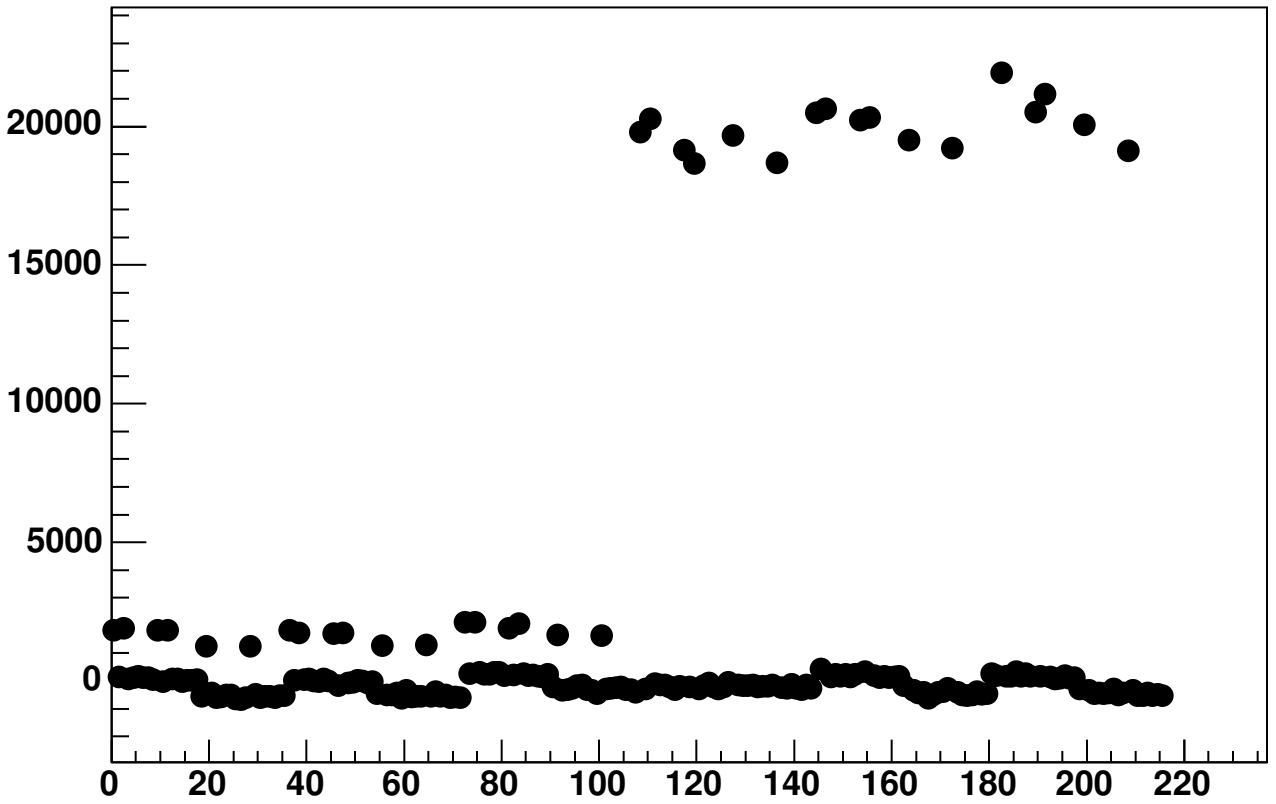
Enable 0, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



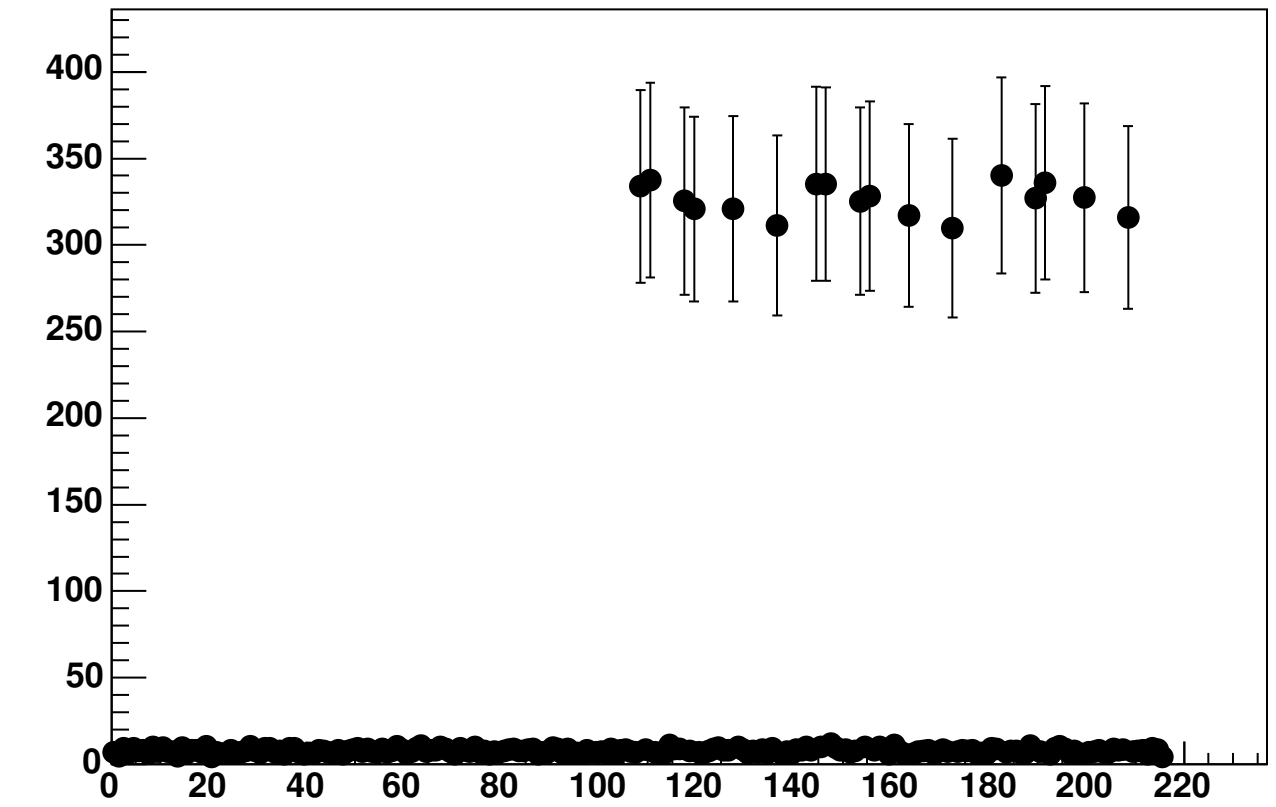
Enable 0, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



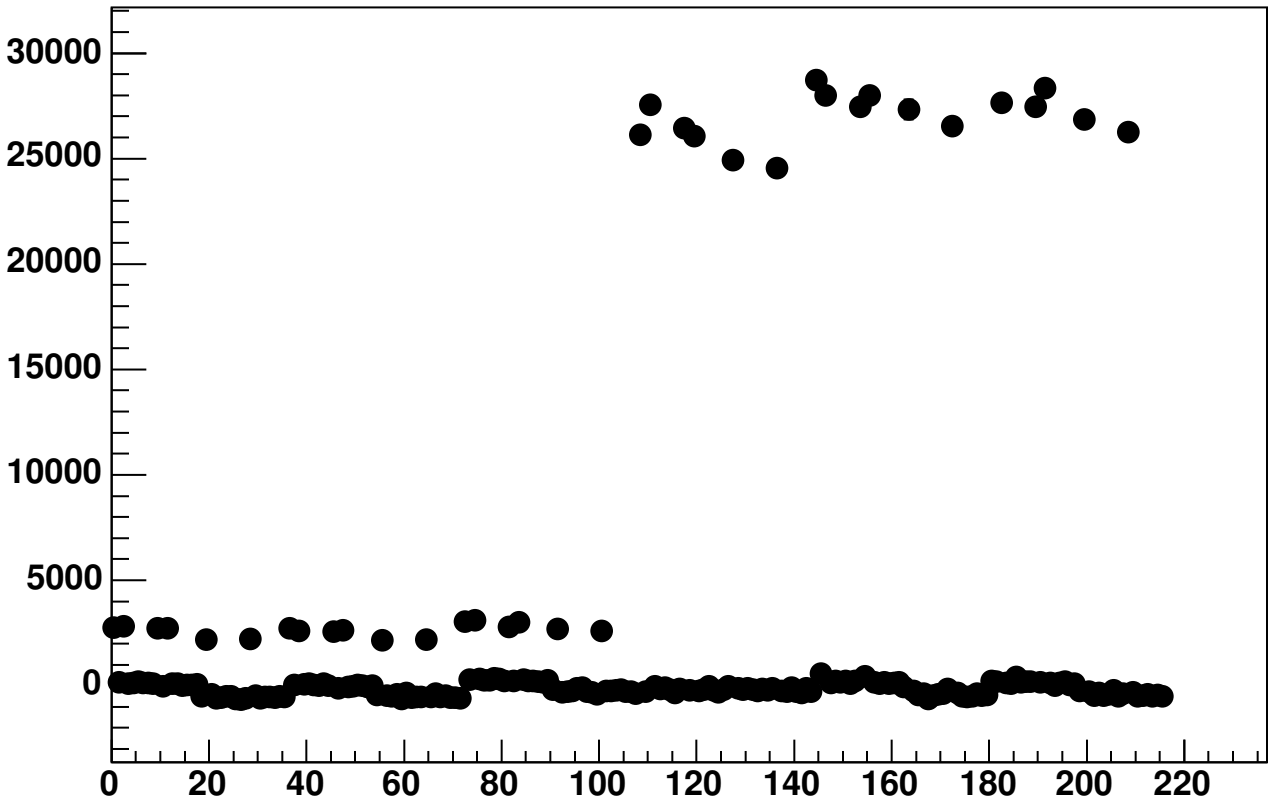
Enable 0, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



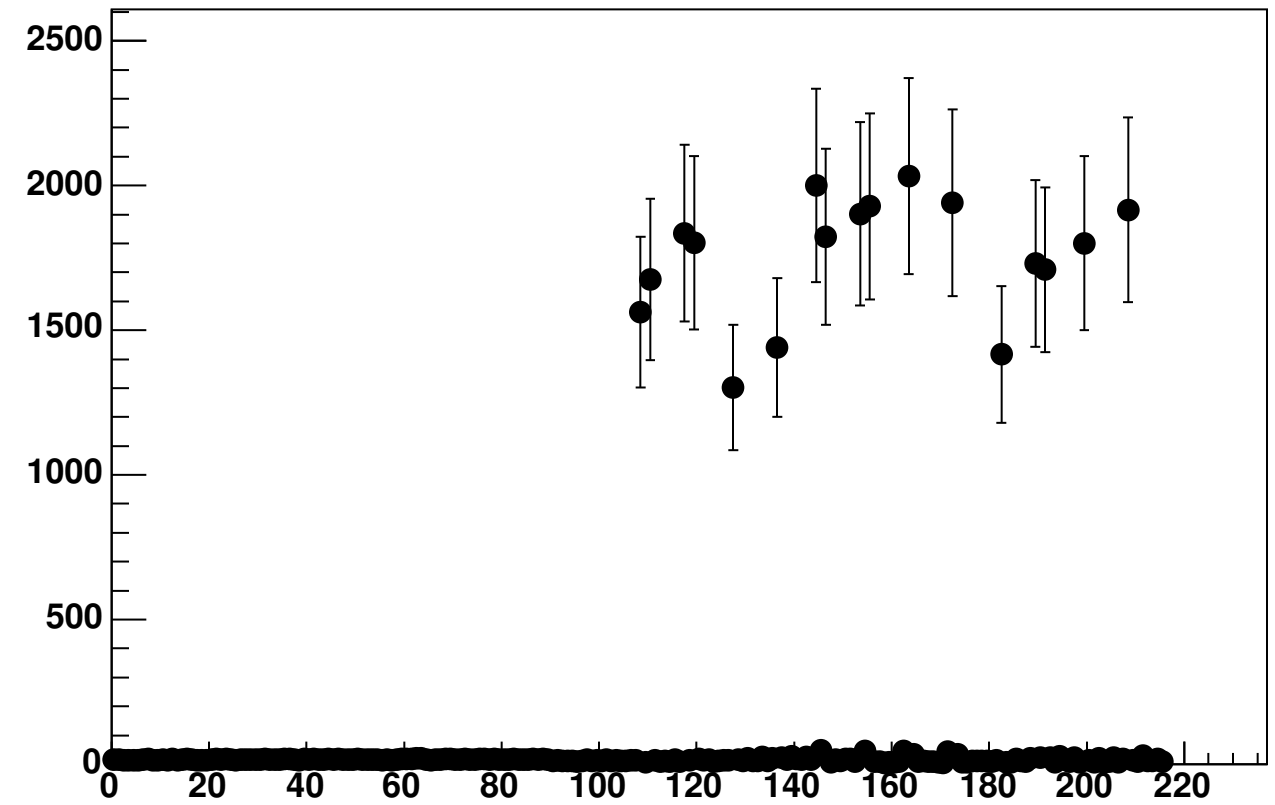
Enable 0, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



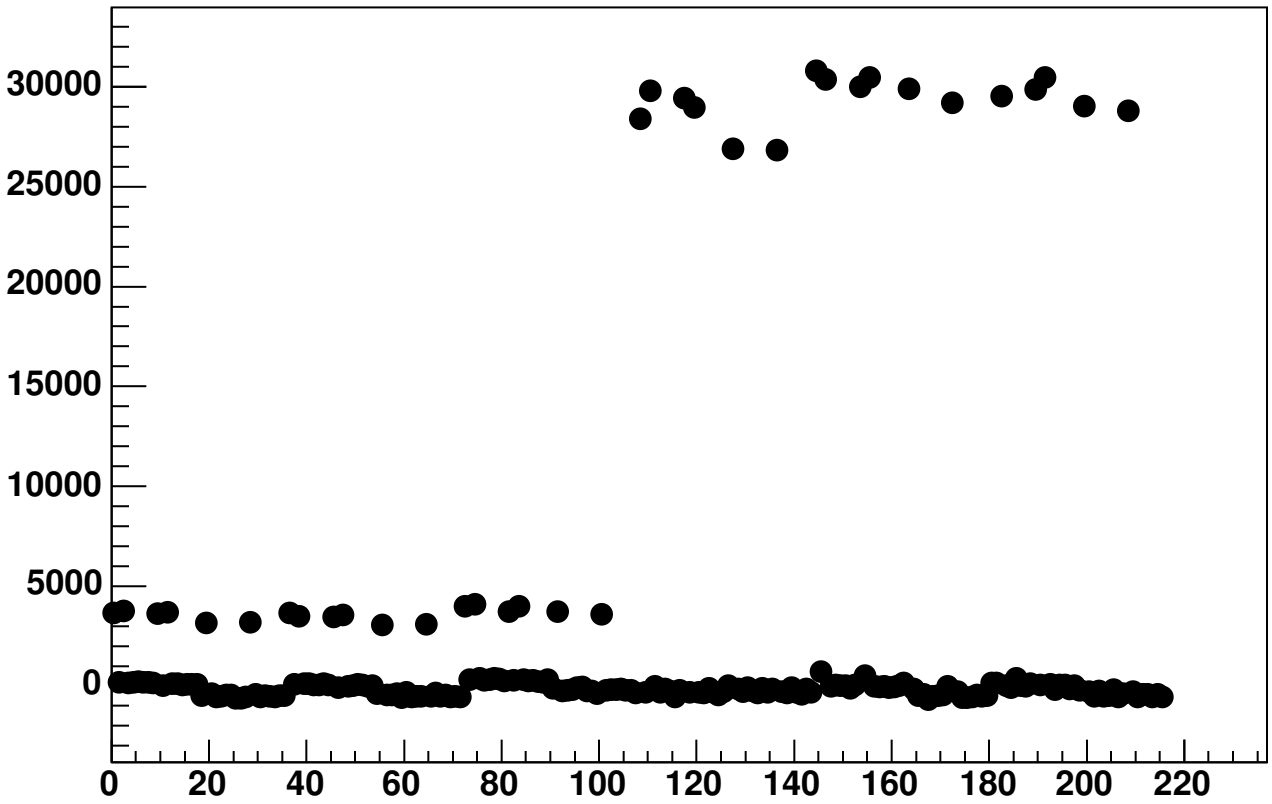
Enable 0, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



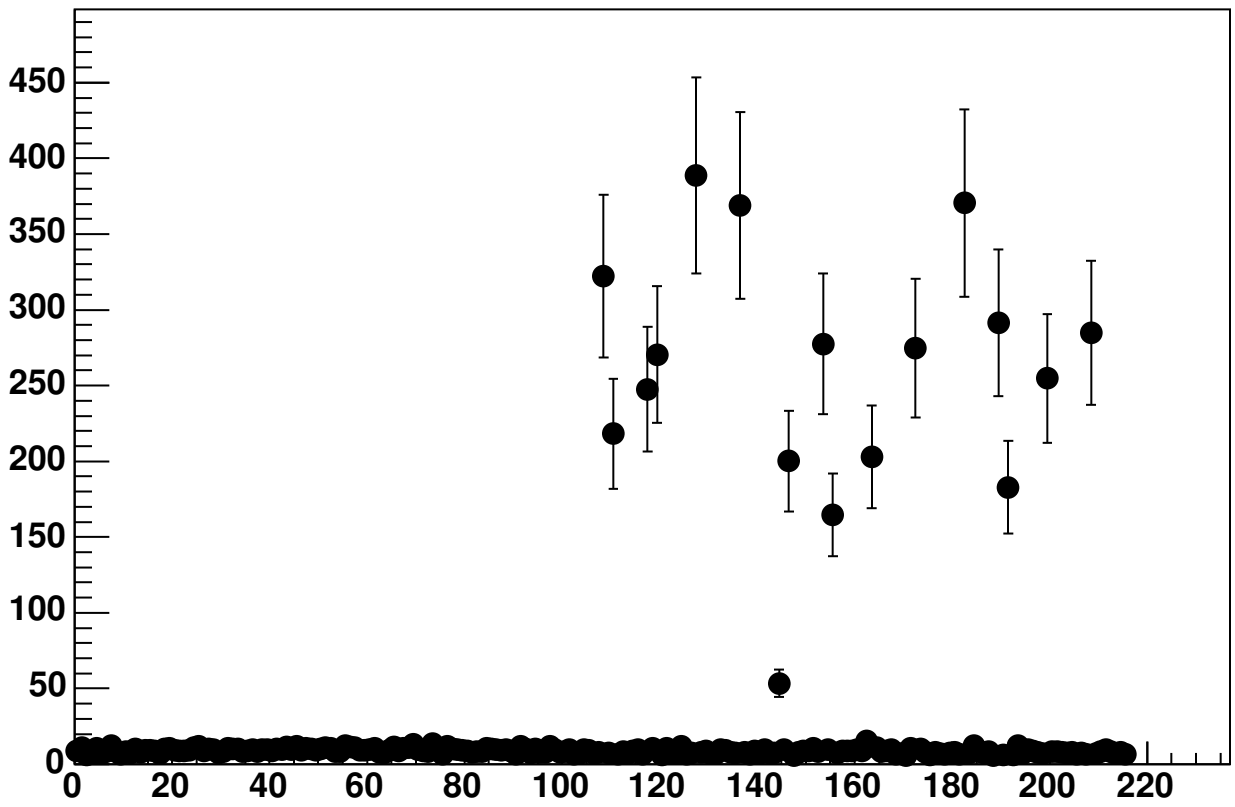
Enable 0, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



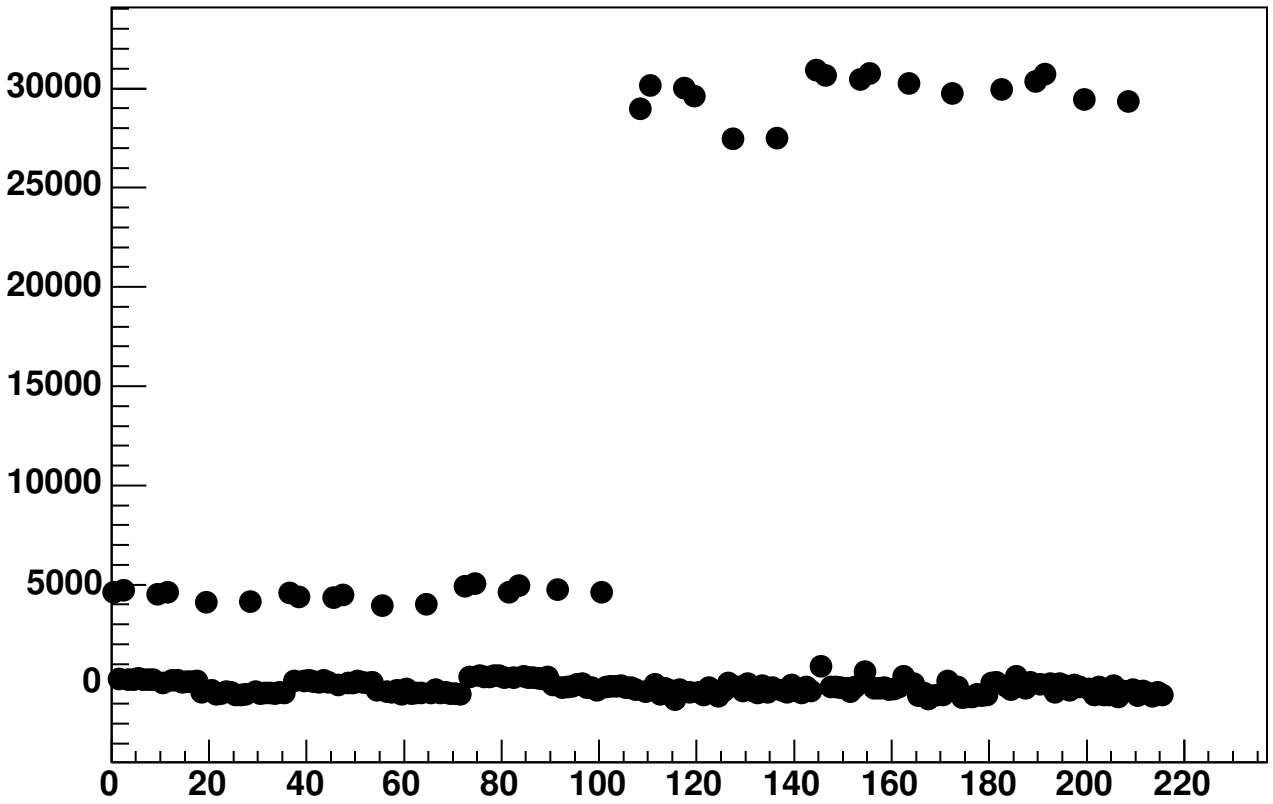
Enable 0, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



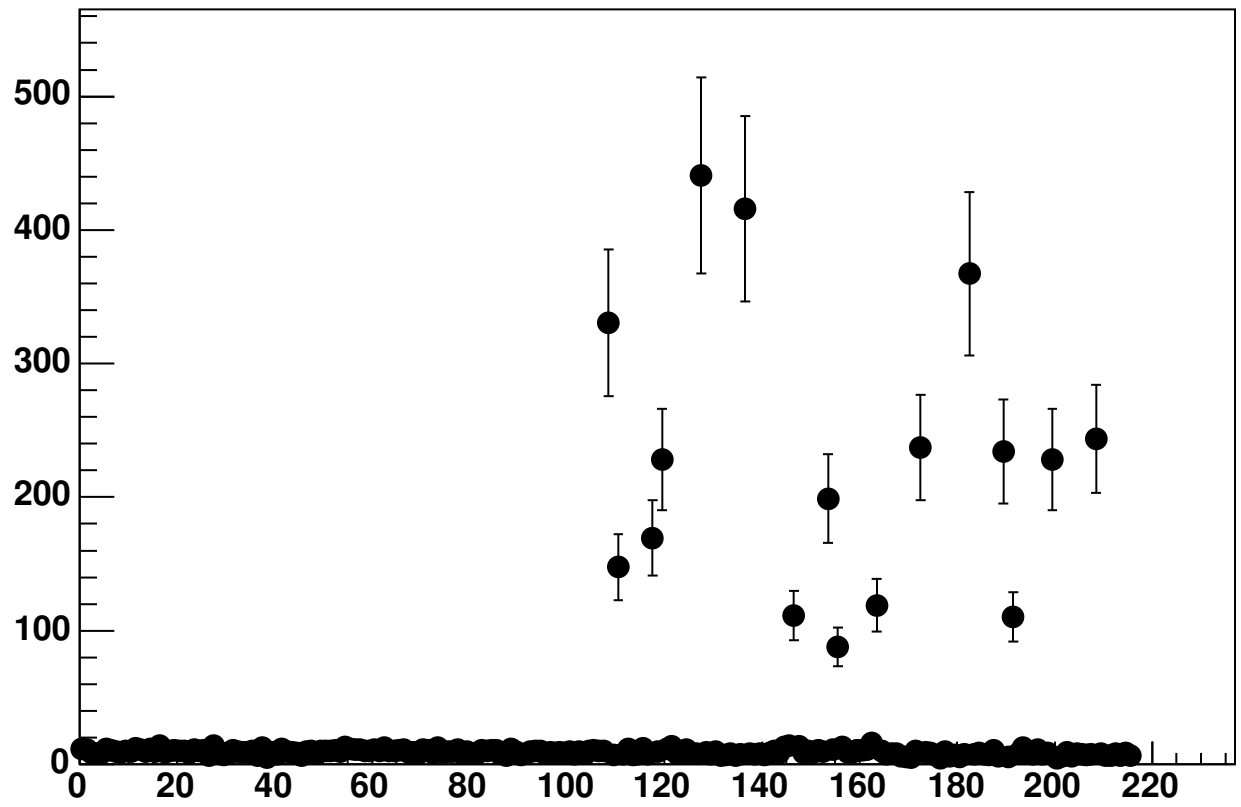
Enable 0, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



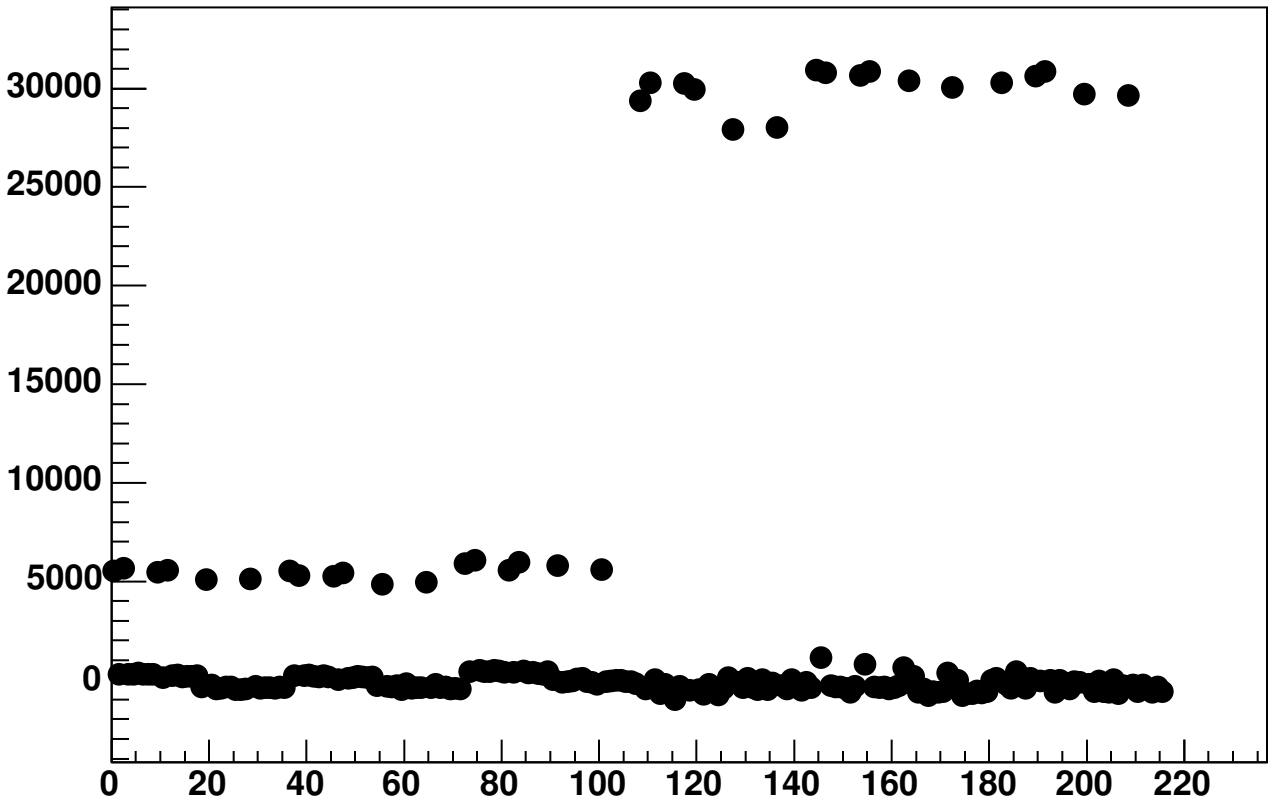
Enable 0, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



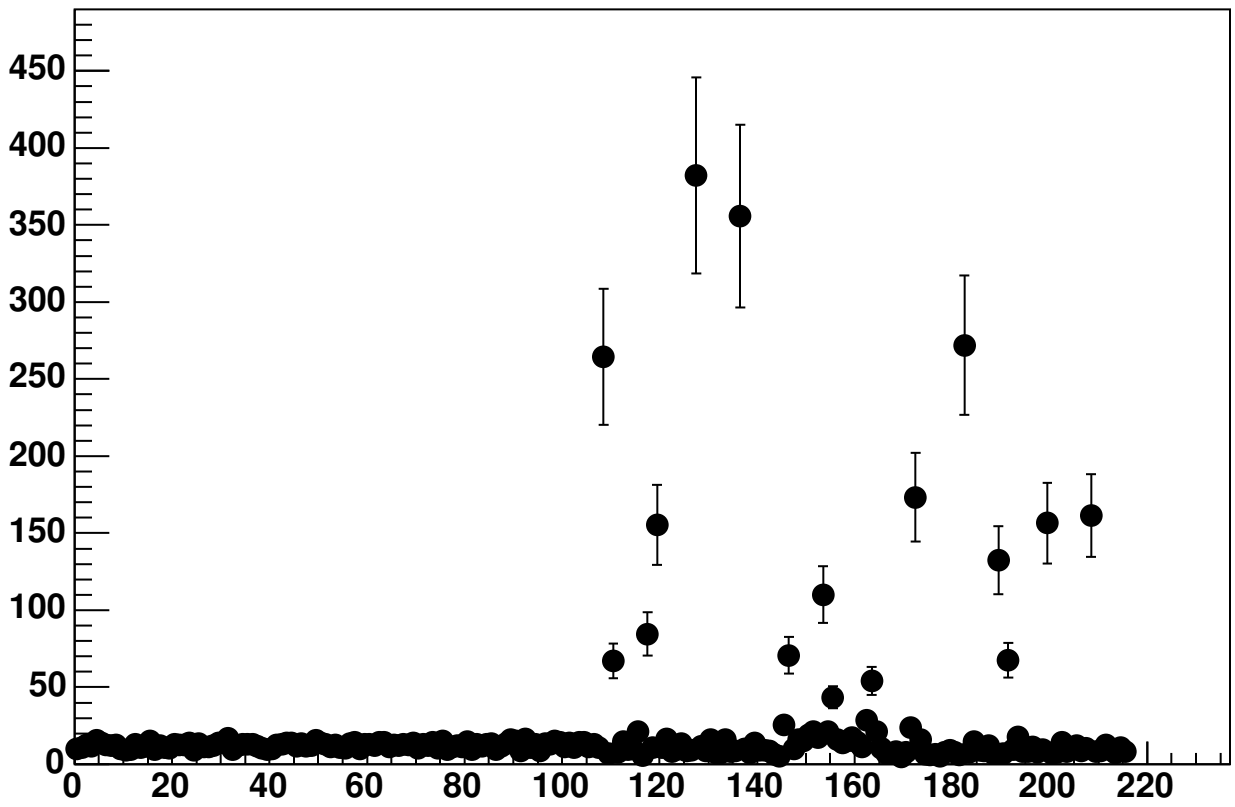
Enable 0, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



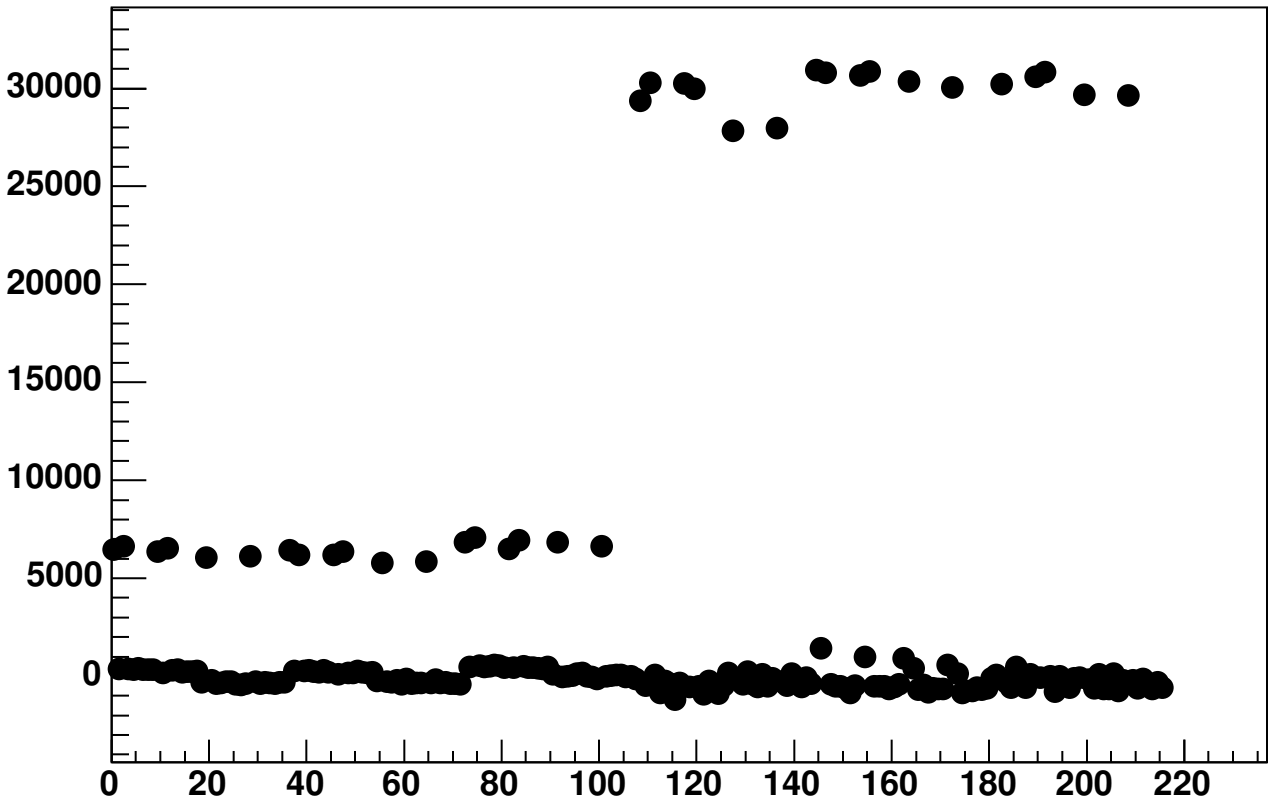
Enable 0, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



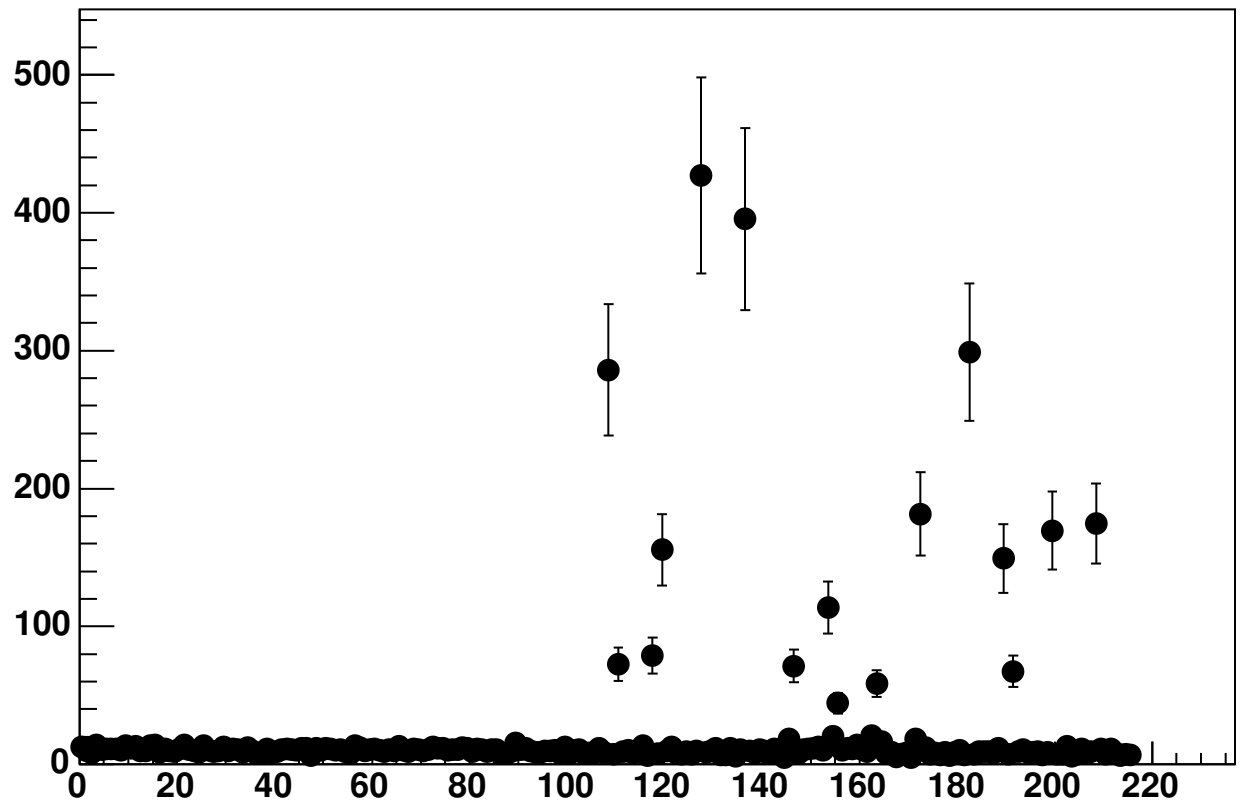
Enable 0, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



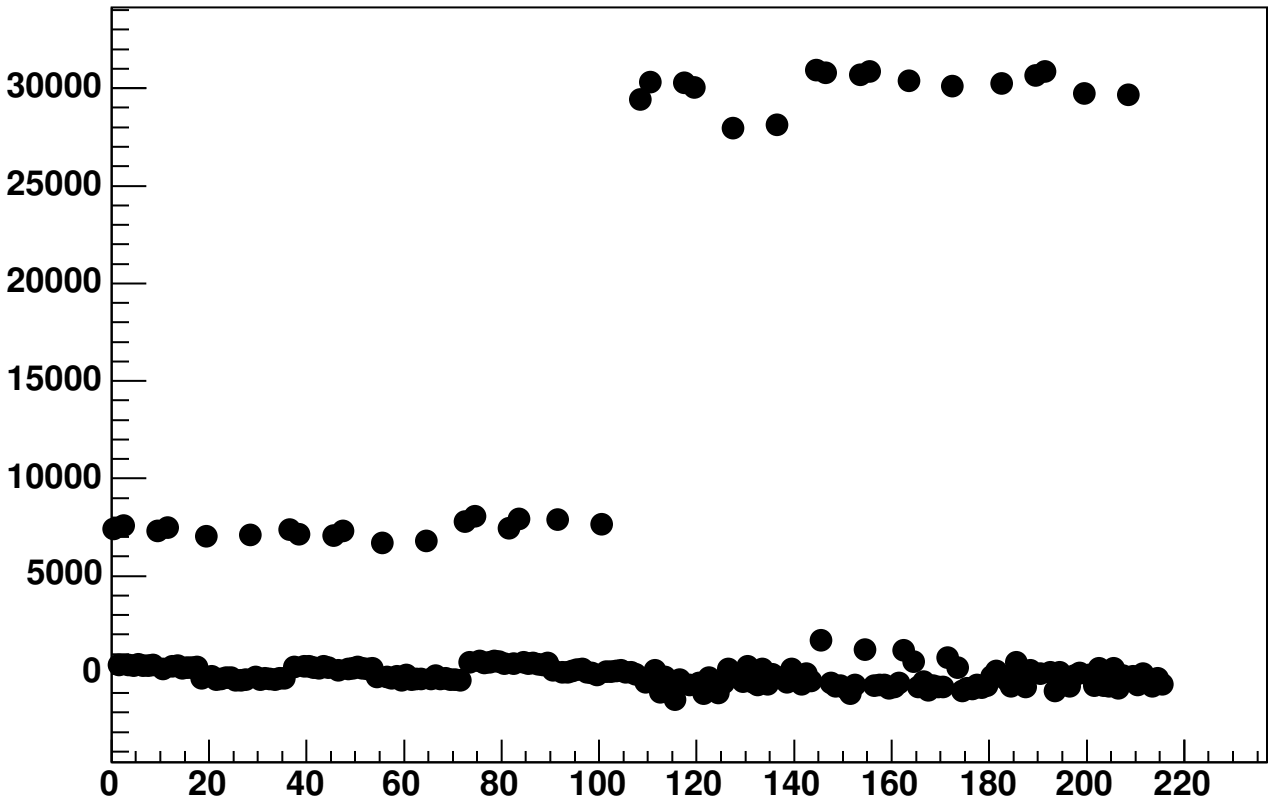
Enable 0, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



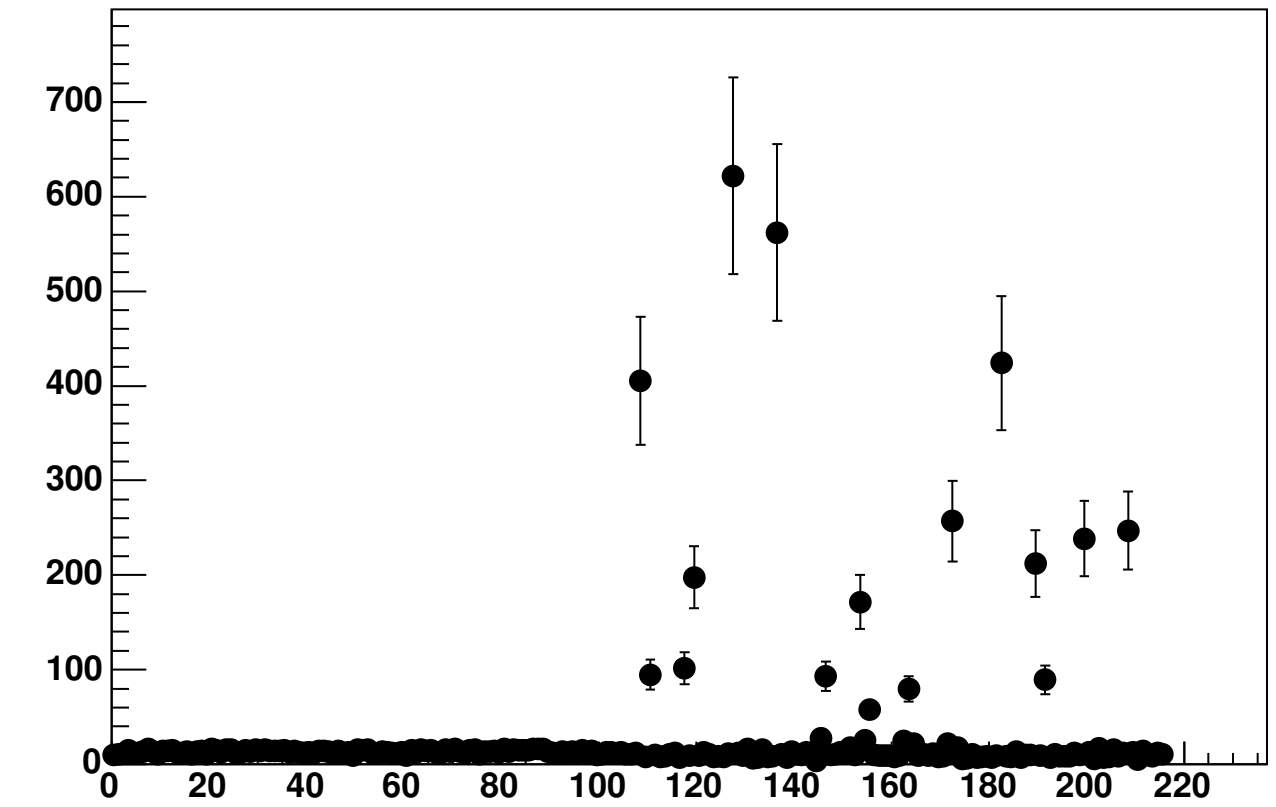
Enable 0, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



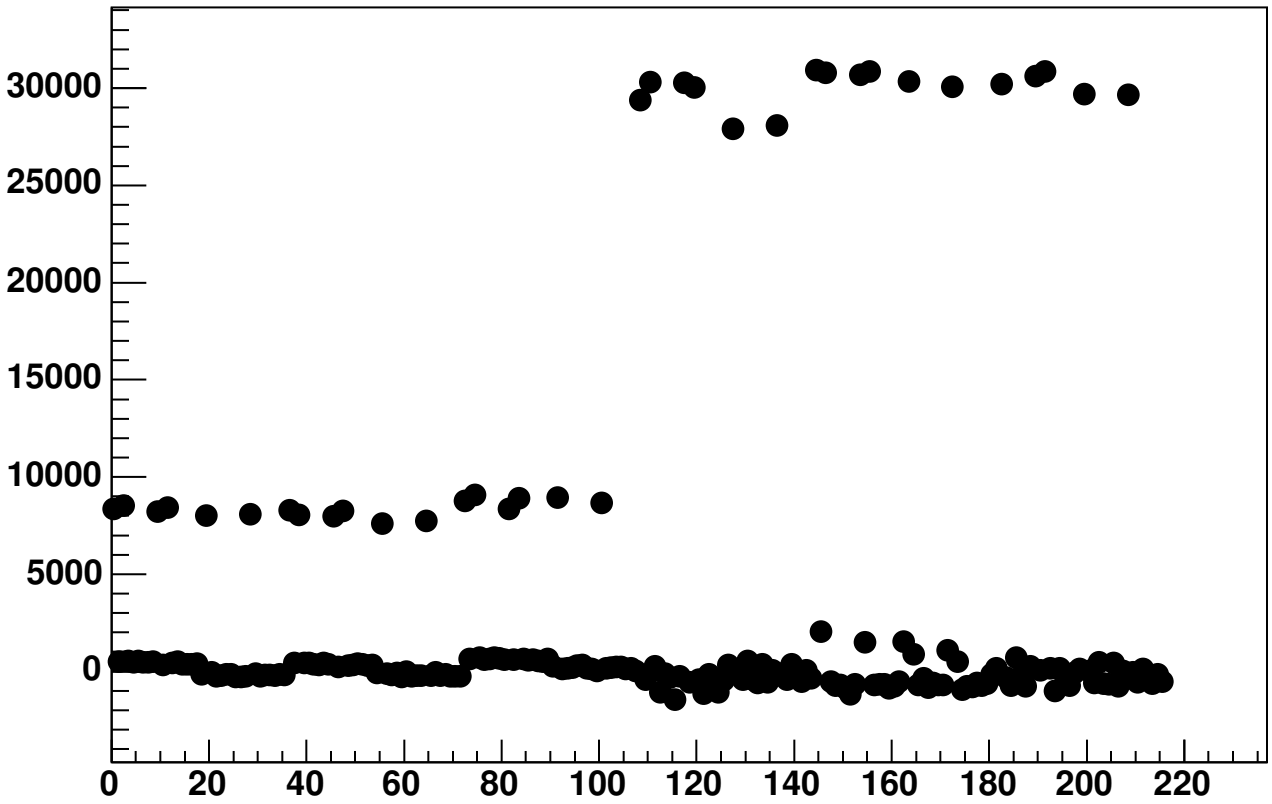
Enable 0, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



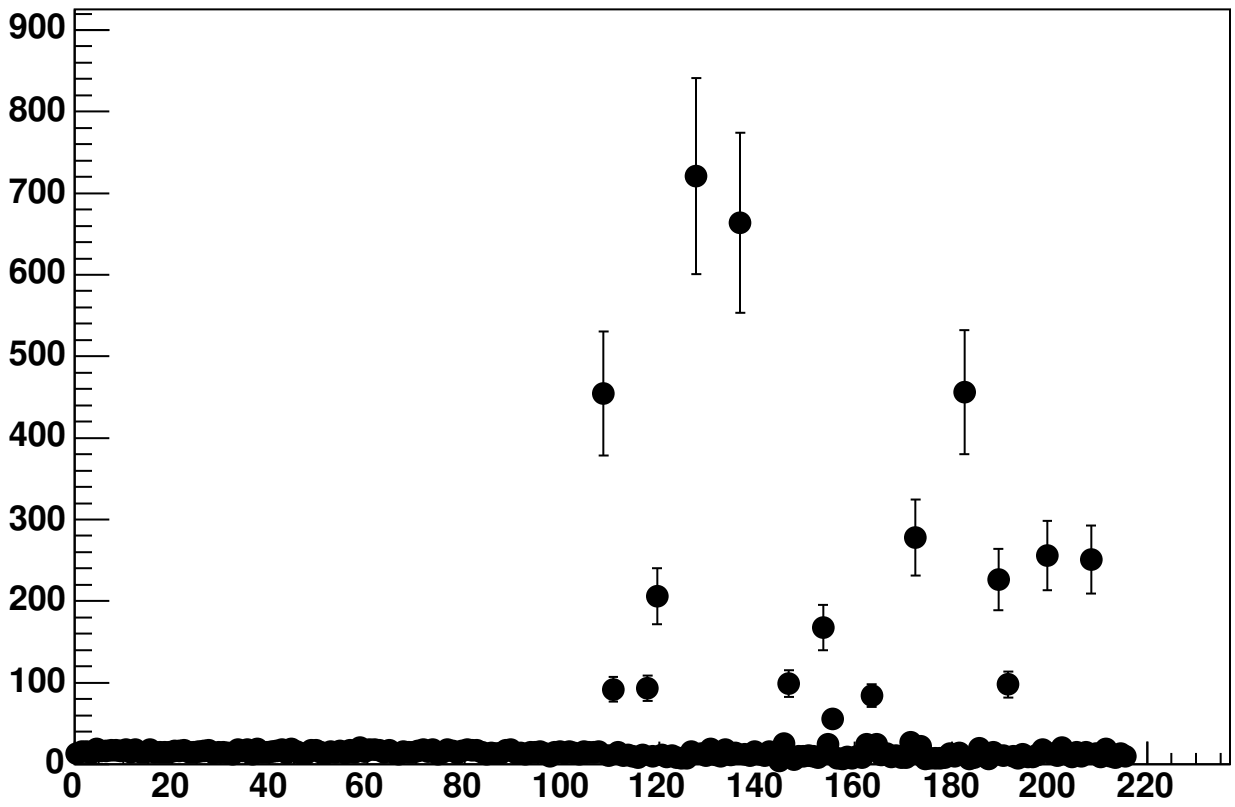
Enable 0, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



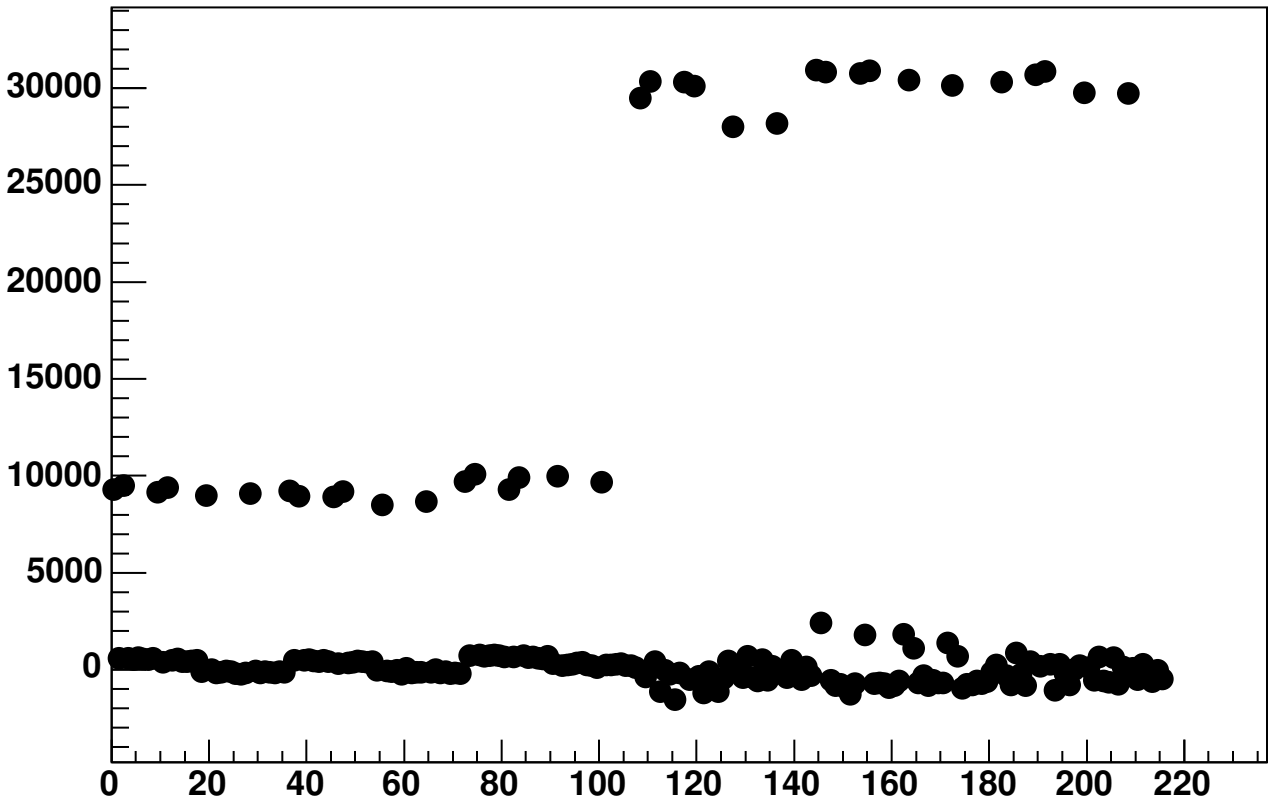
Enable 0, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



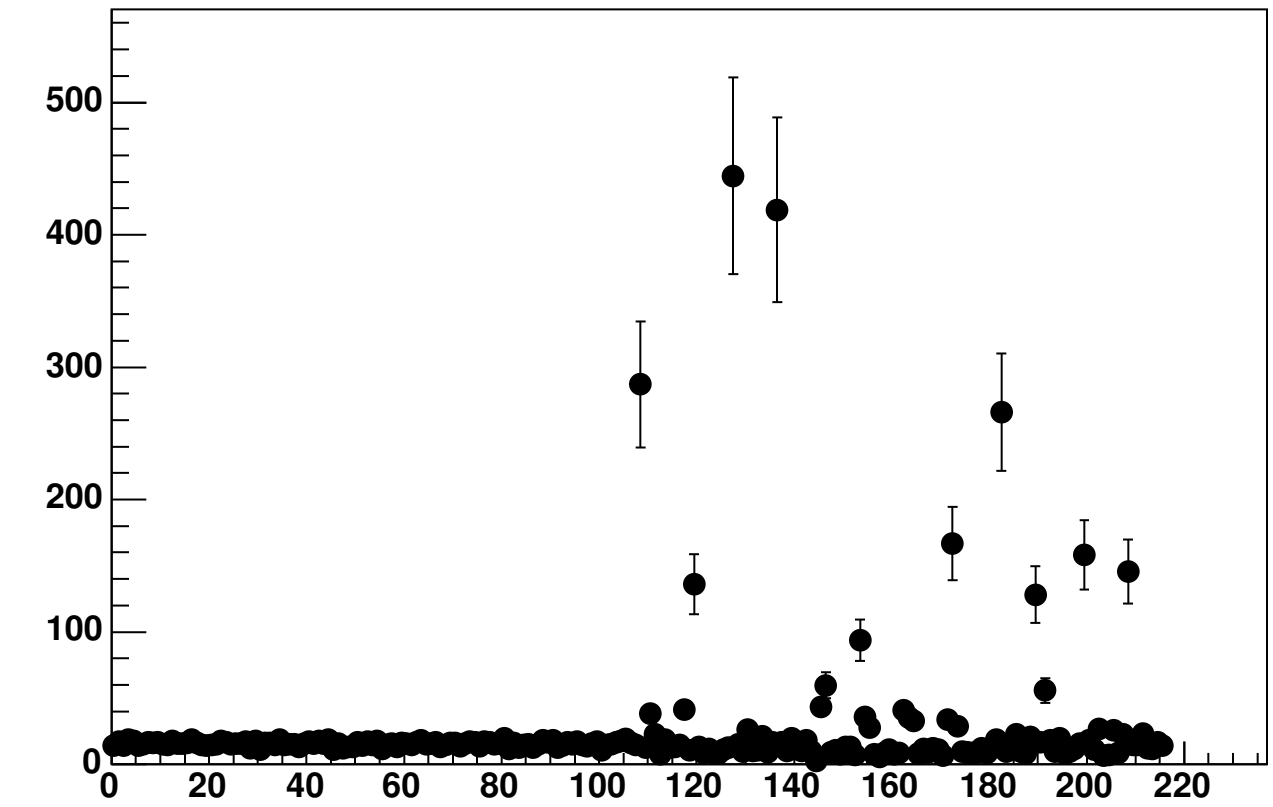
Enable 0, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



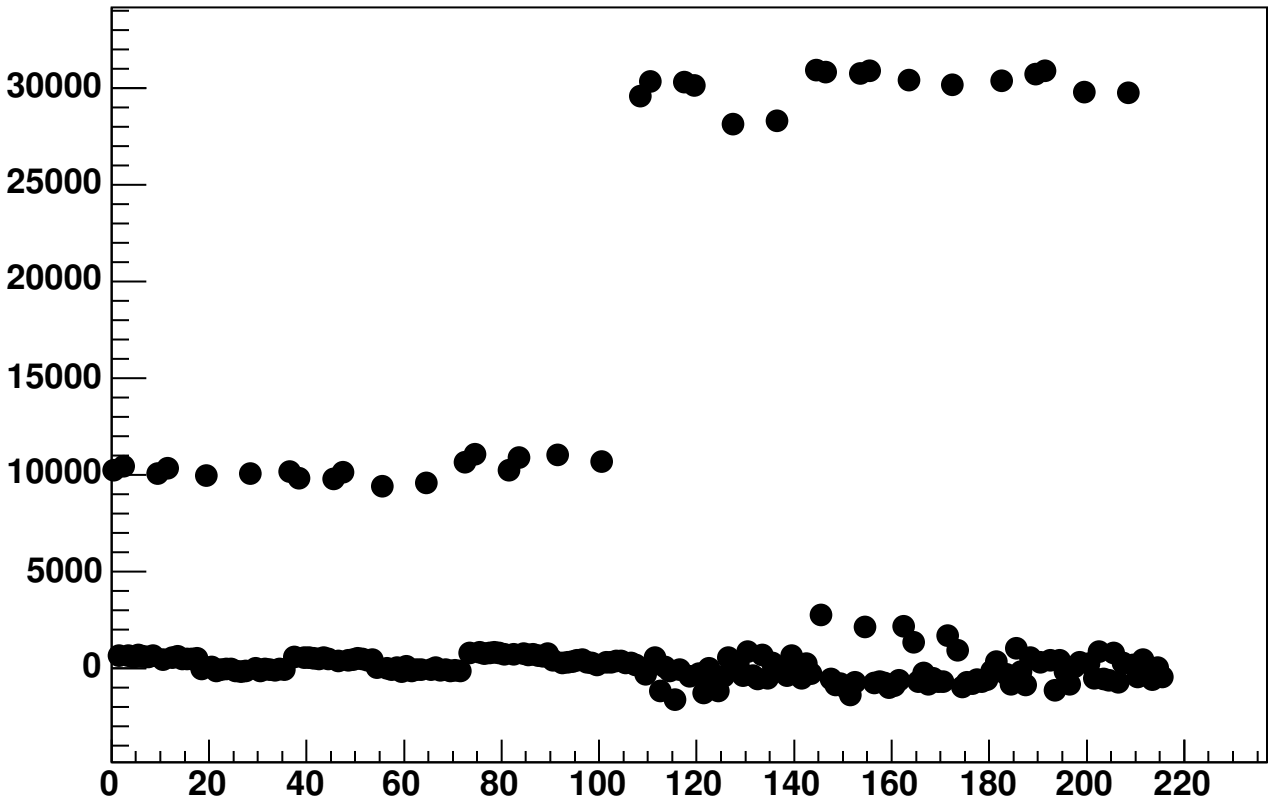
Enable 0, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



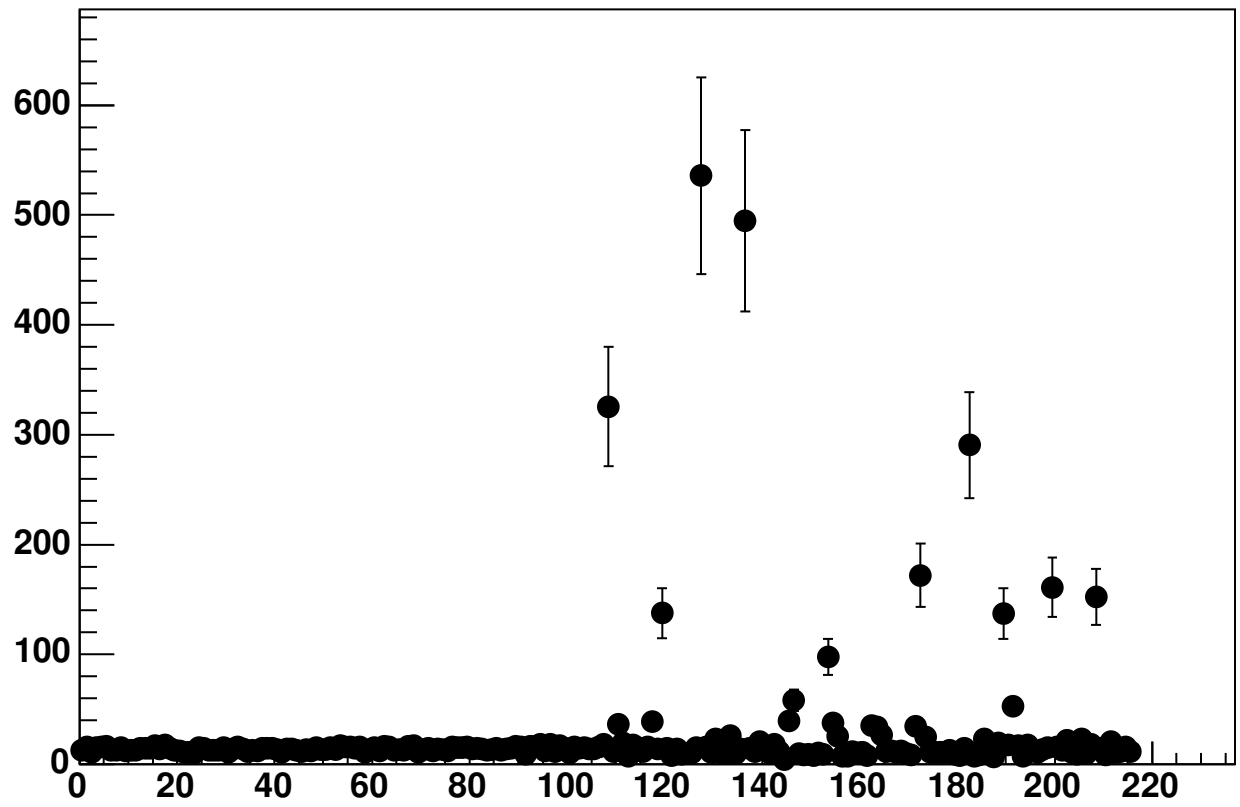
Enable 0, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



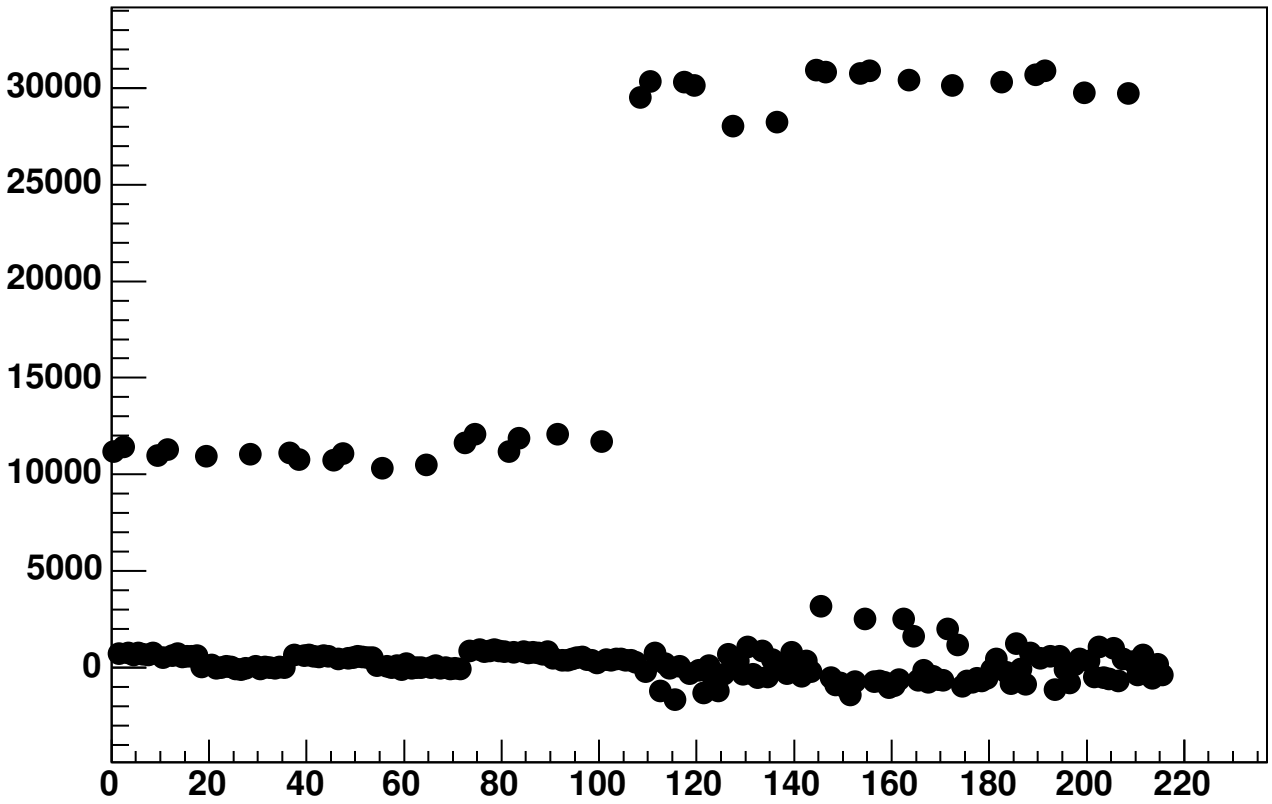
Enable 0, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



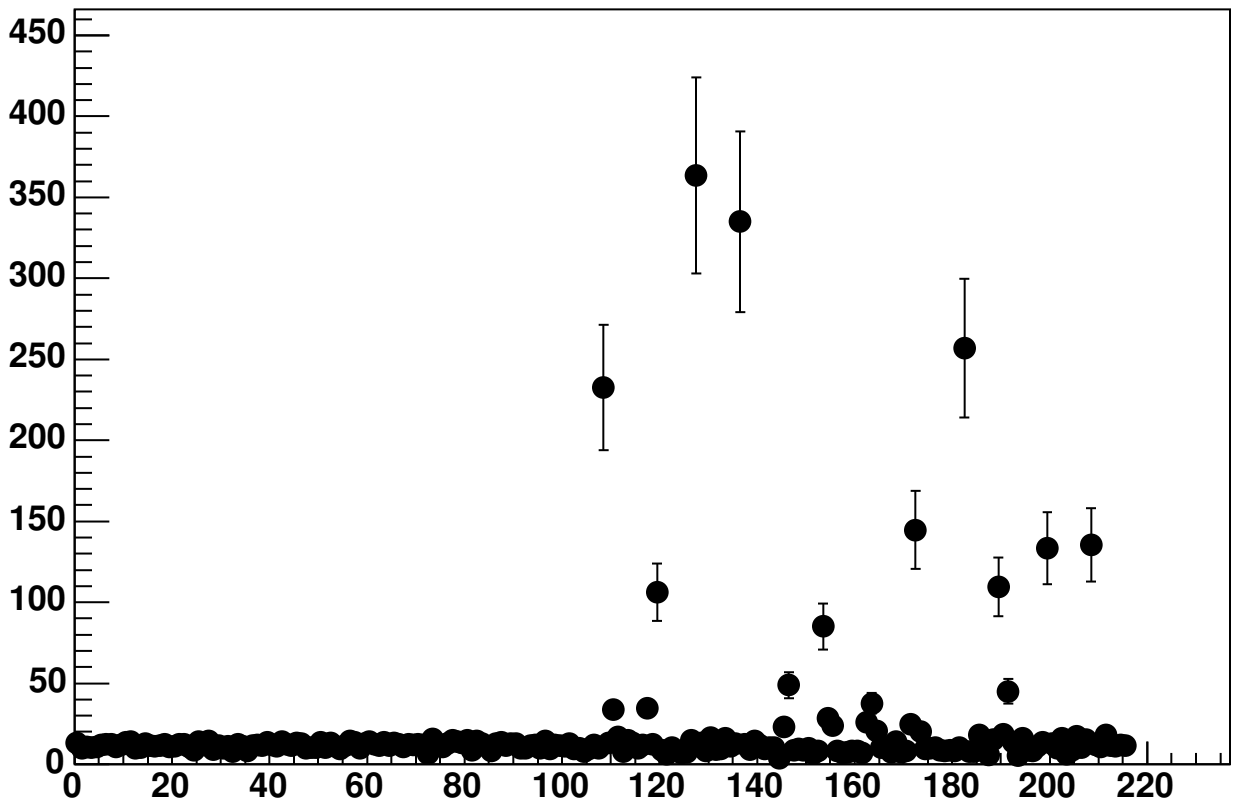
Enable 0, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



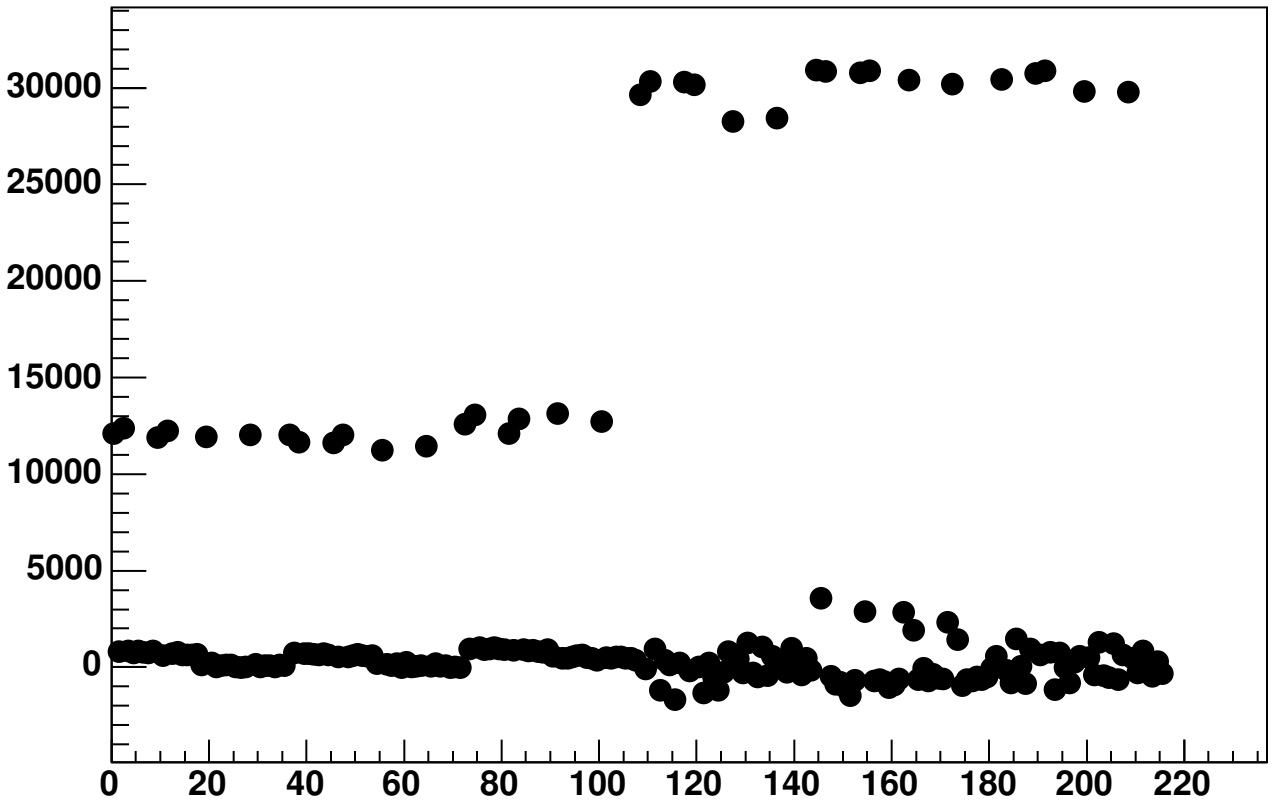
Enable 0, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



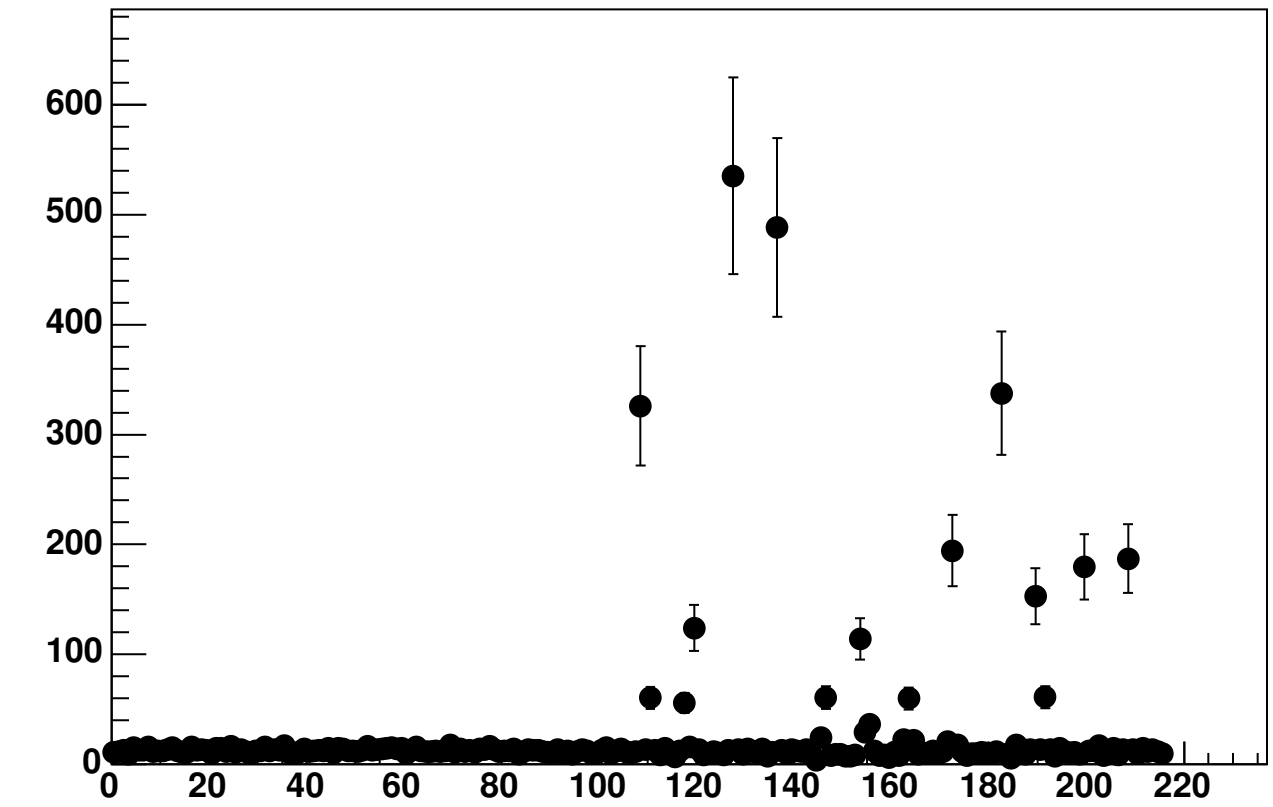
Enable 0, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



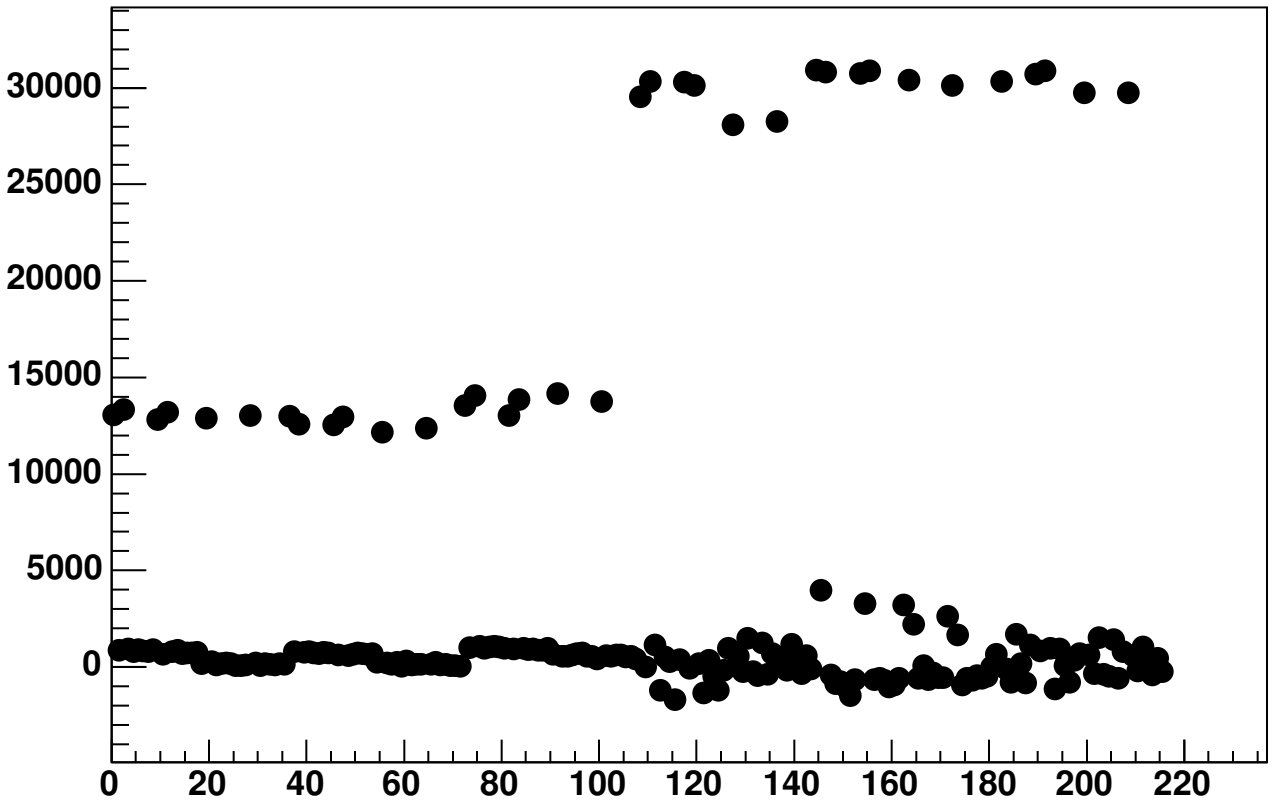
Enable 0, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



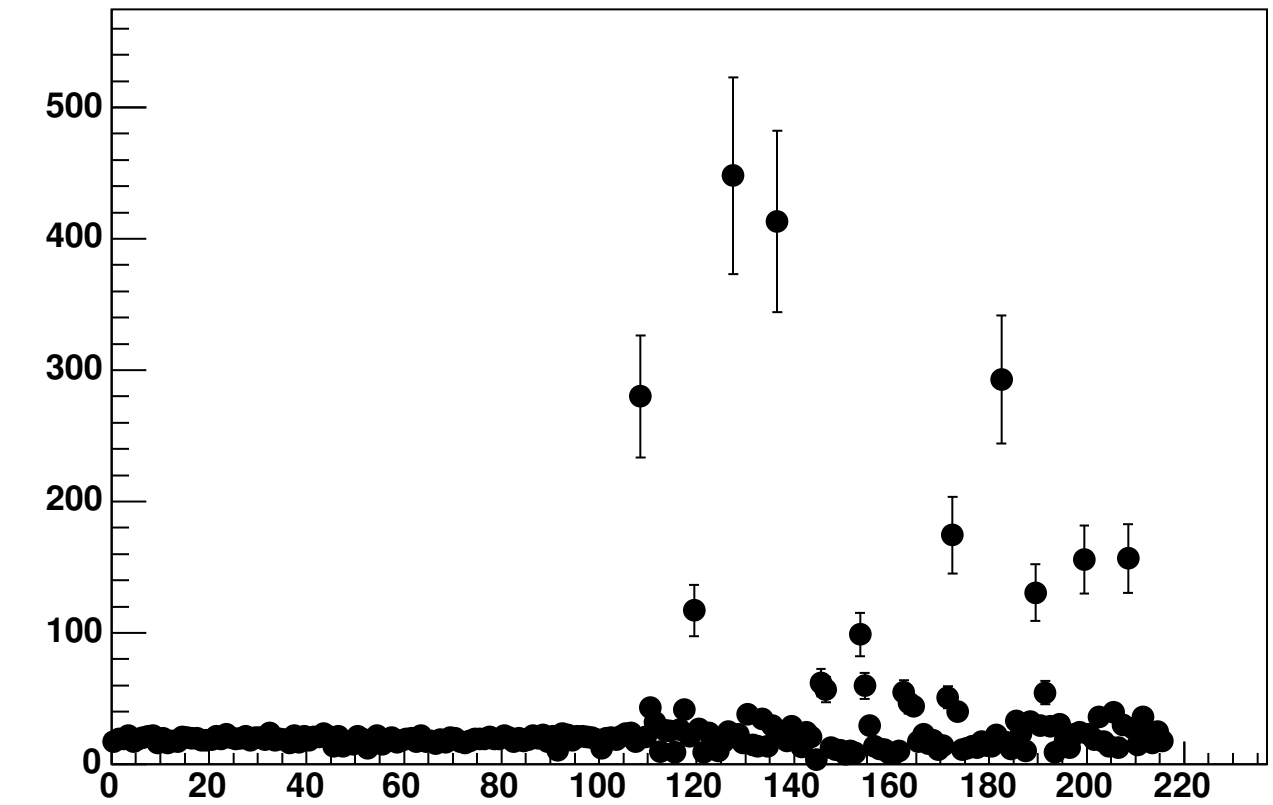
Enable 0, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



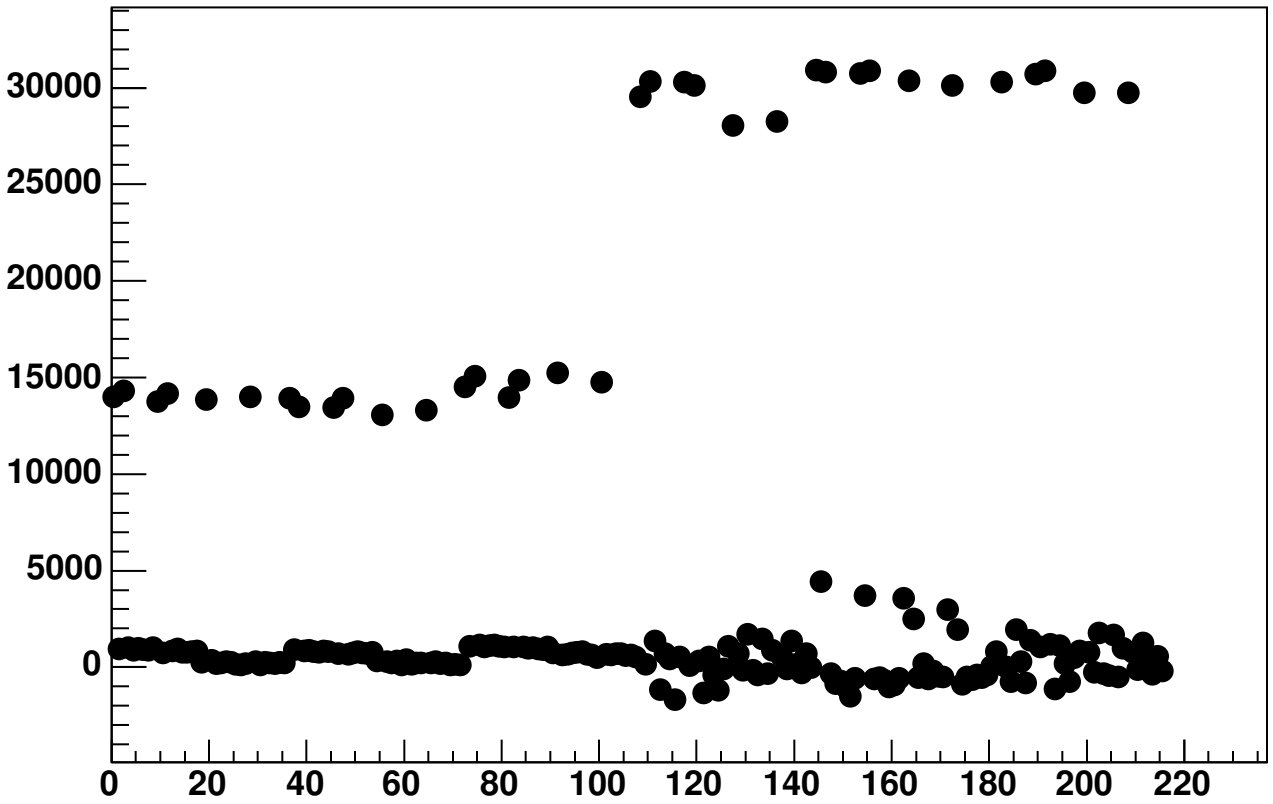
Enable 0, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



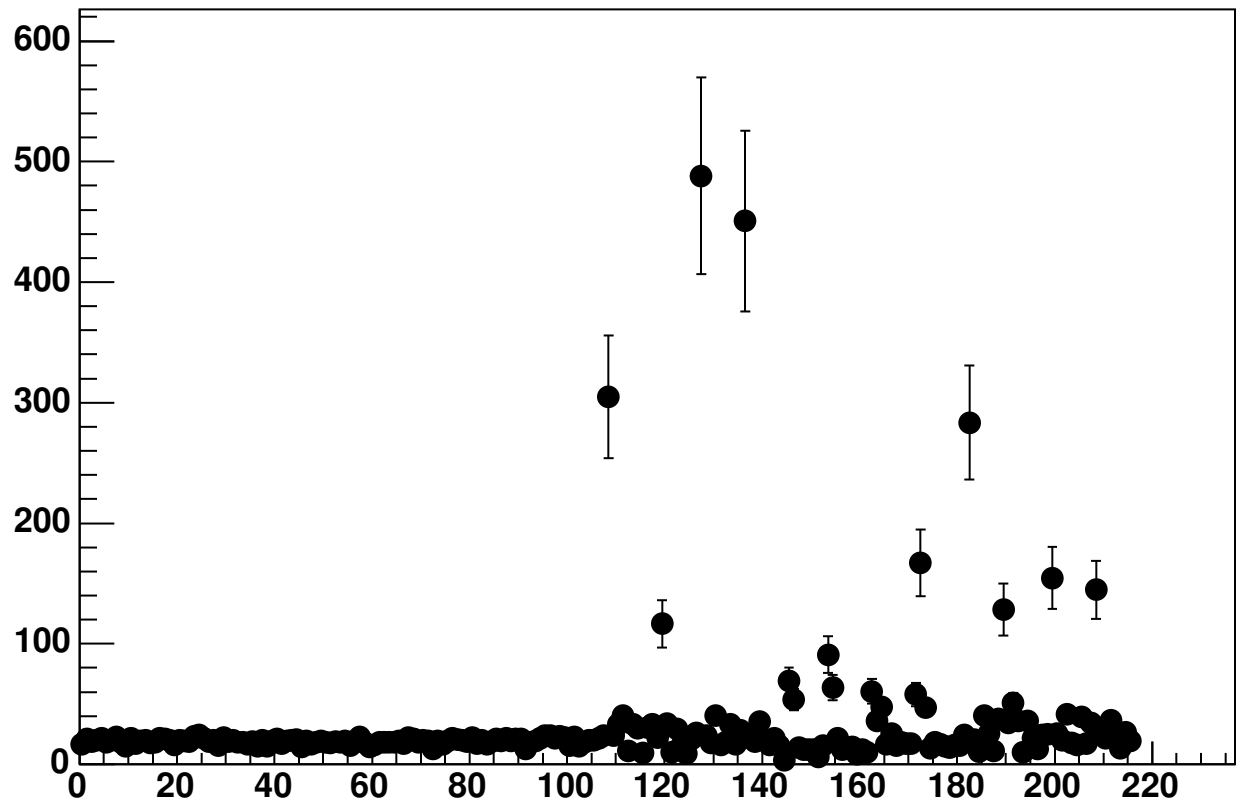
Enable 0, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



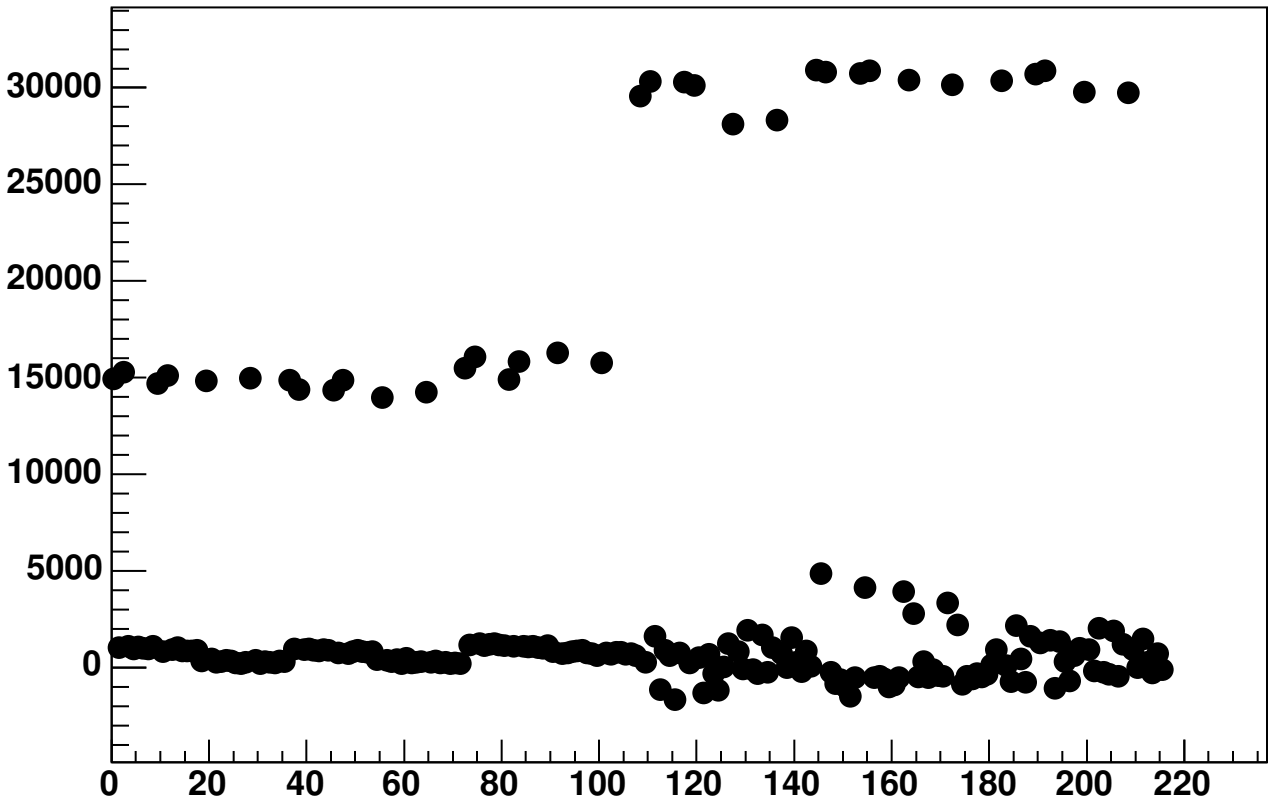
Enable 0, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



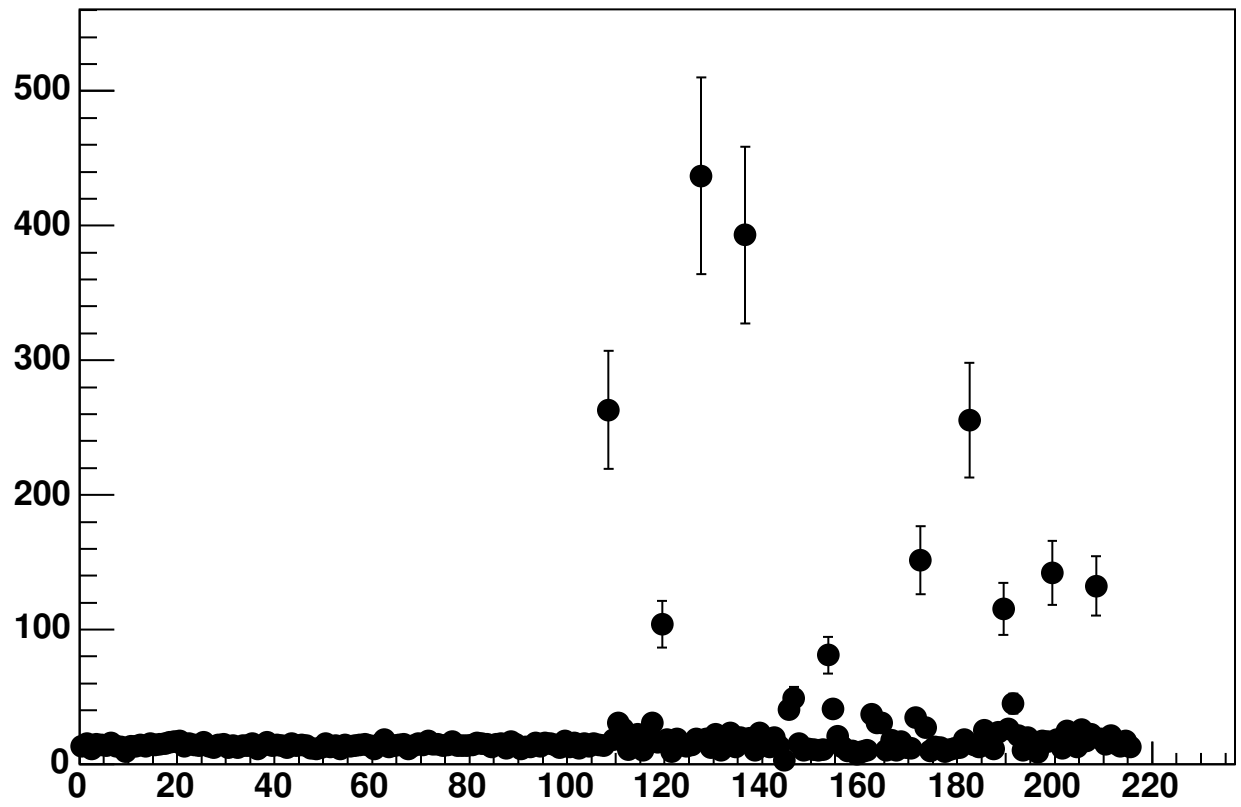
Enable 0, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



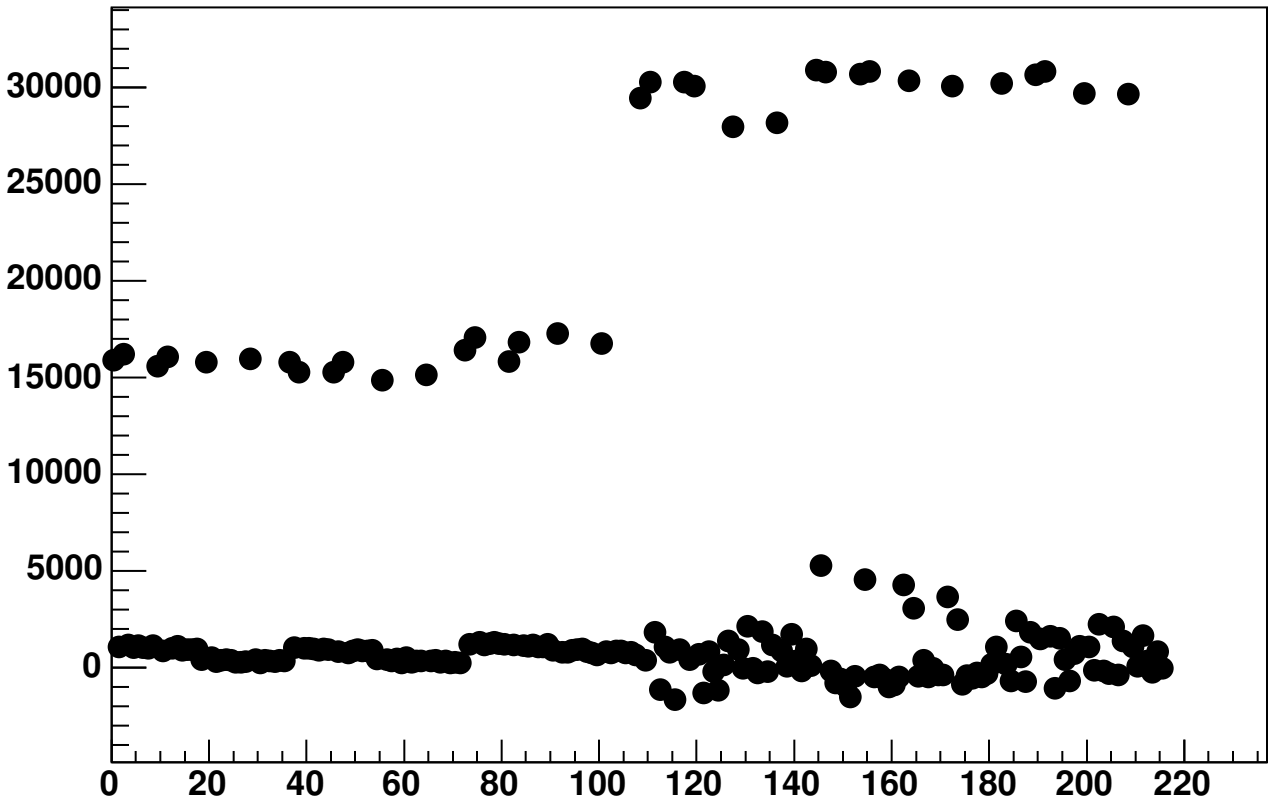
Enable 0, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



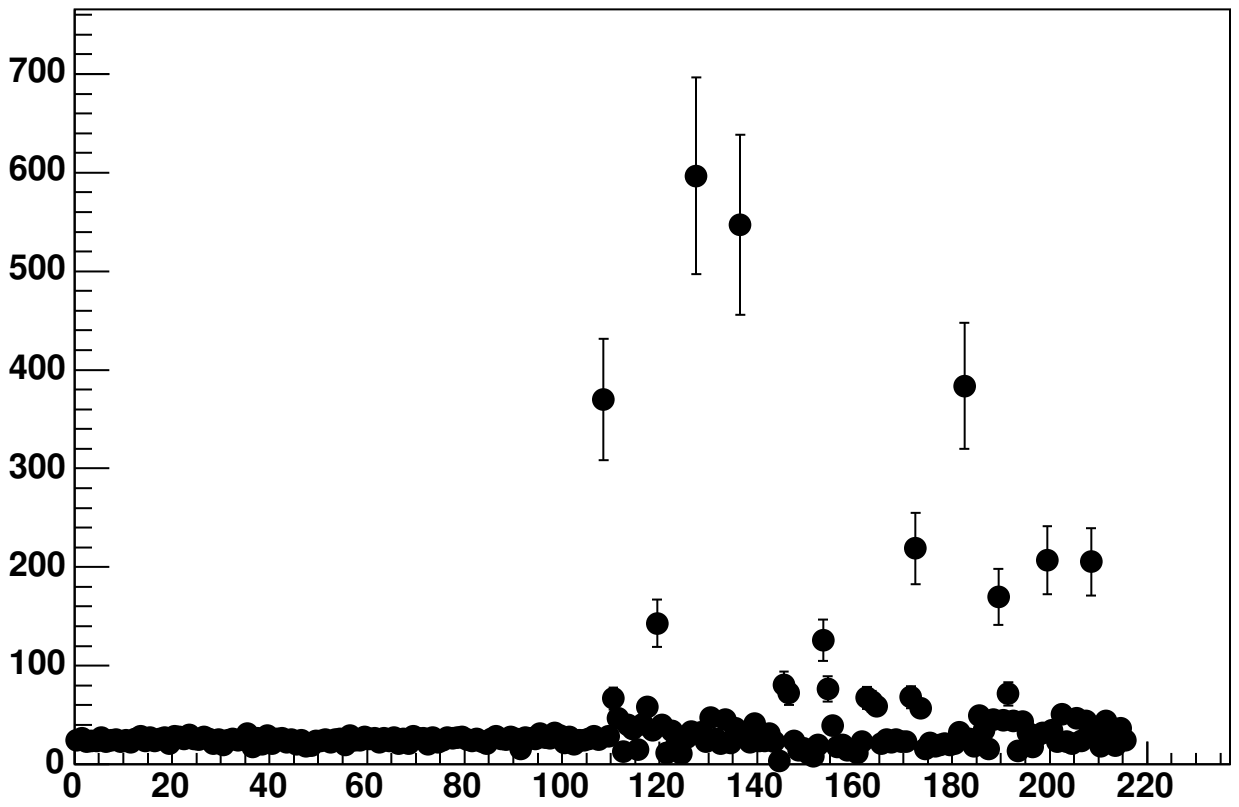
Enable 0, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



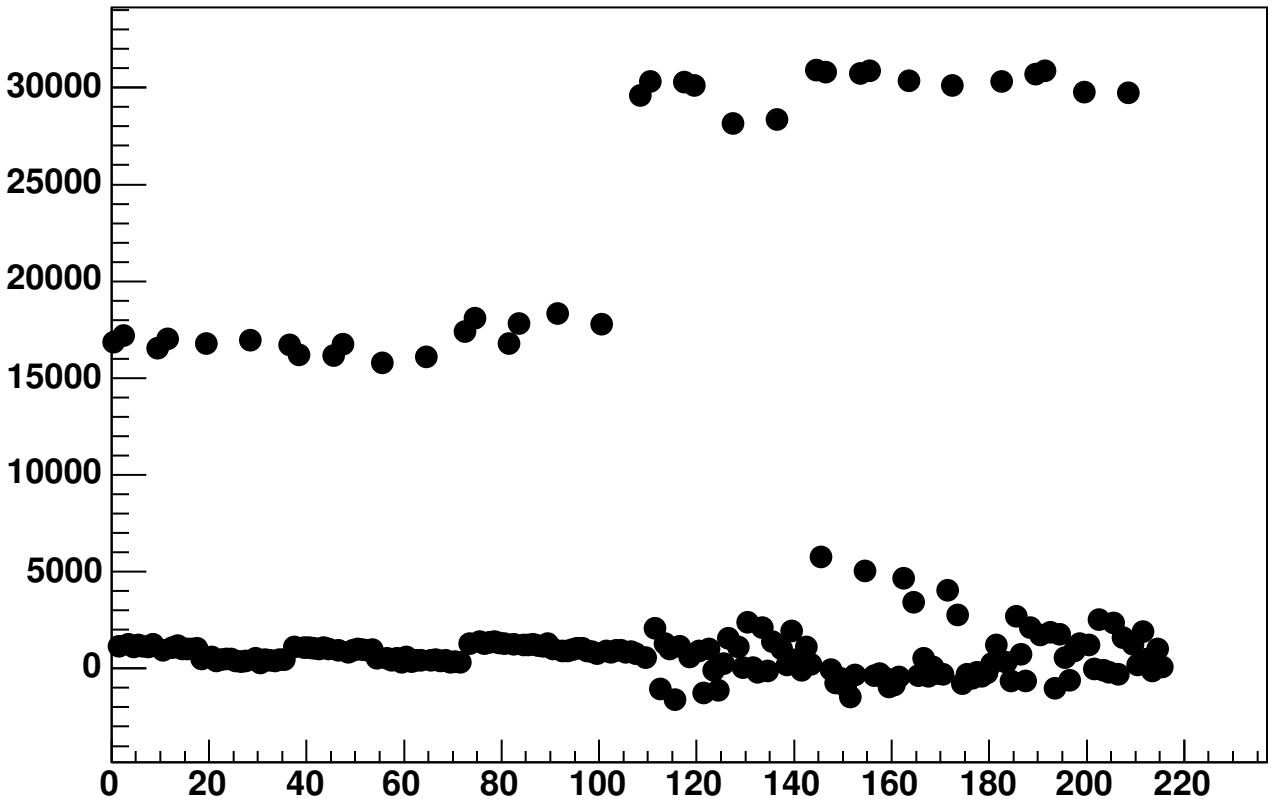
Enable 0, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



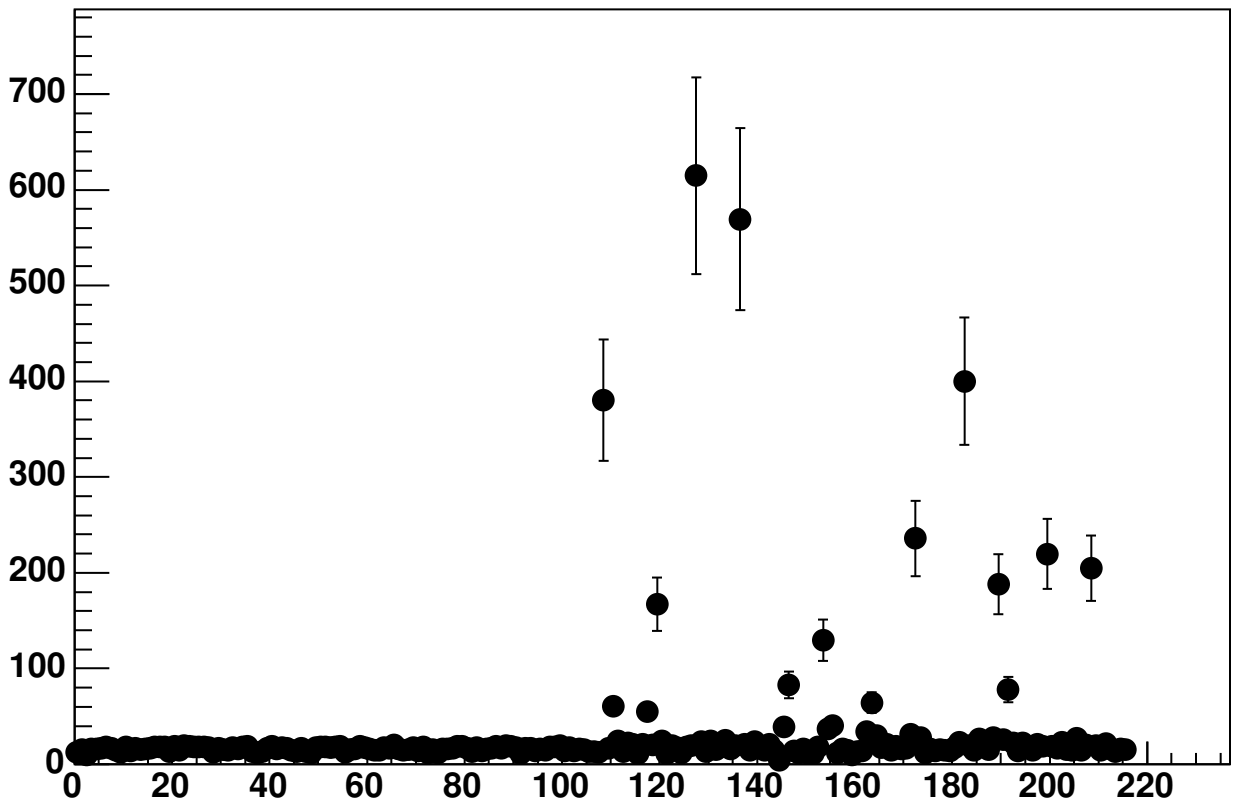
Enable 0, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



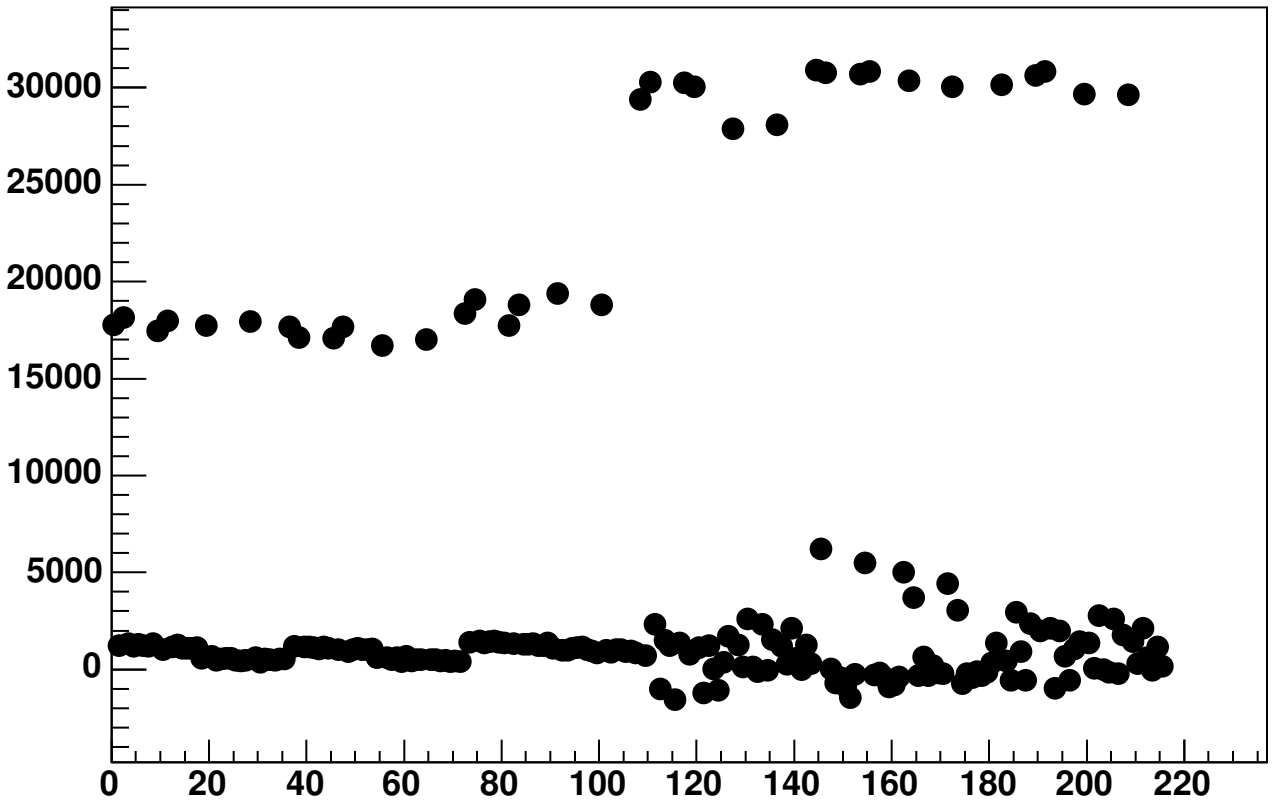
Enable 0, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



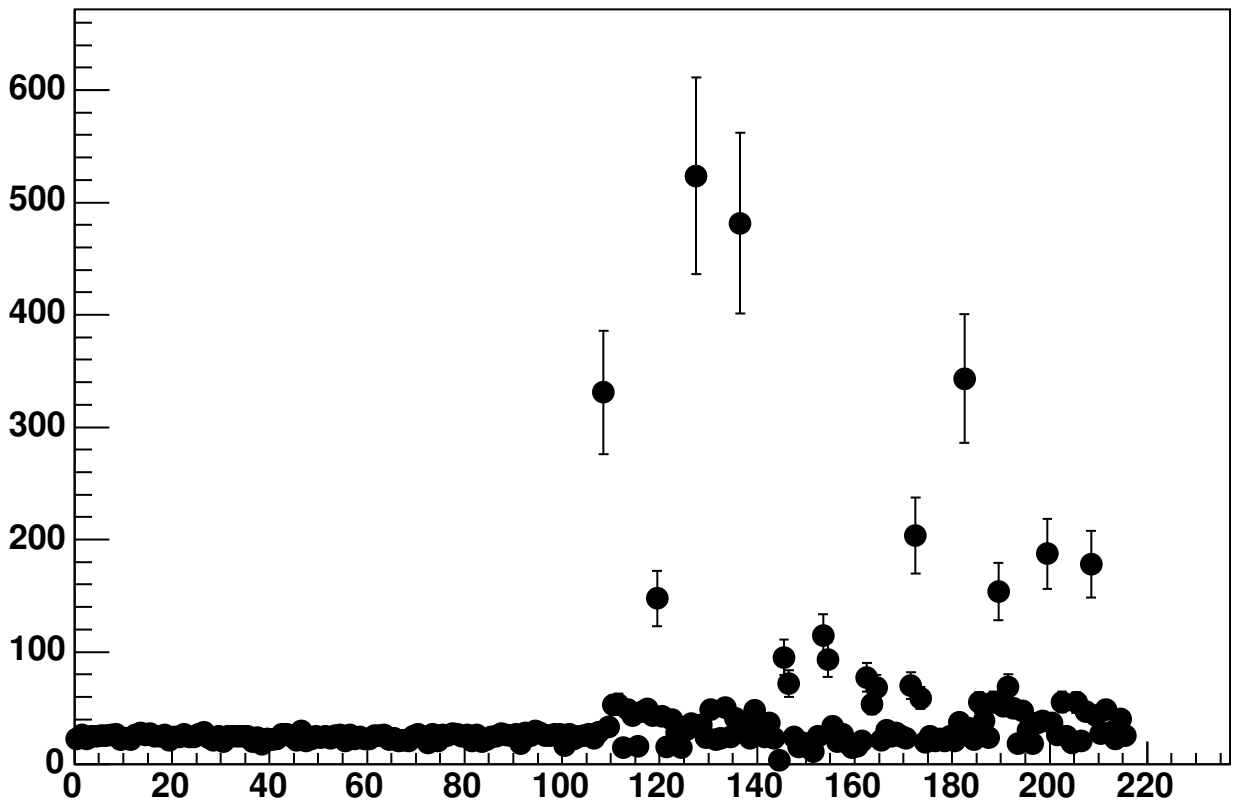
Enable 0, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



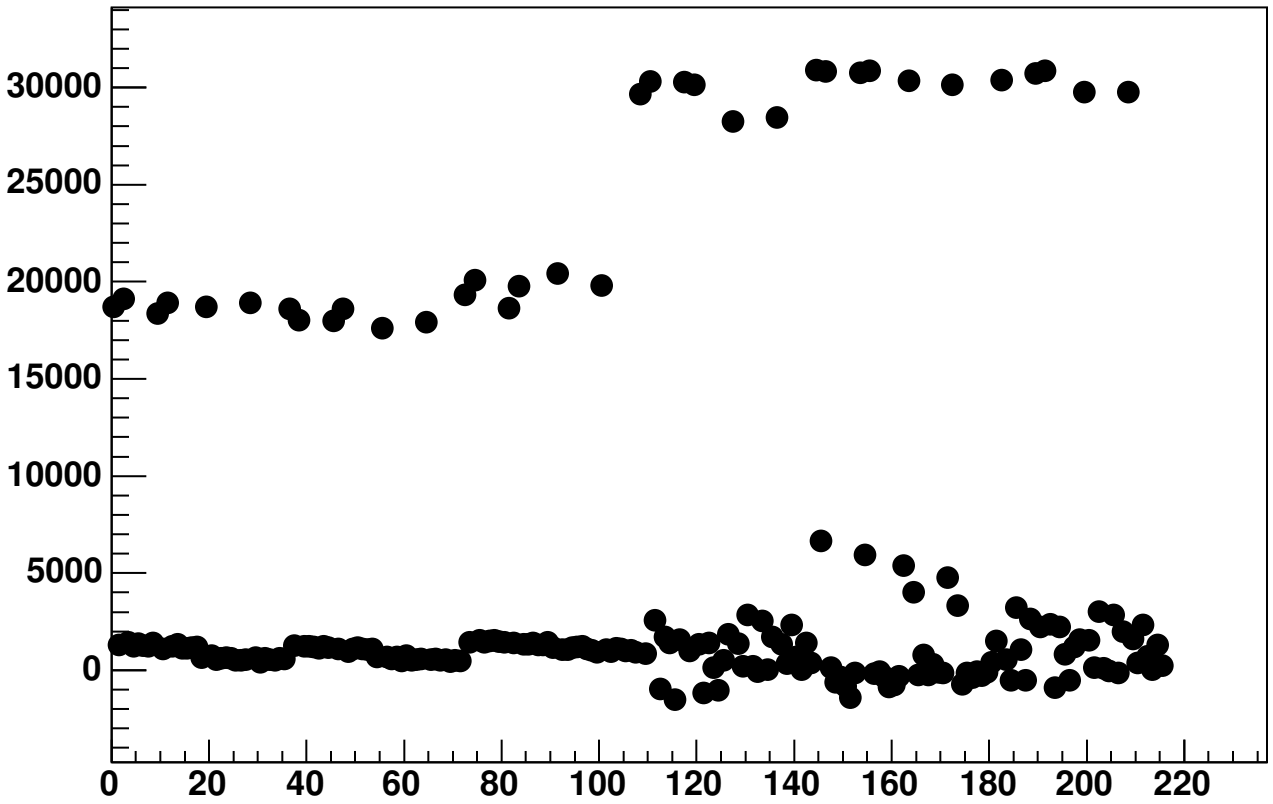
Enable 0, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



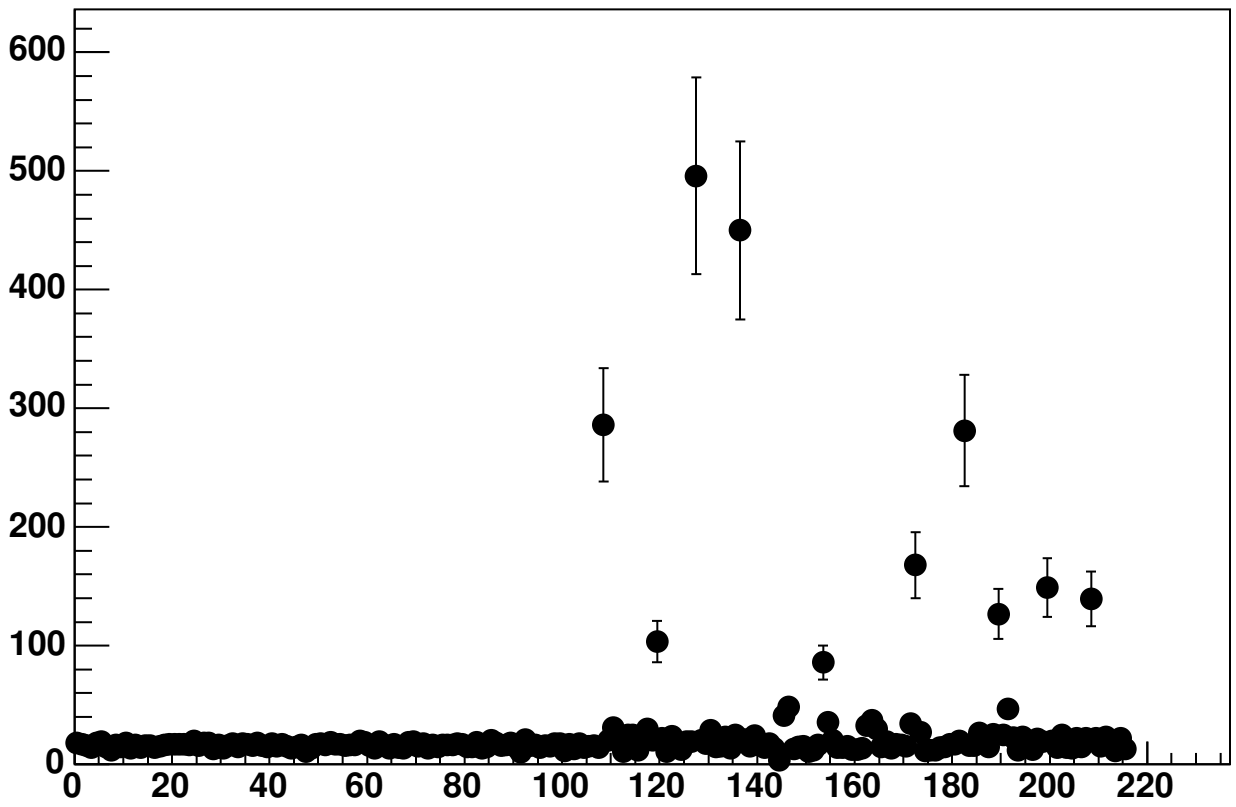
Enable 0, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



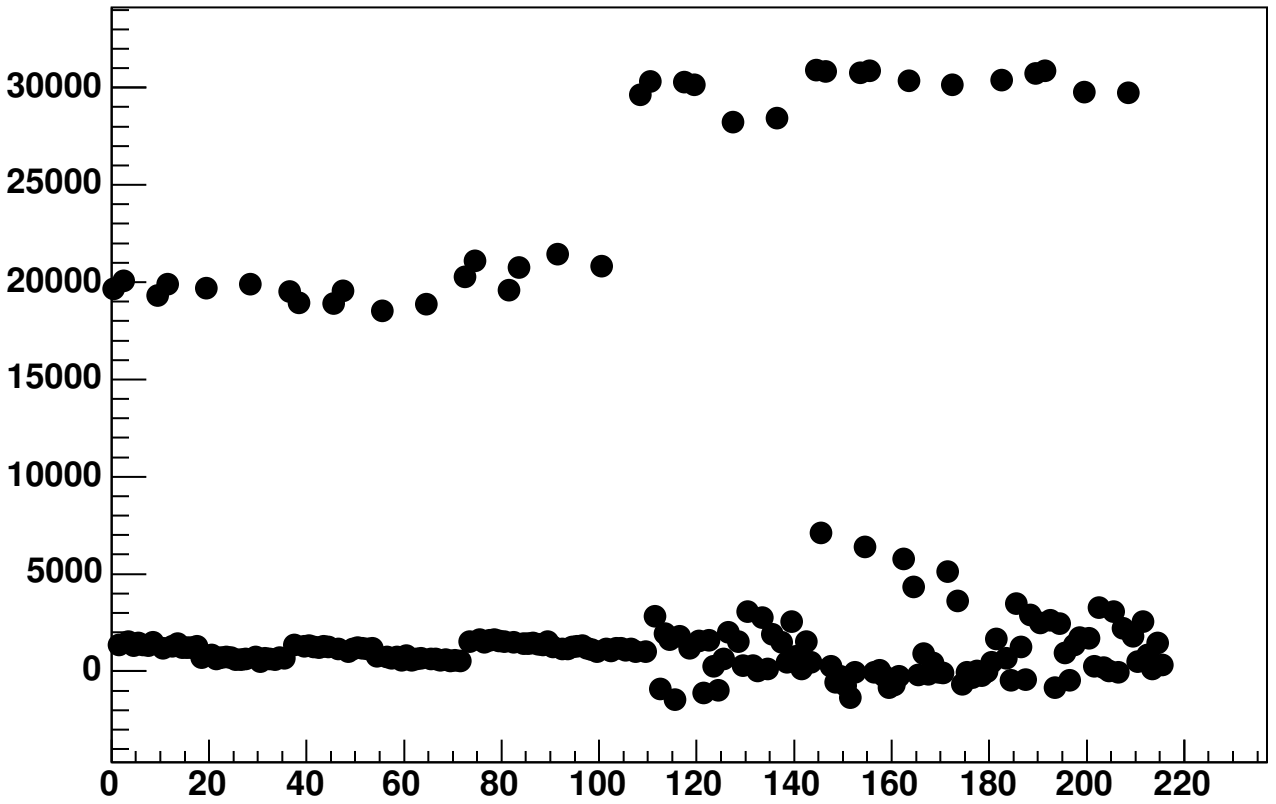
Enable 0, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



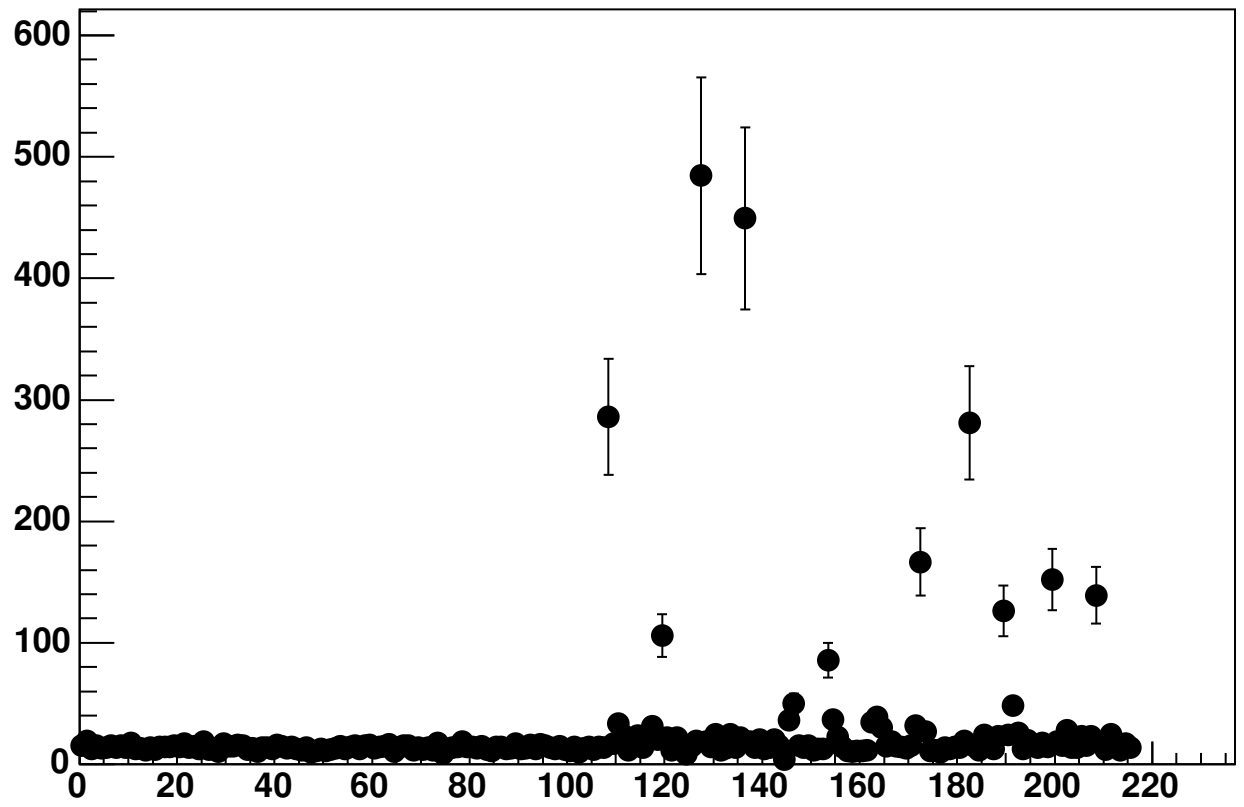
Enable 0, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



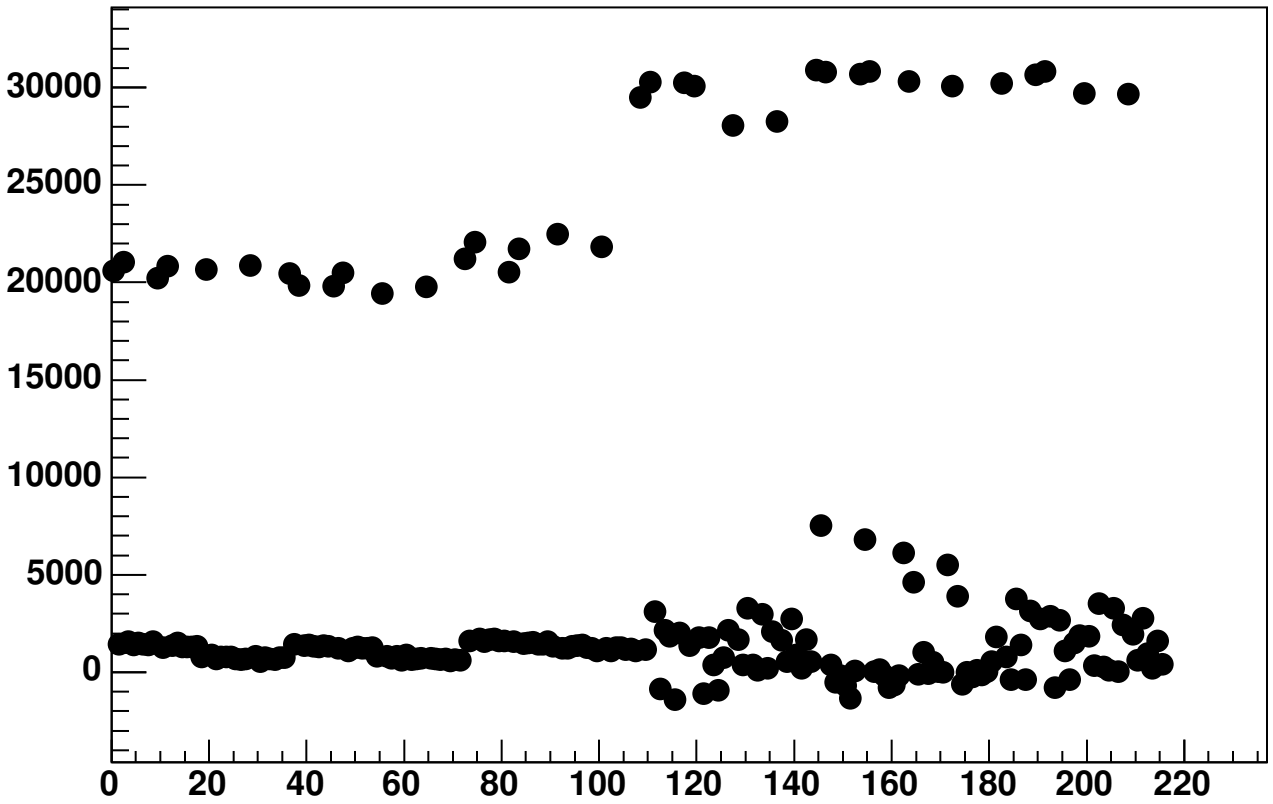
Enable 0, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



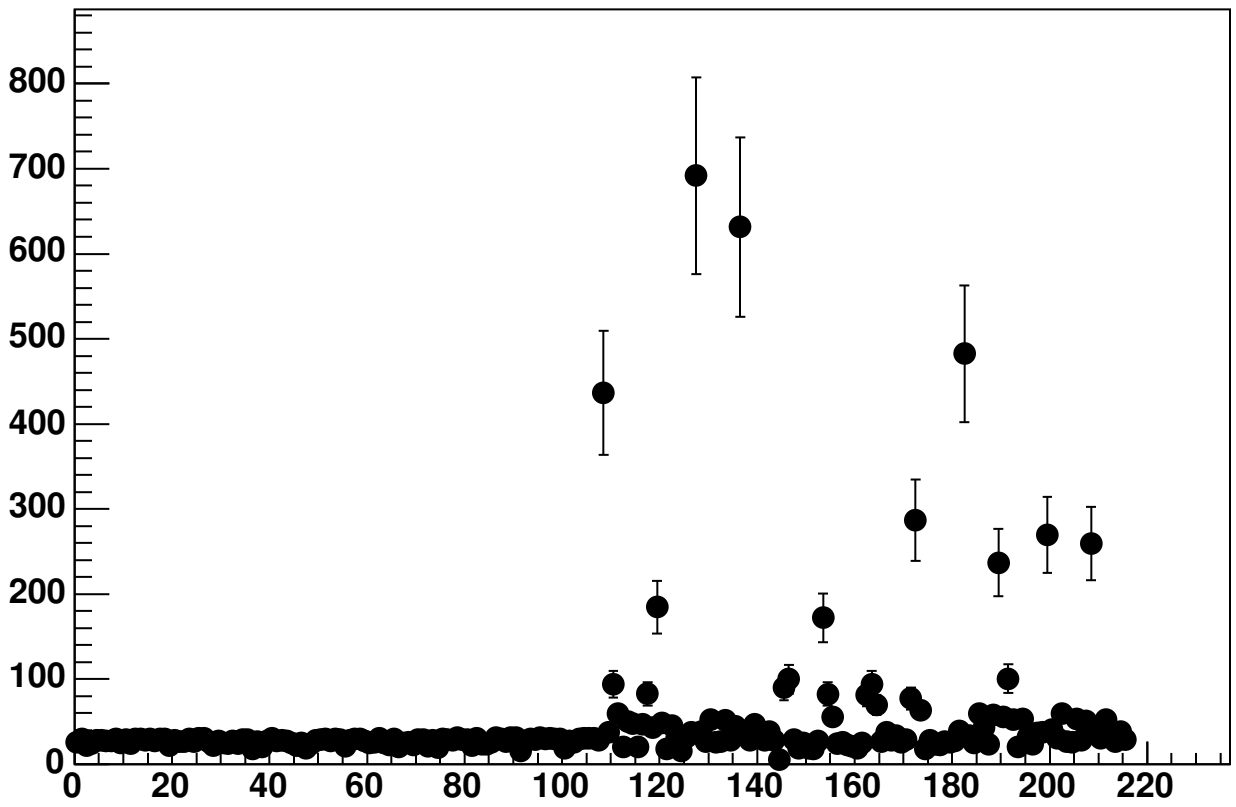
Enable 0, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



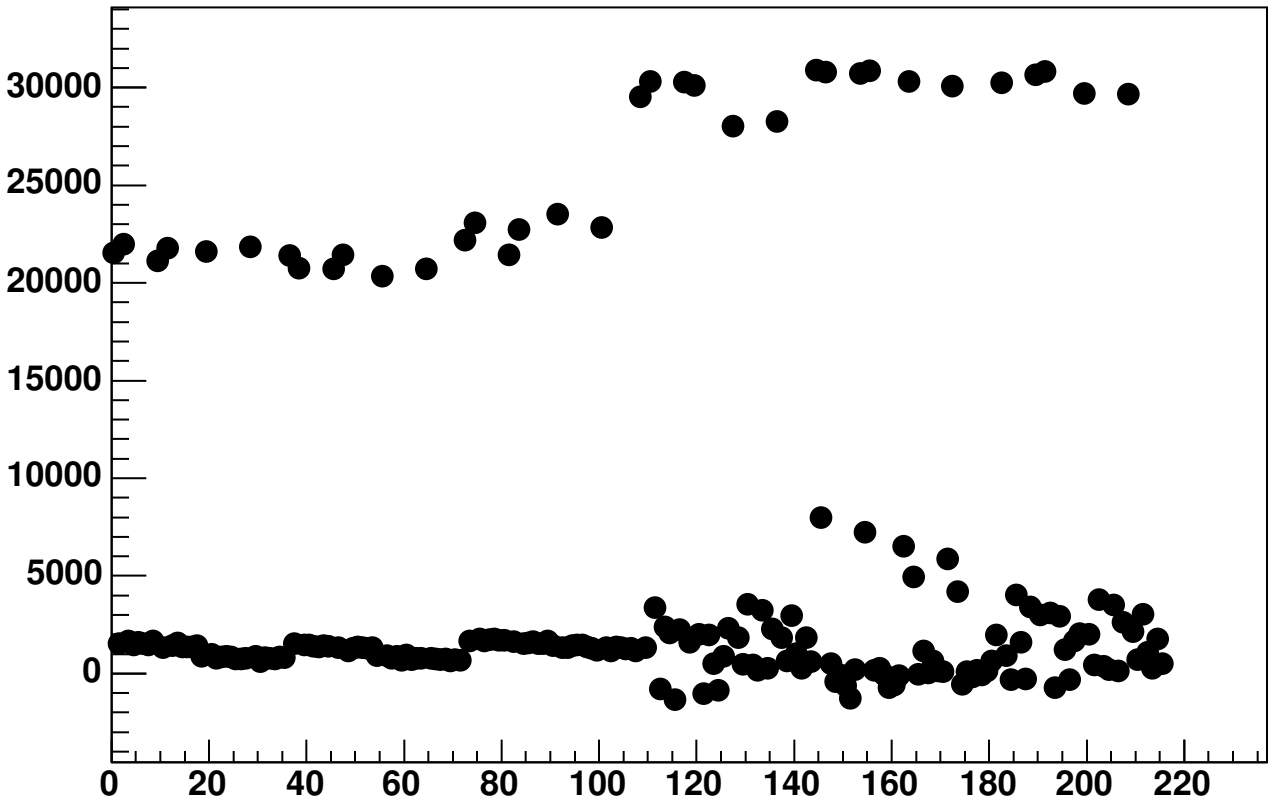
Enable 0, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



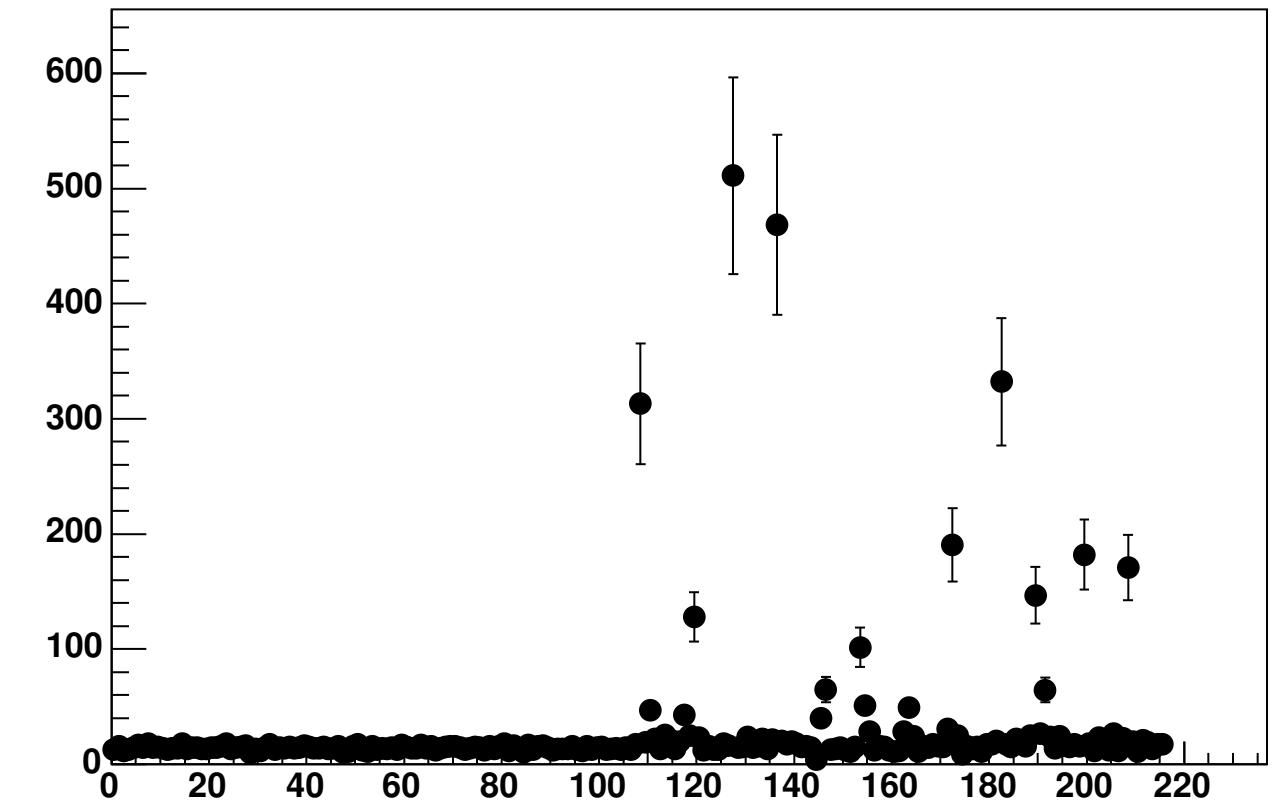
Enable 0, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



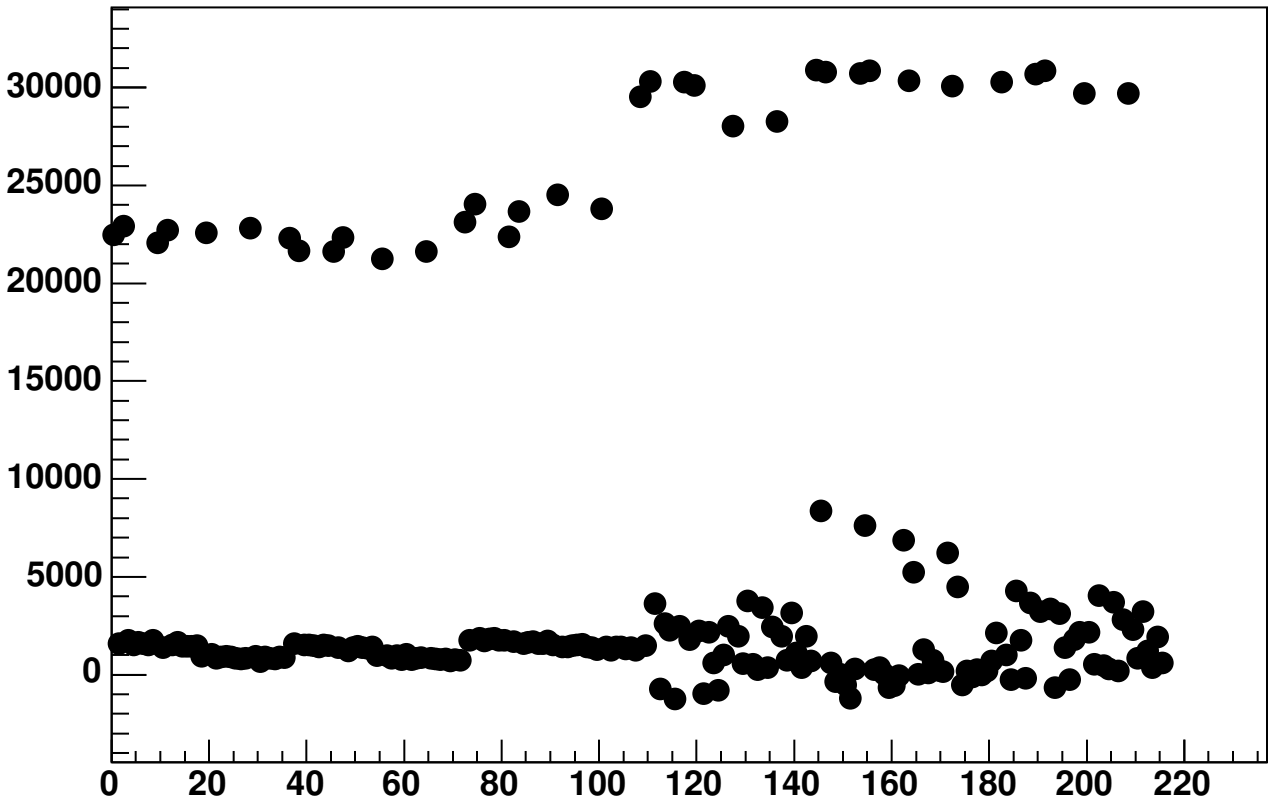
Enable 0, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



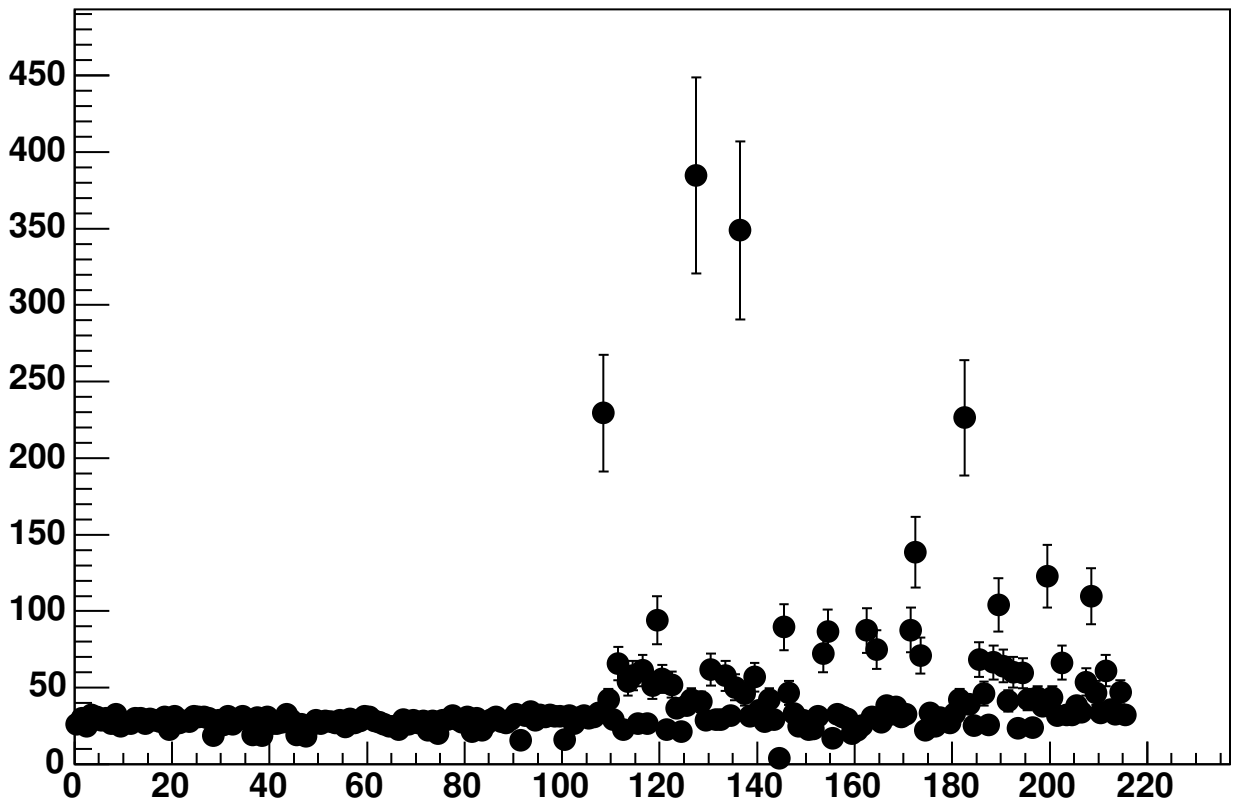
Enable 0, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



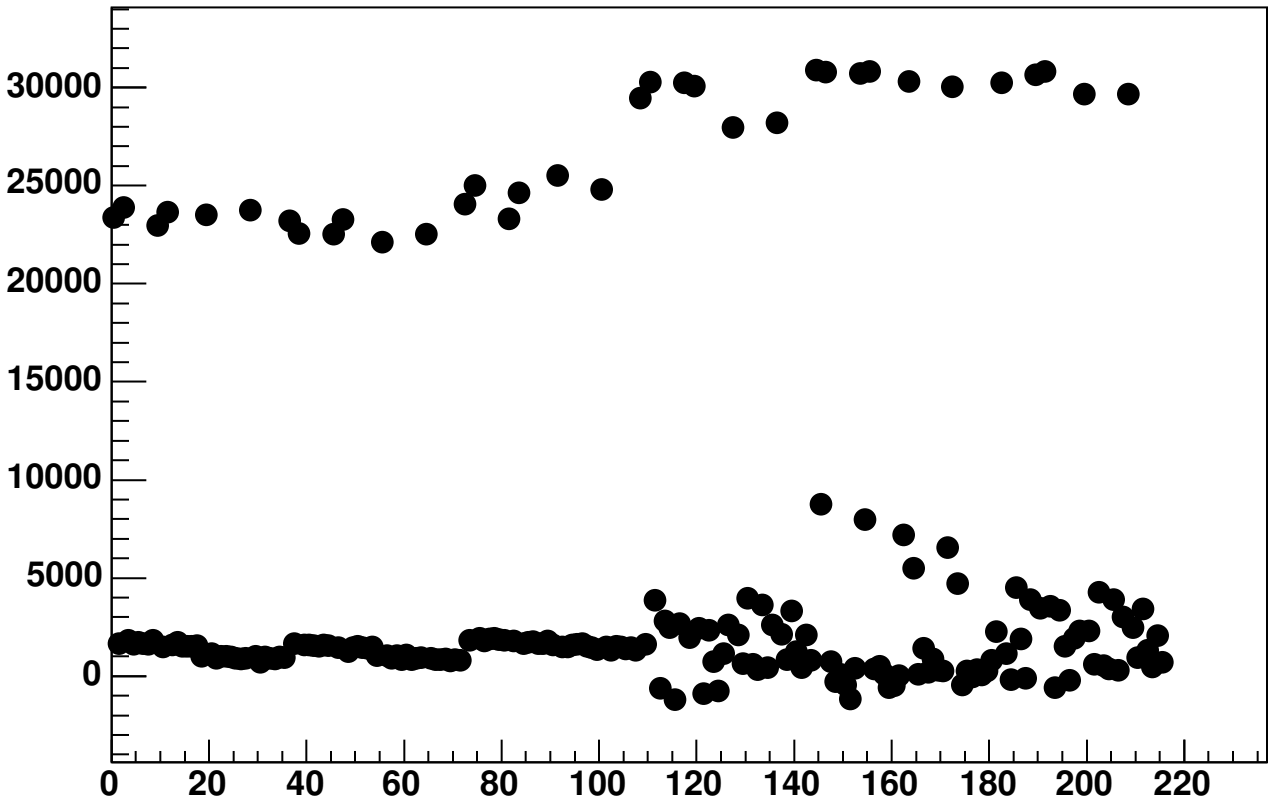
Enable 0, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



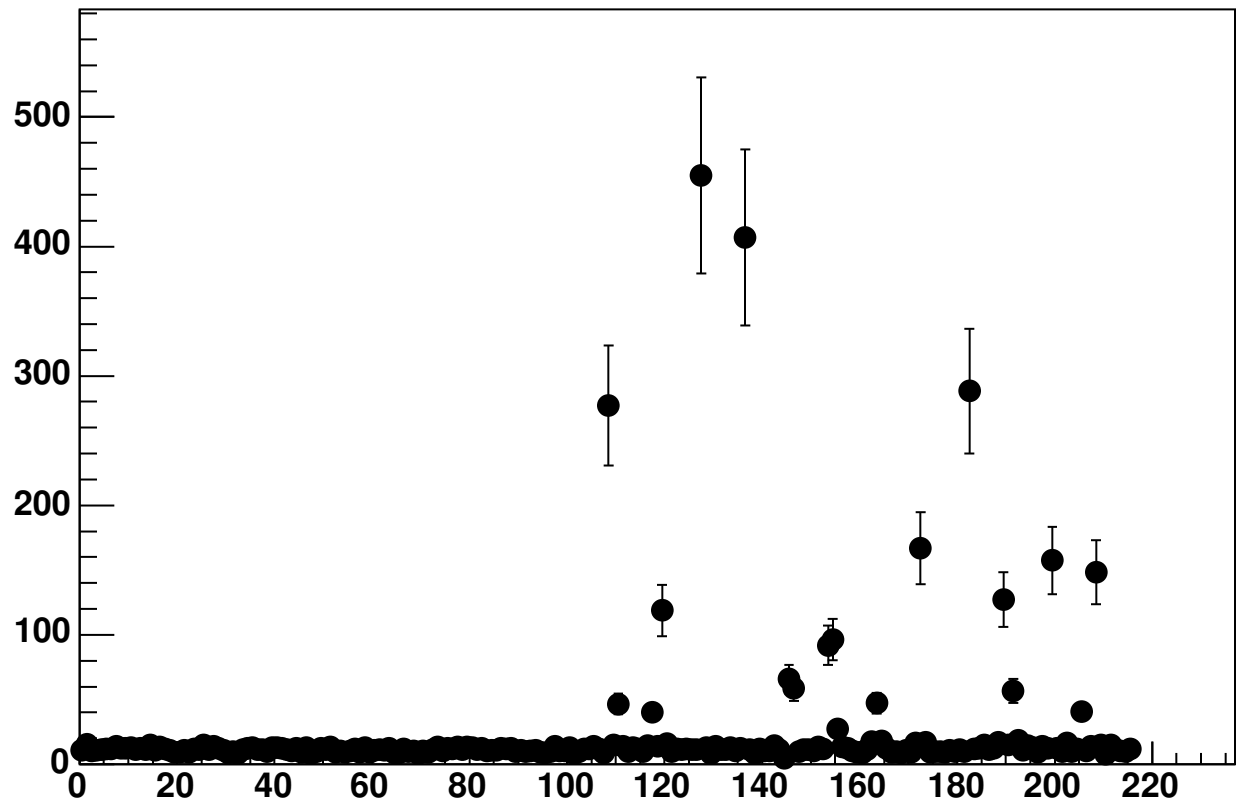
Enable 0, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



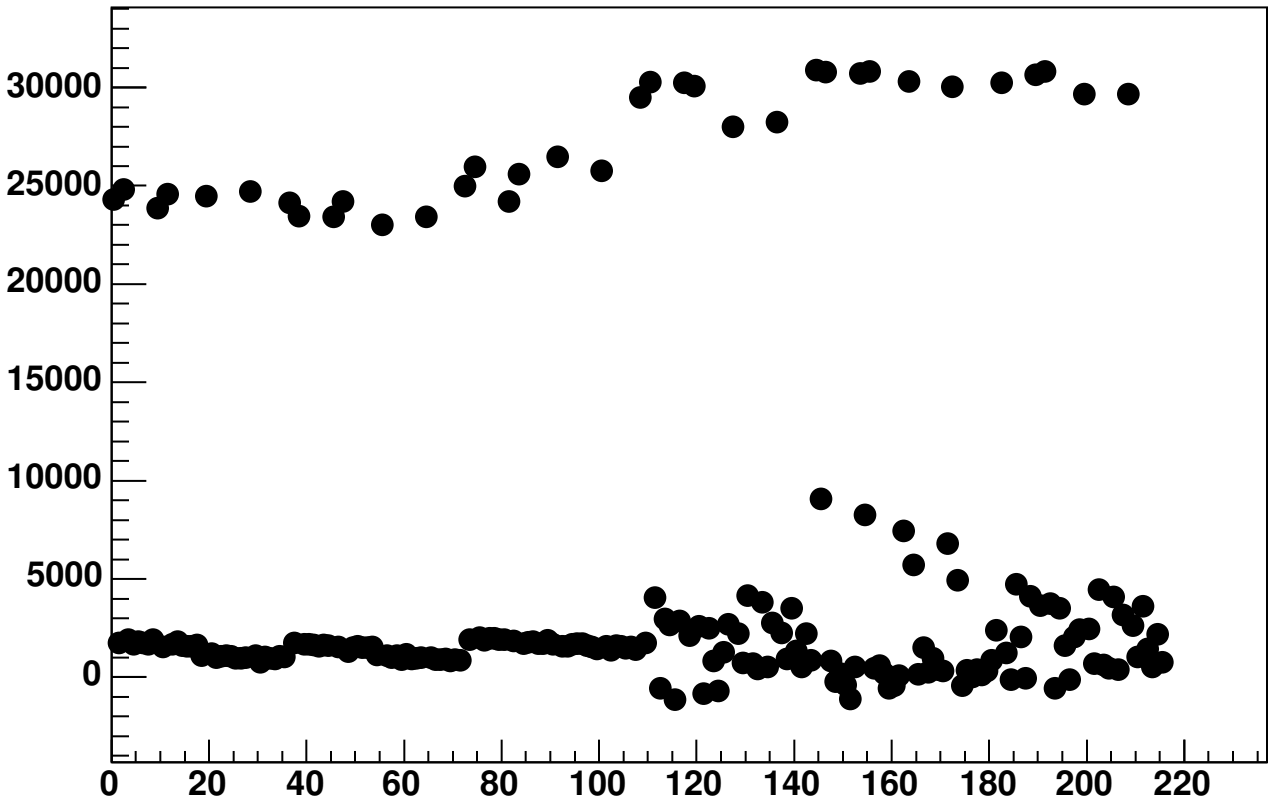
Enable 0, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



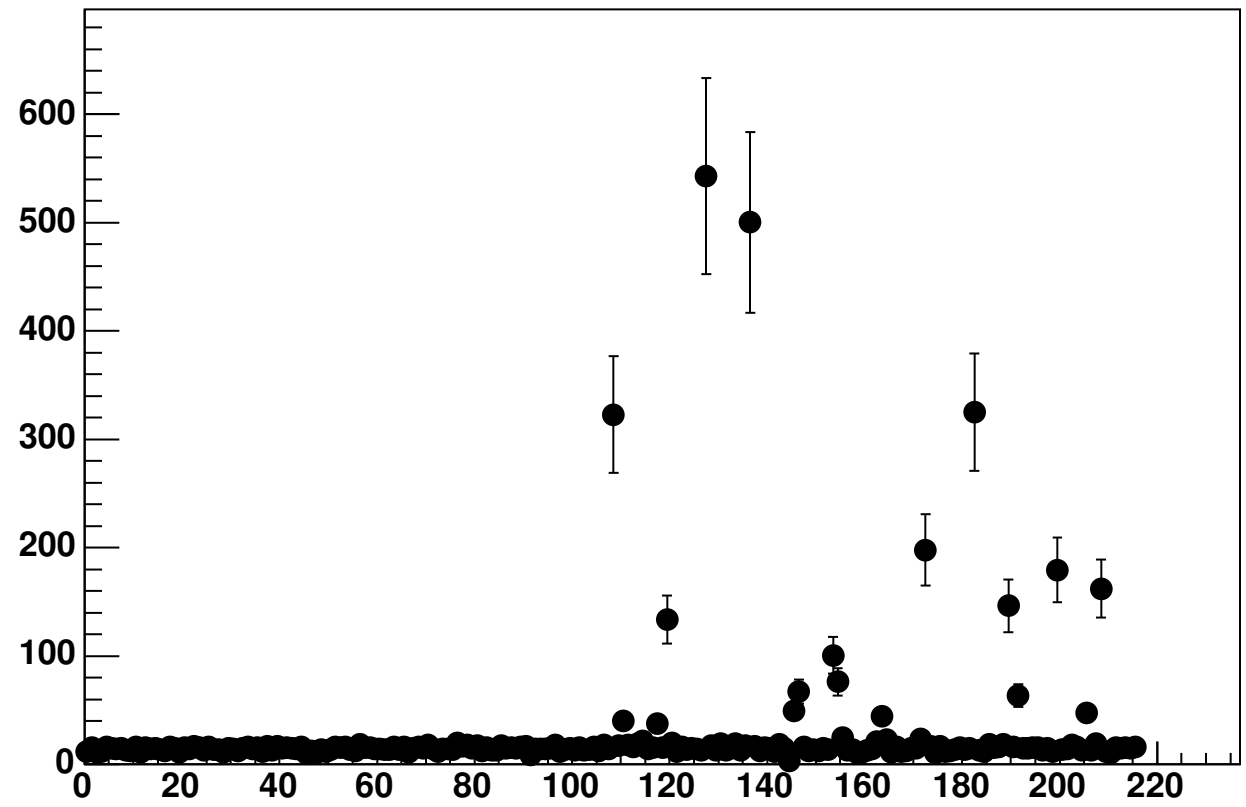
Enable 0, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



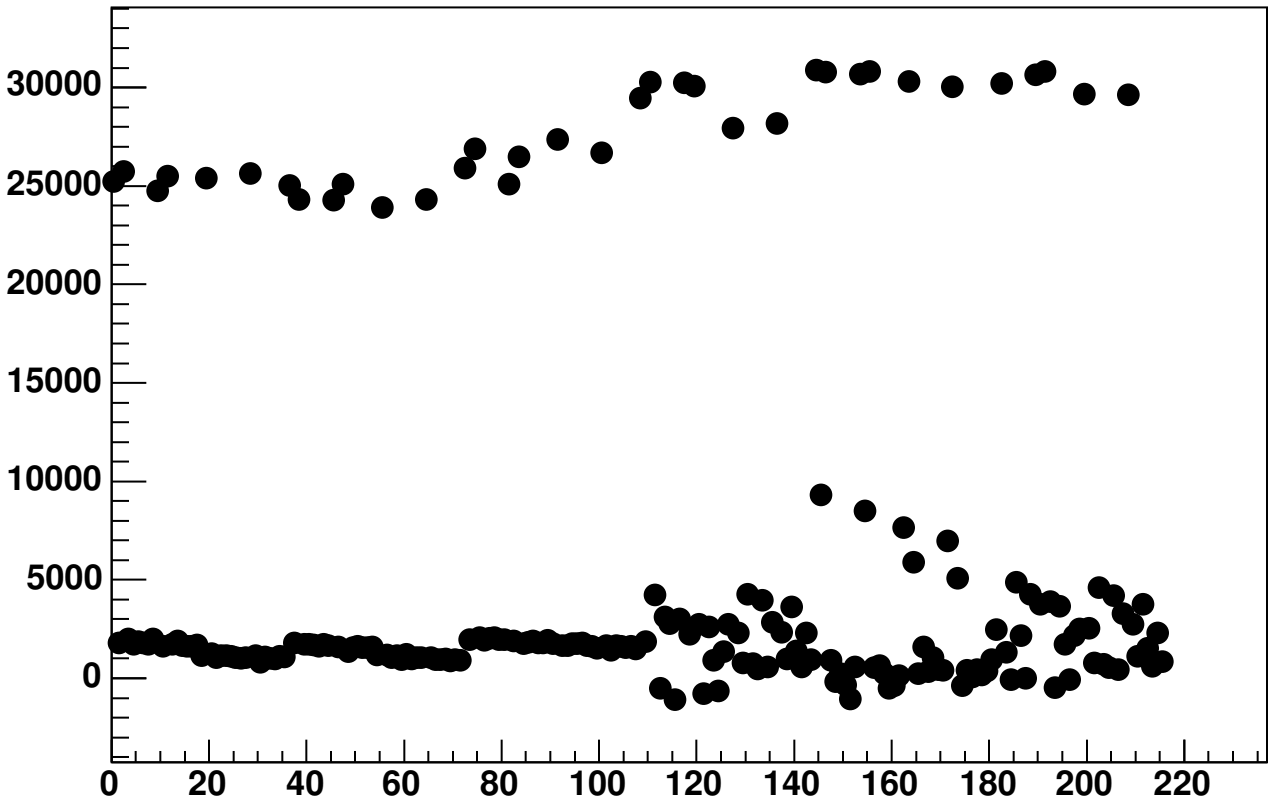
Enable 0, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



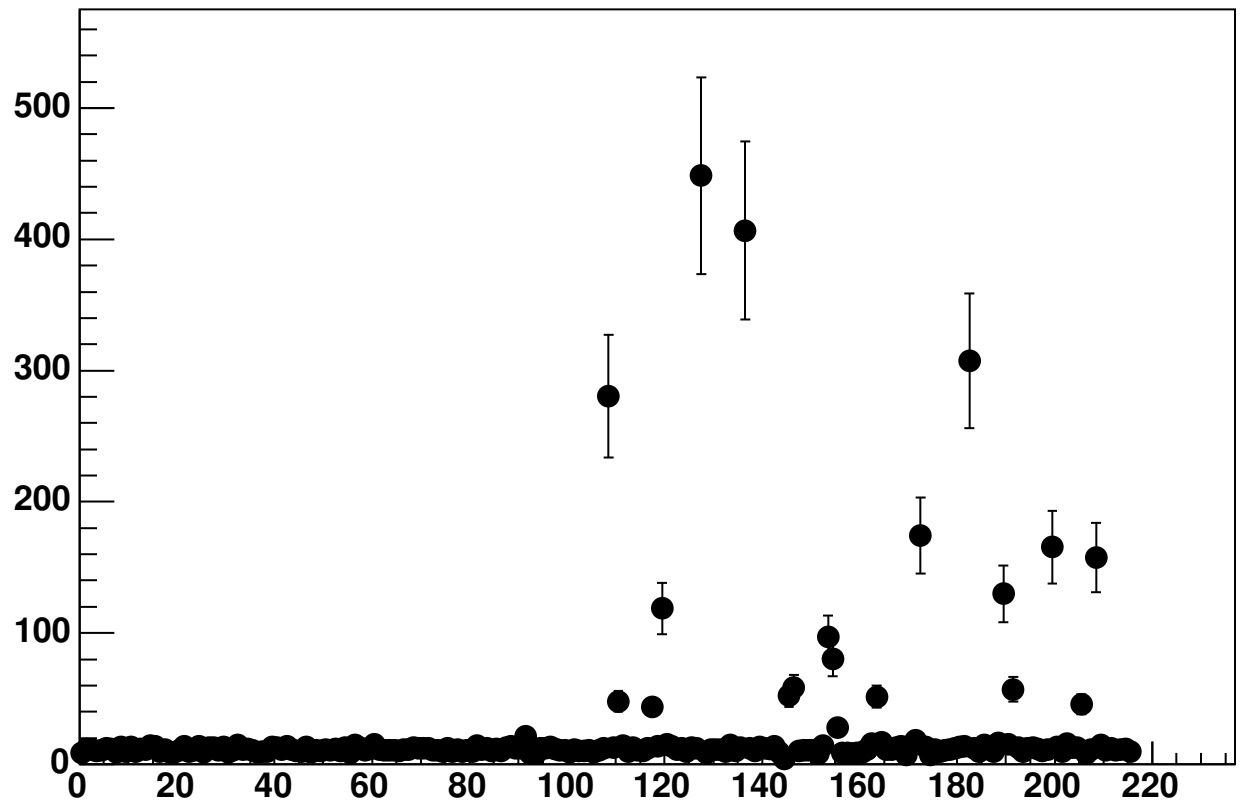
Enable 0, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



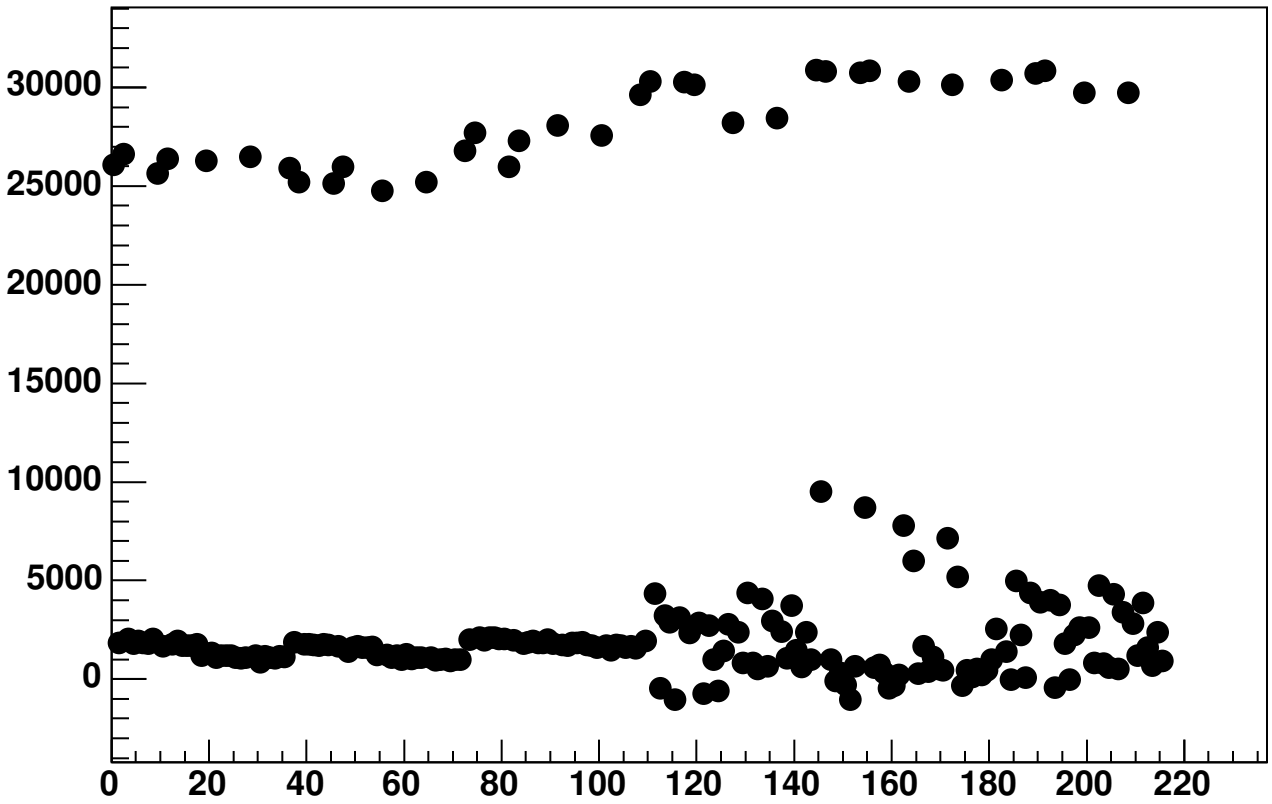
Enable 0, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



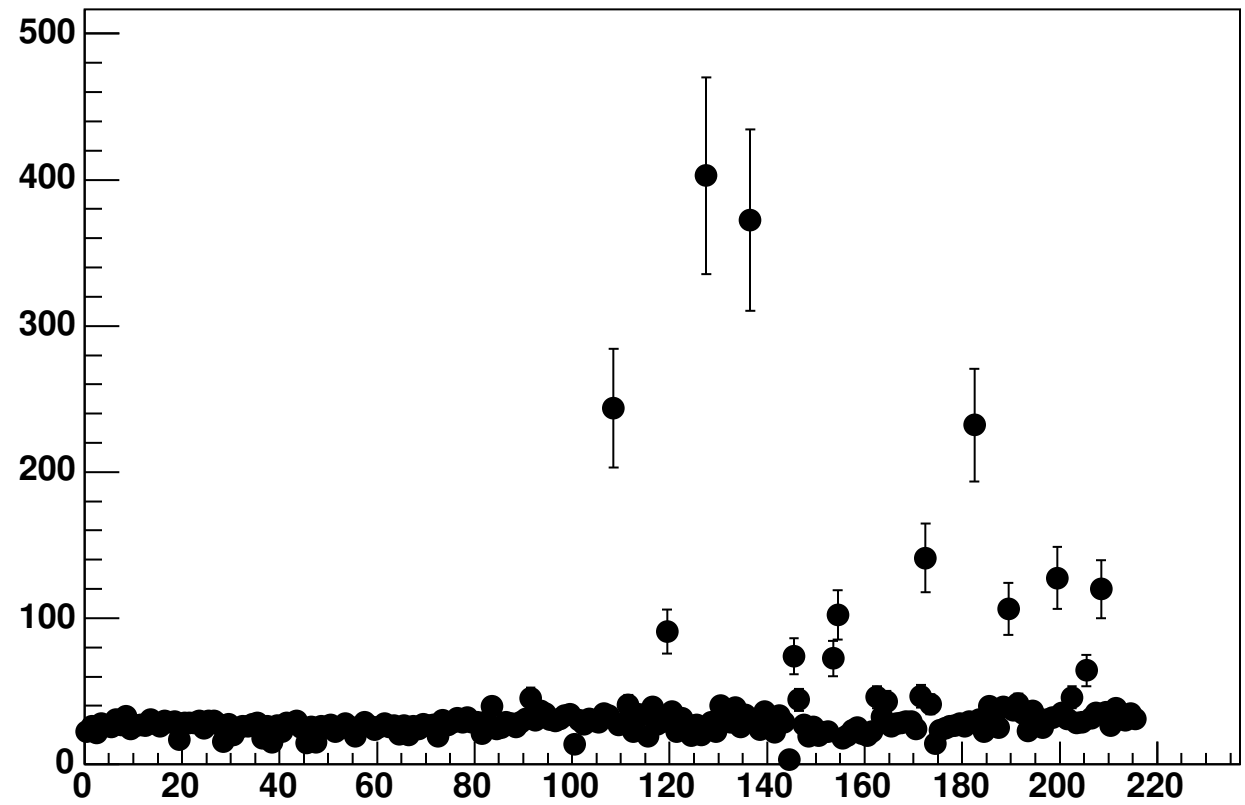
Enable 0, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



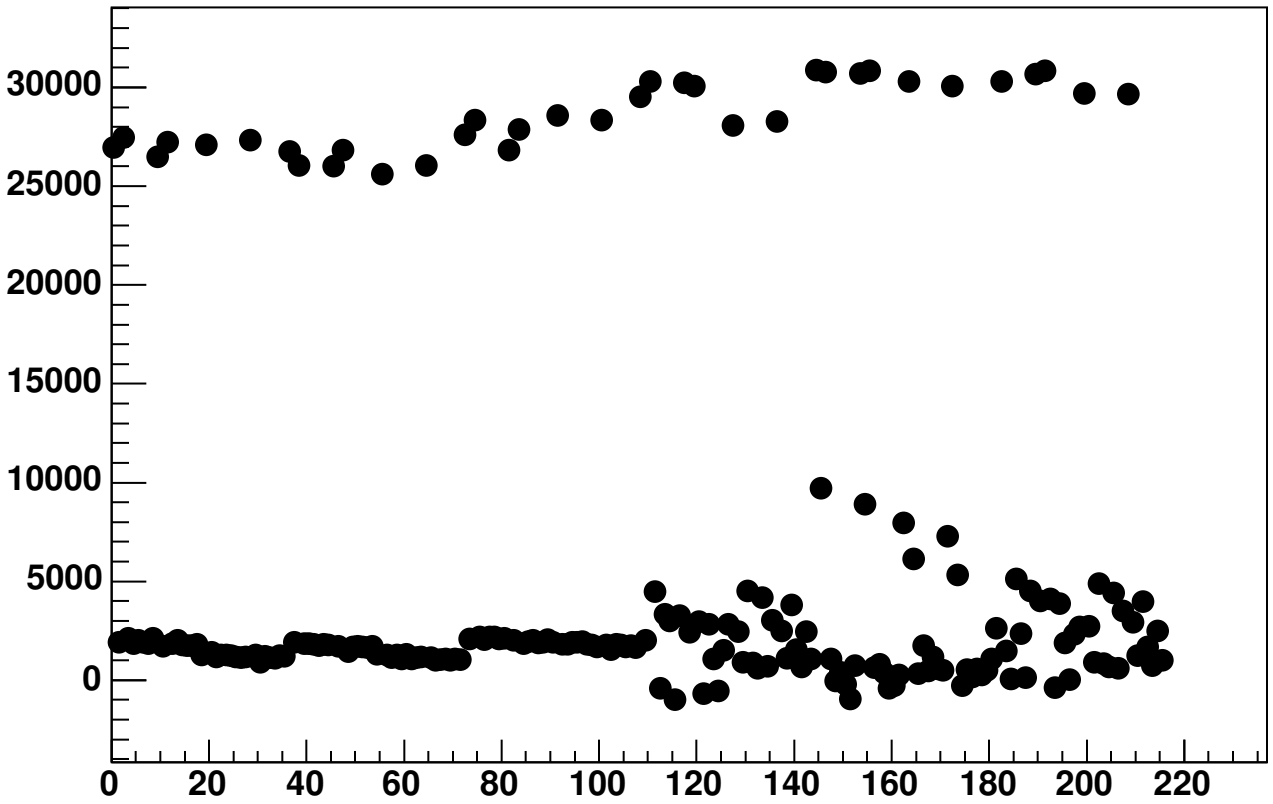
Enable 0, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



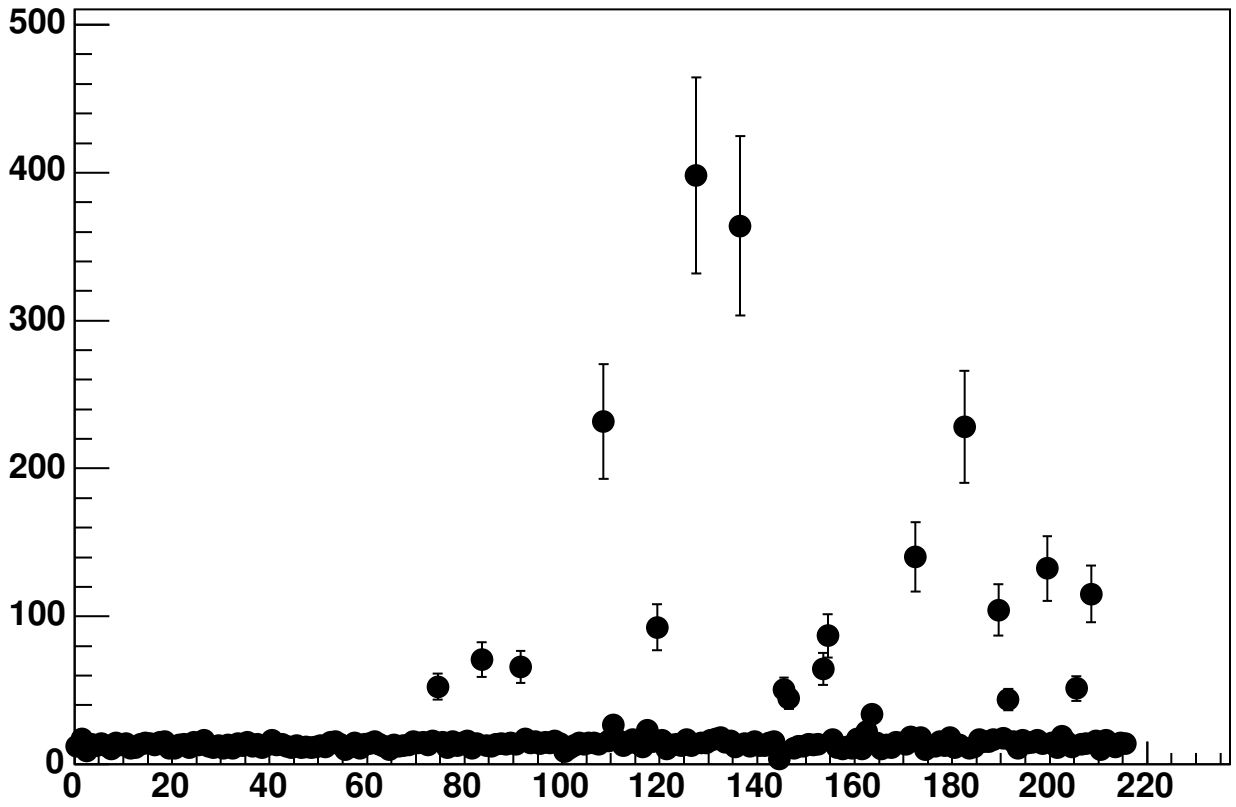
Enable 0, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



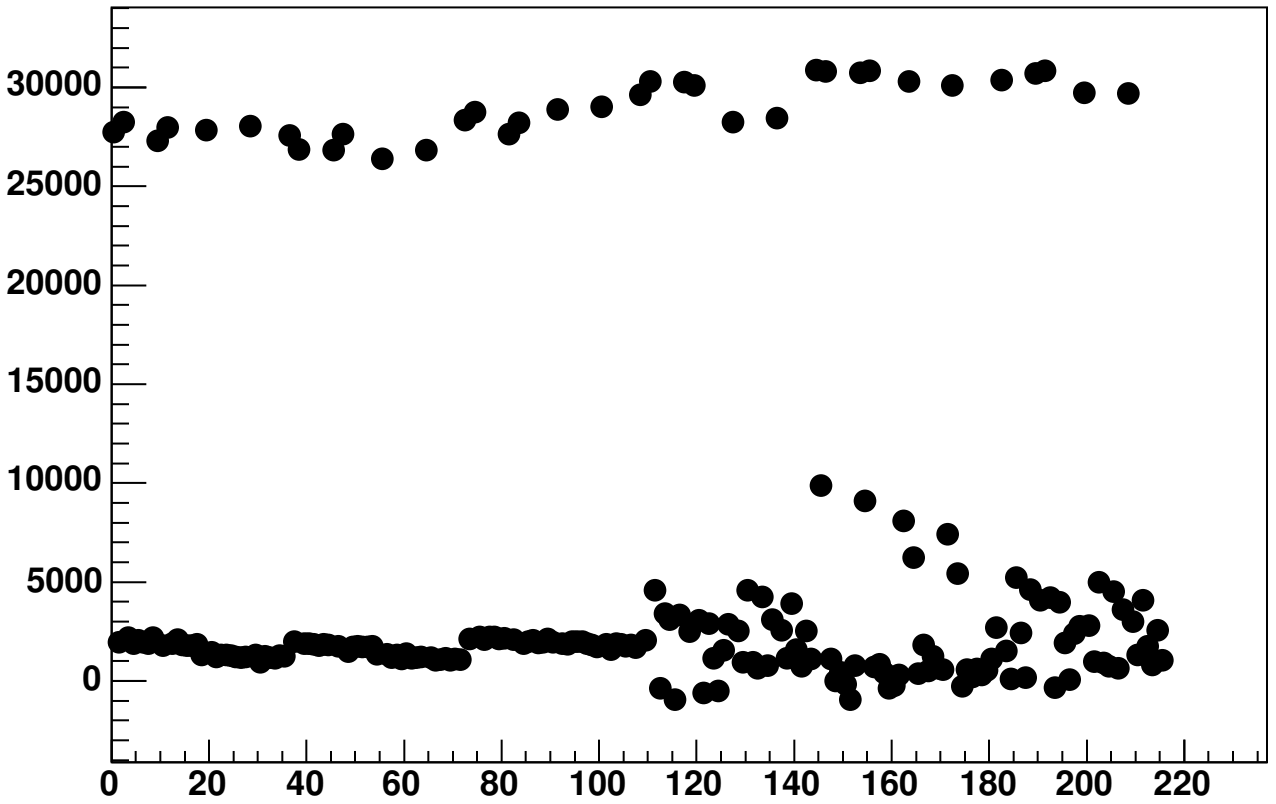
Enable 0, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



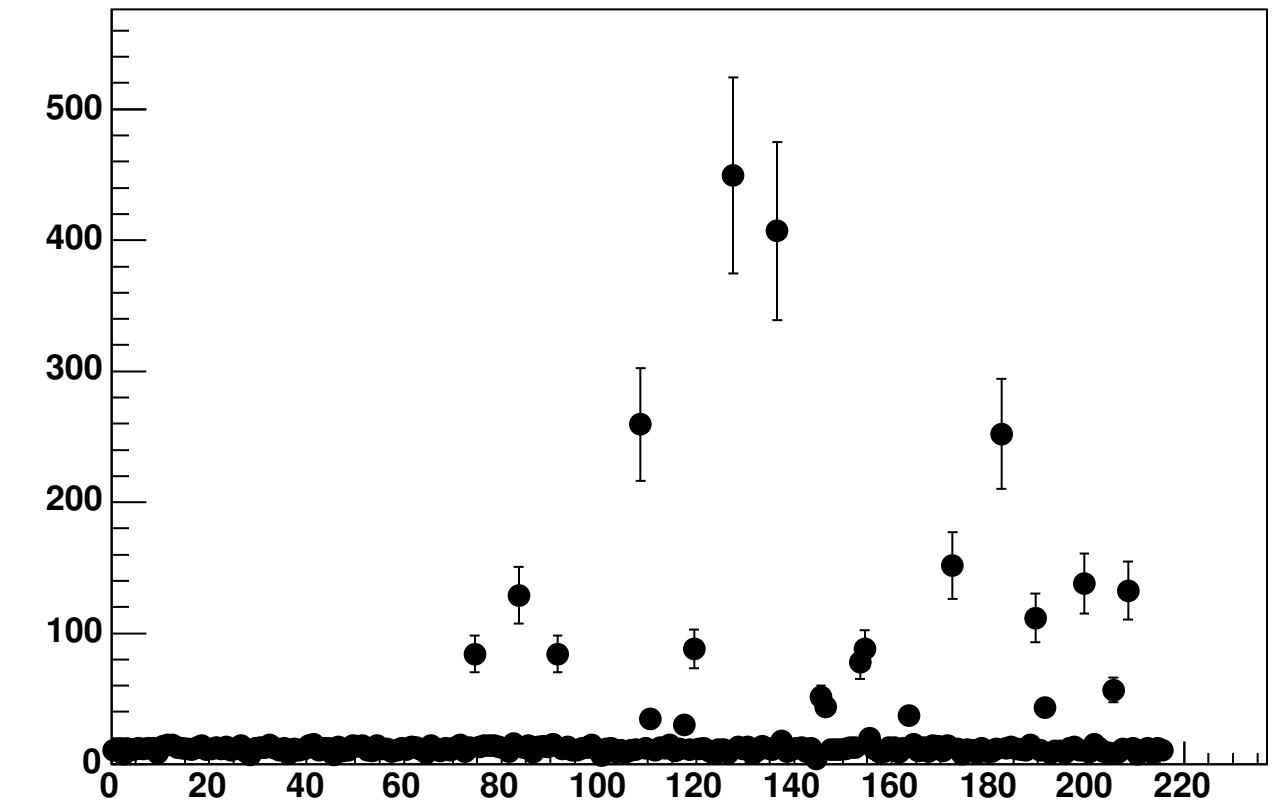
Enable 0, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



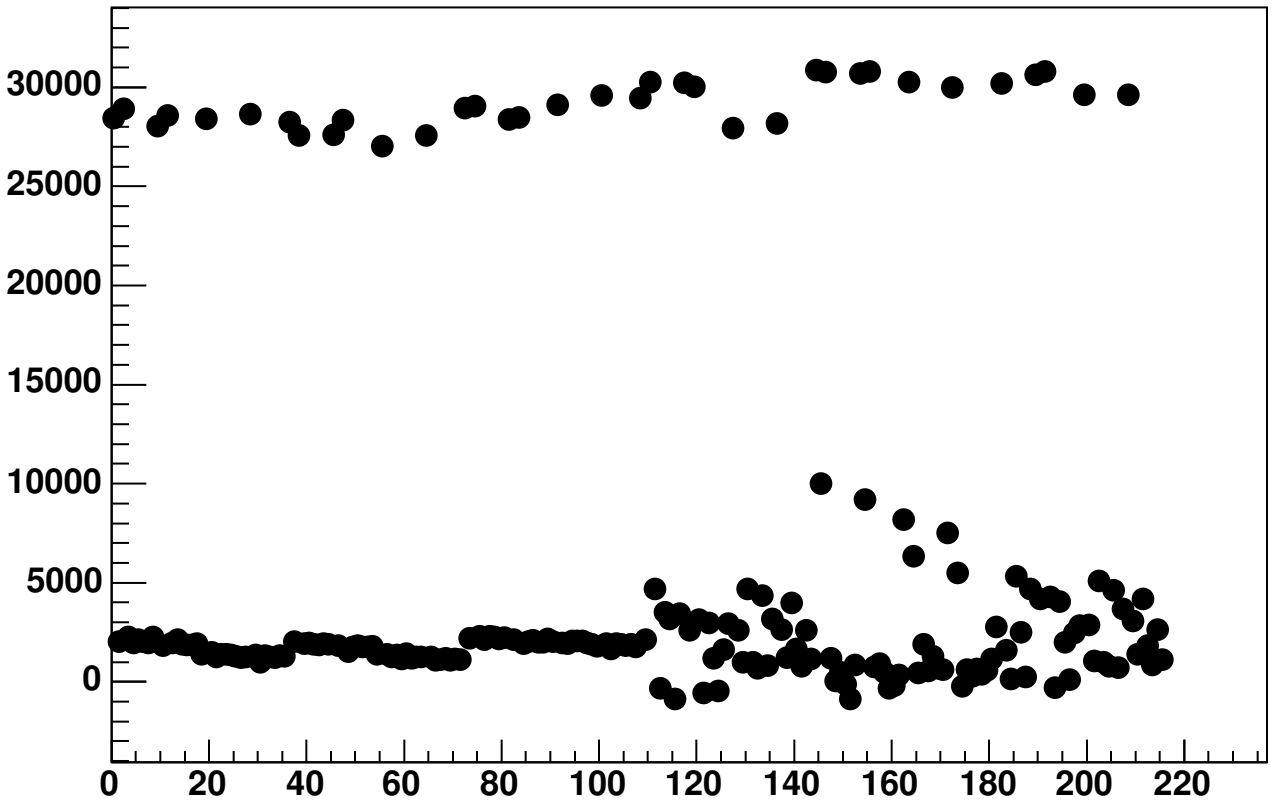
Enable 0, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



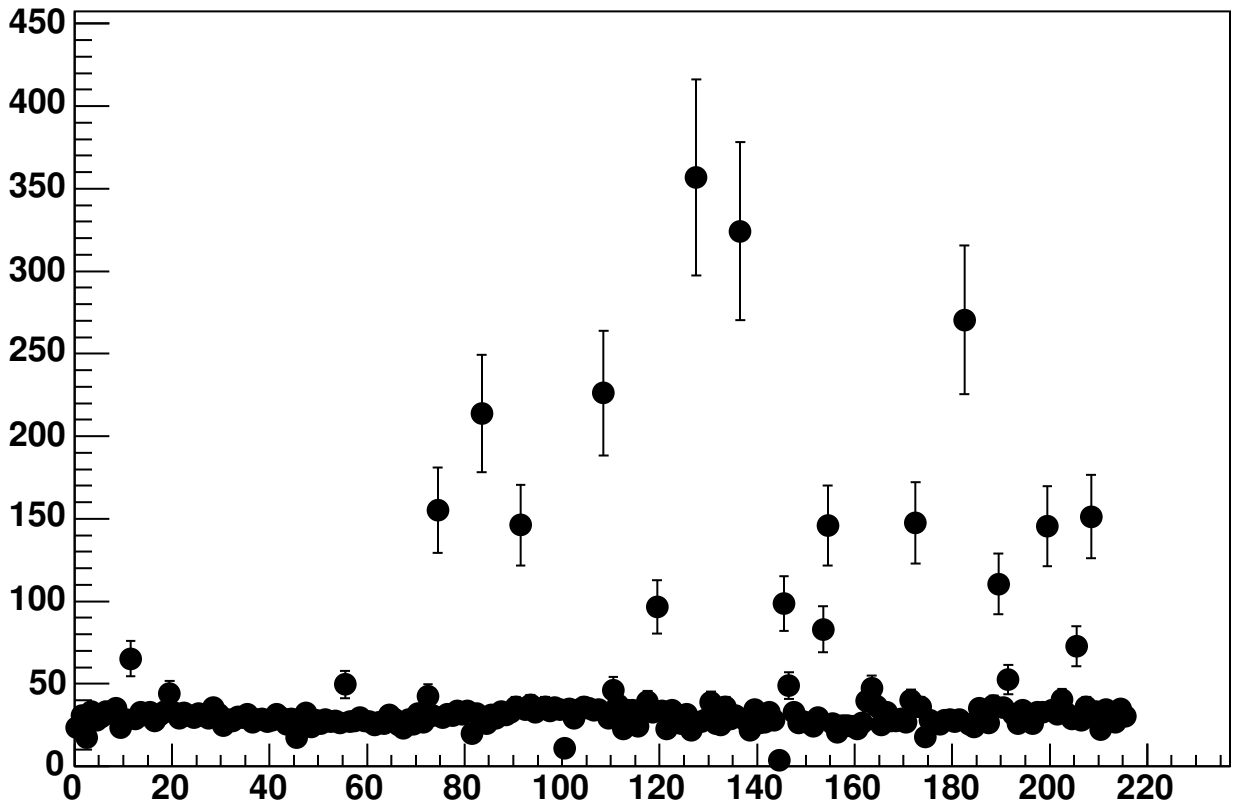
Enable 0, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



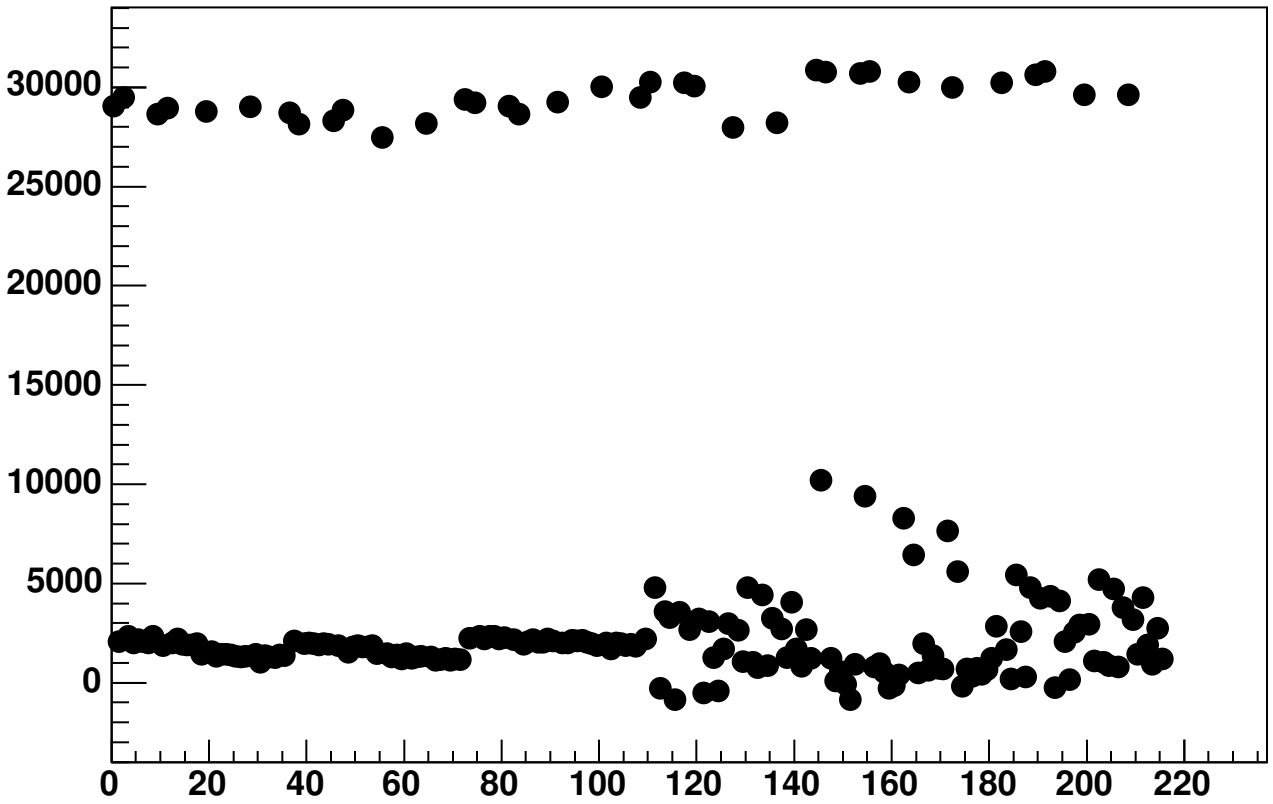
Enable 0, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



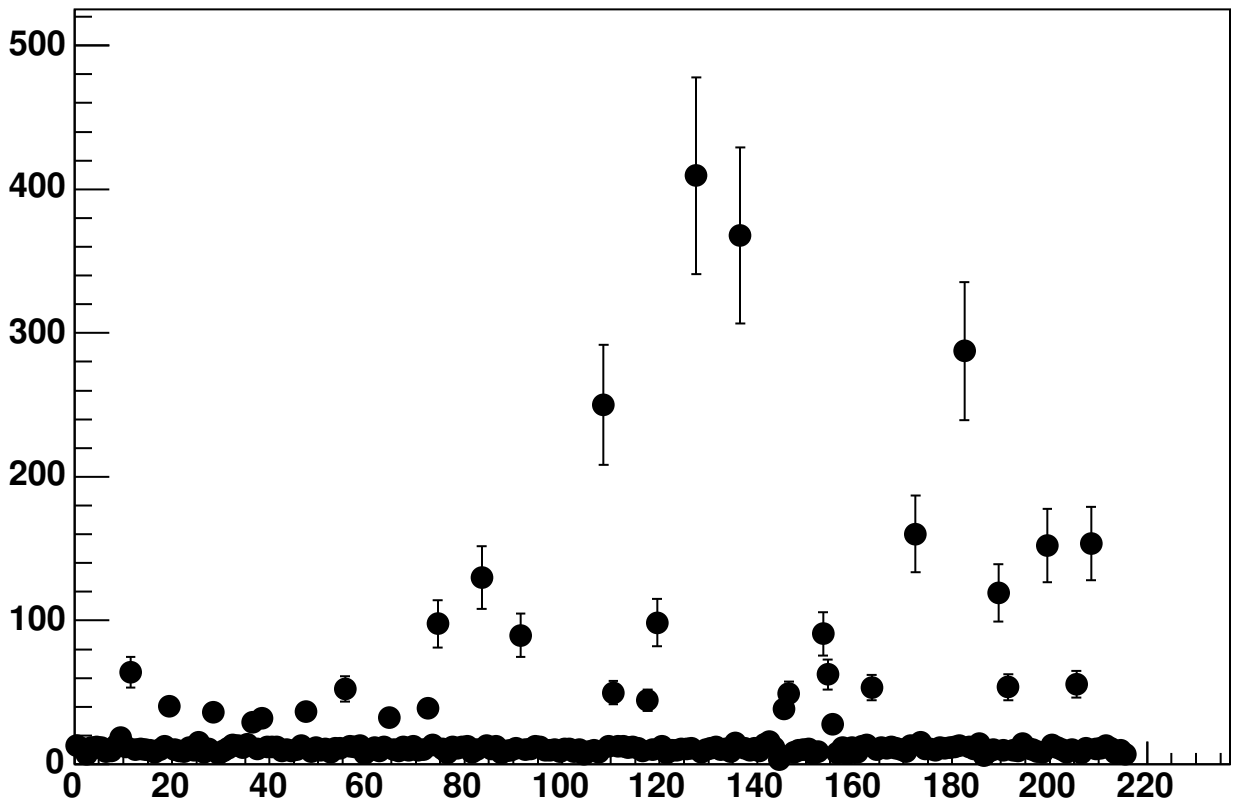
Enable 0, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



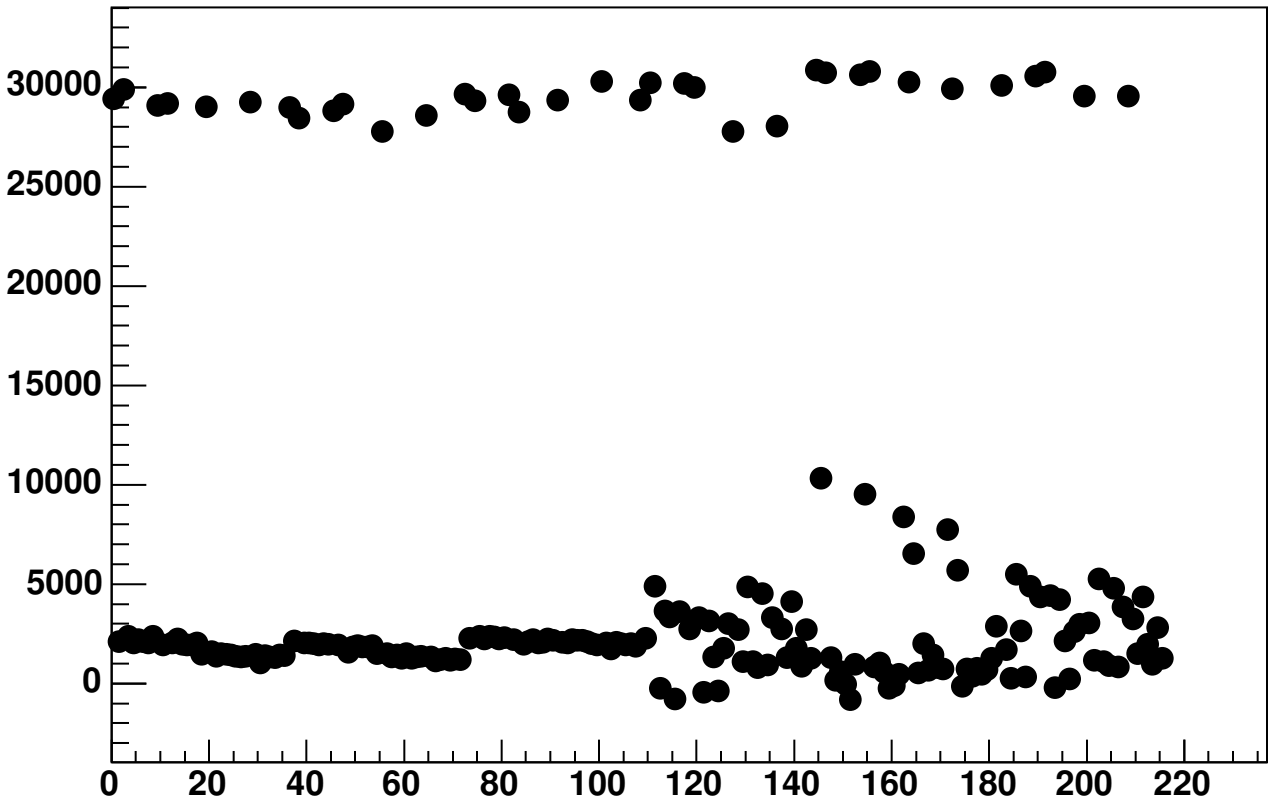
Enable 0, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



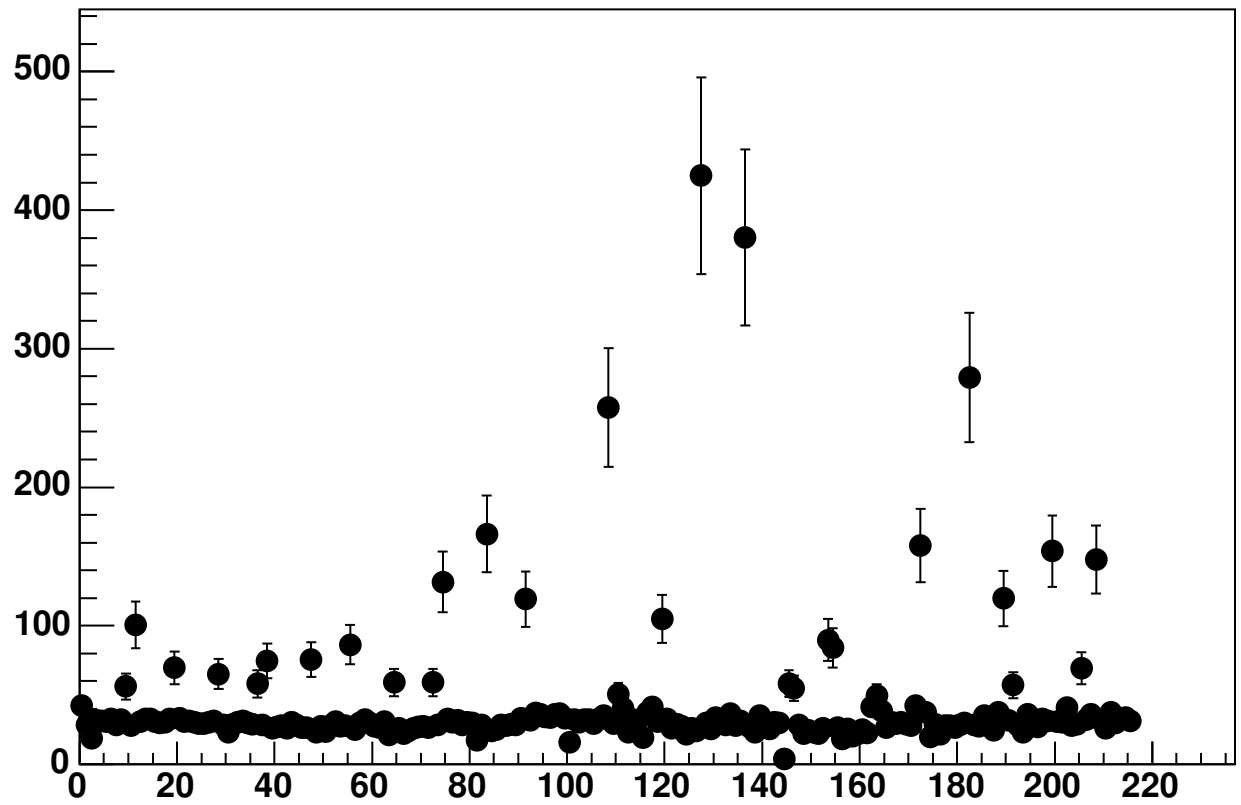
Enable 0, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



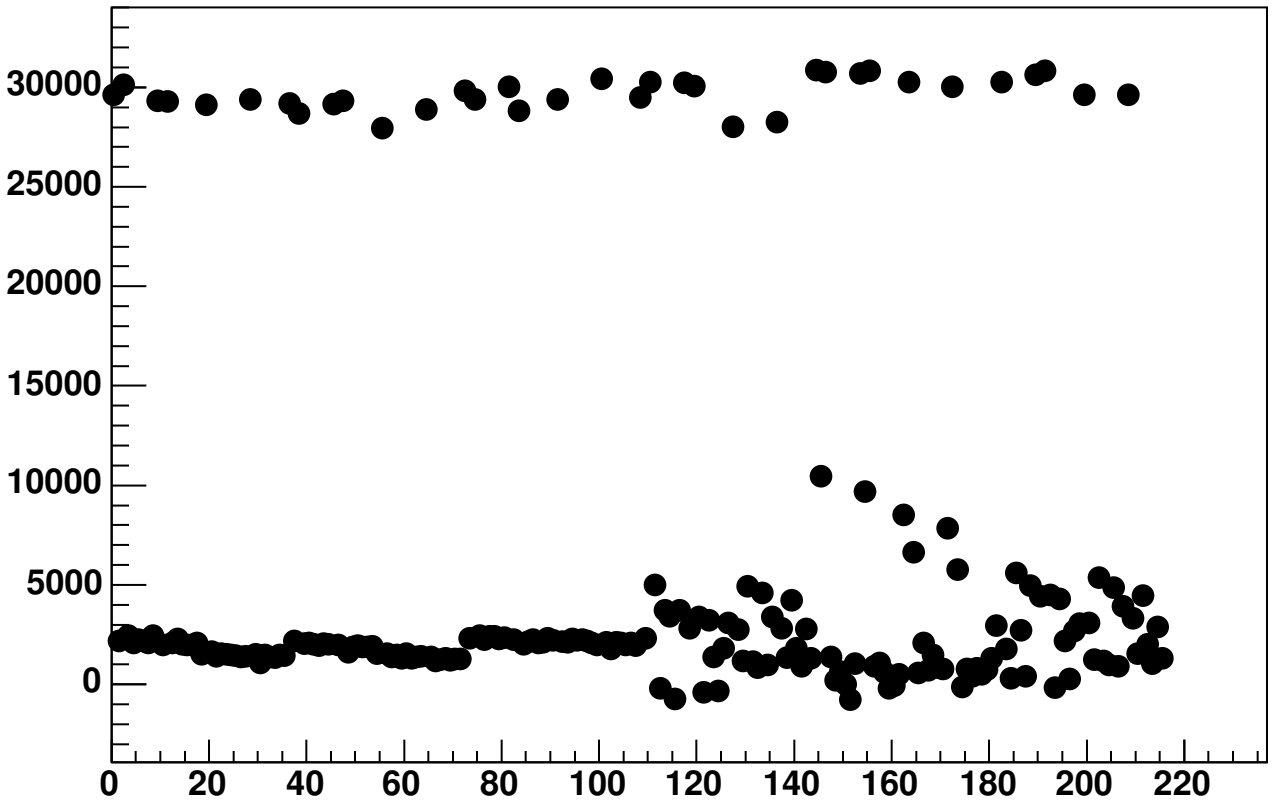
Enable 0, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



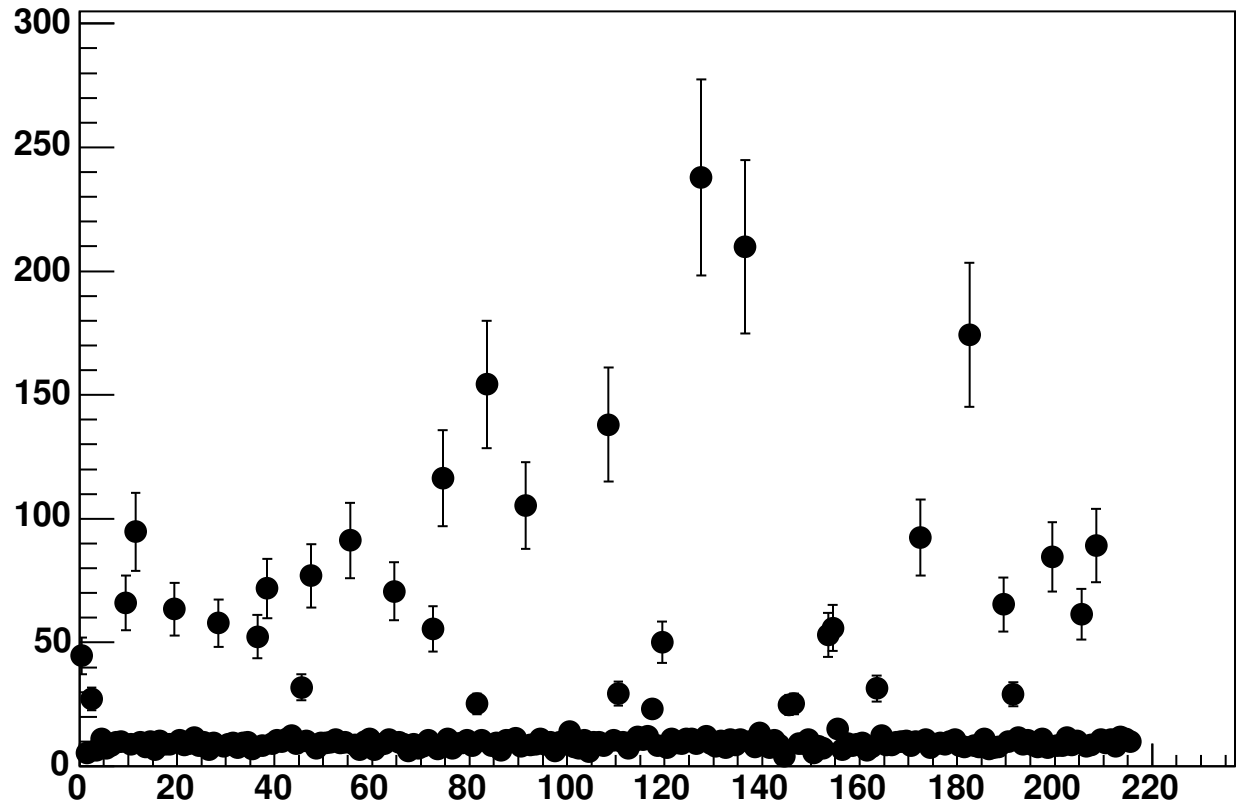
Enable 0, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



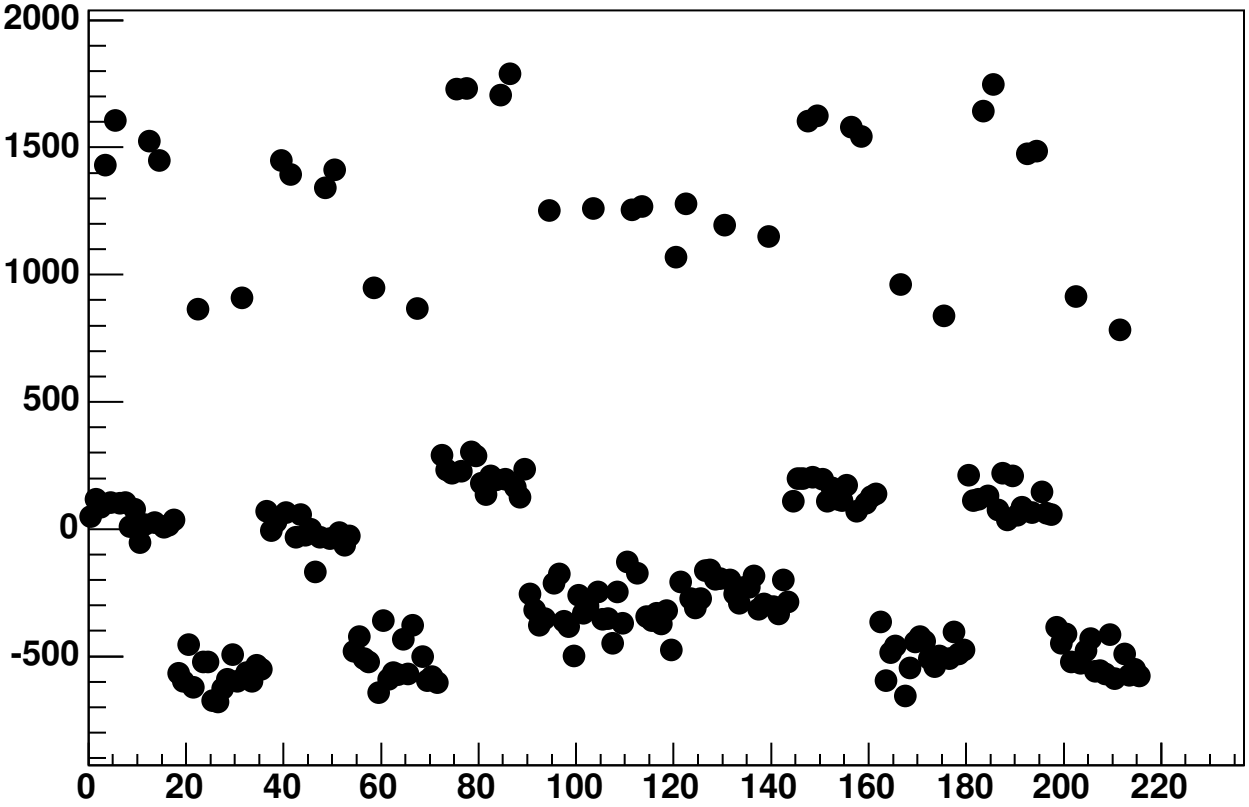
Enable 0, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



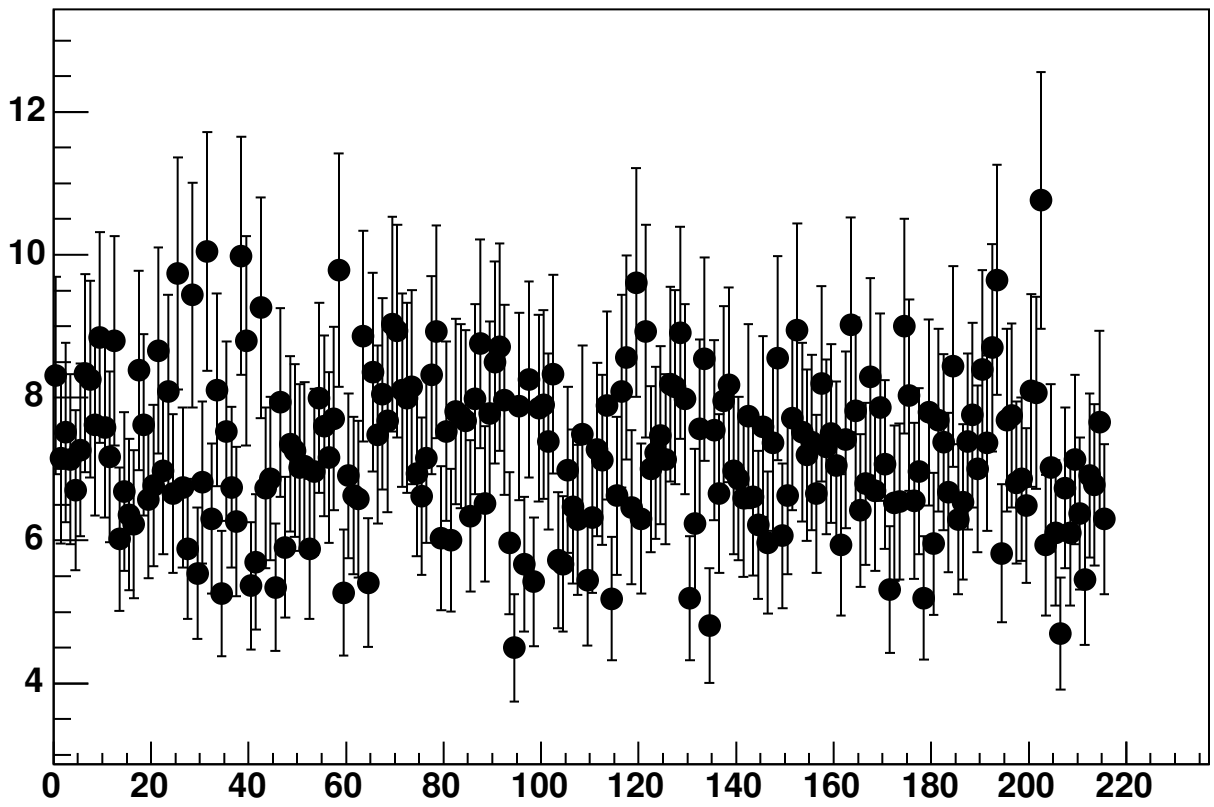
Enable 0, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



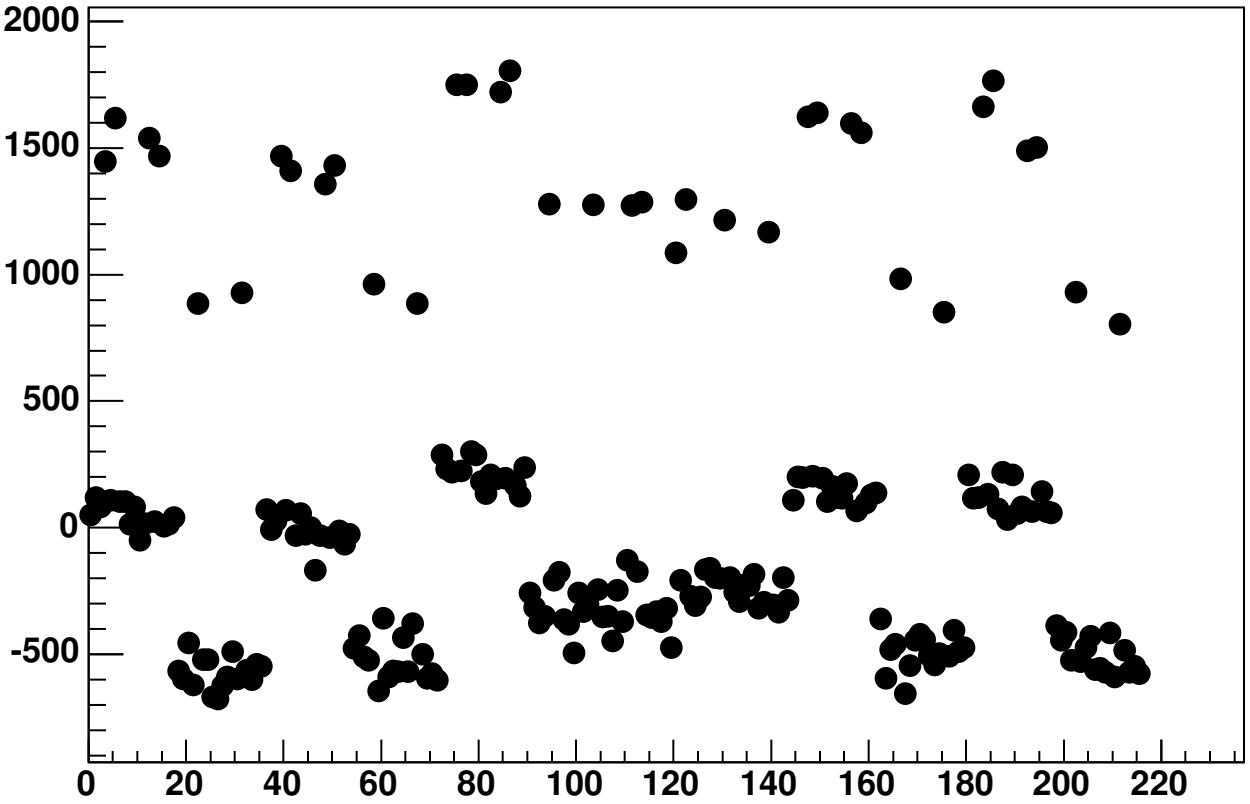
Enable 1, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



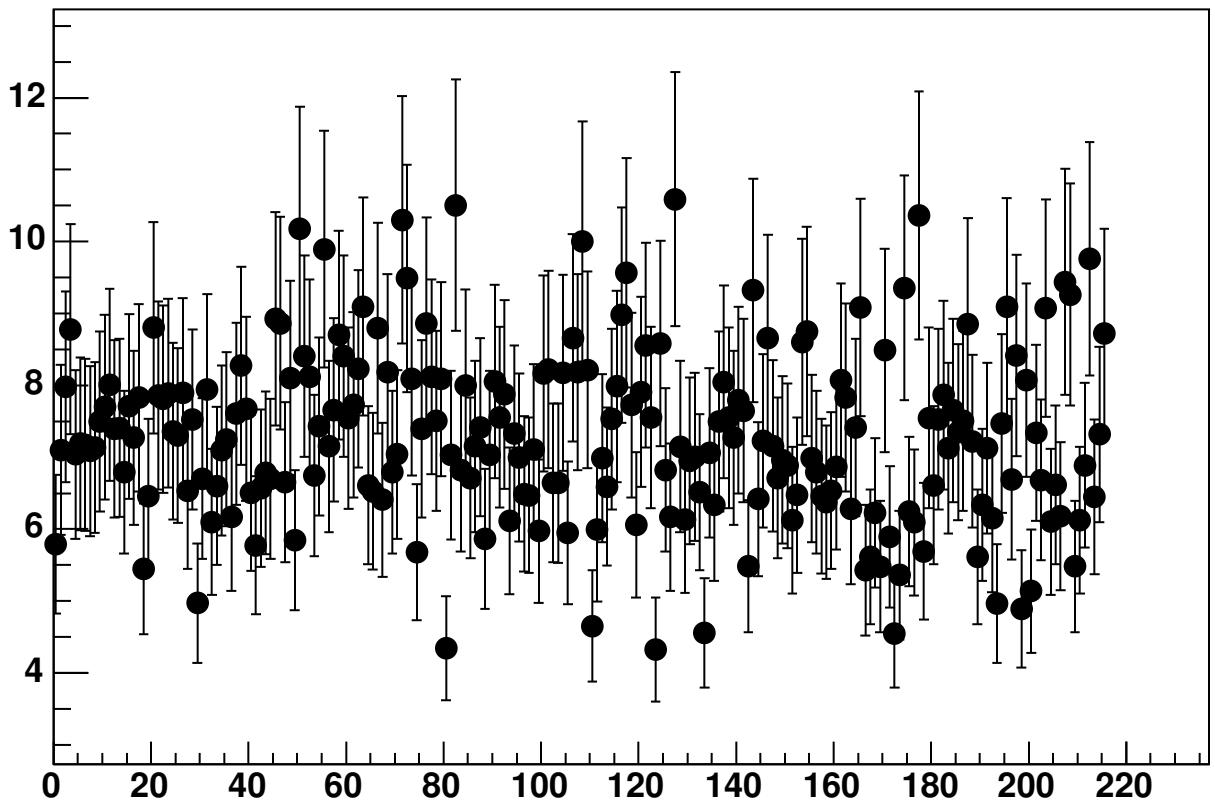
Enable 1, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



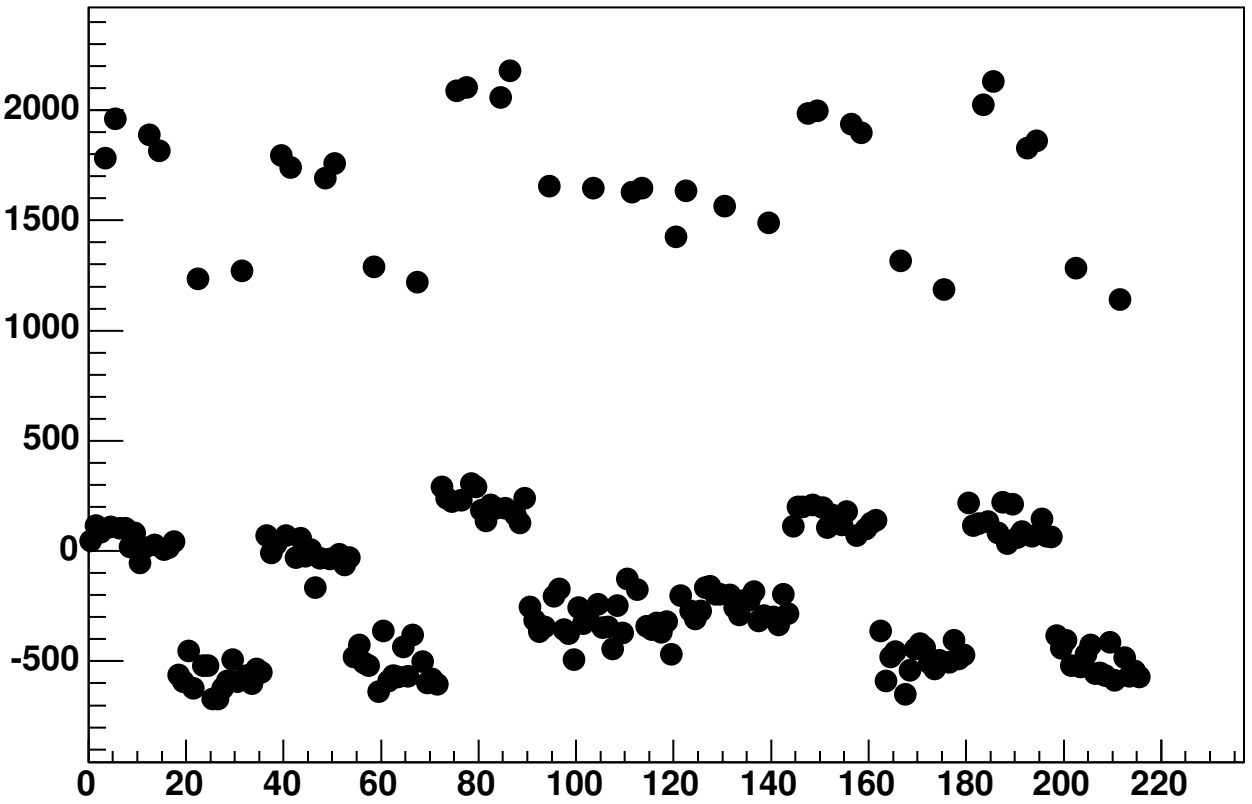
Enable 1, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



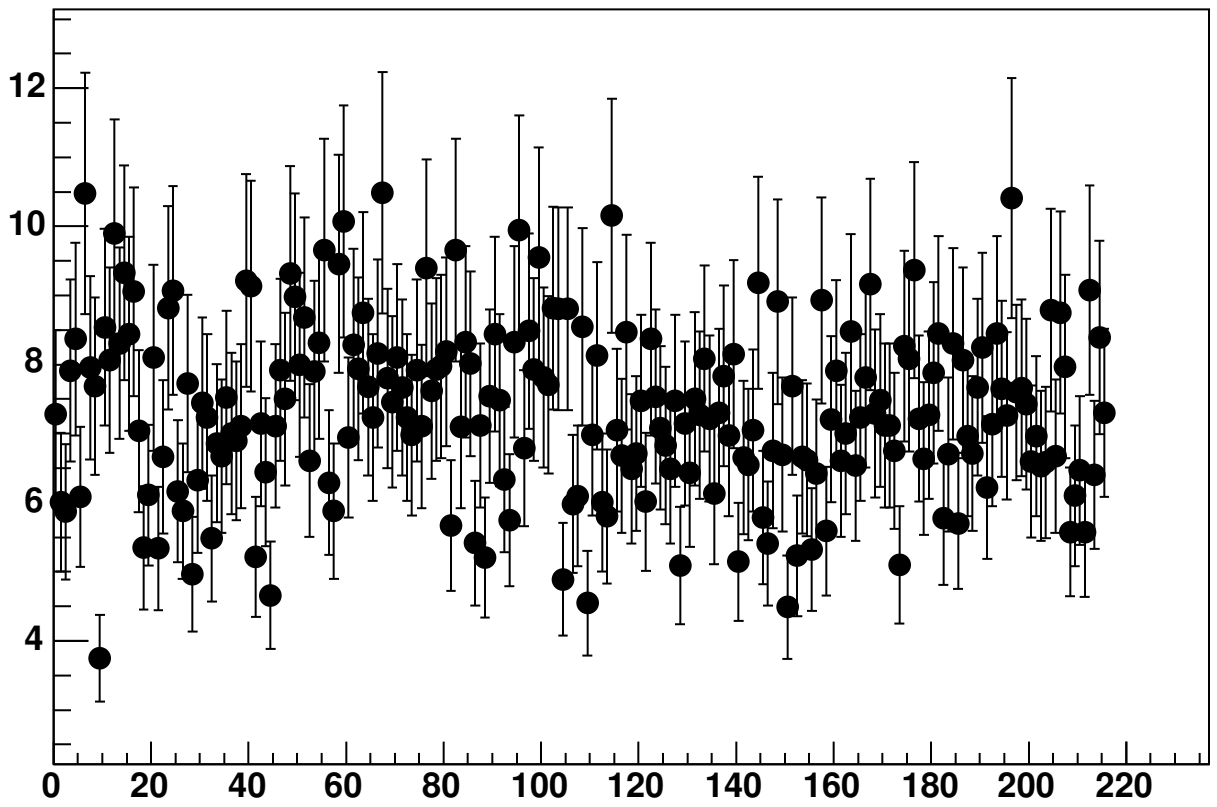
Enable 1, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



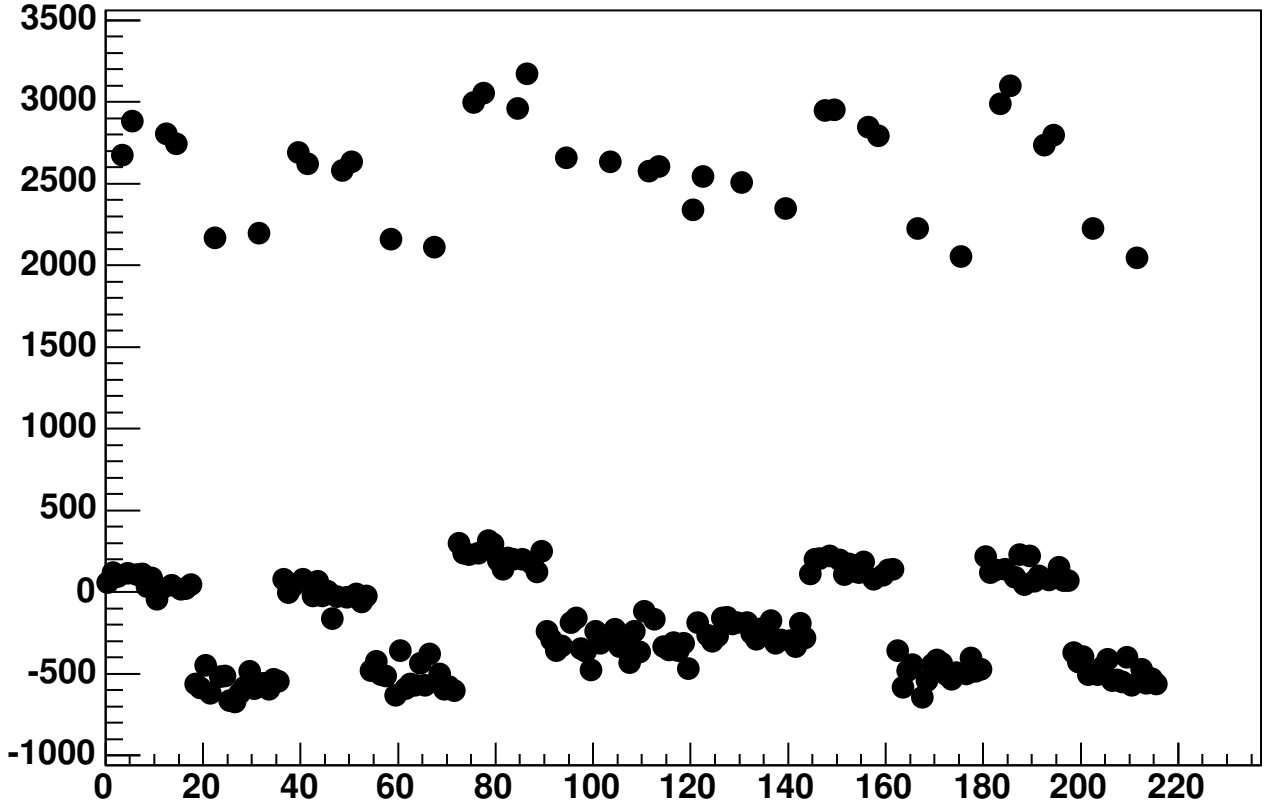
Enable 1, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



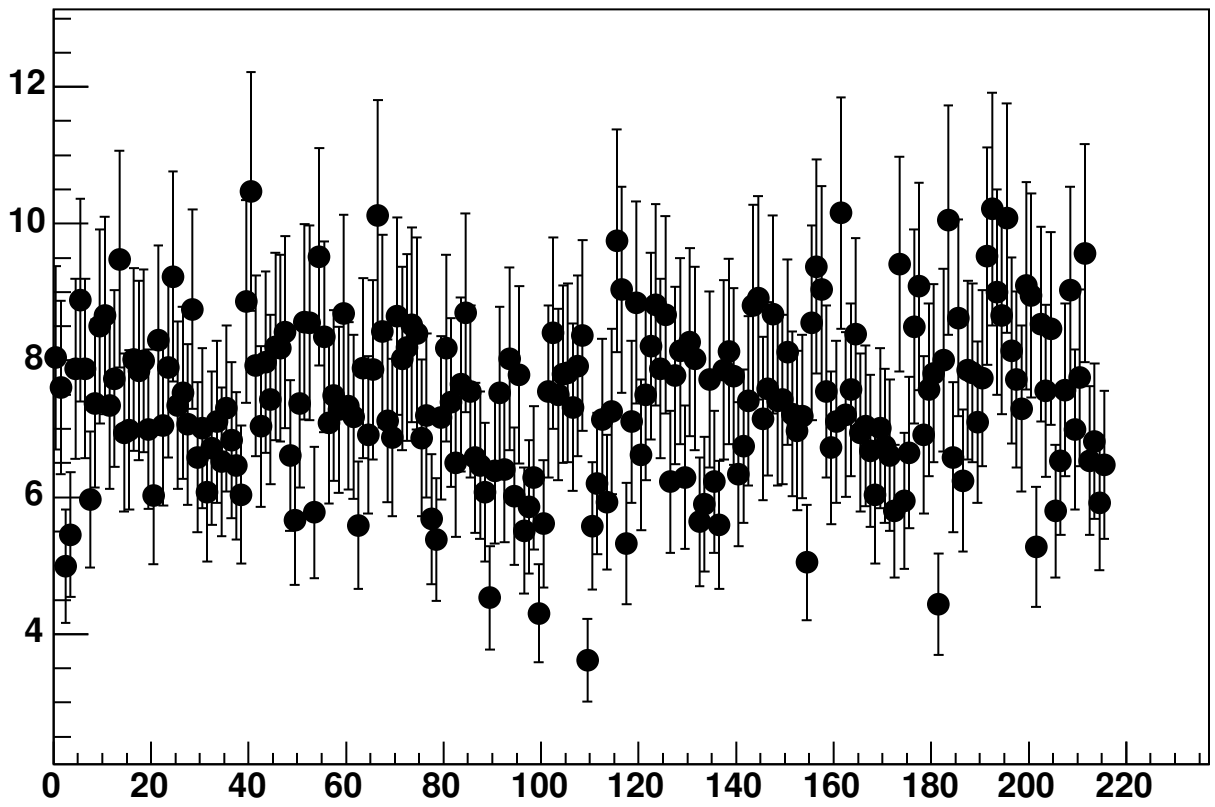
Enable 1, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



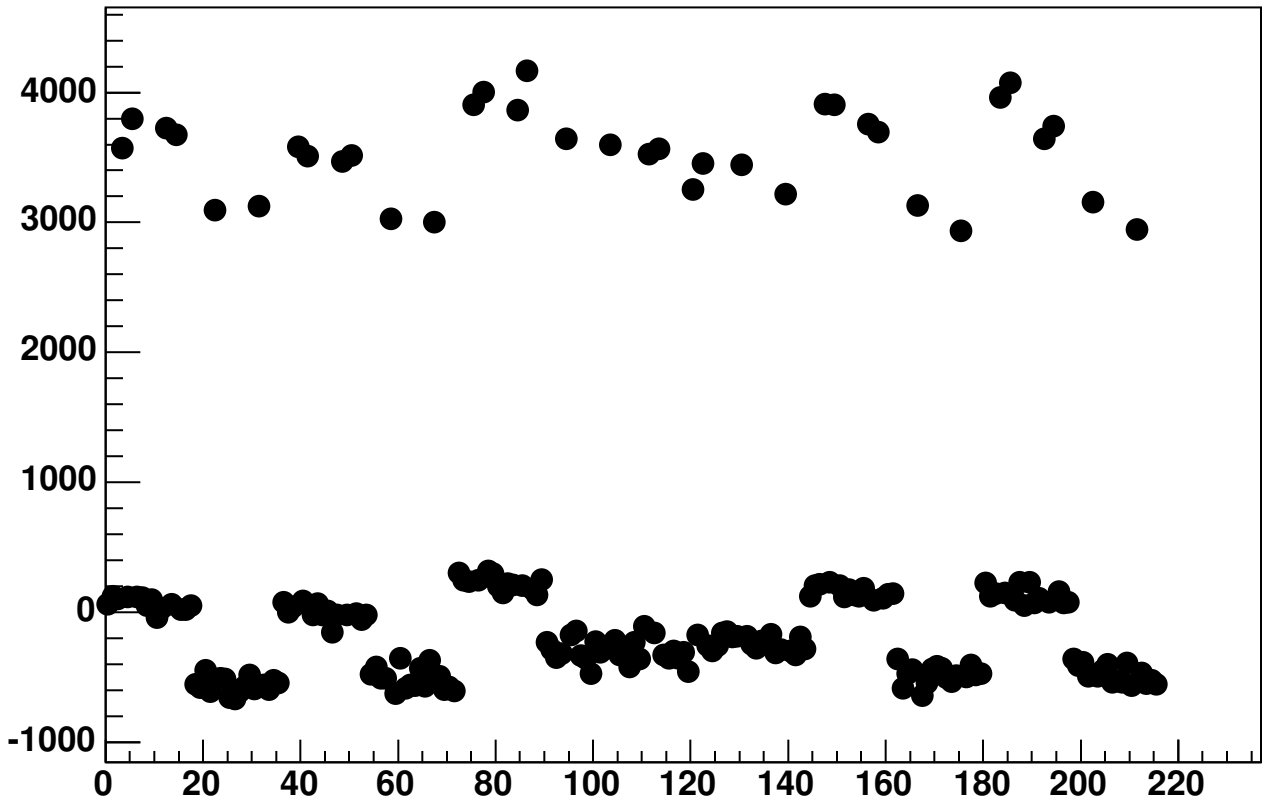
Enable 1, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



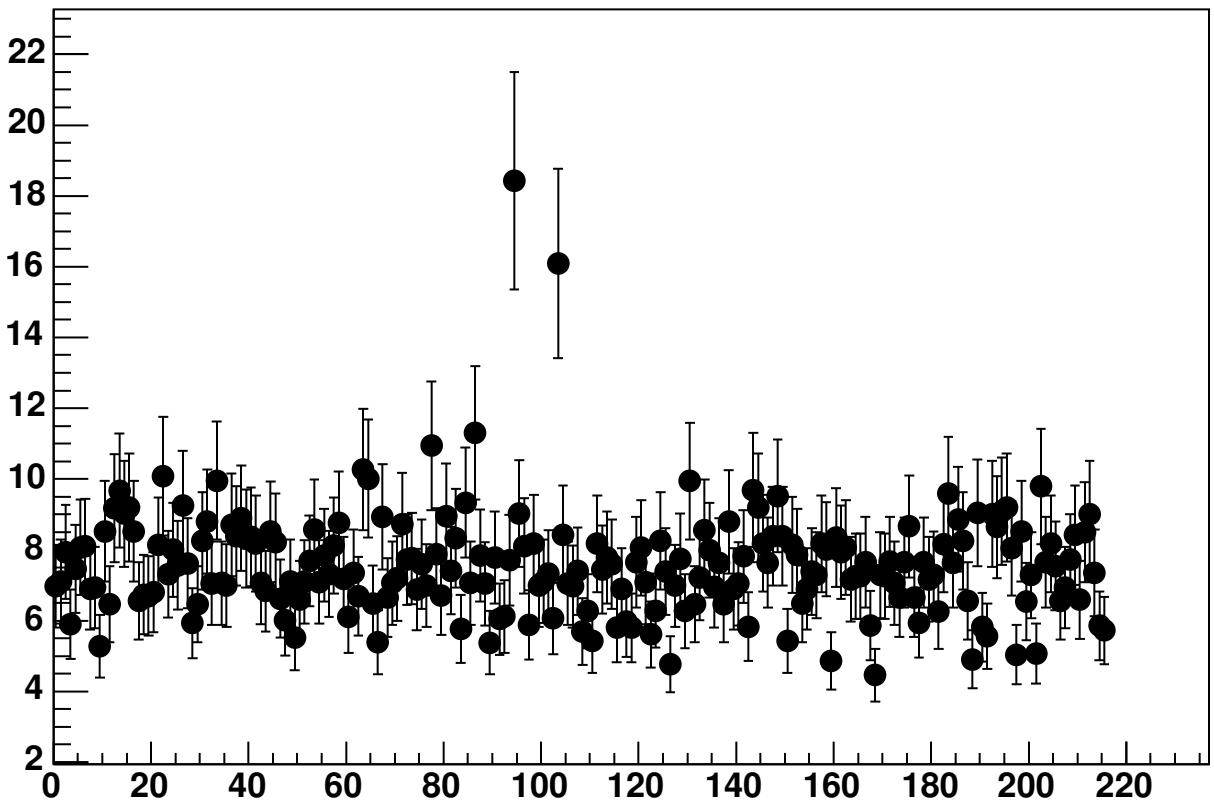
Enable 1, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



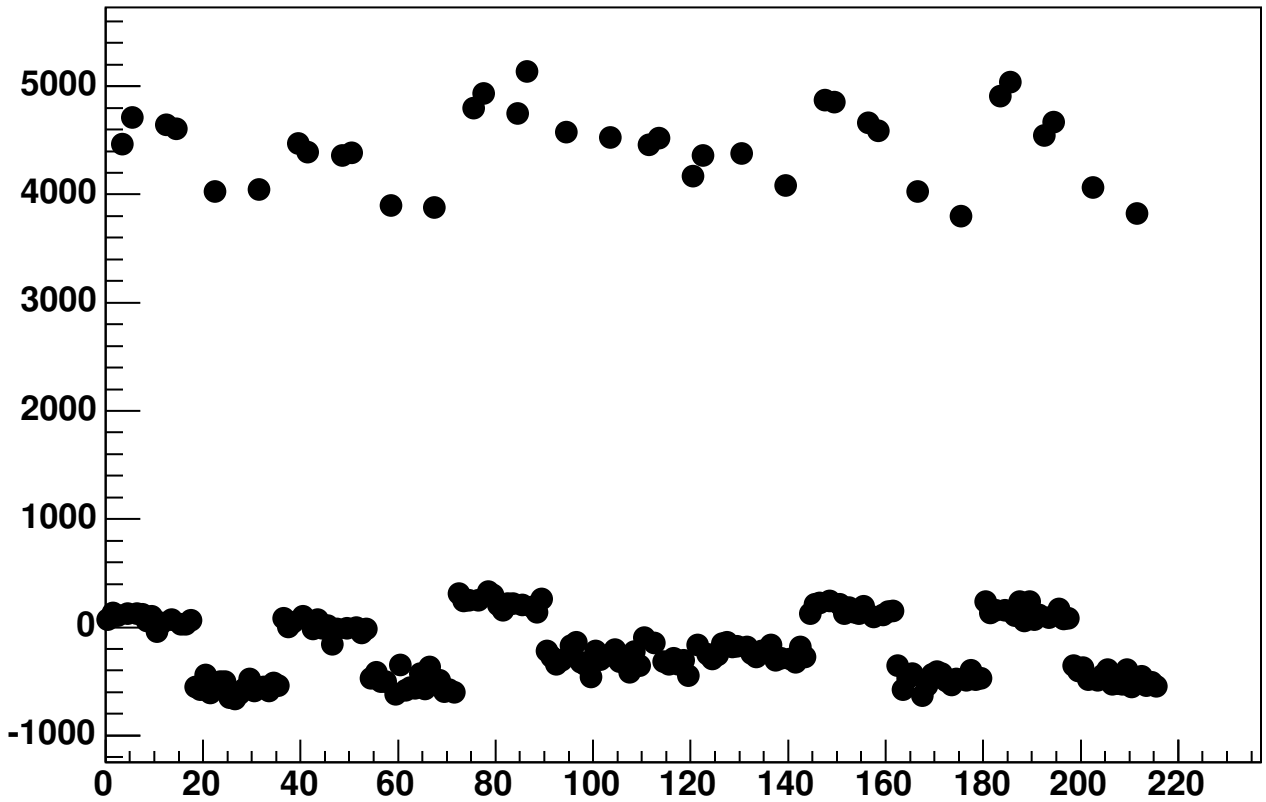
Enable 1, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



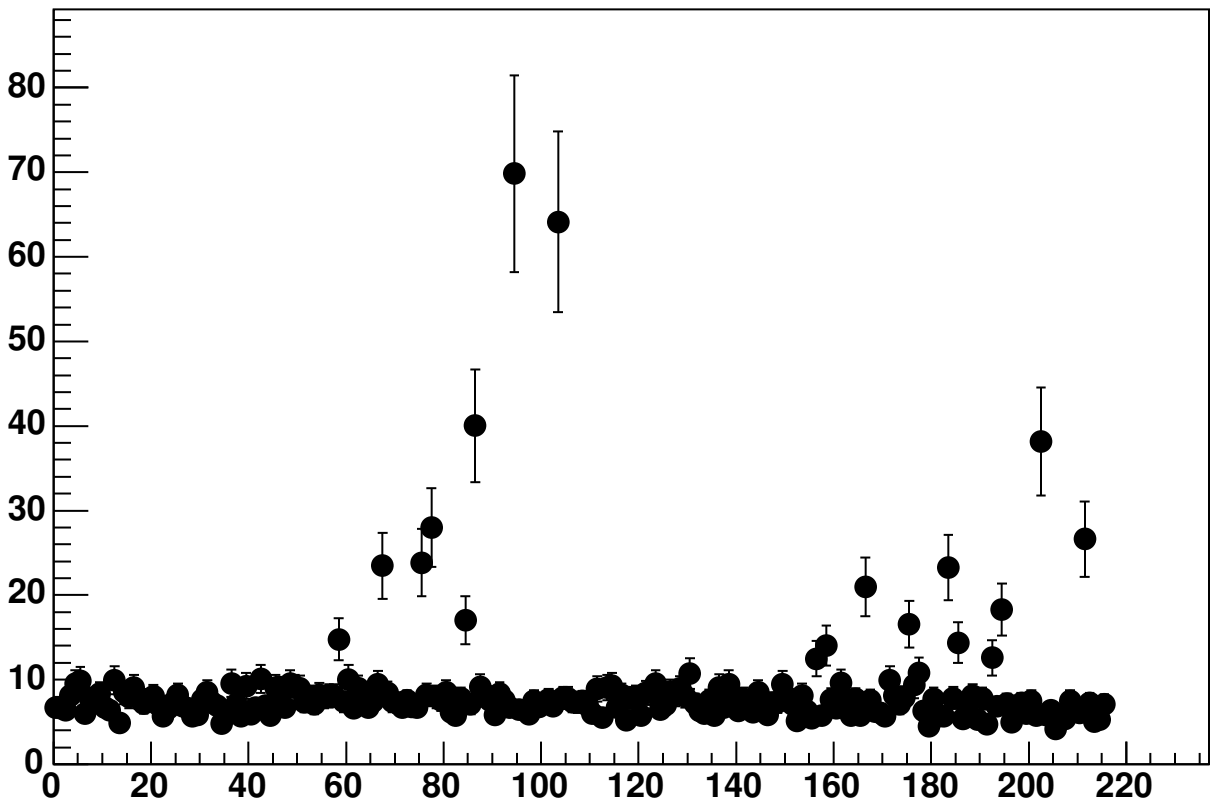
Enable 1, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



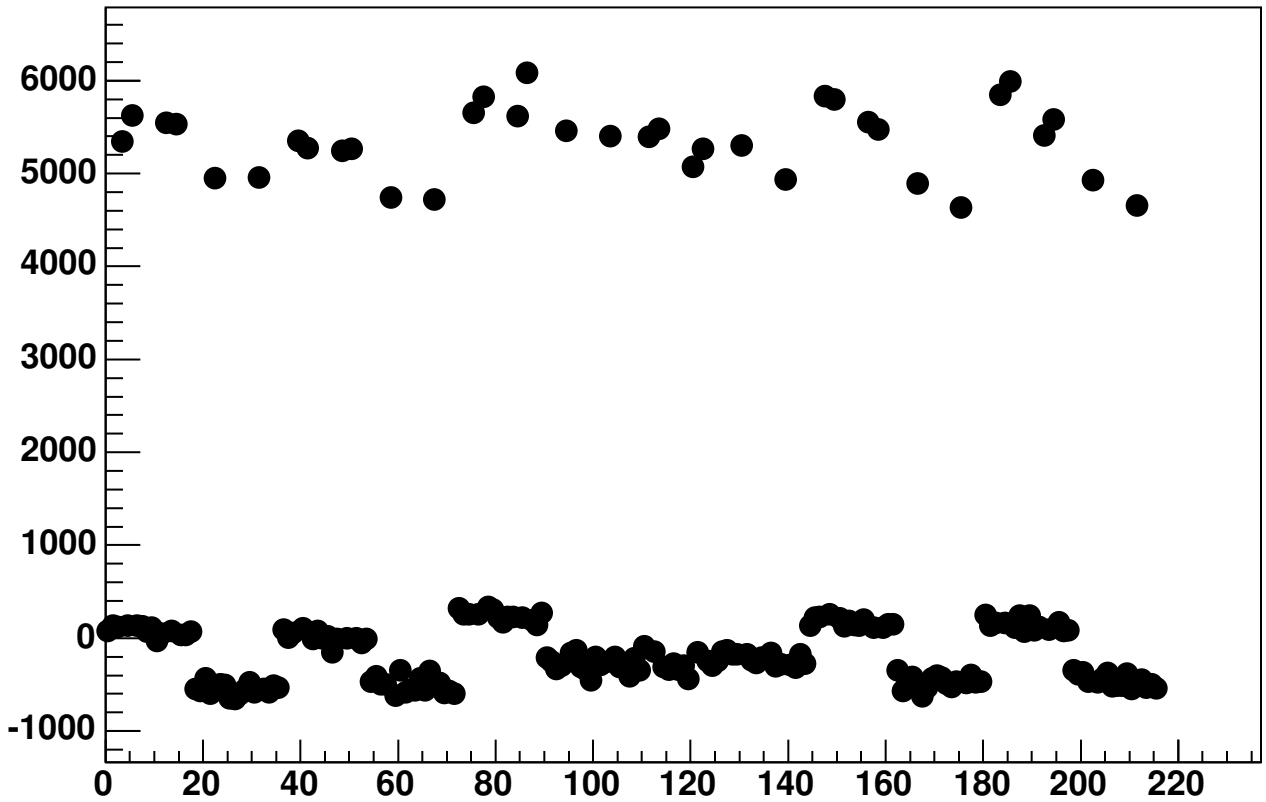
Enable 1, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



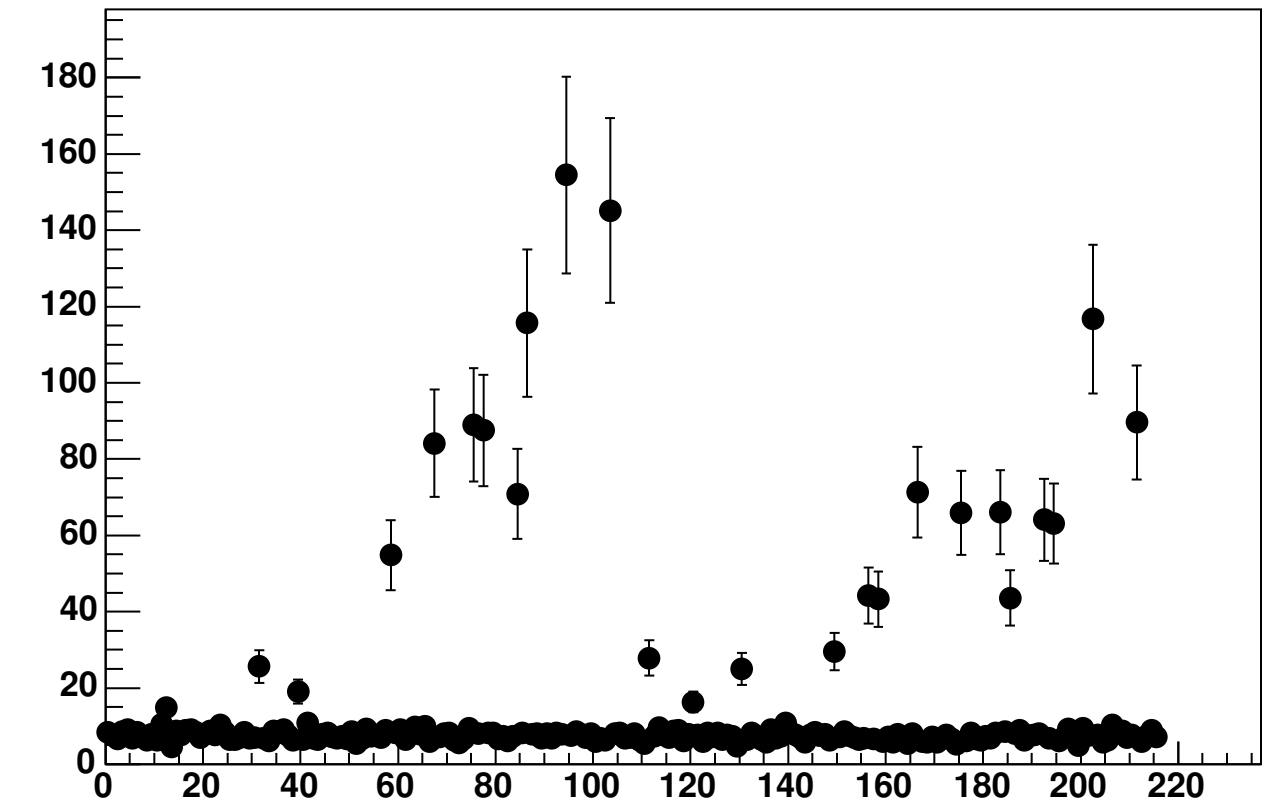
Enable 1, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



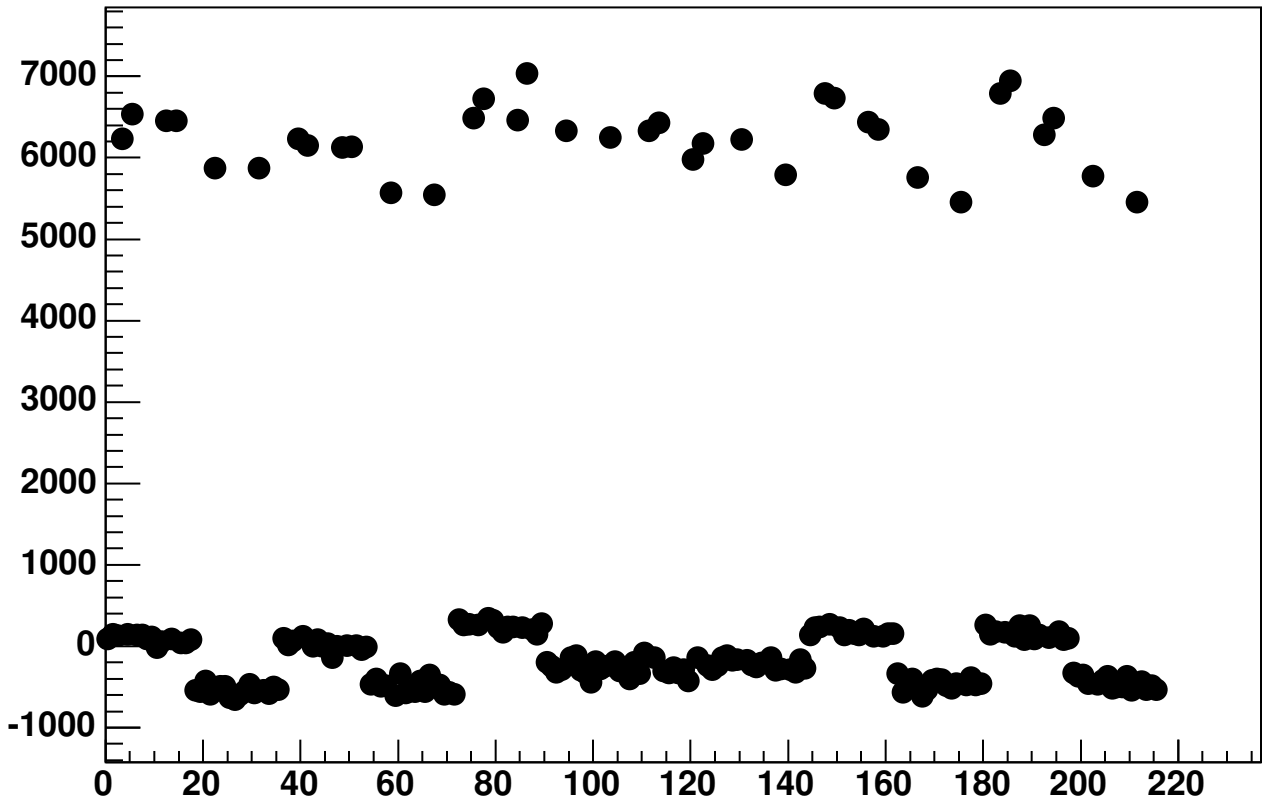
Enable 1, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



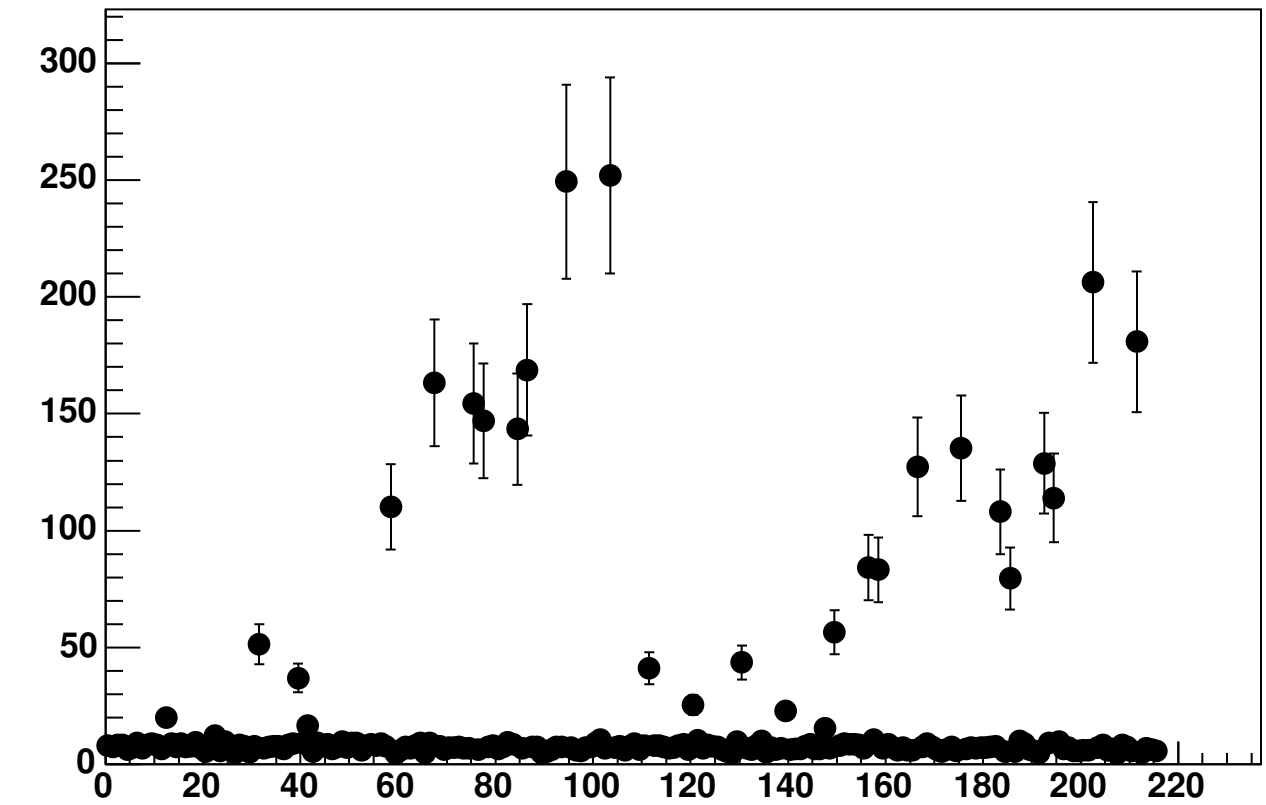
Enable 1, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



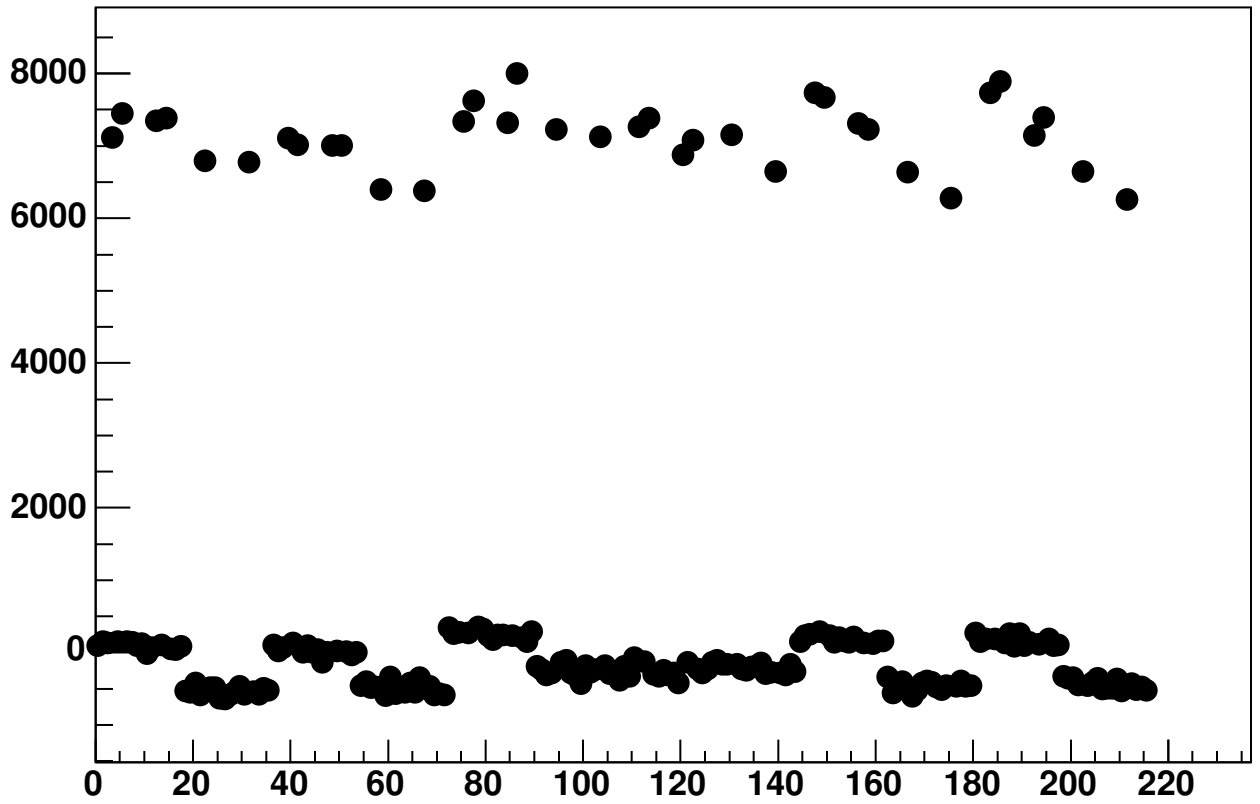
Enable 1, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



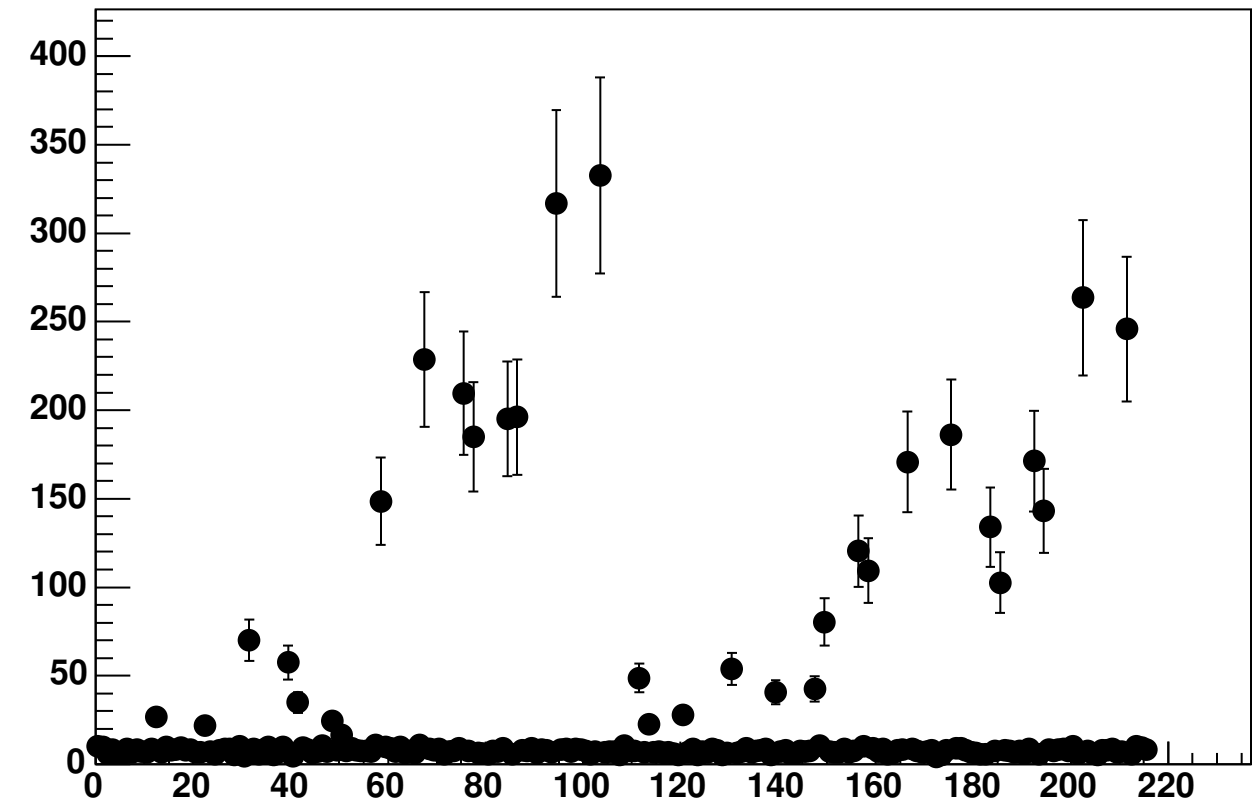
Enable 1, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



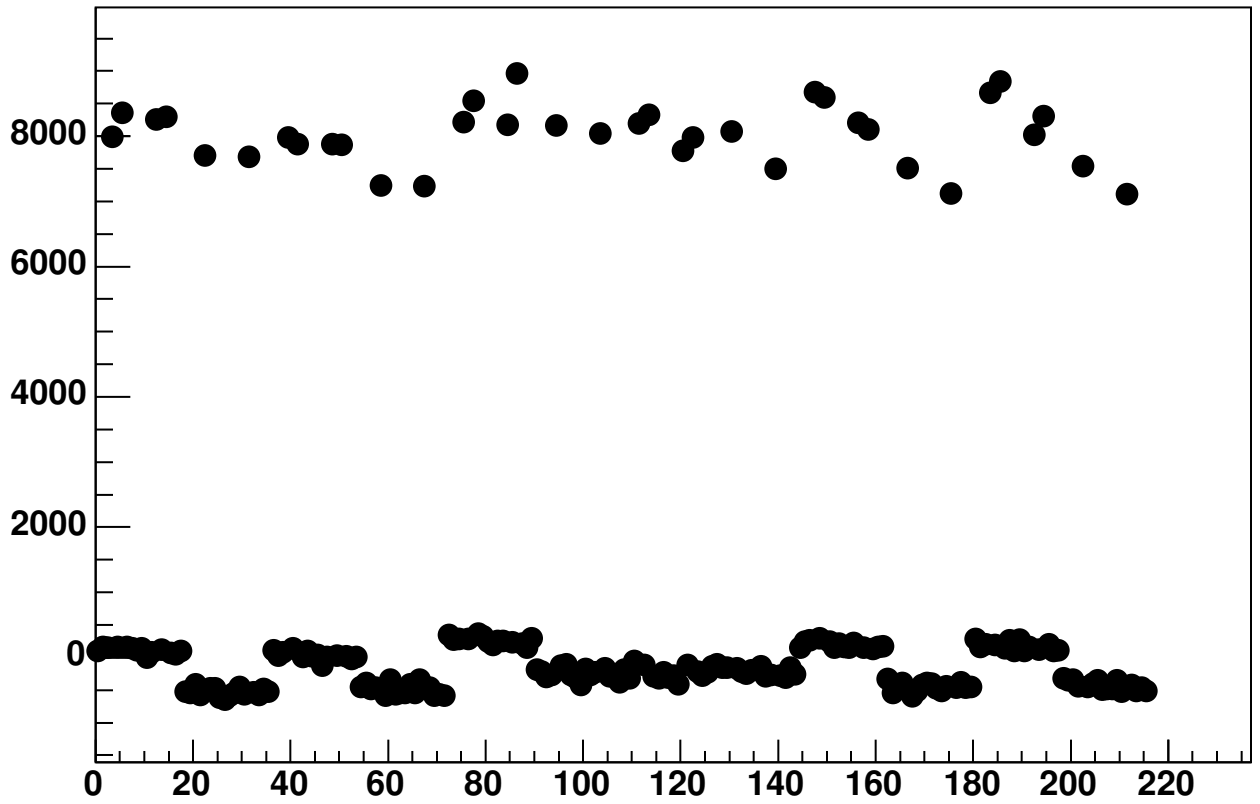
Enable 1, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



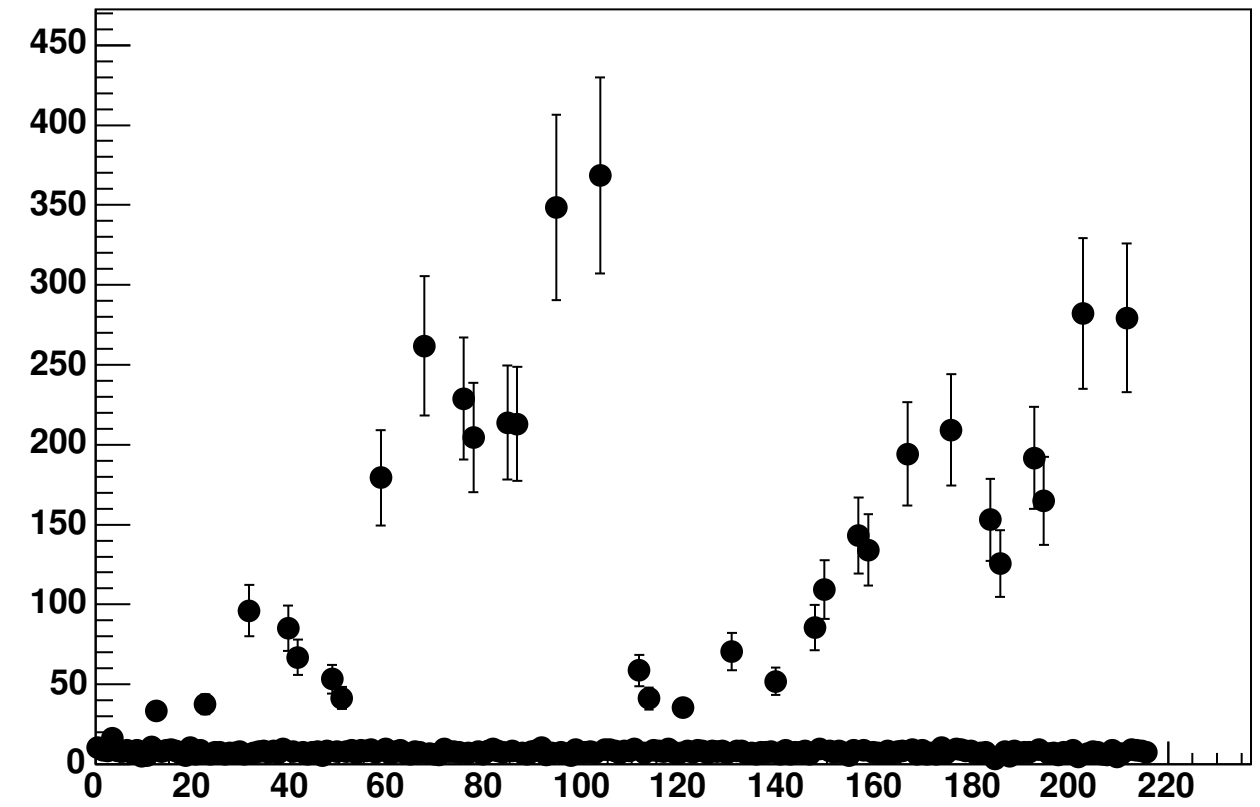
Enable 1, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



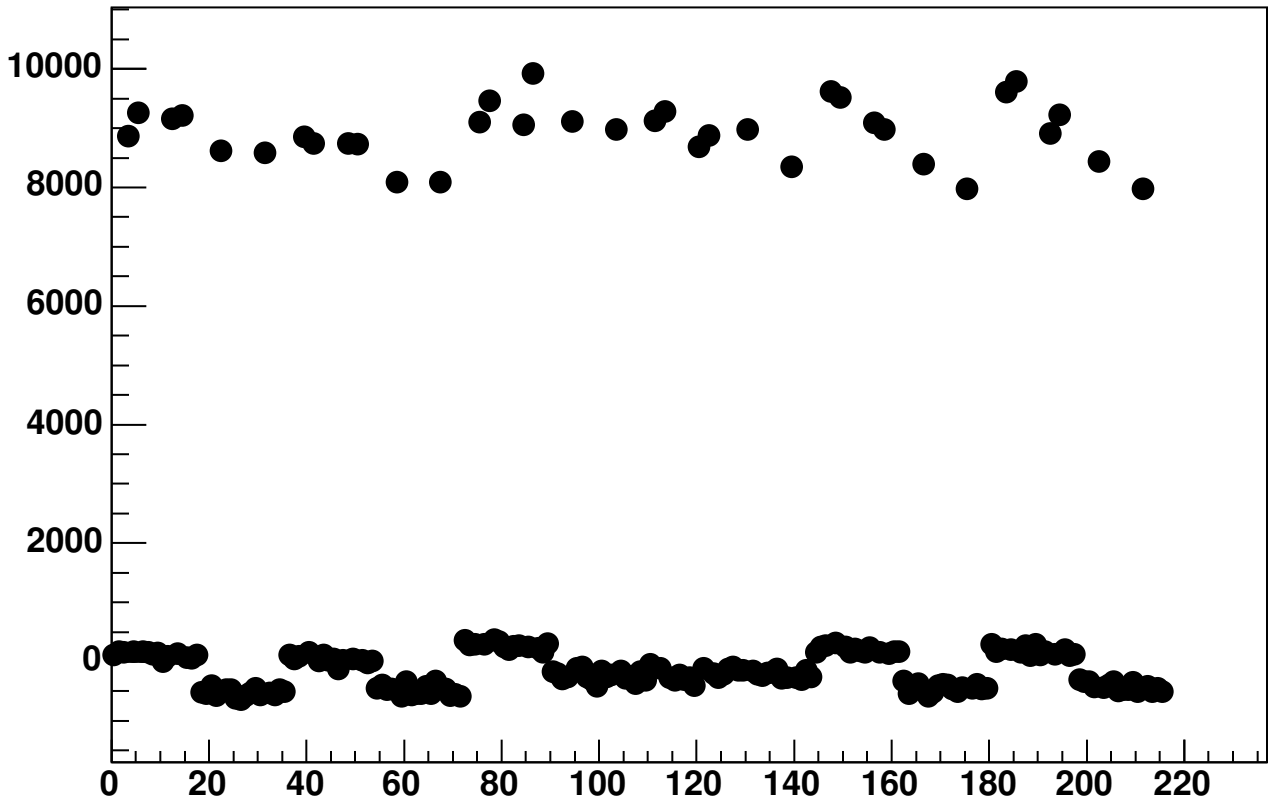
Enable 1, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



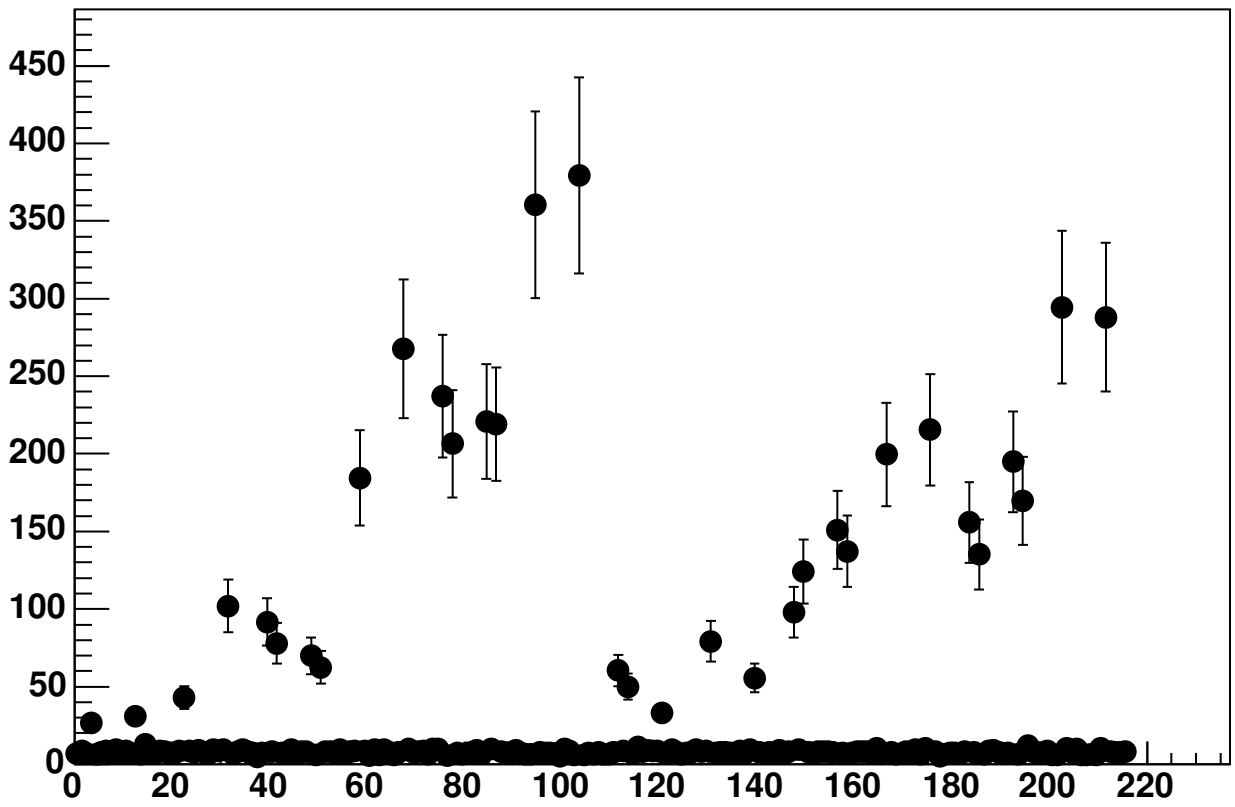
Enable 1, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



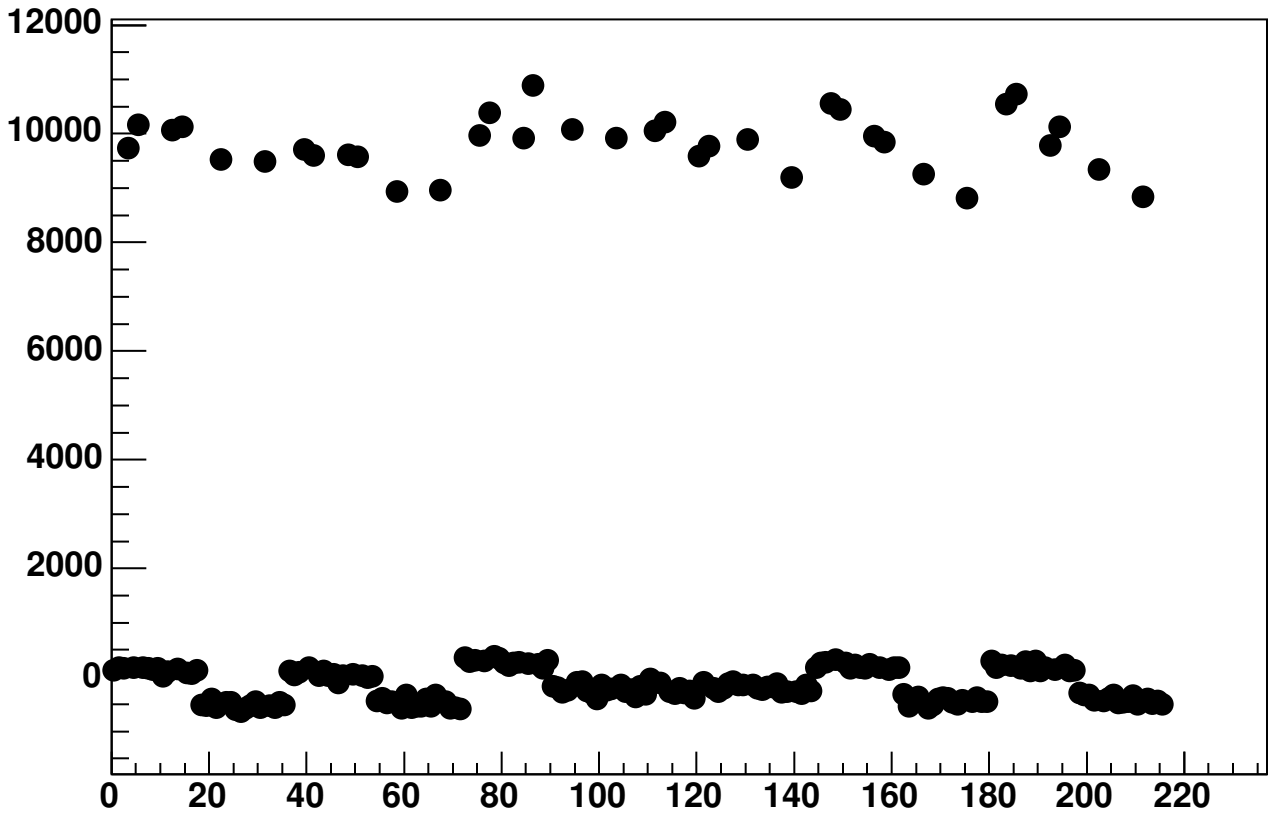
Enable 1, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



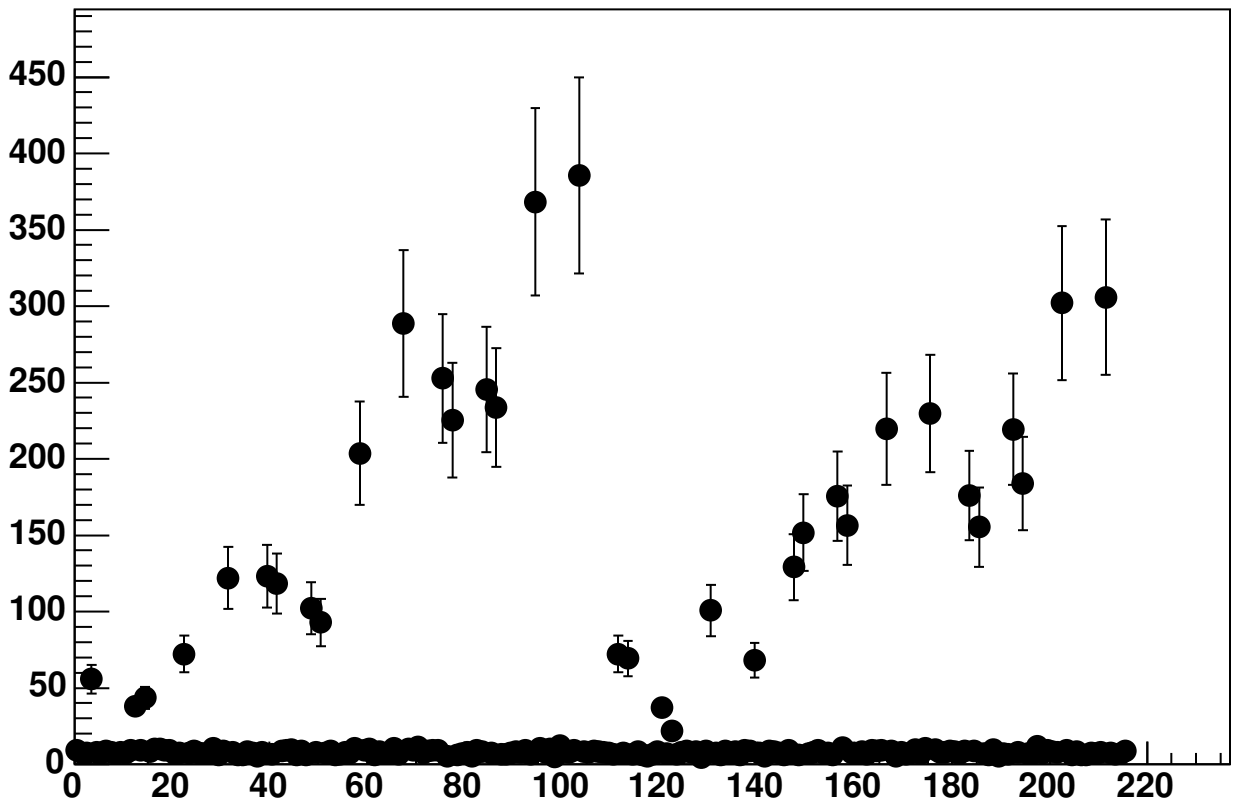
Enable 1, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



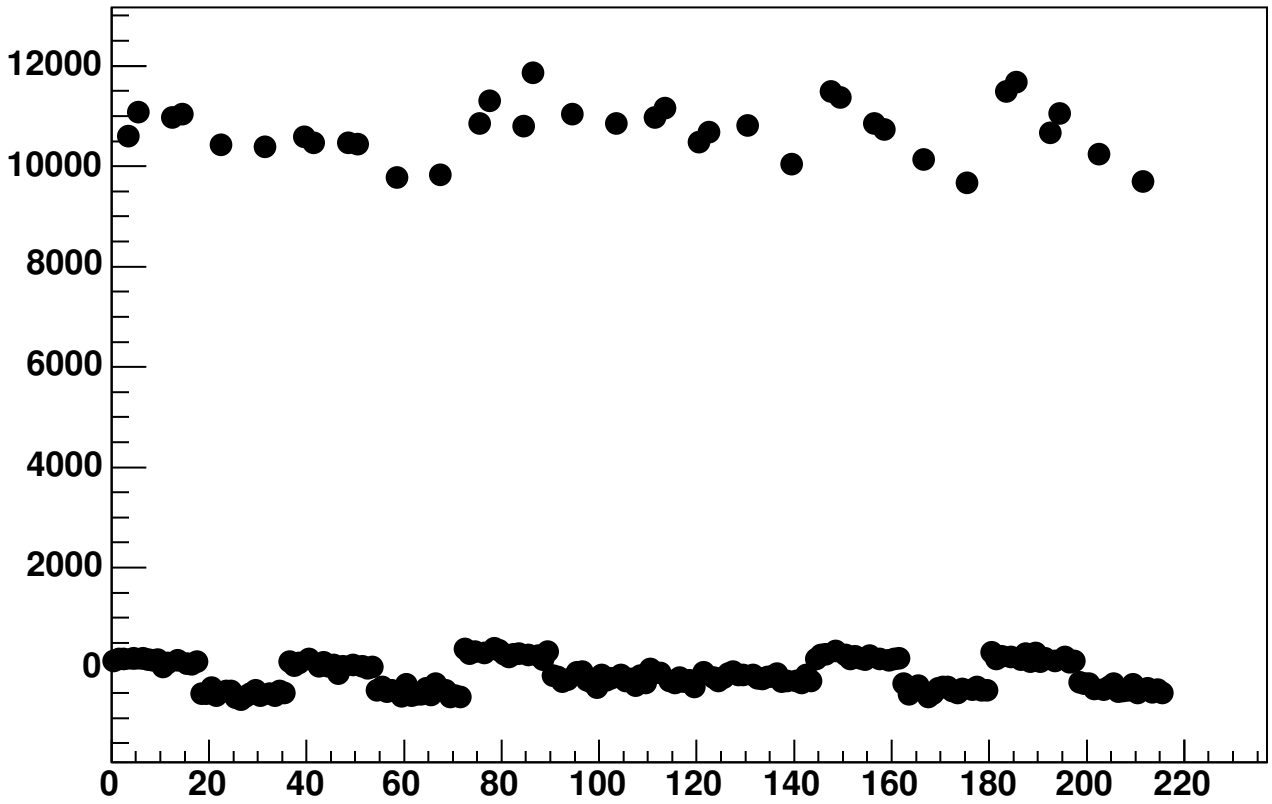
Enable 1, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



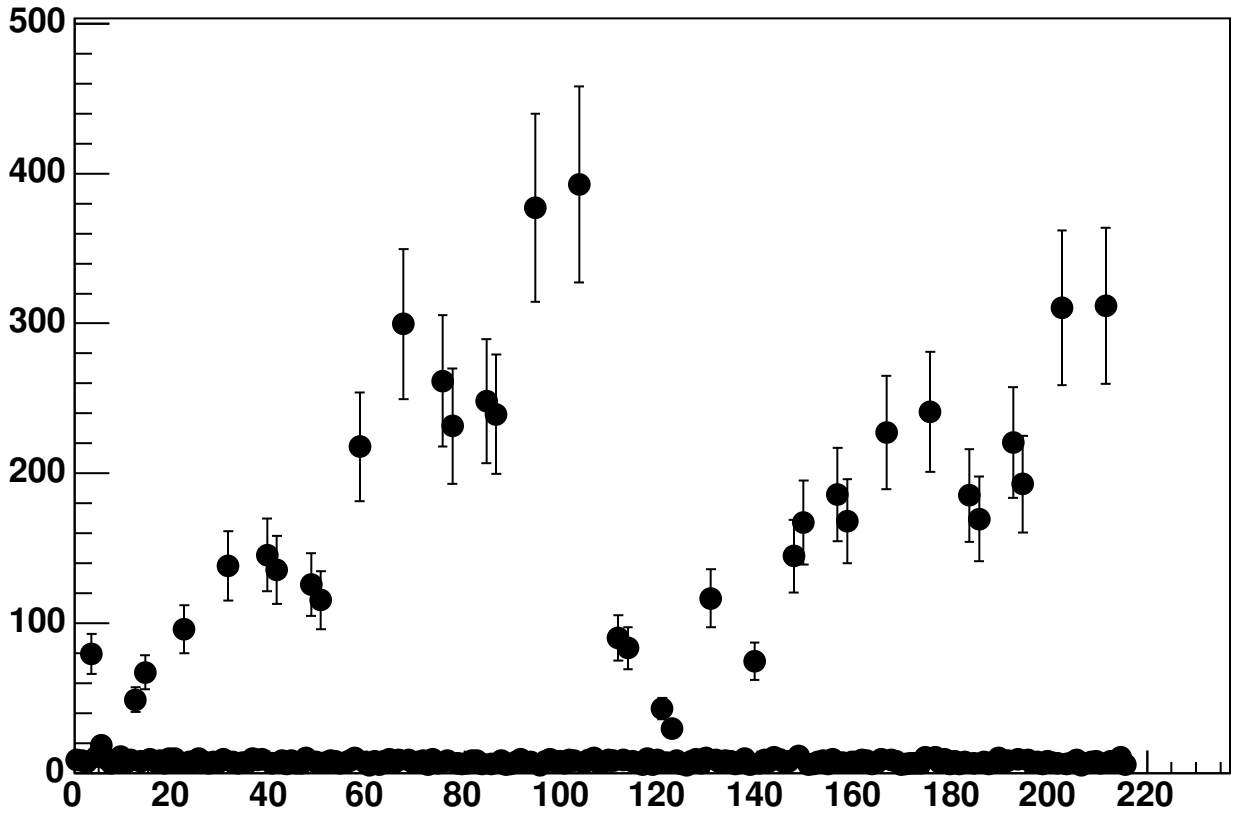
Enable 1, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



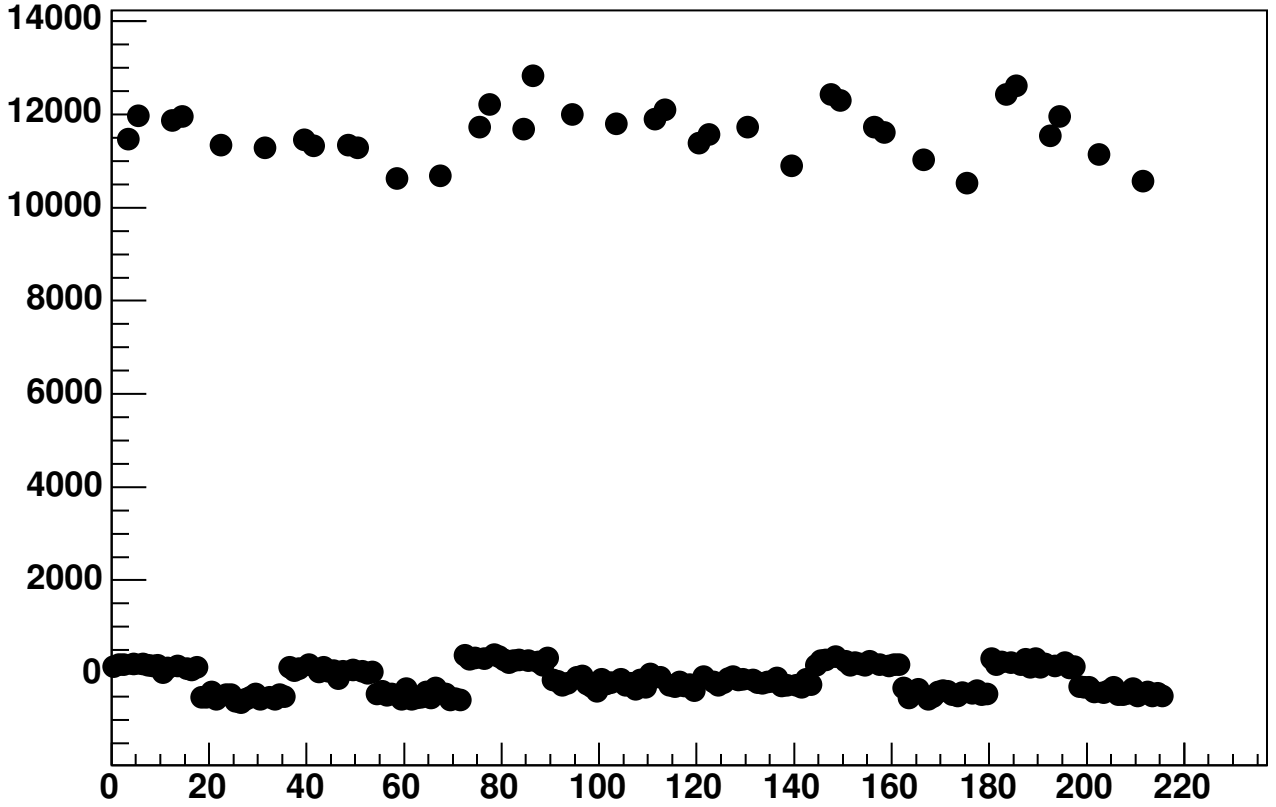
Enable 1, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



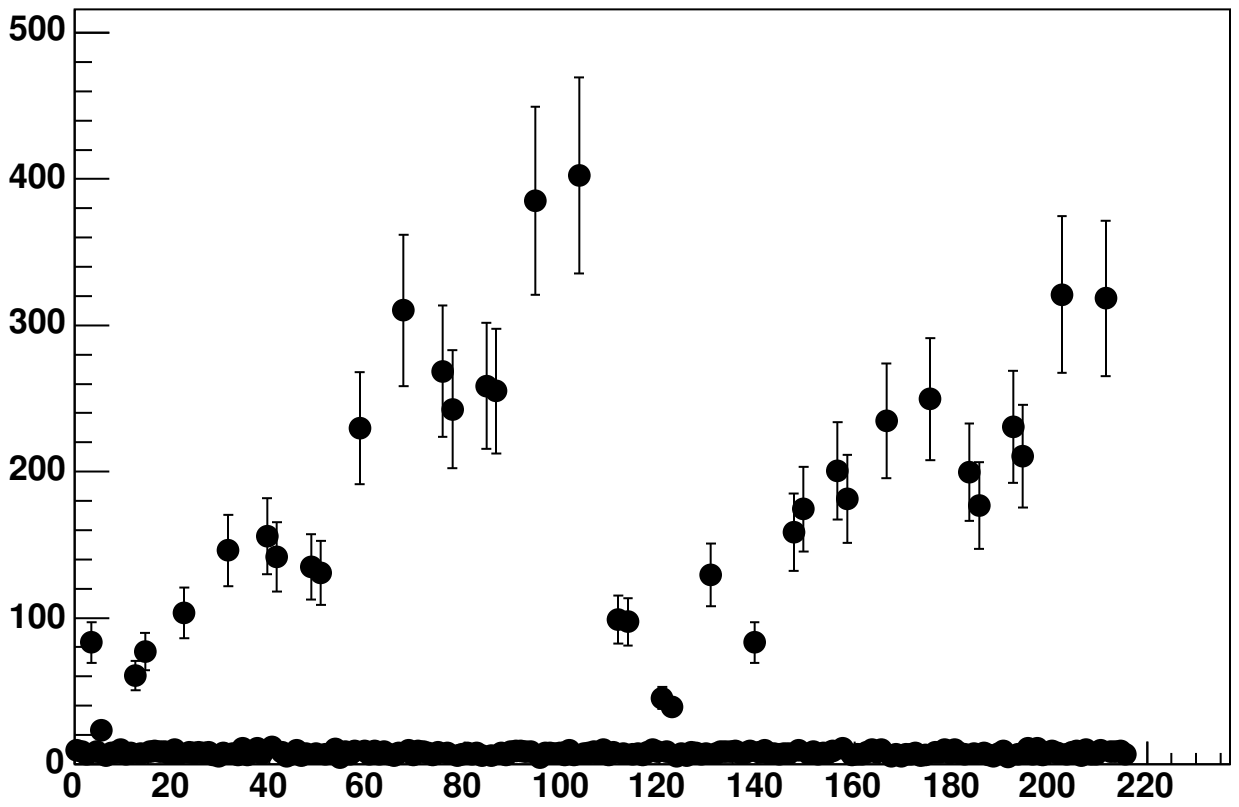
Enable 1, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



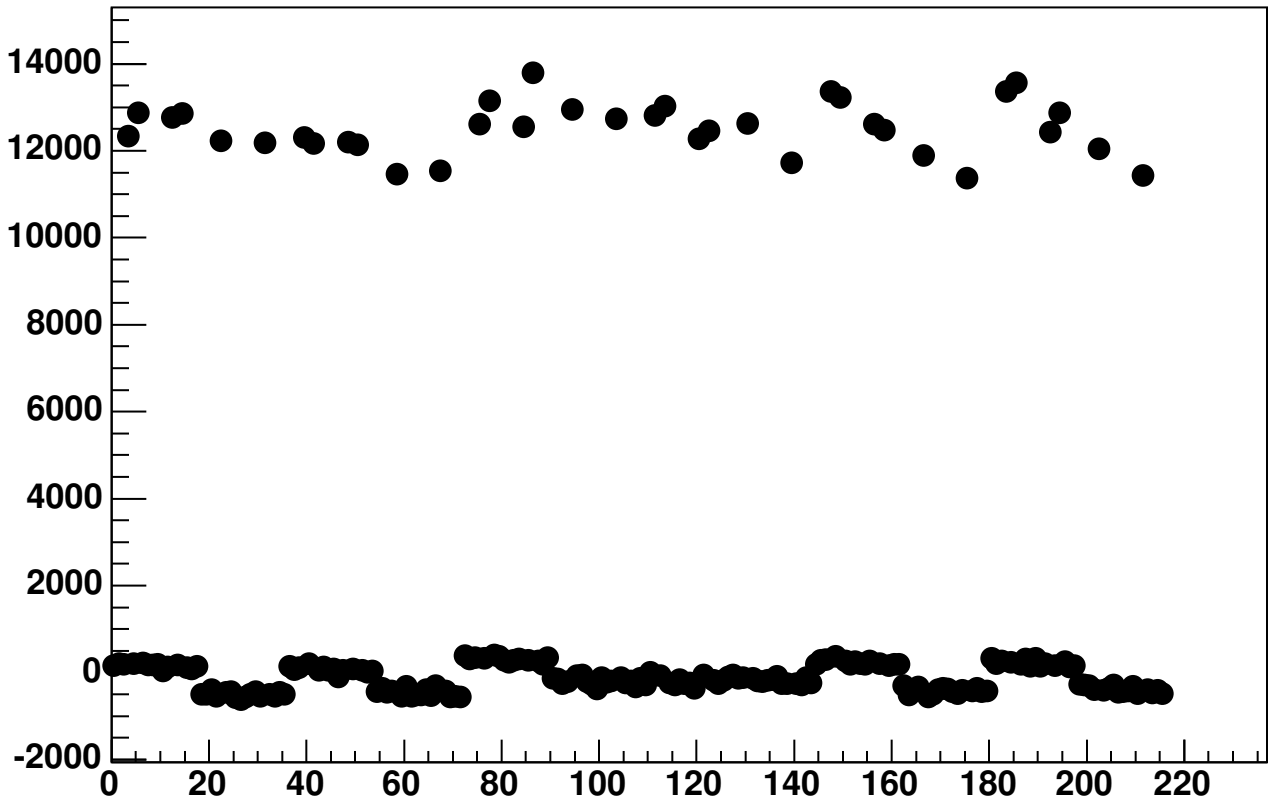
Enable 1, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



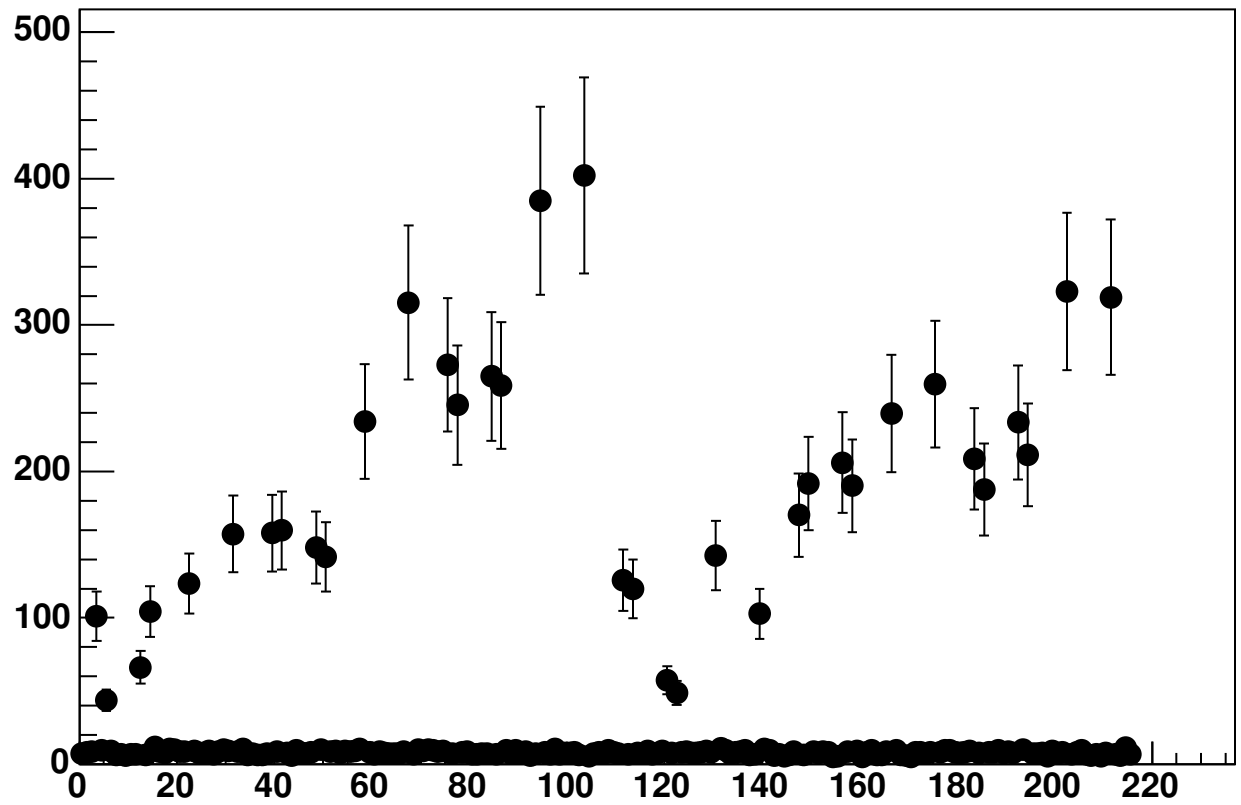
Enable 1, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



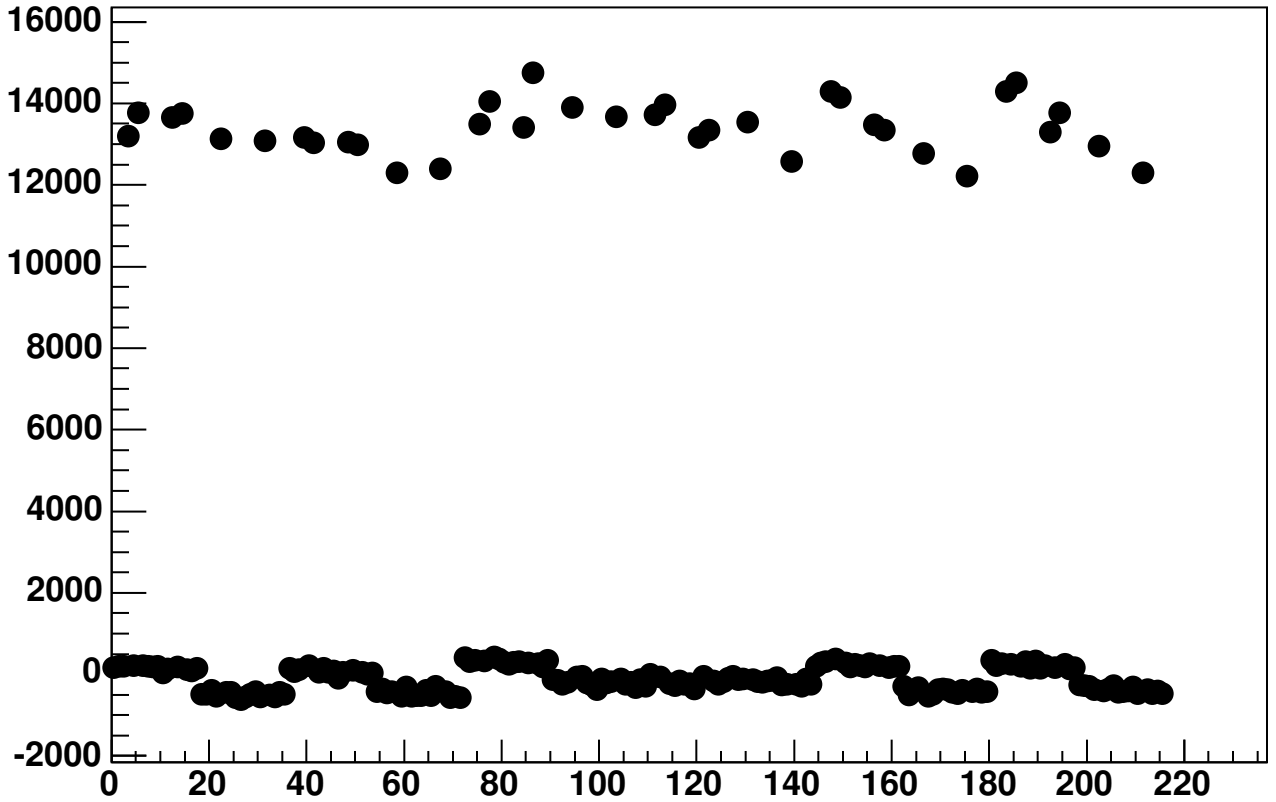
Enable 1, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



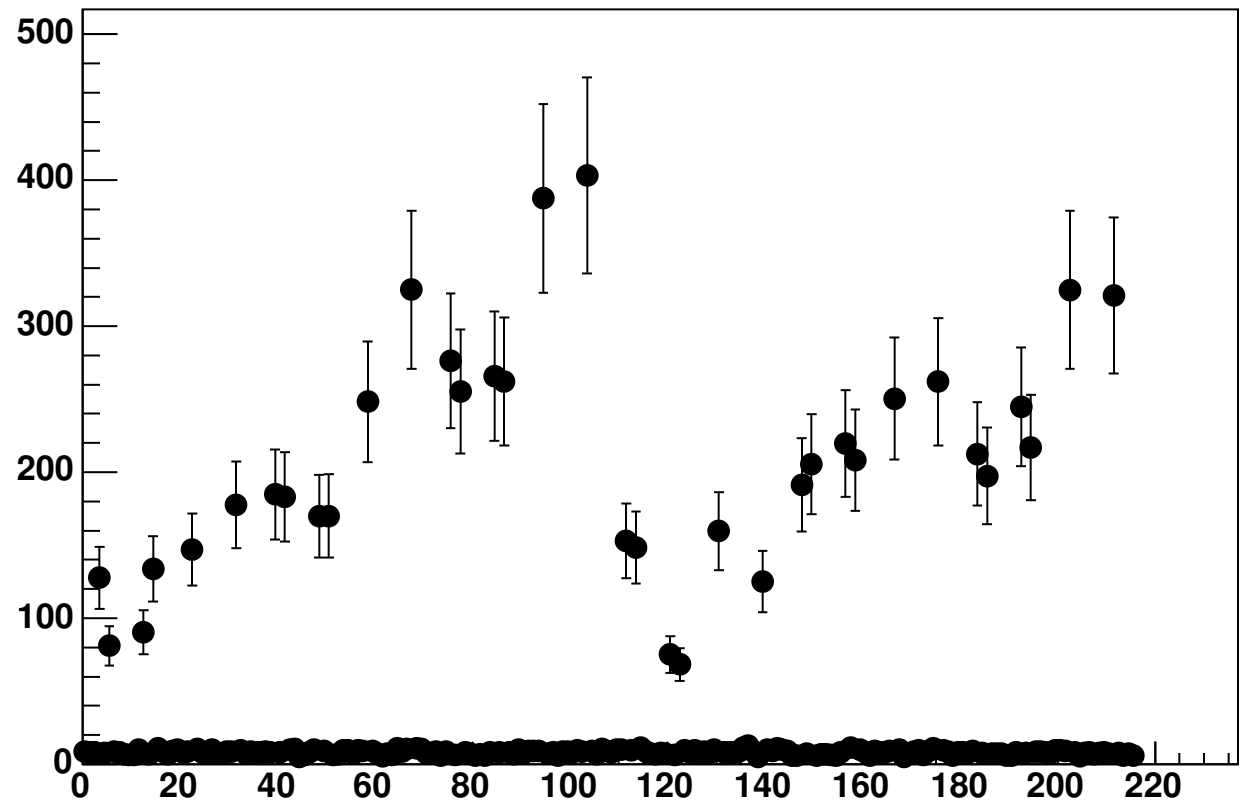
Enable 1, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



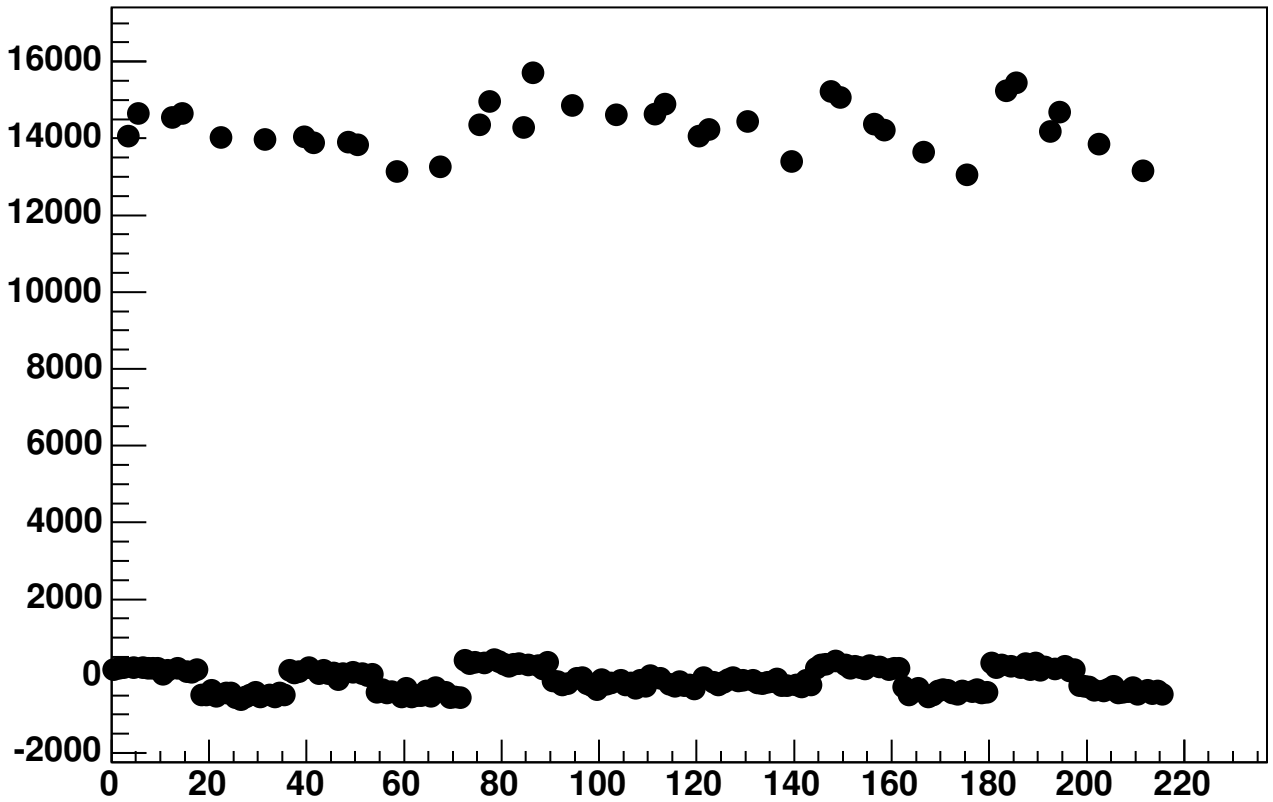
Enable 1, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



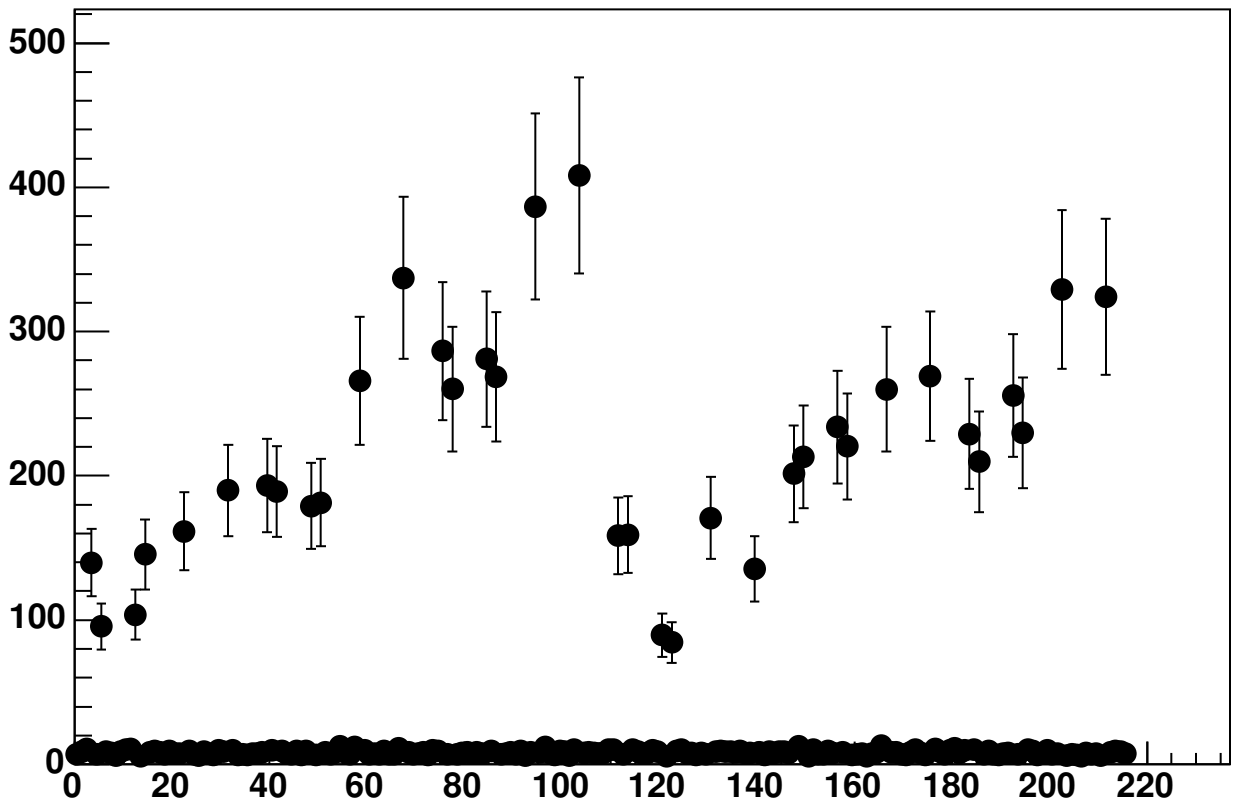
Enable 1, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



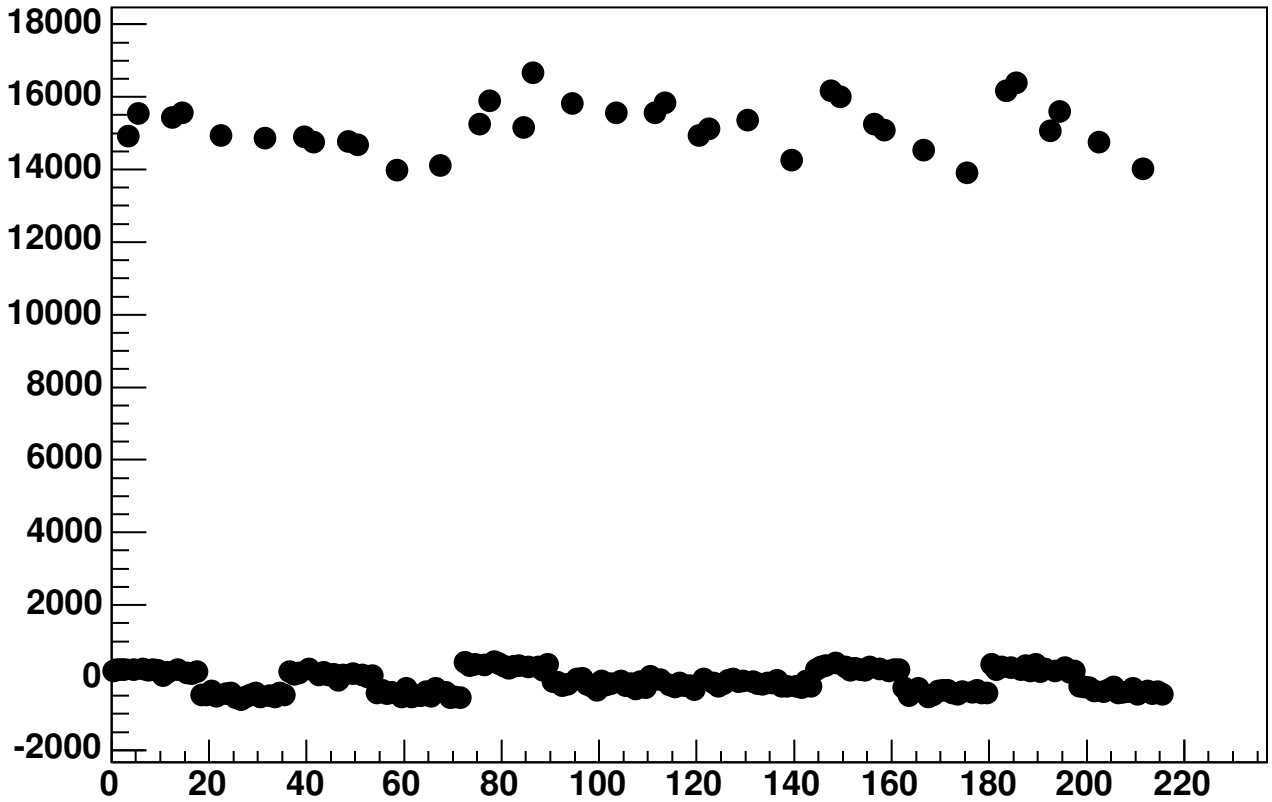
Enable 1, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



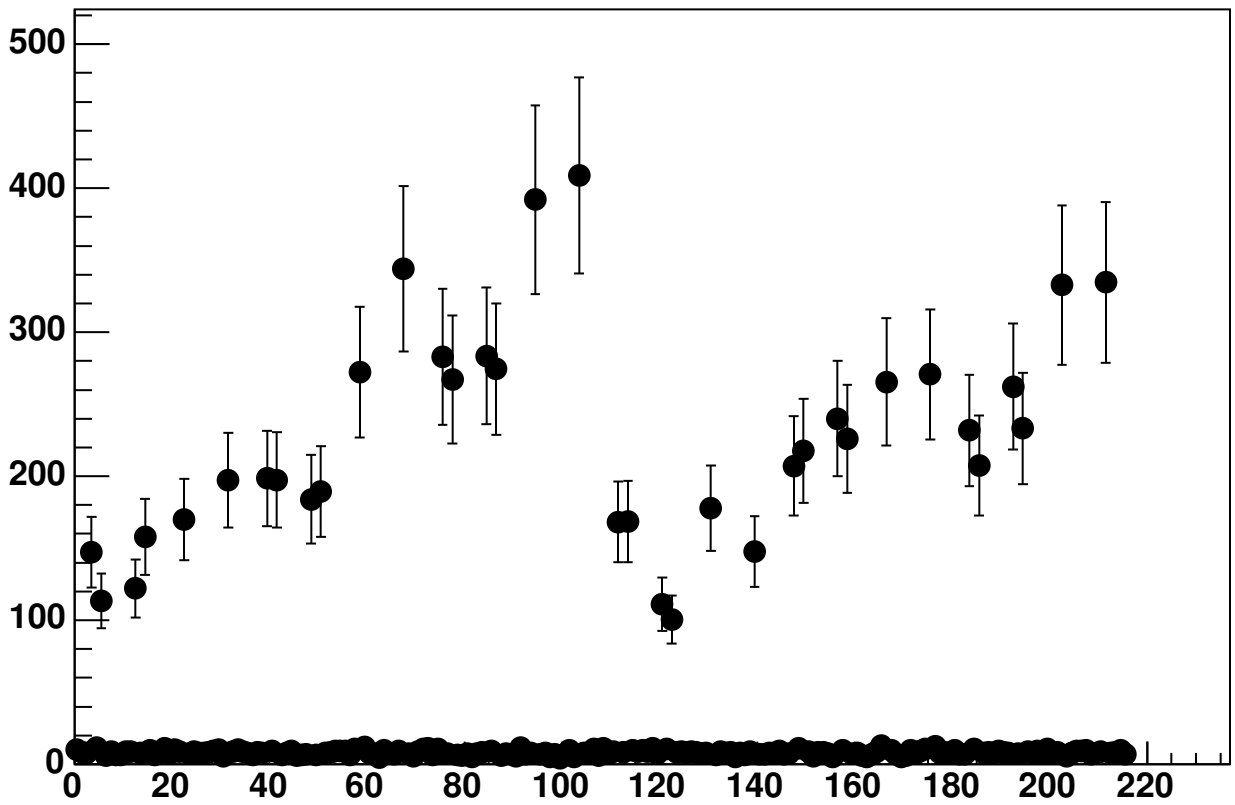
Enable 1, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



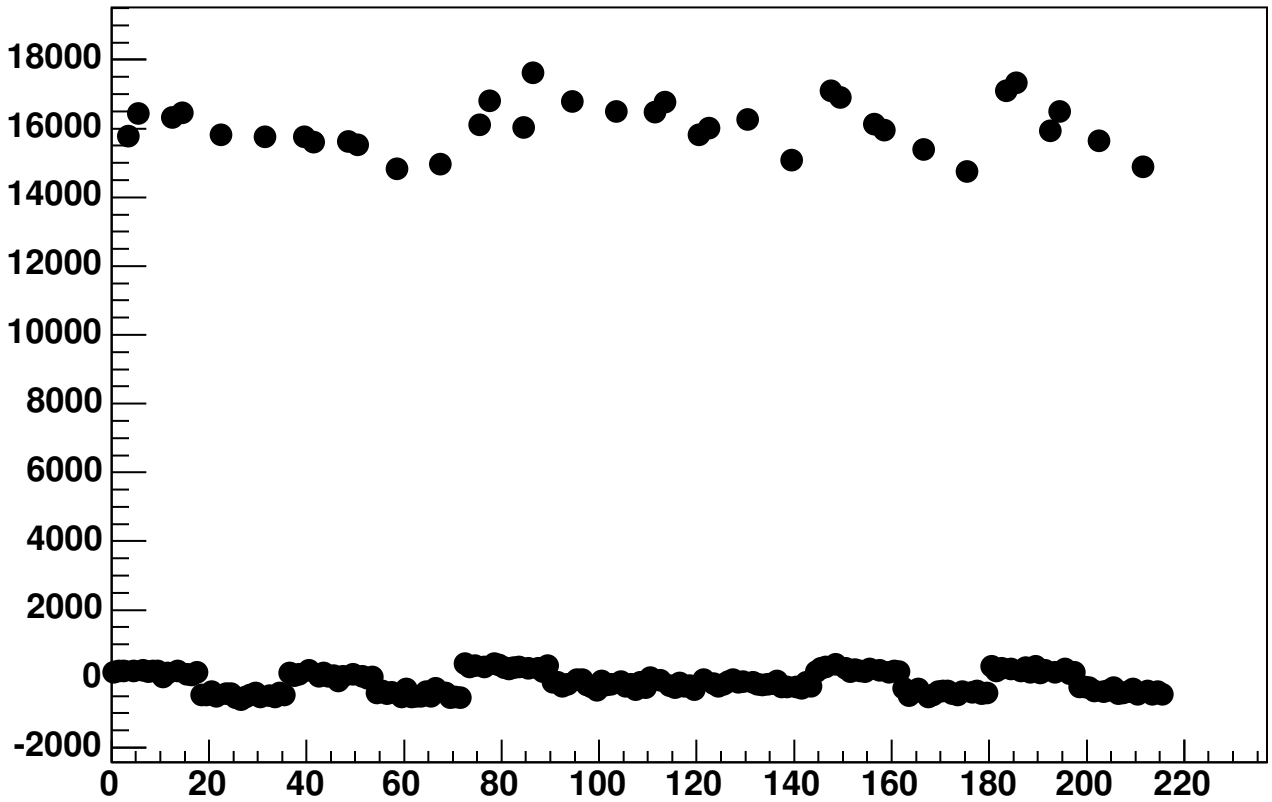
Enable 1, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



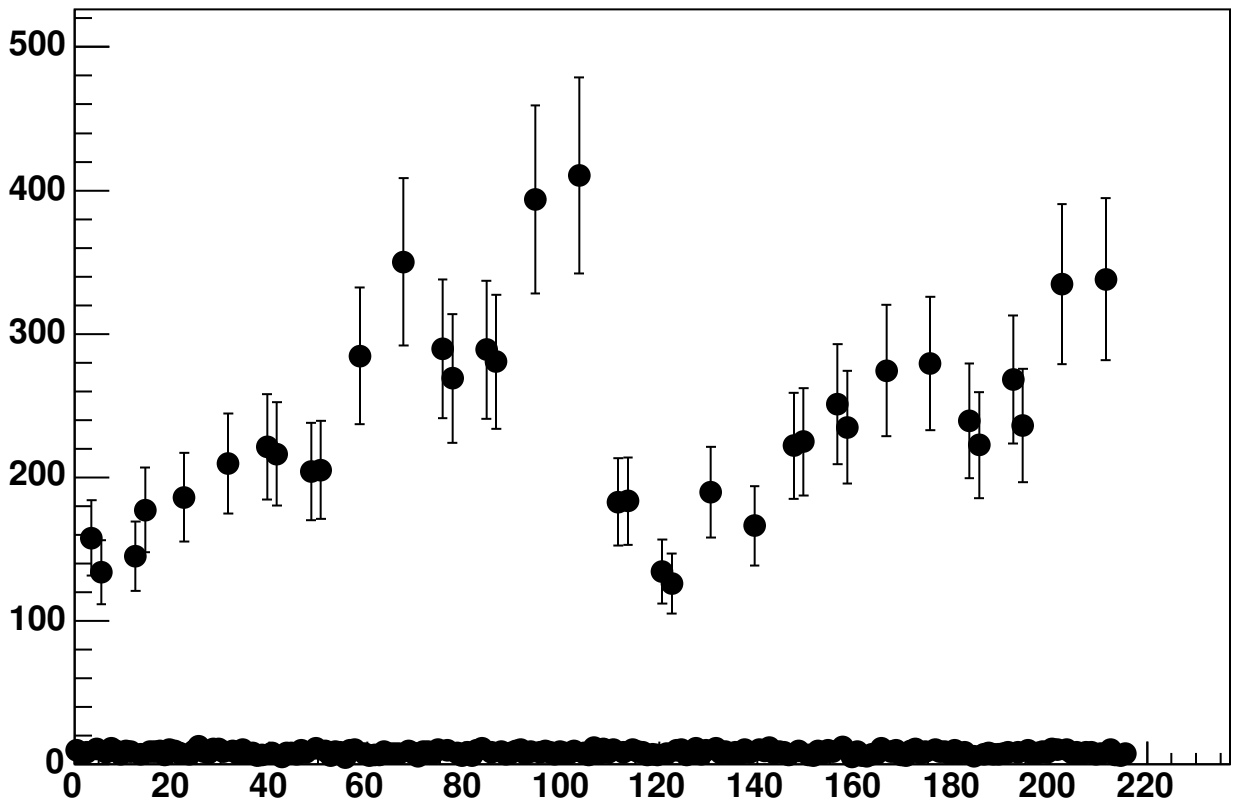
Enable 1, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



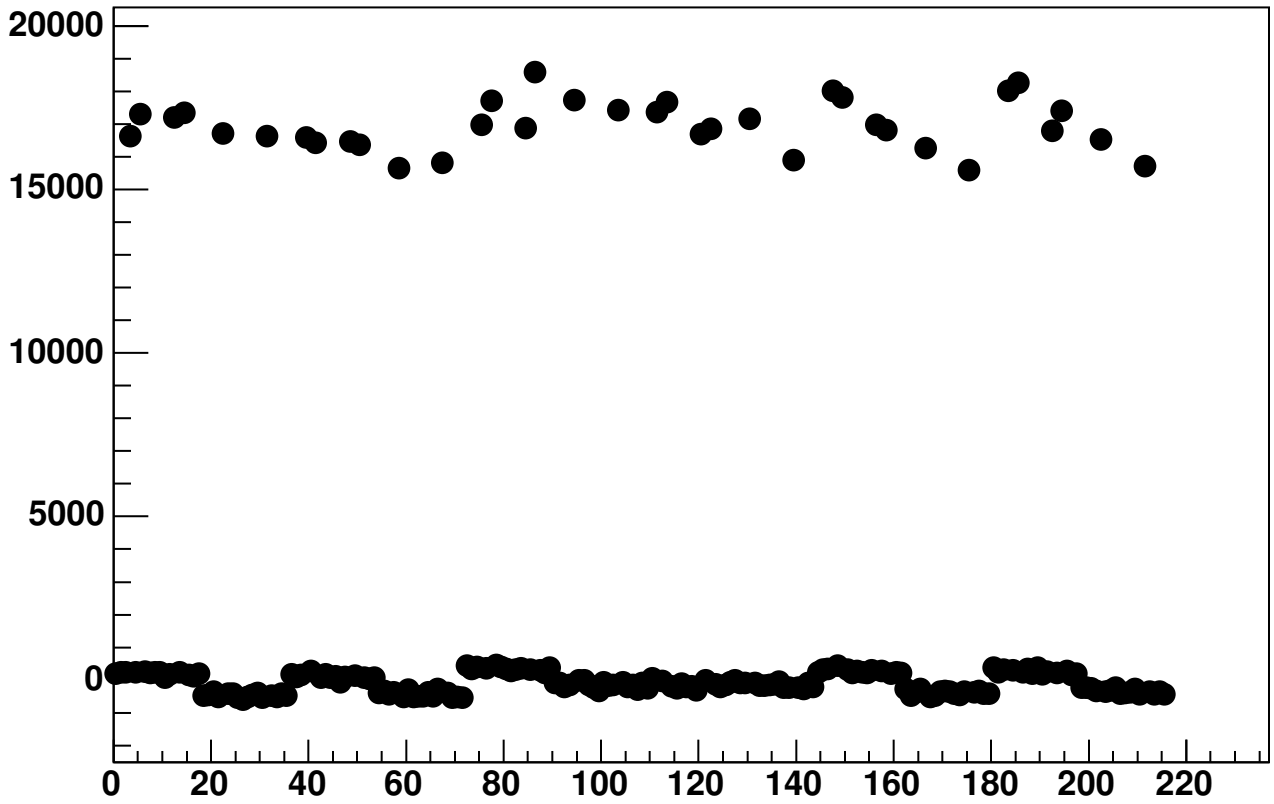
Enable 1, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



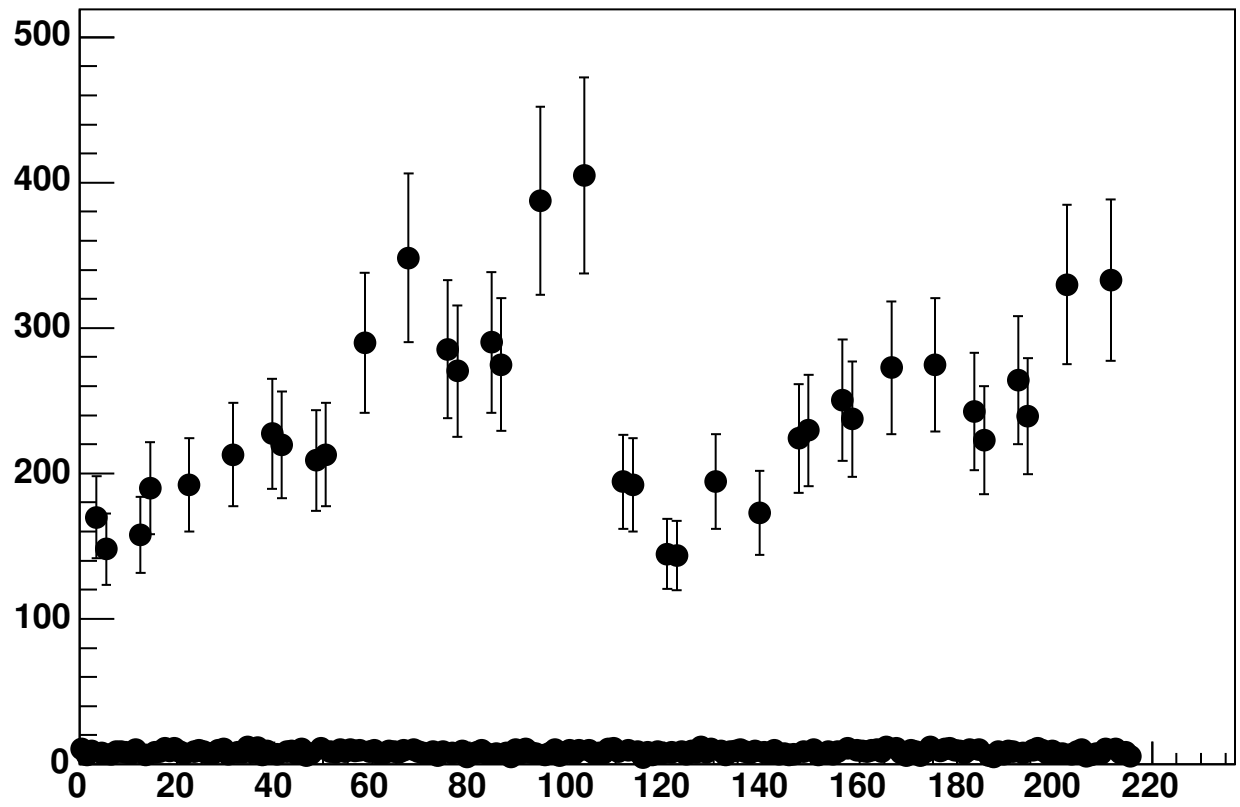
Enable 1, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



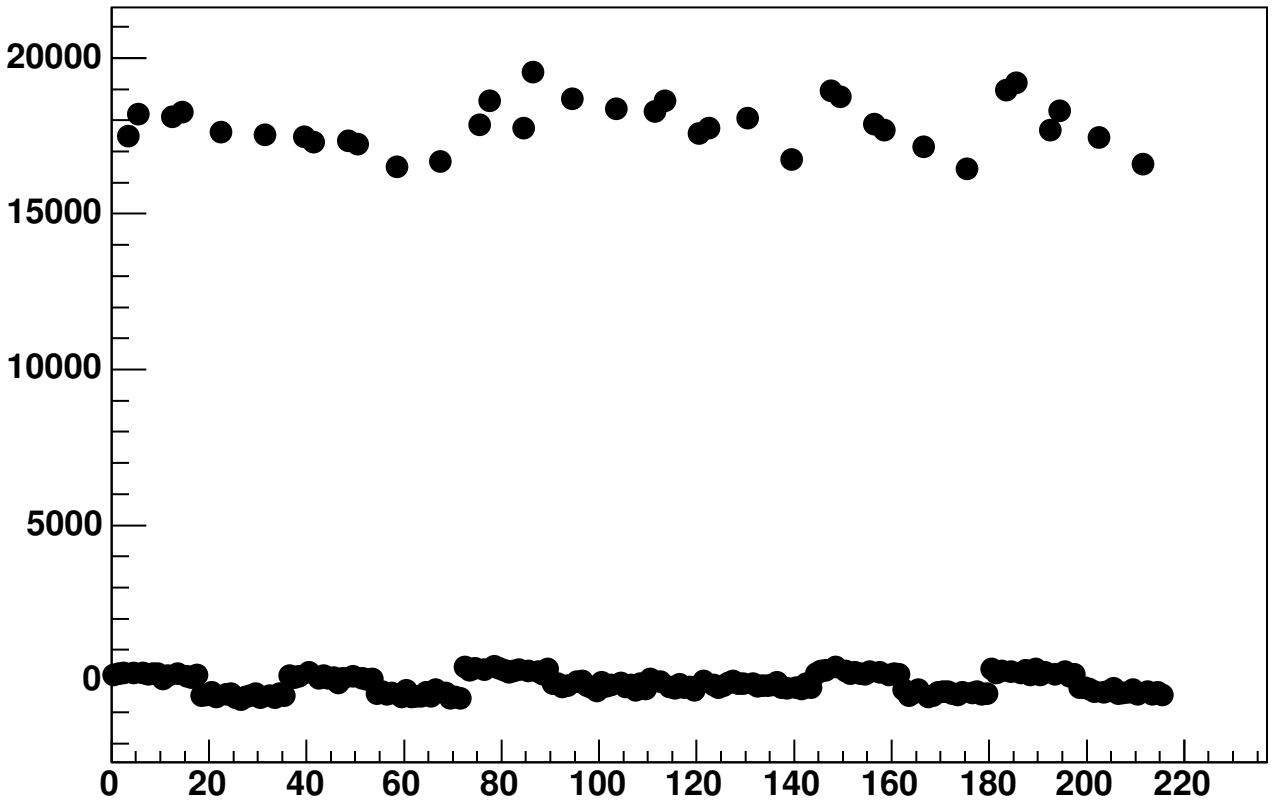
Enable 1, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



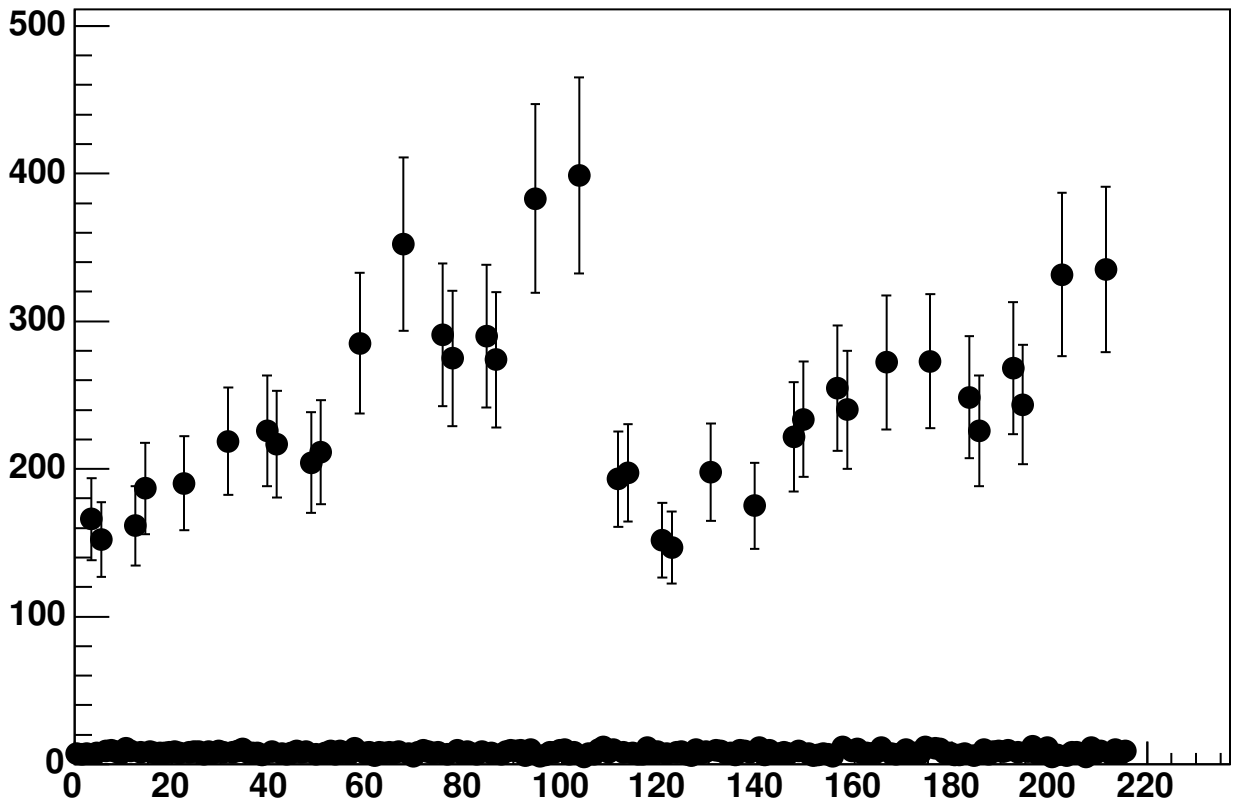
Enable 1, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



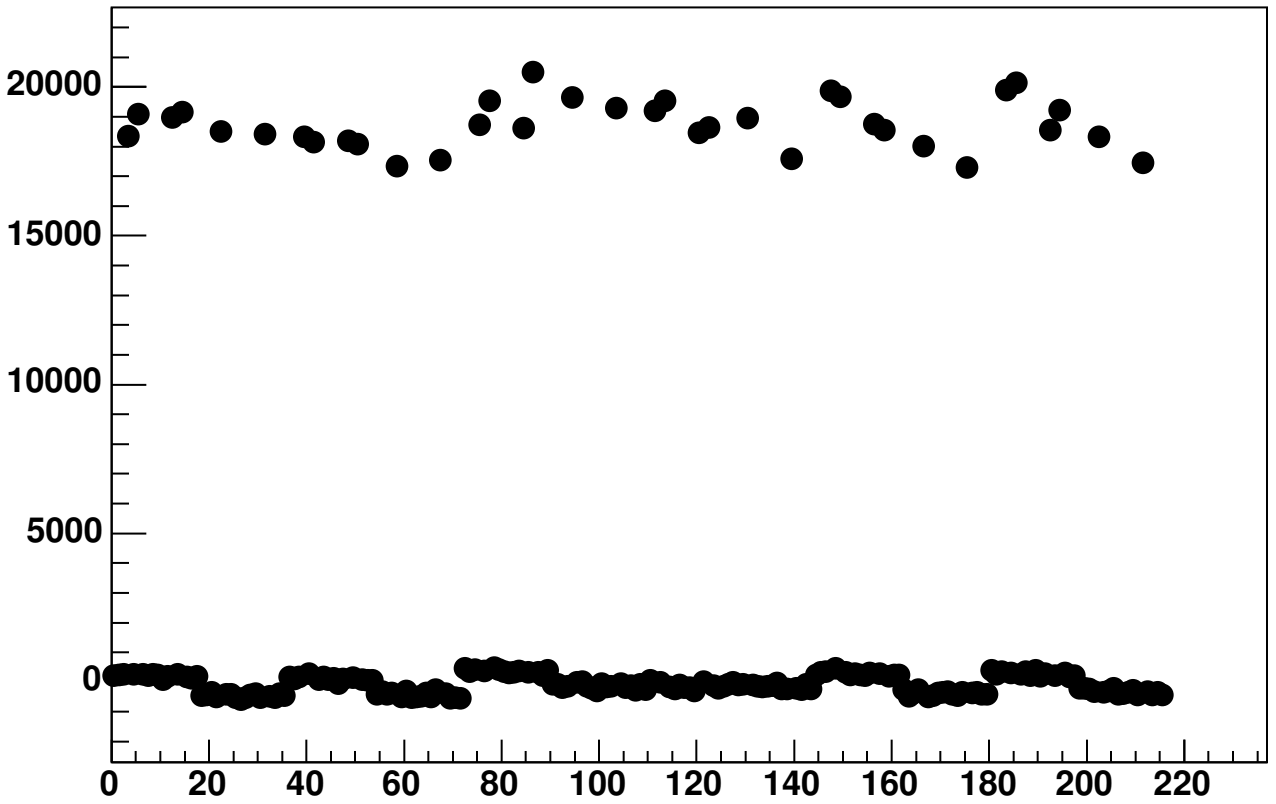
Enable 1, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



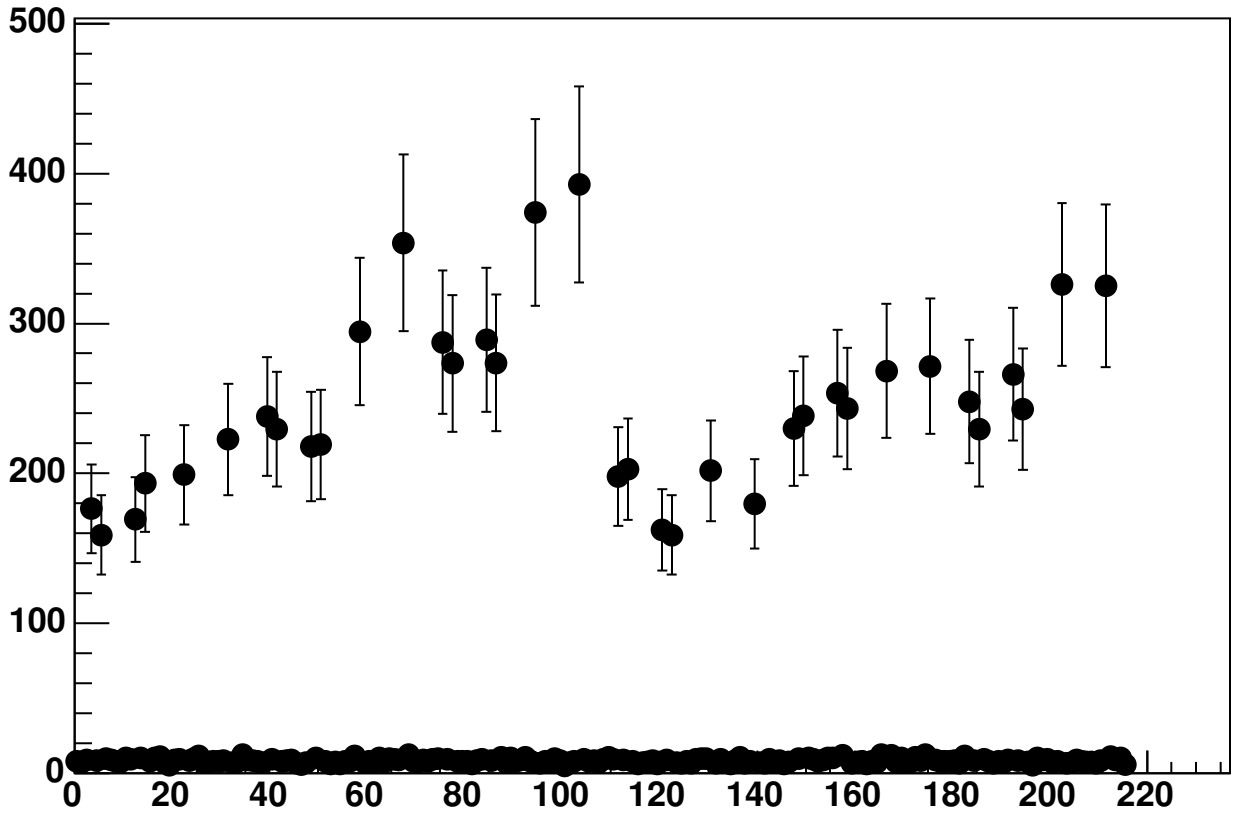
Enable 1, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



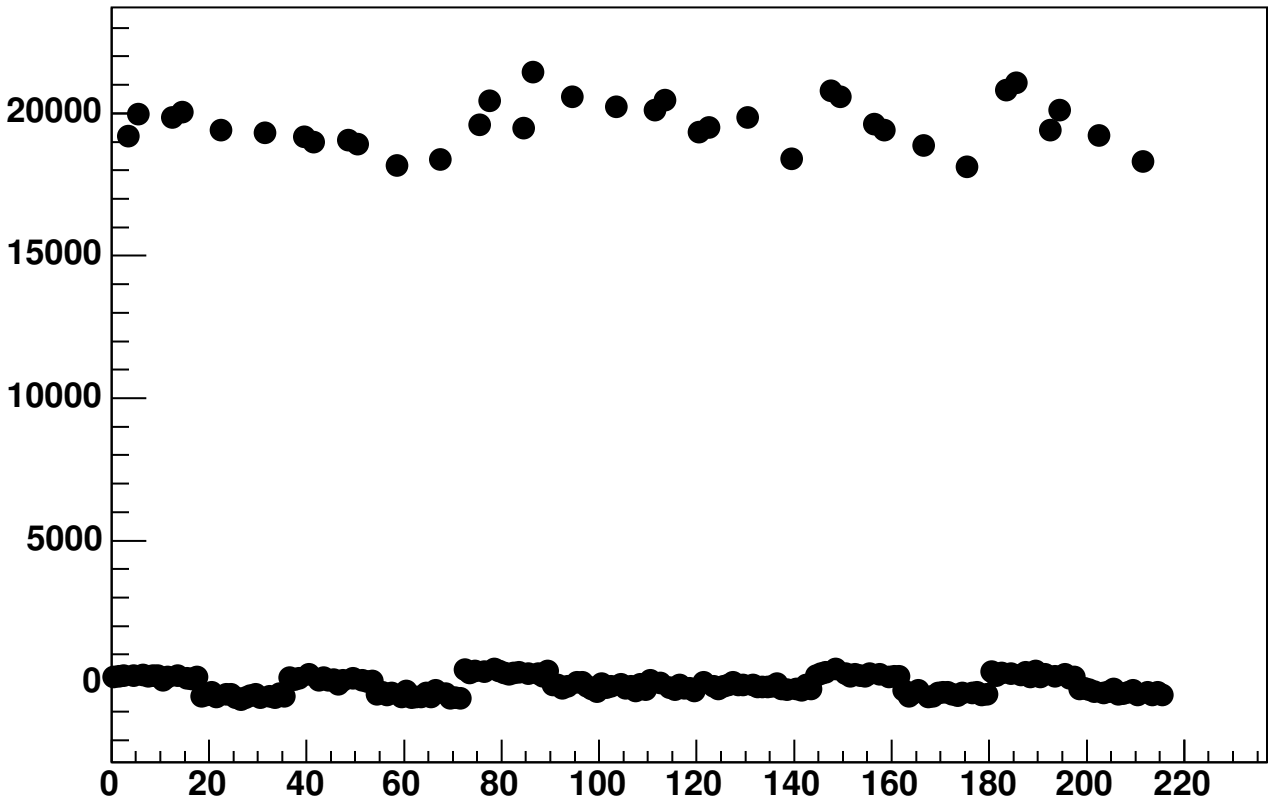
Enable 1, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



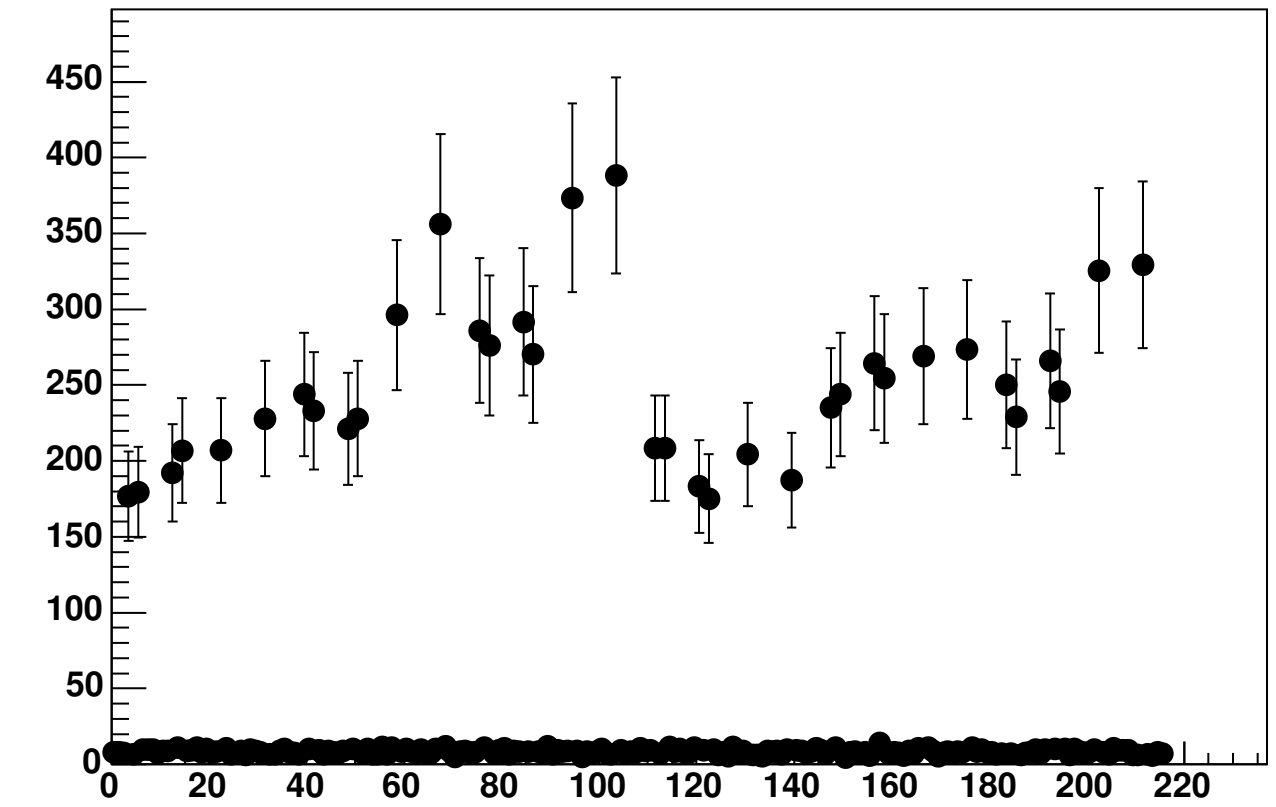
Enable 1, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



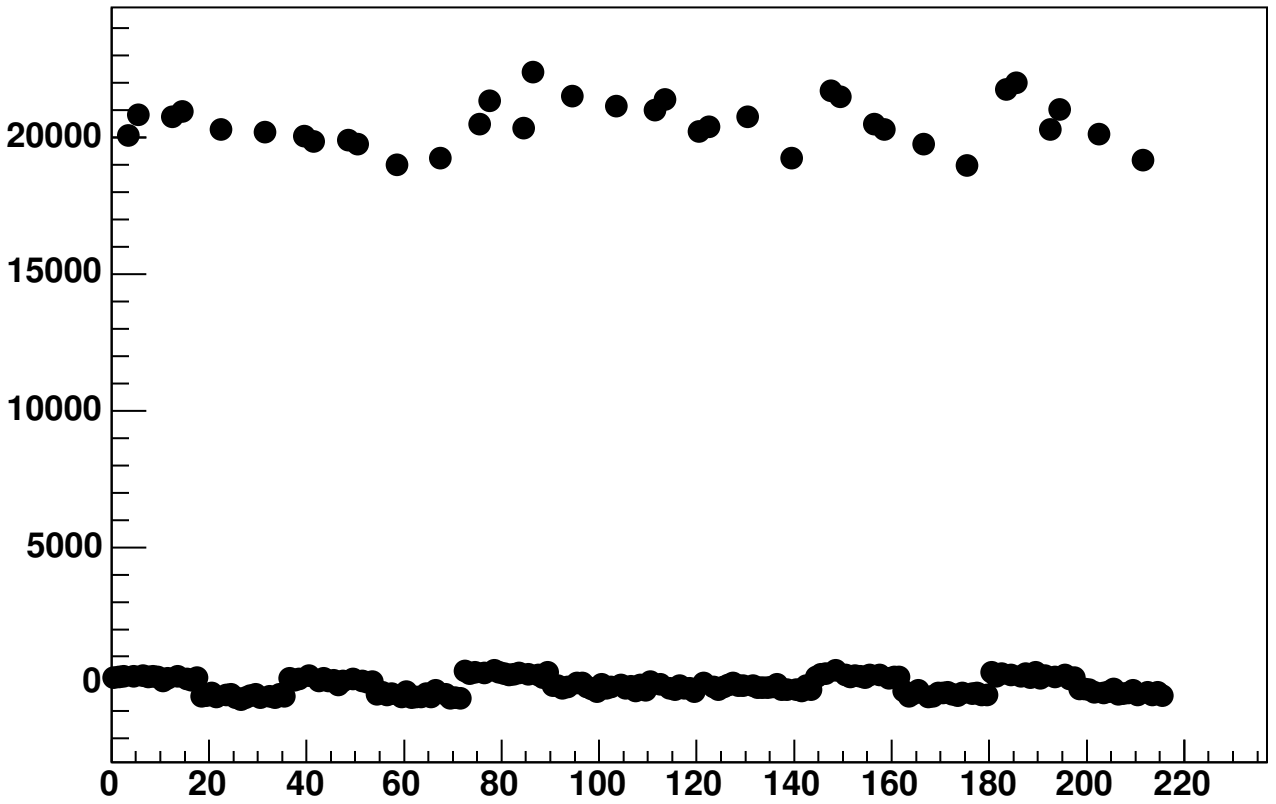
Enable 1, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



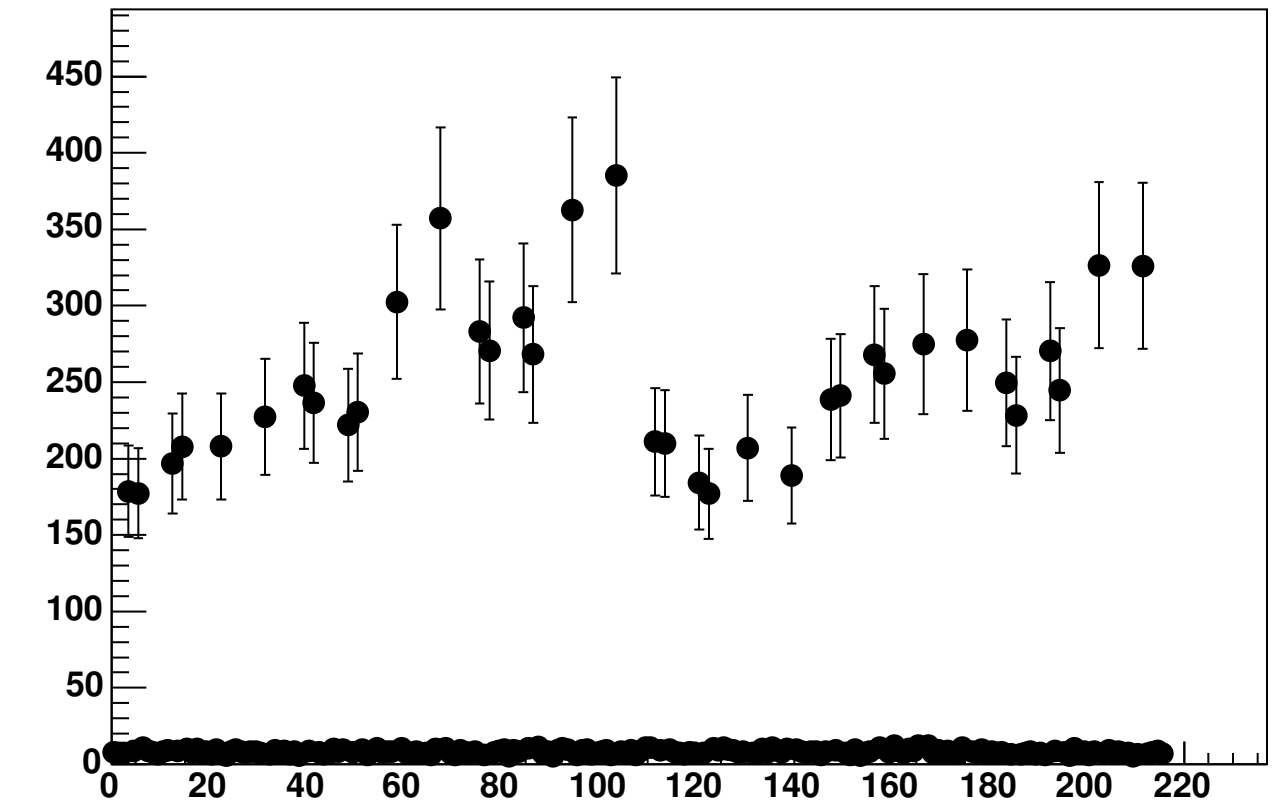
Enable 1, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



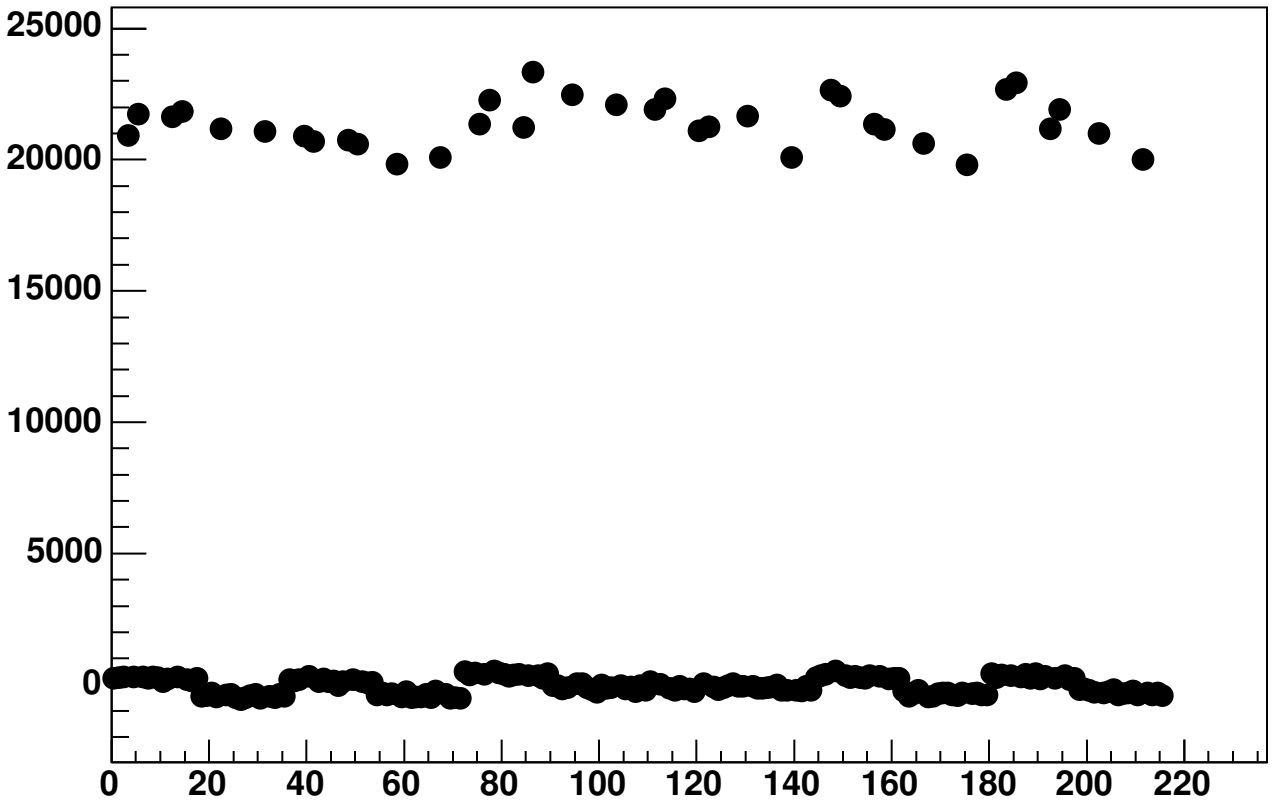
Enable 1, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



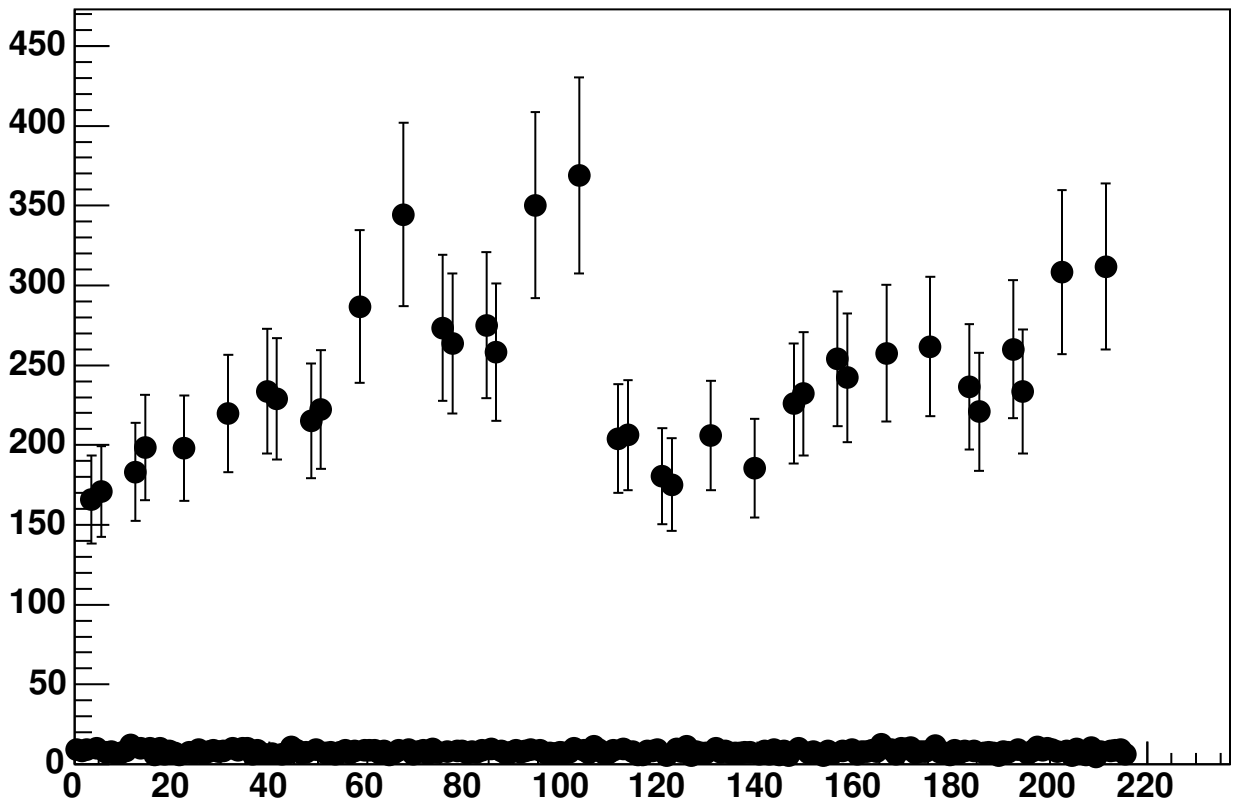
Enable 1, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



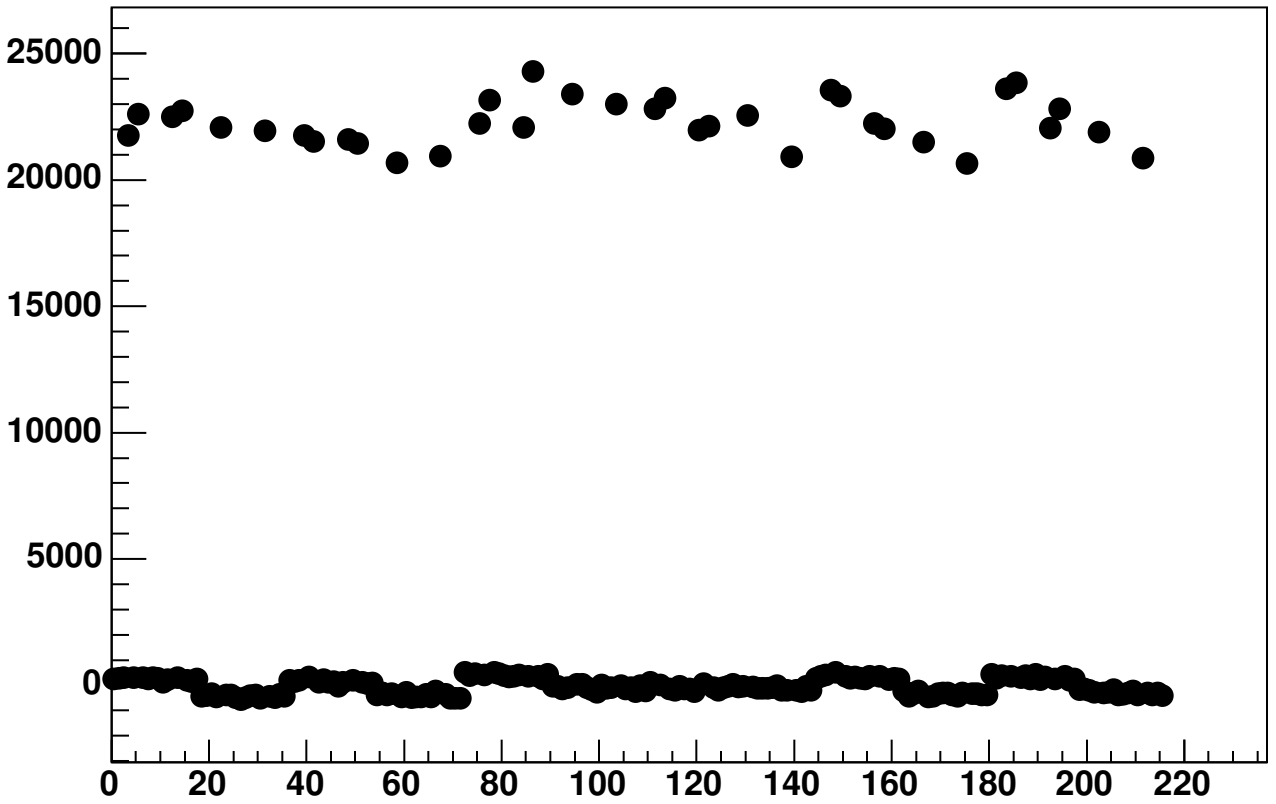
Enable 1, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



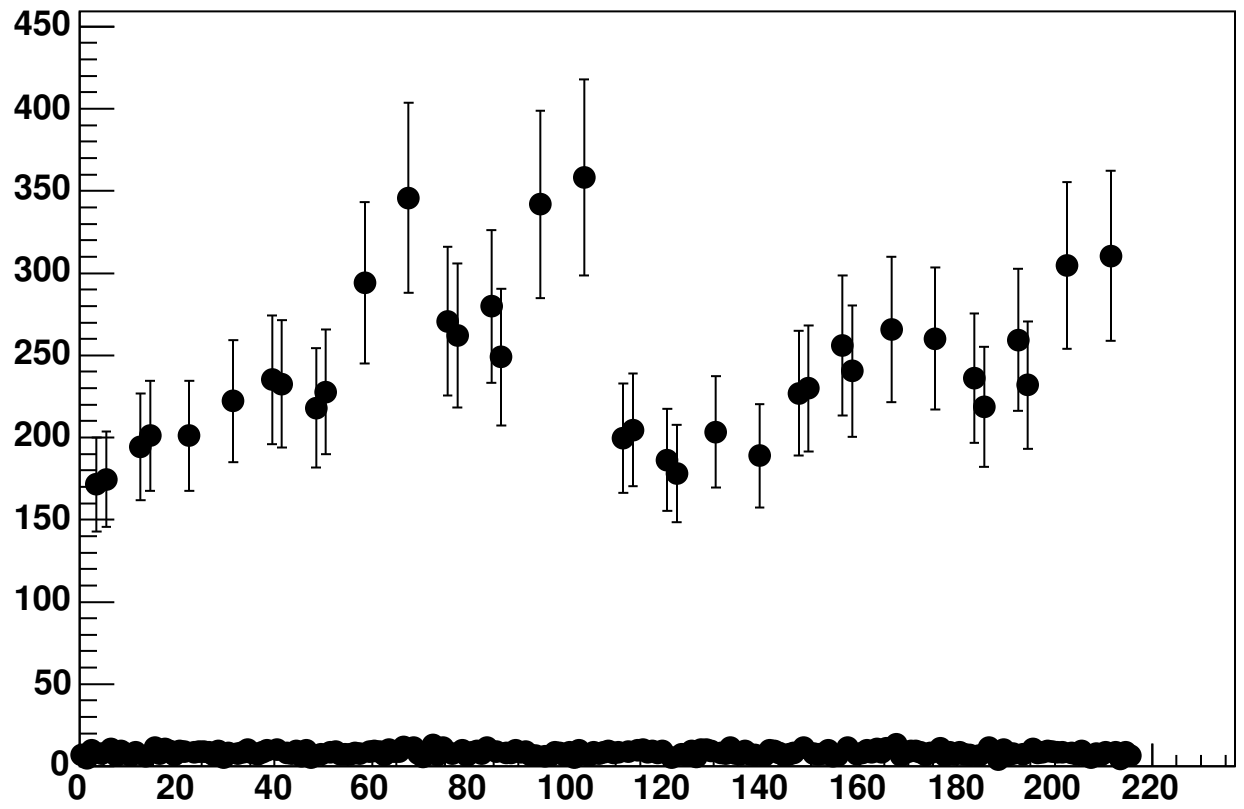
Enable 1, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



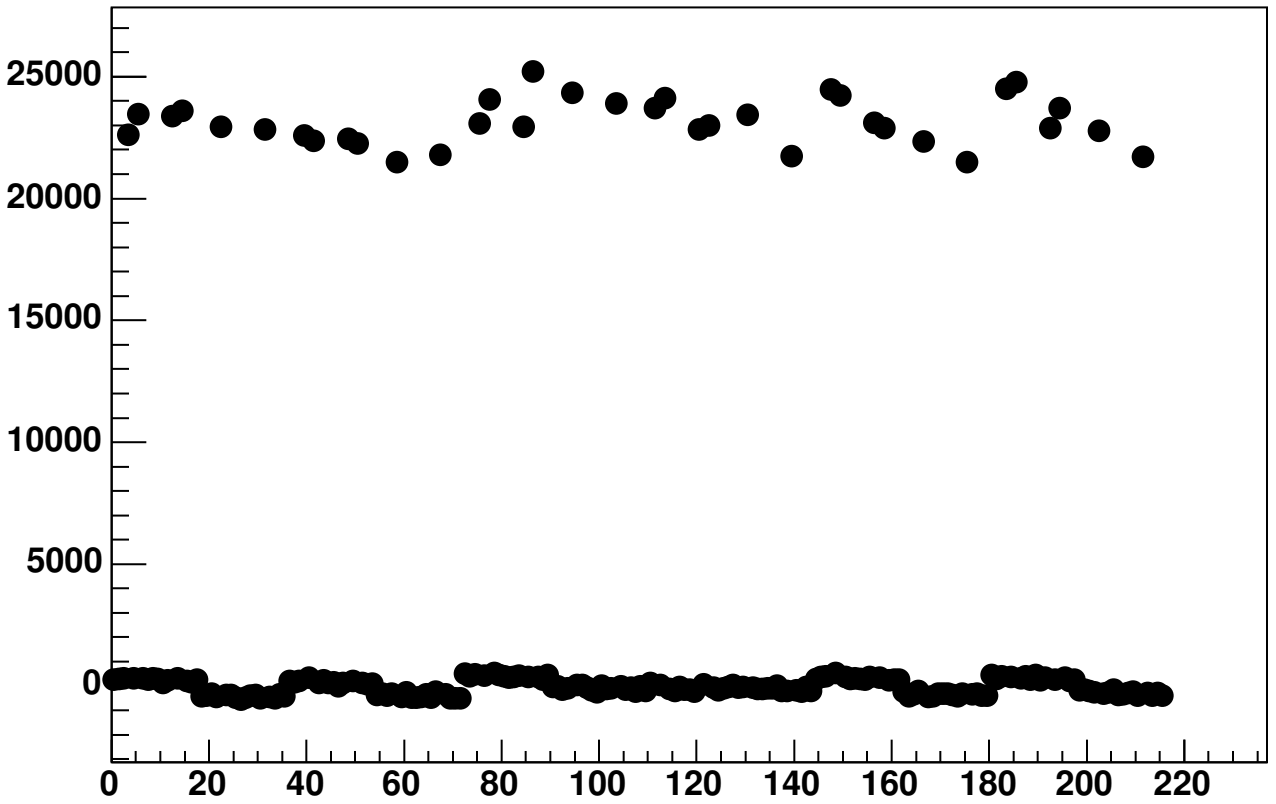
Enable 1, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



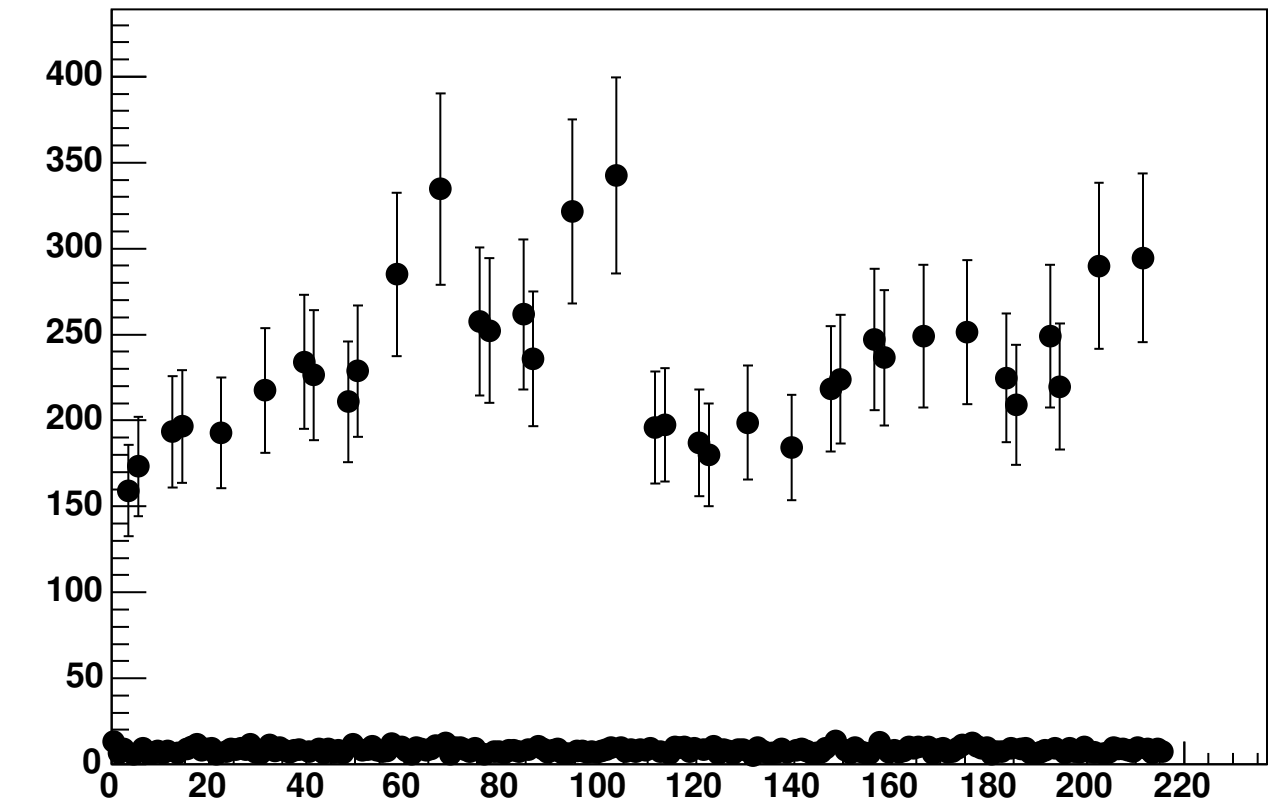
Enable 1, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



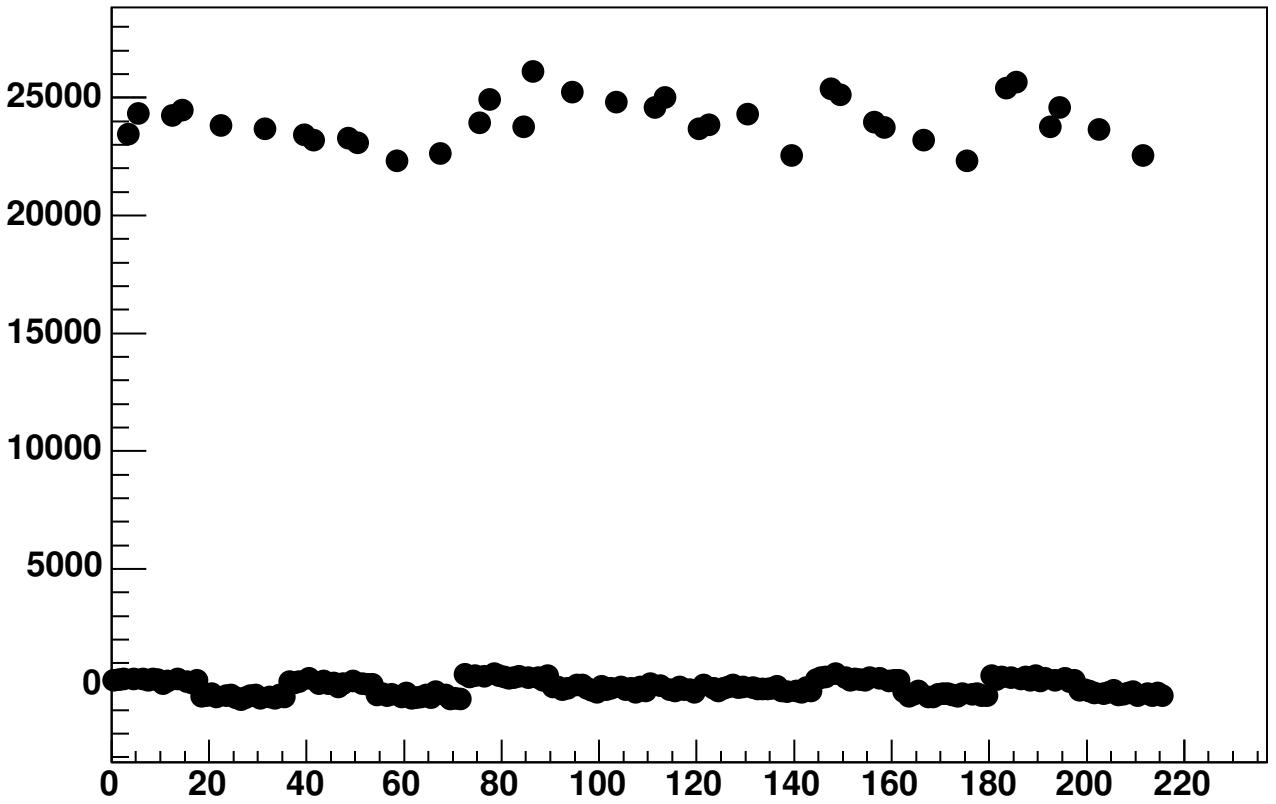
Enable 1, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



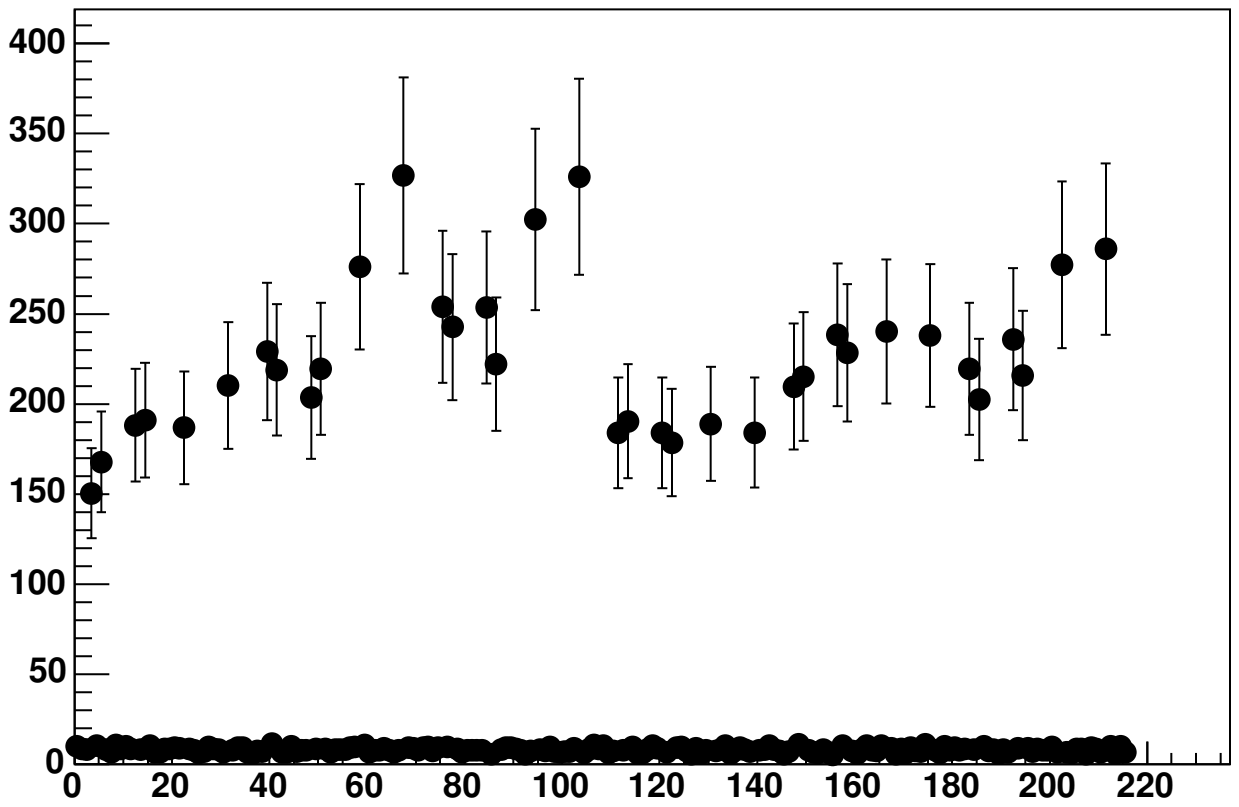
Enable 1, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



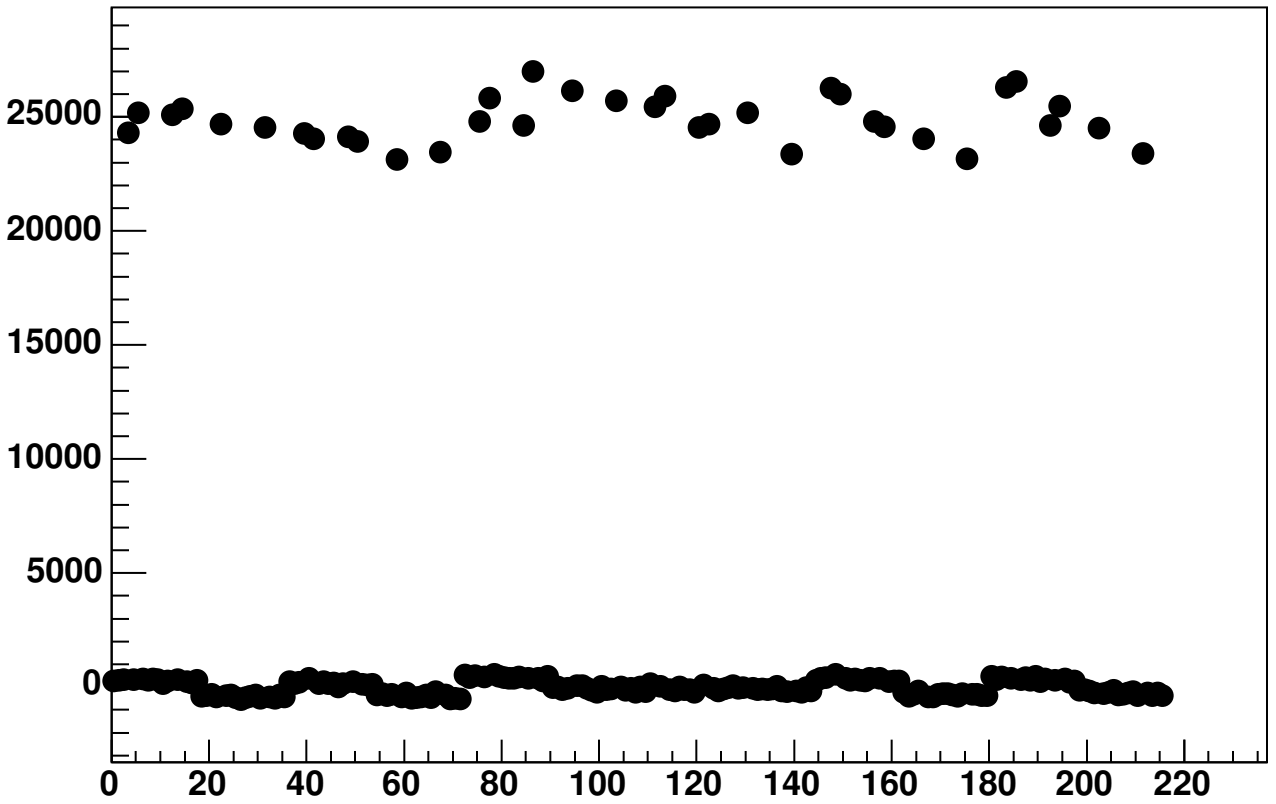
Enable 1, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



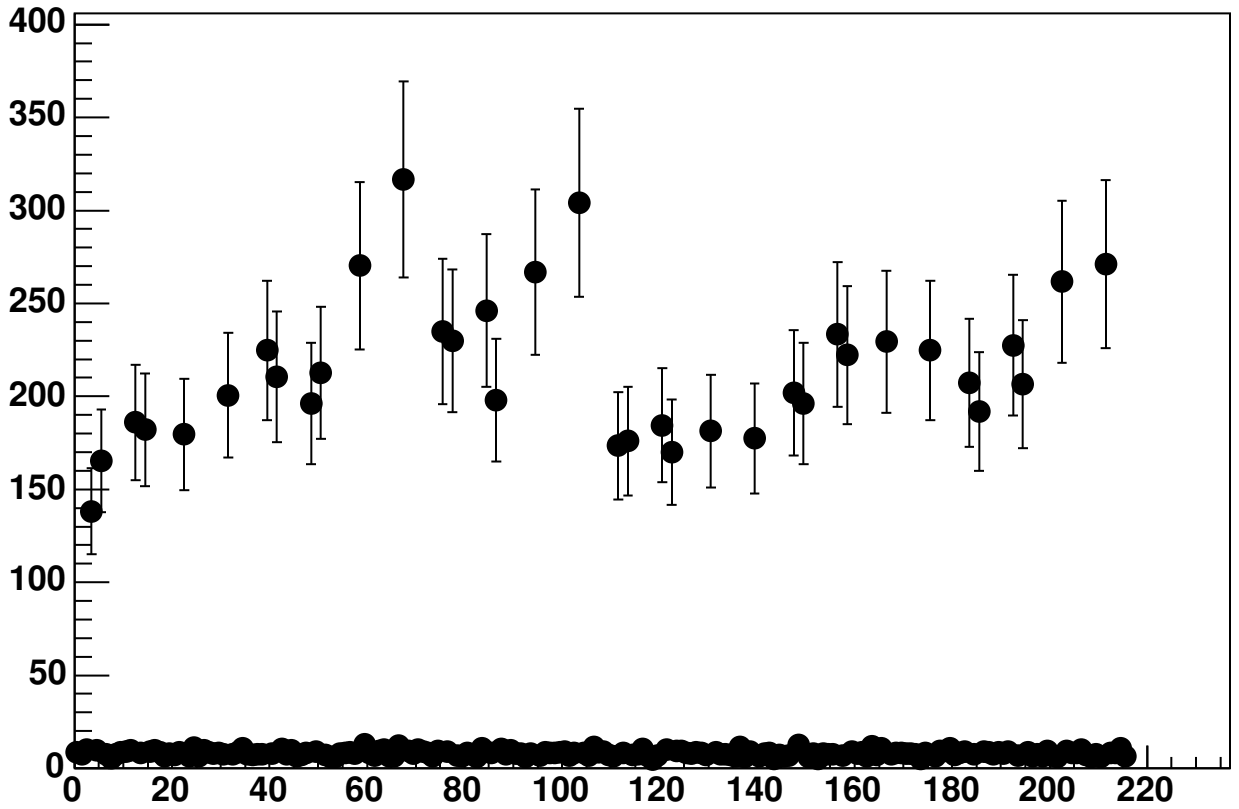
Enable 1, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



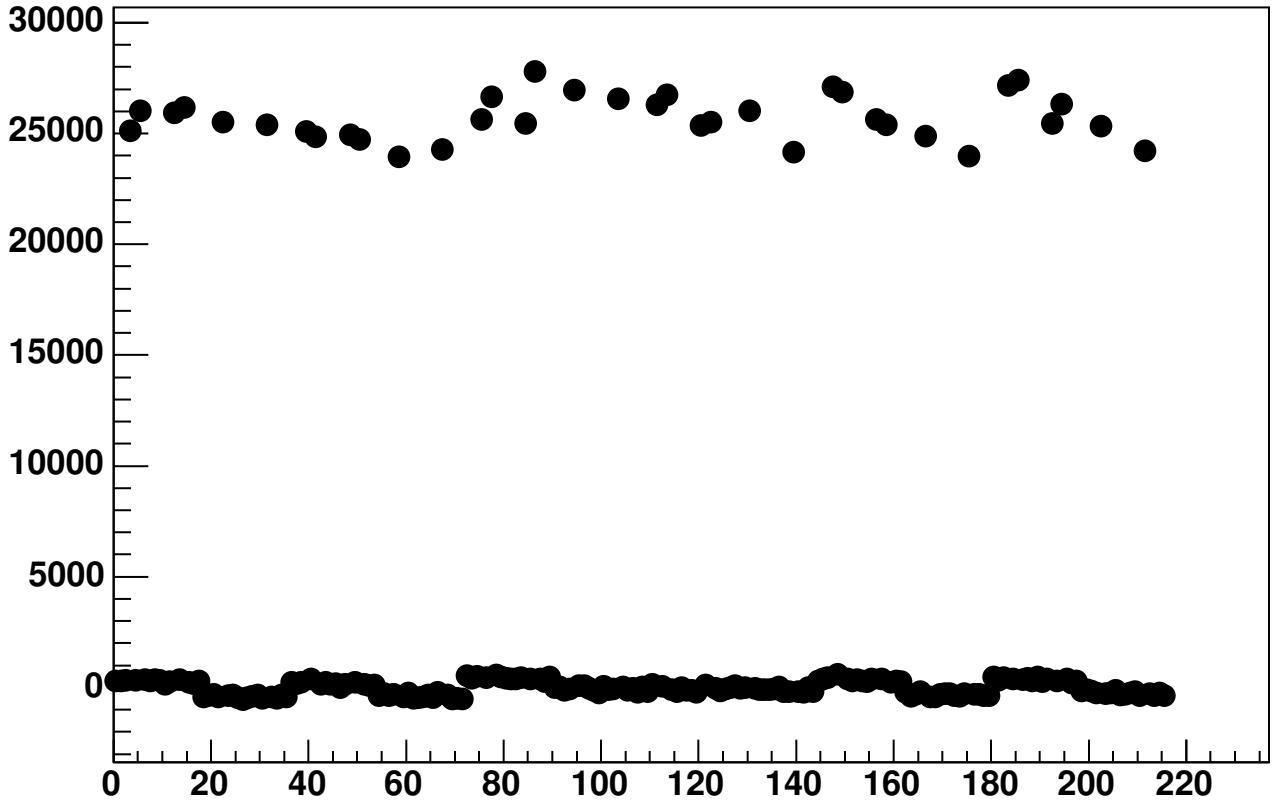
Enable 1, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



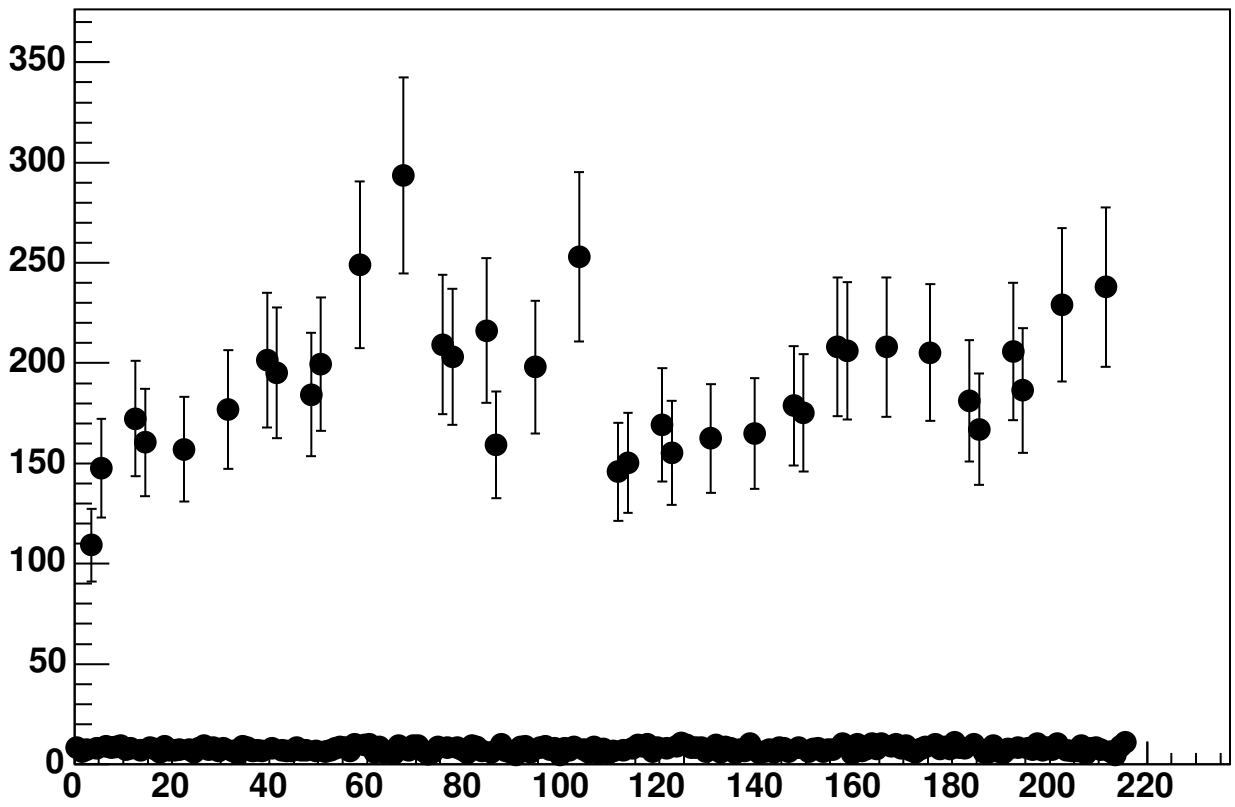
Enable 1, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



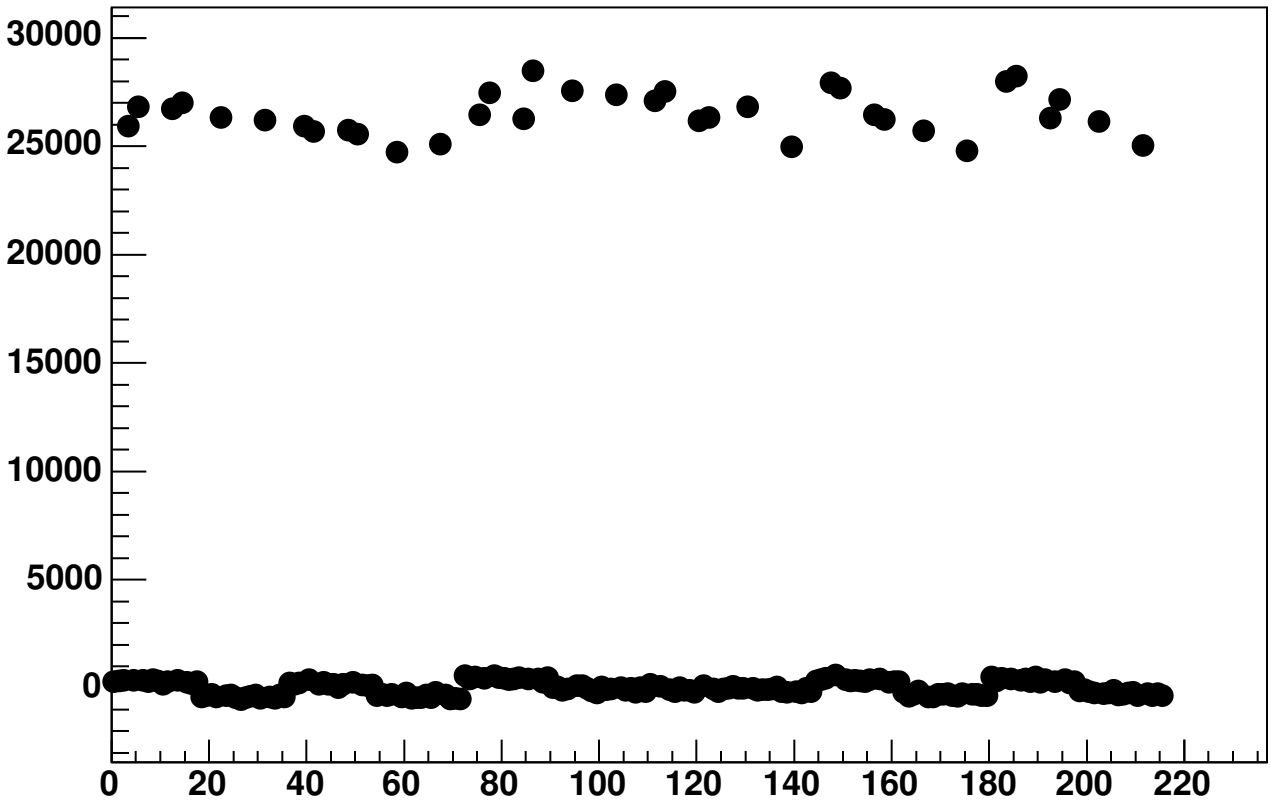
Enable 1, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



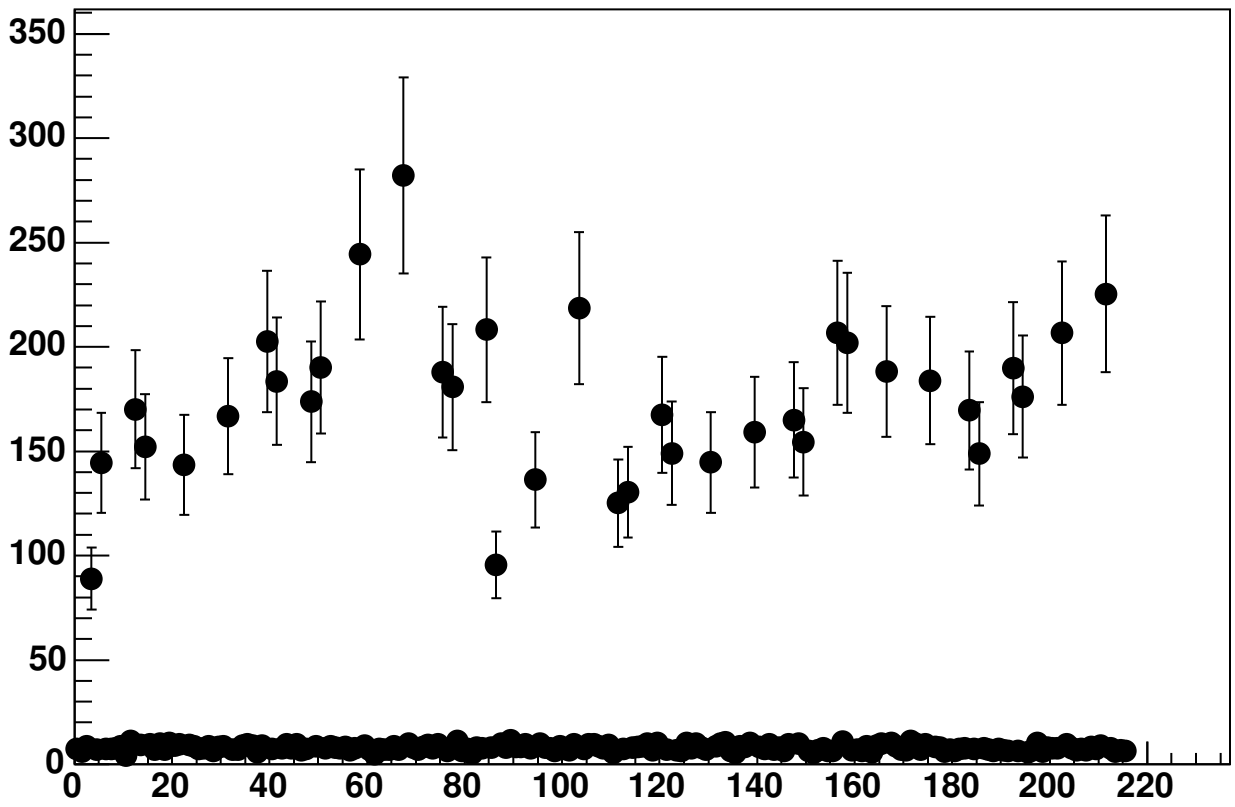
Enable 1, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



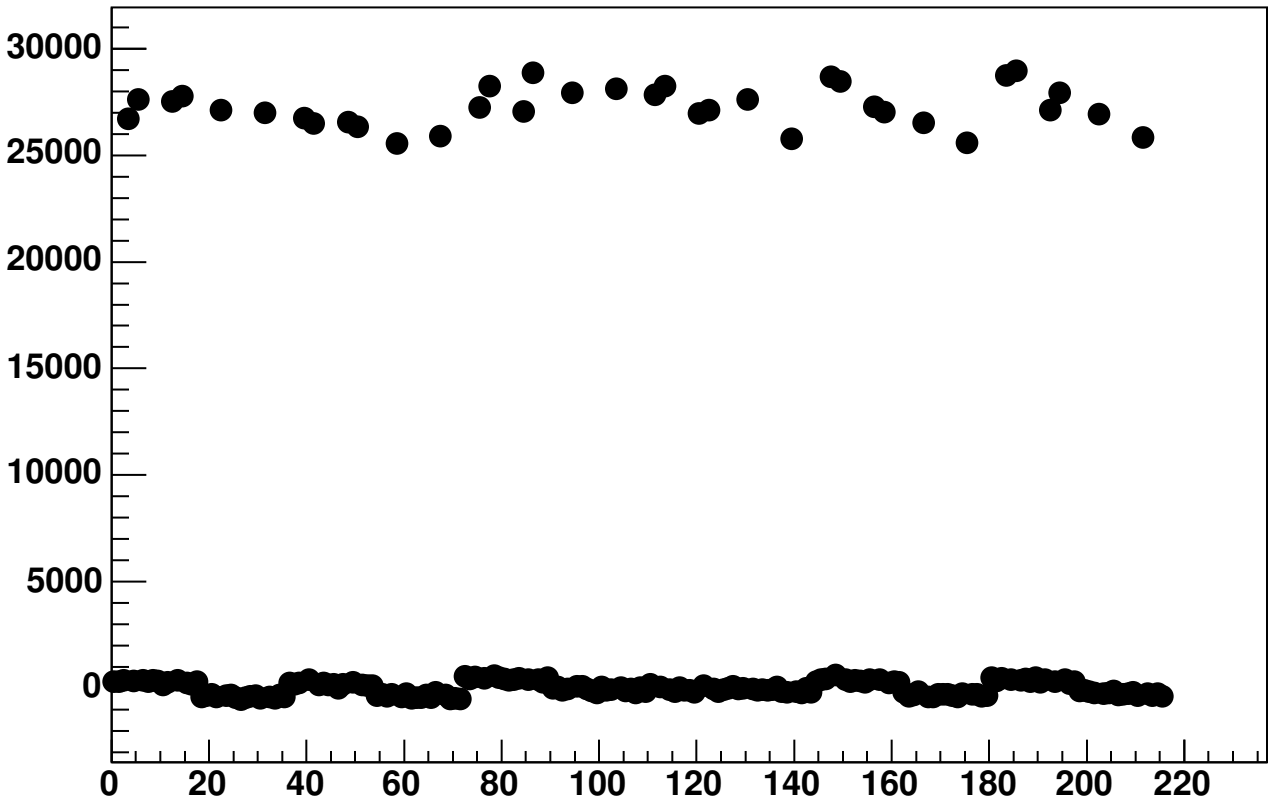
Enable 1, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



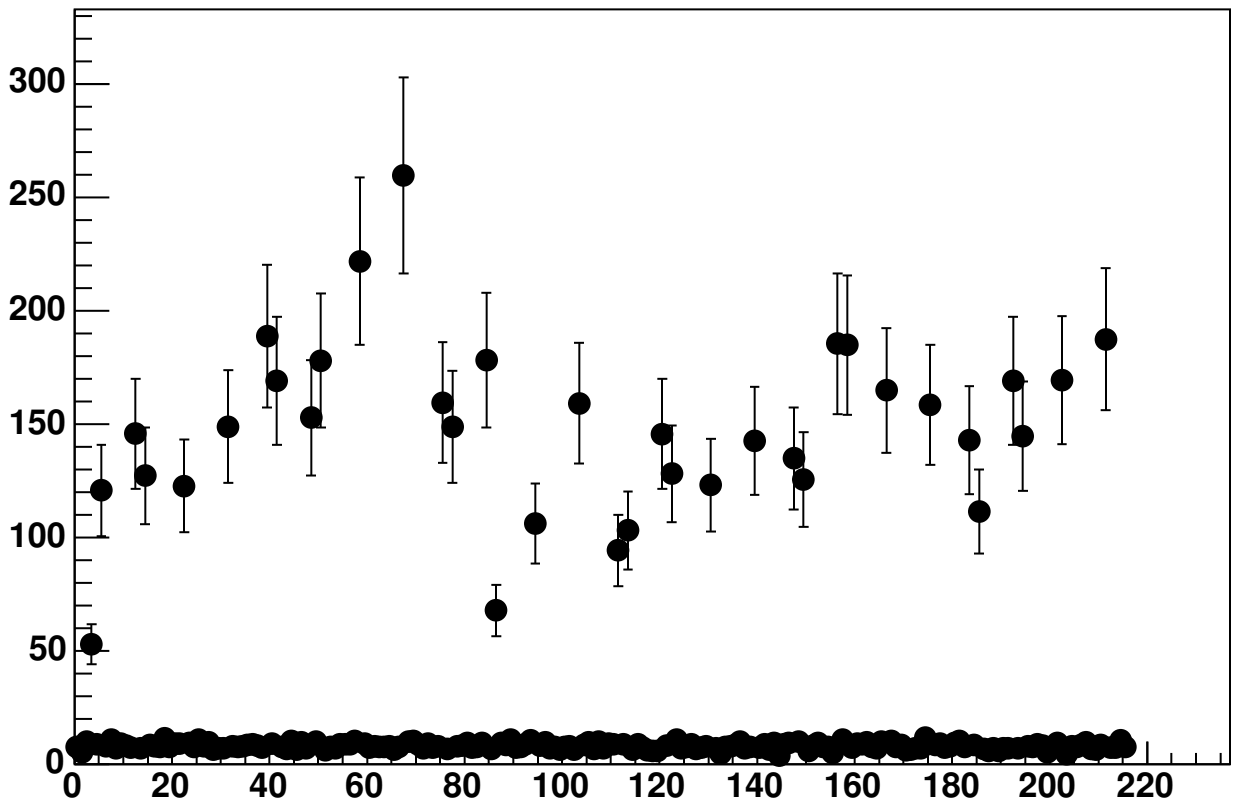
Enable 1, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



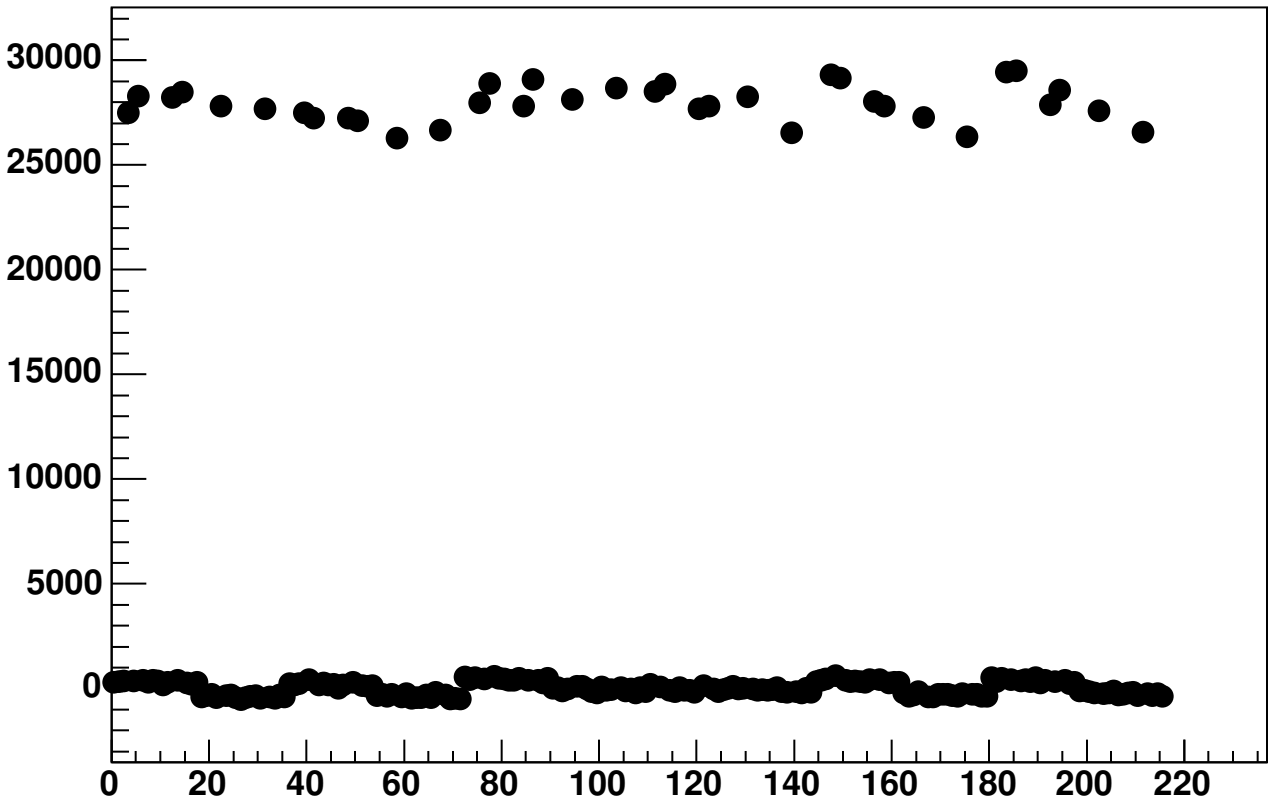
Enable 1, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



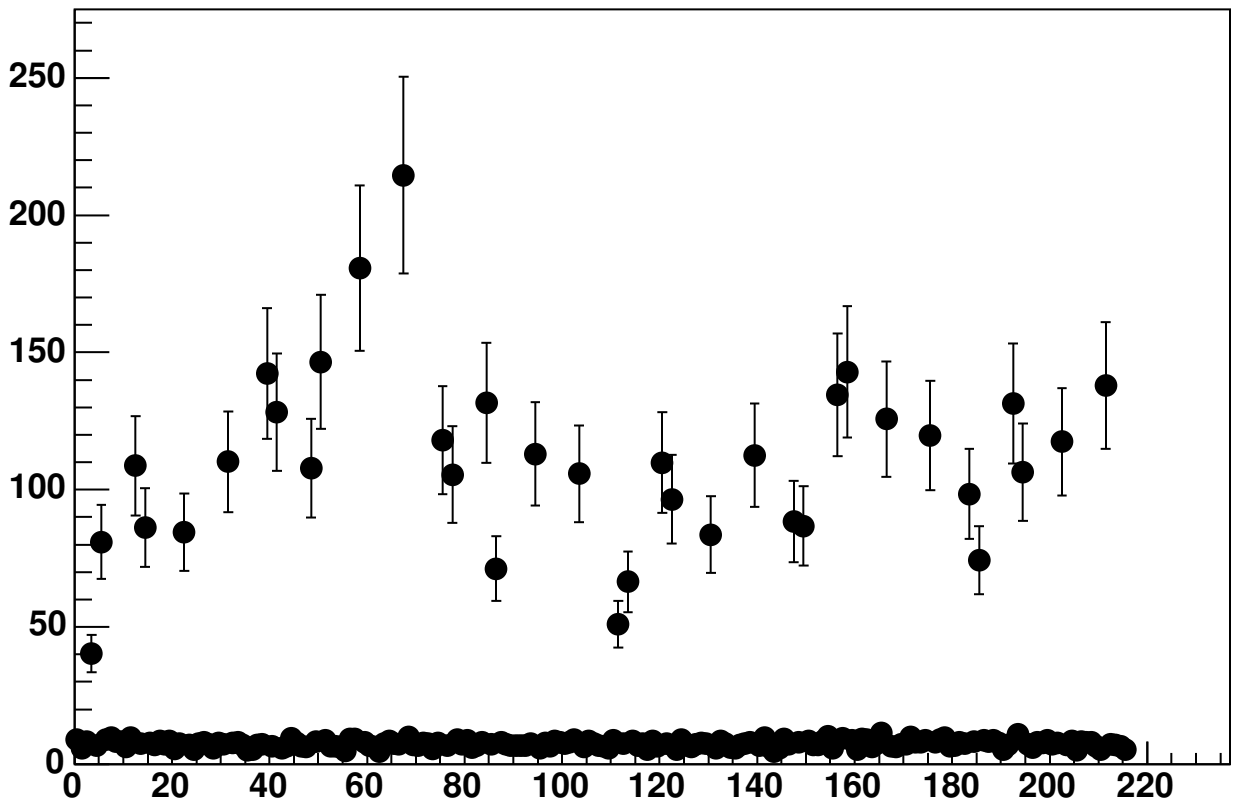
Enable 1, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



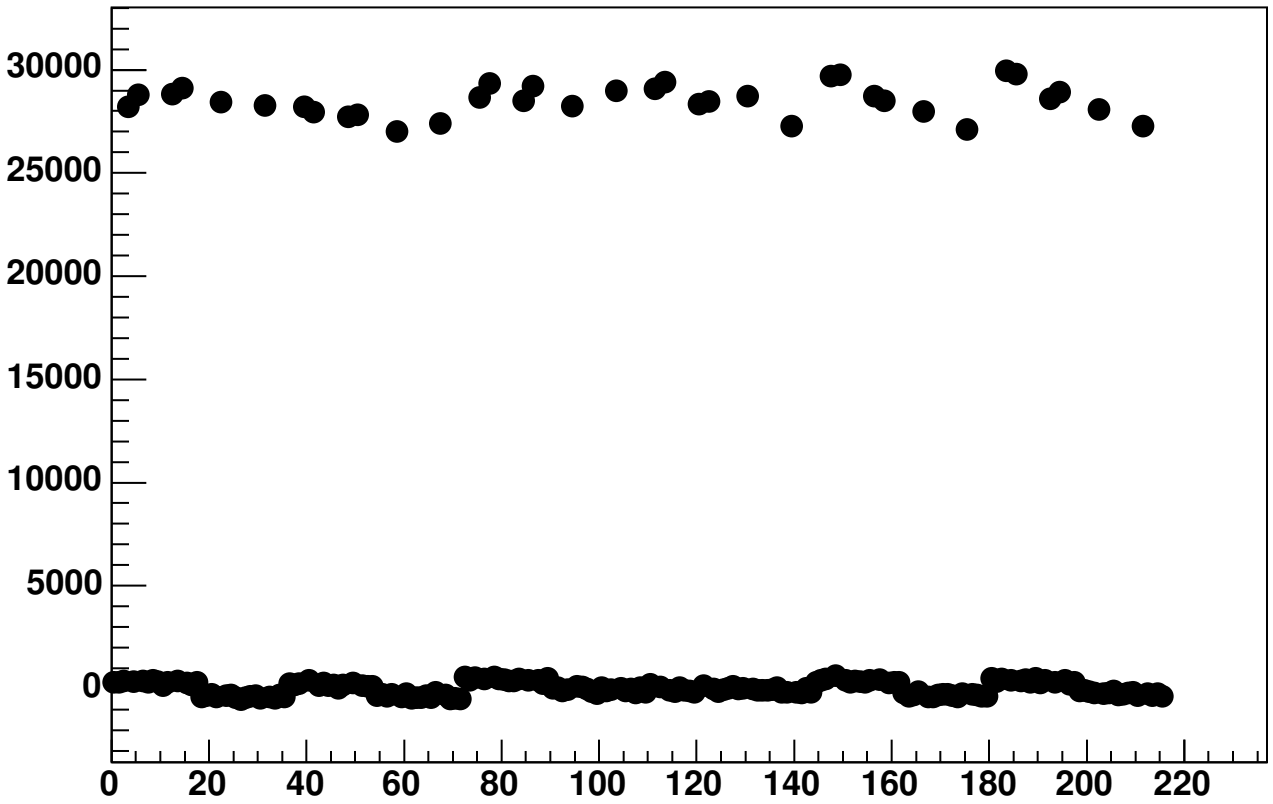
Enable 1, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



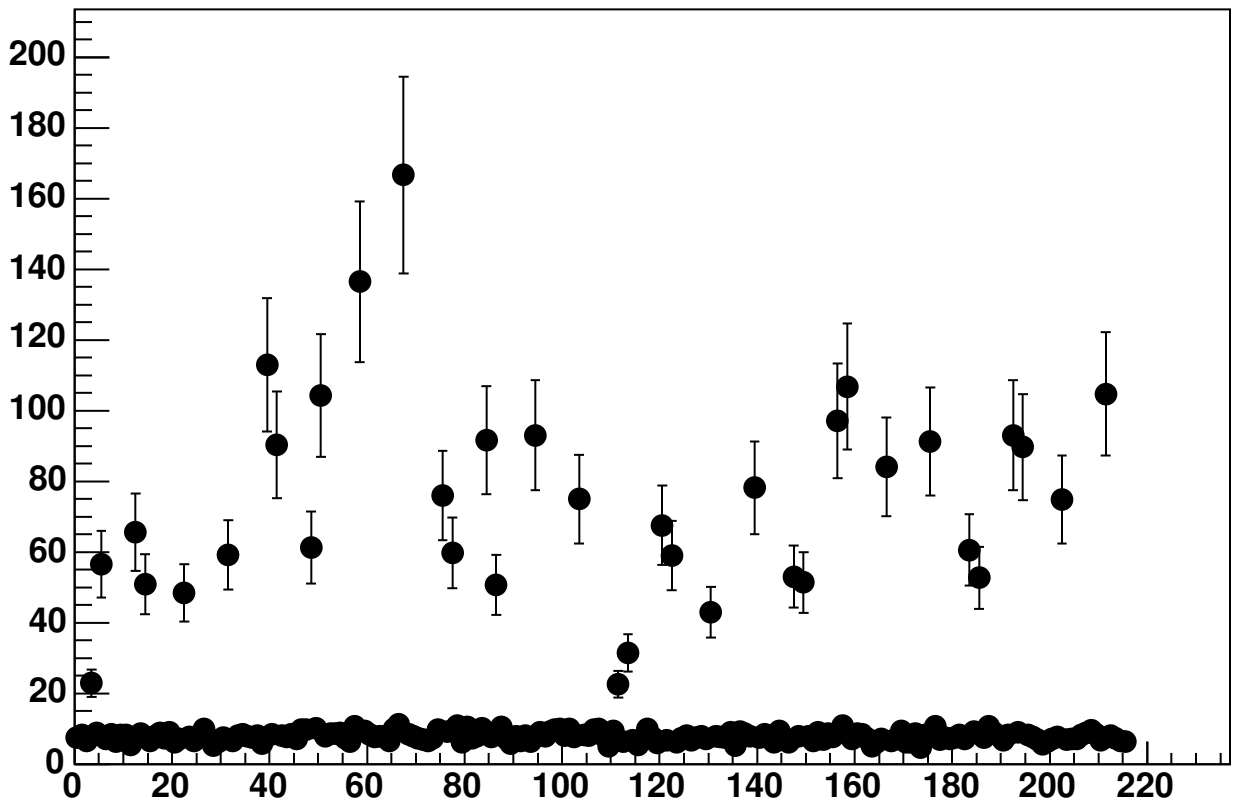
Enable 1, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



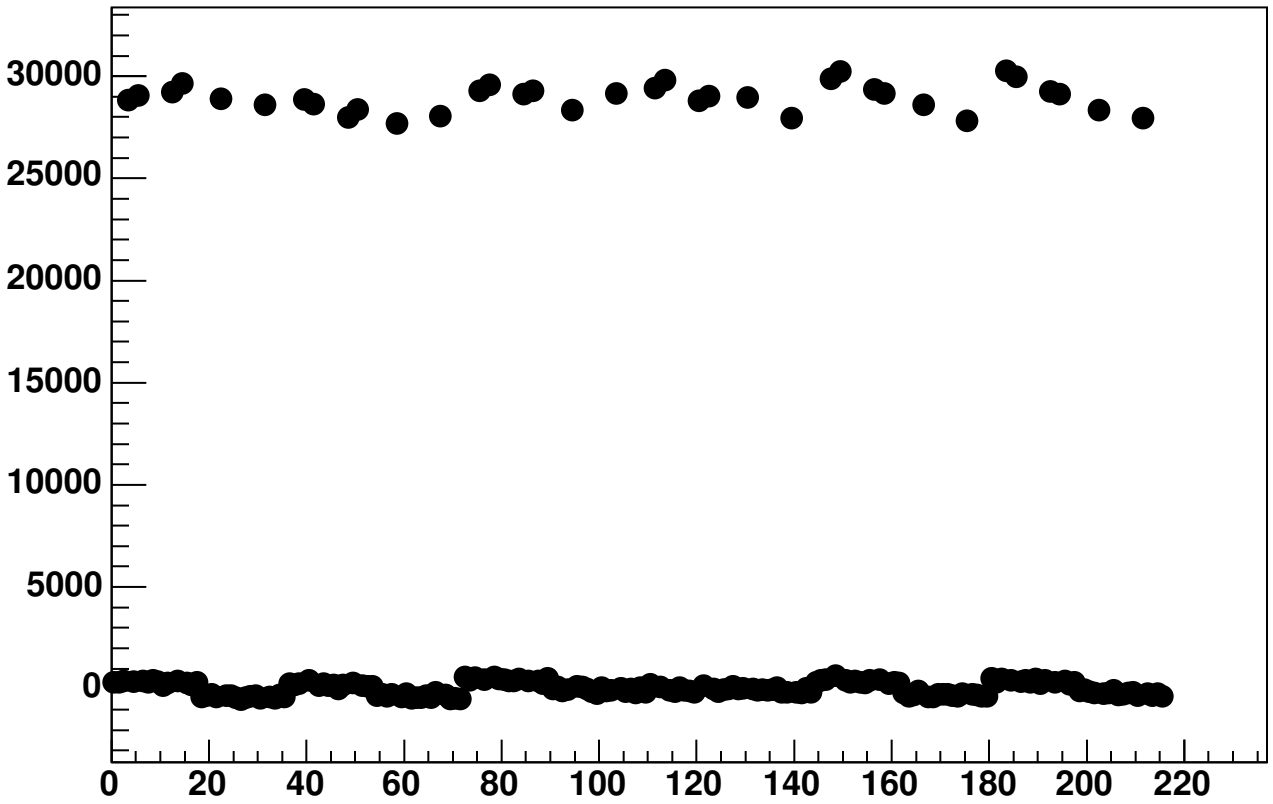
Enable 1, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



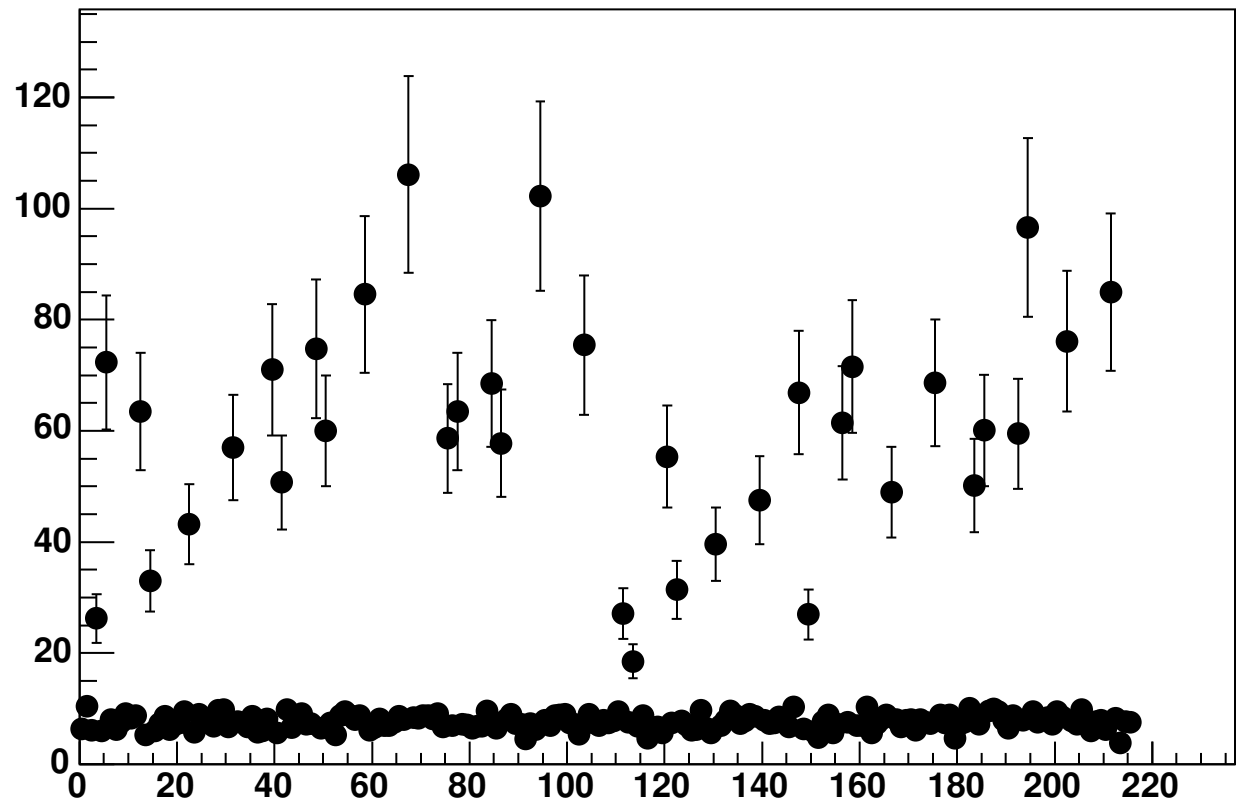
Enable 1, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



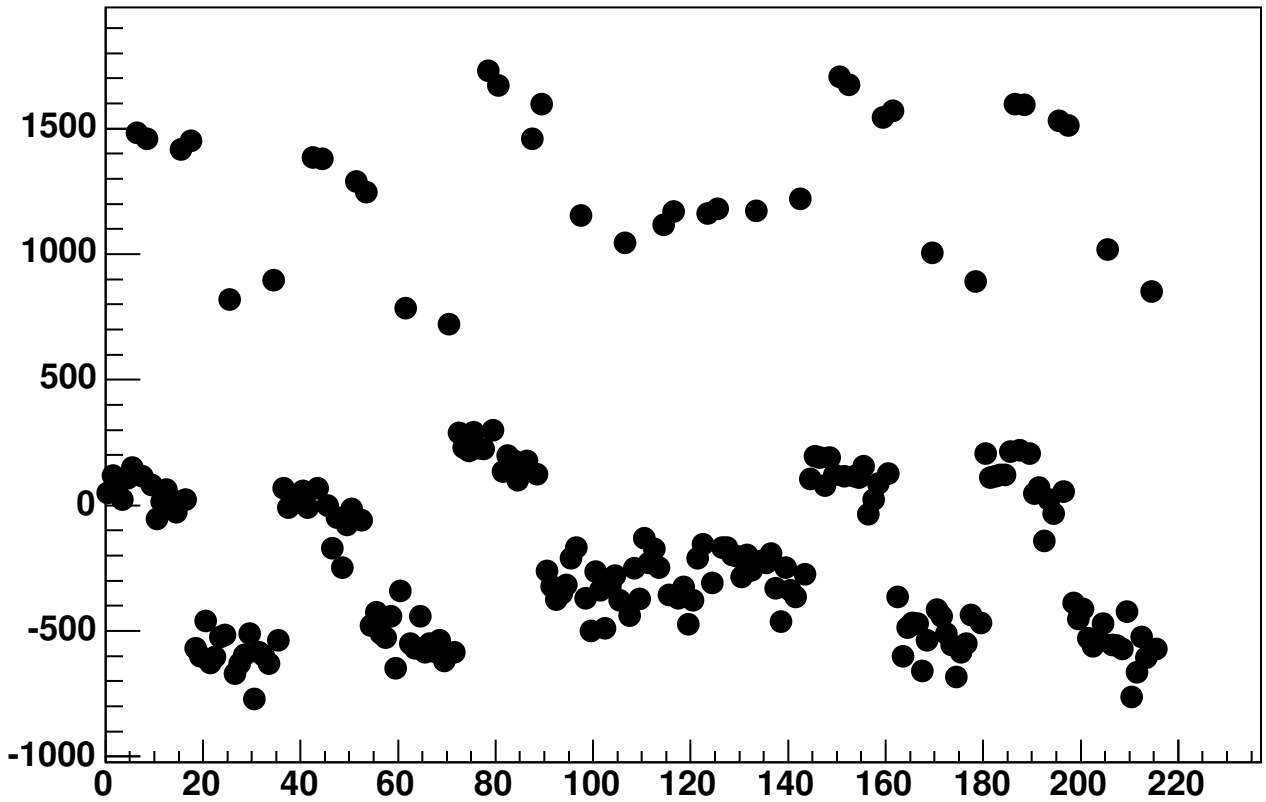
Enable 1, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



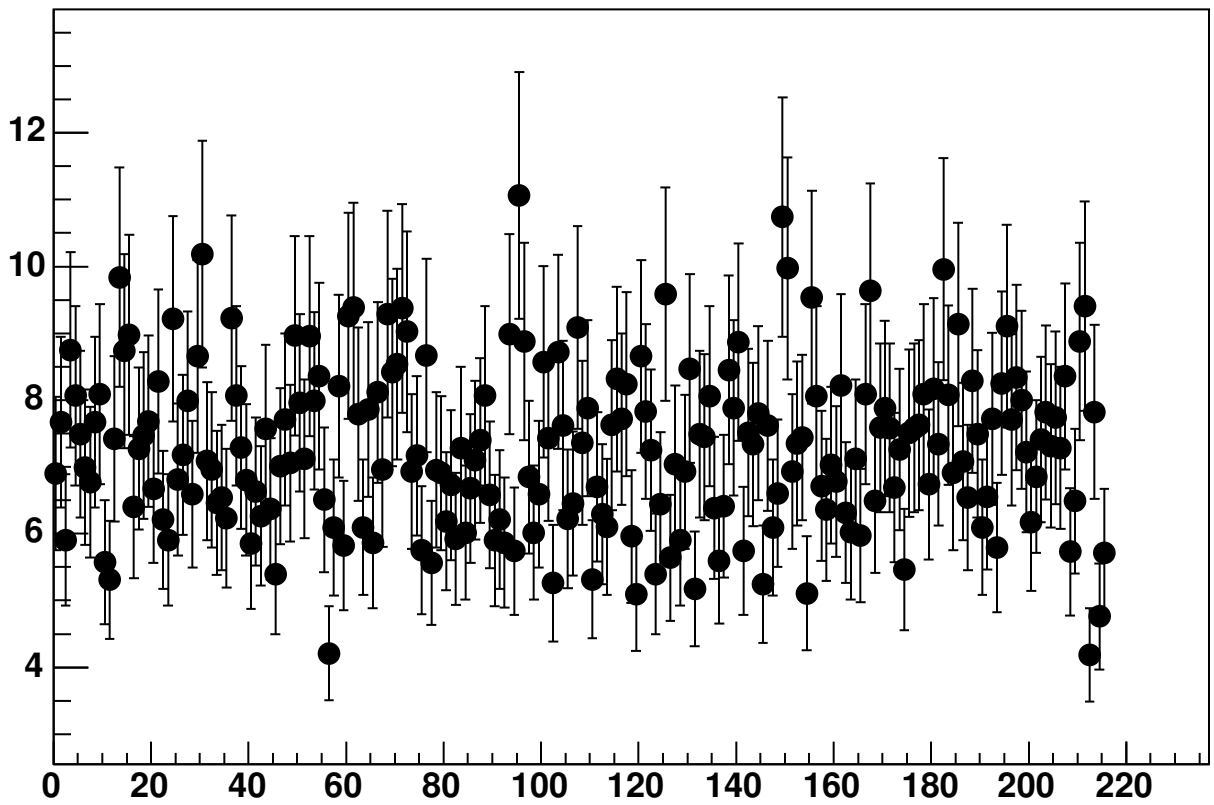
Enable 1, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



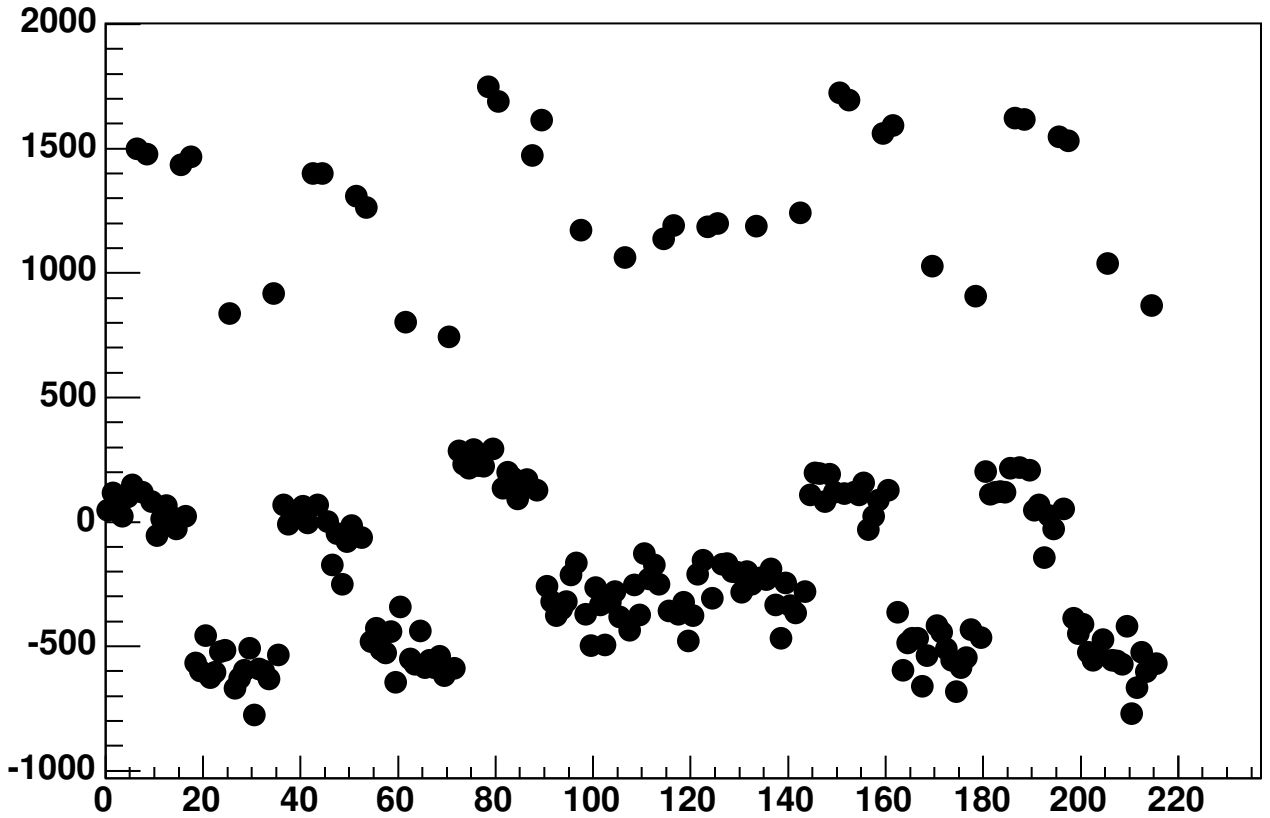
Enable 2, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



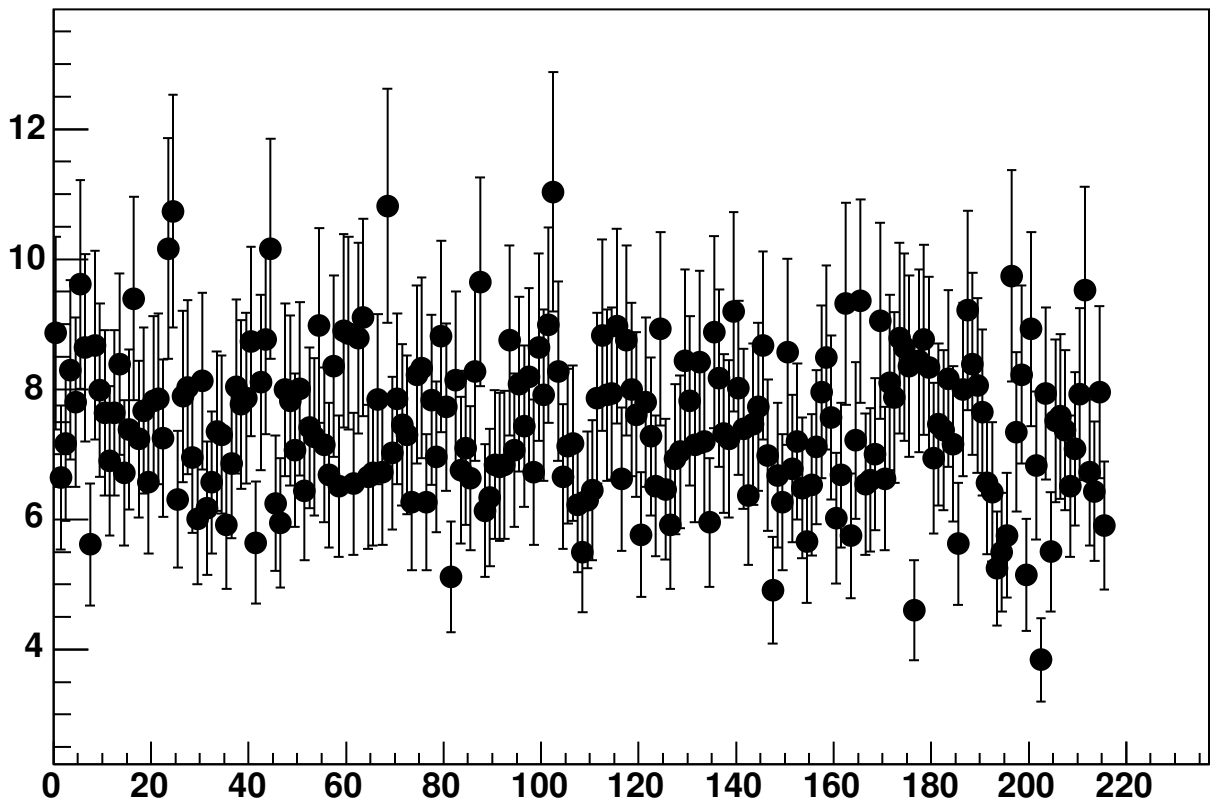
Enable 2, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



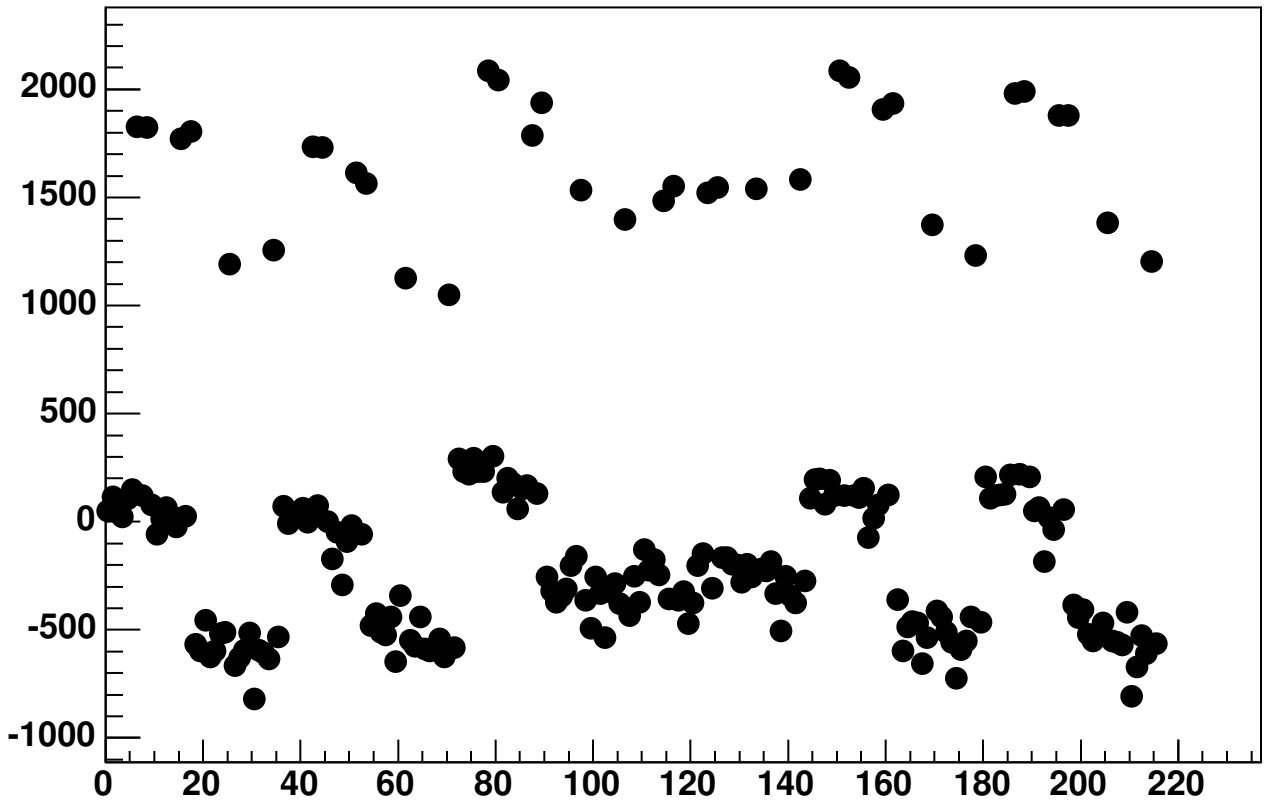
Enable 2, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



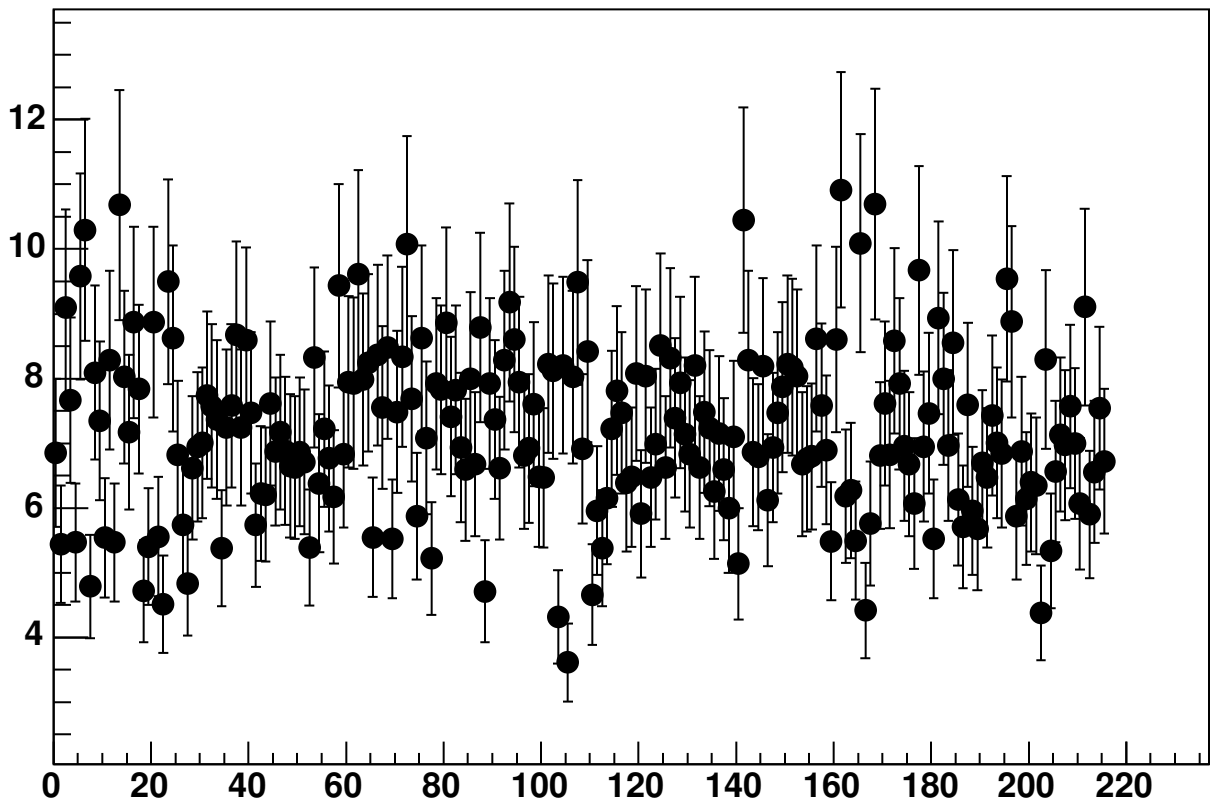
Enable 2, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



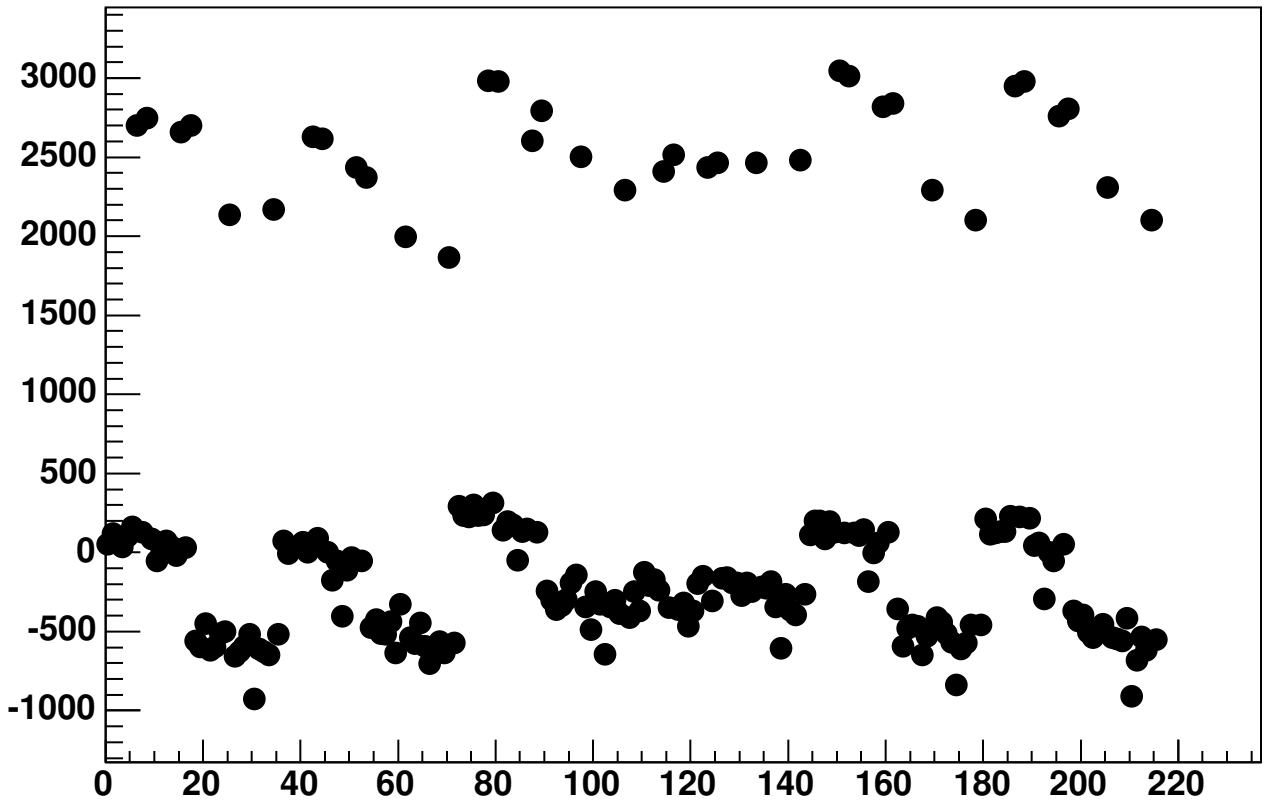
Enable 2, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



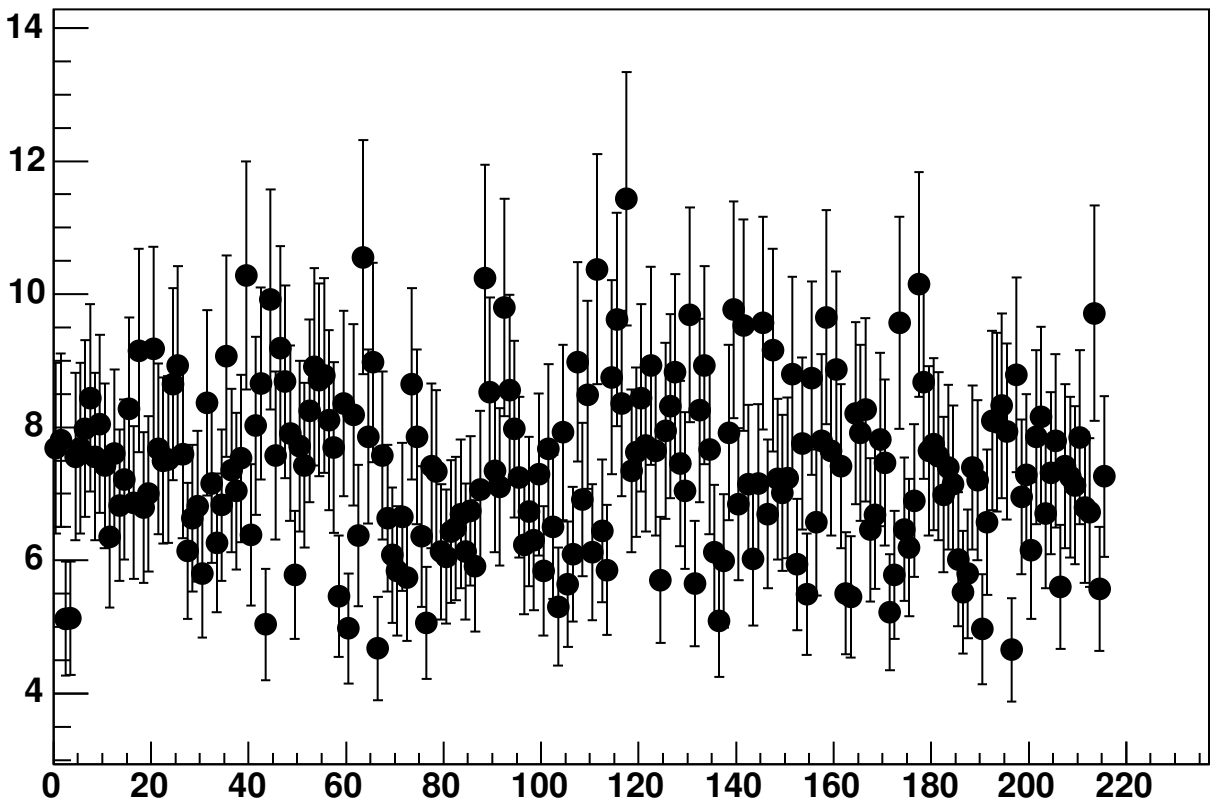
Enable 2, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



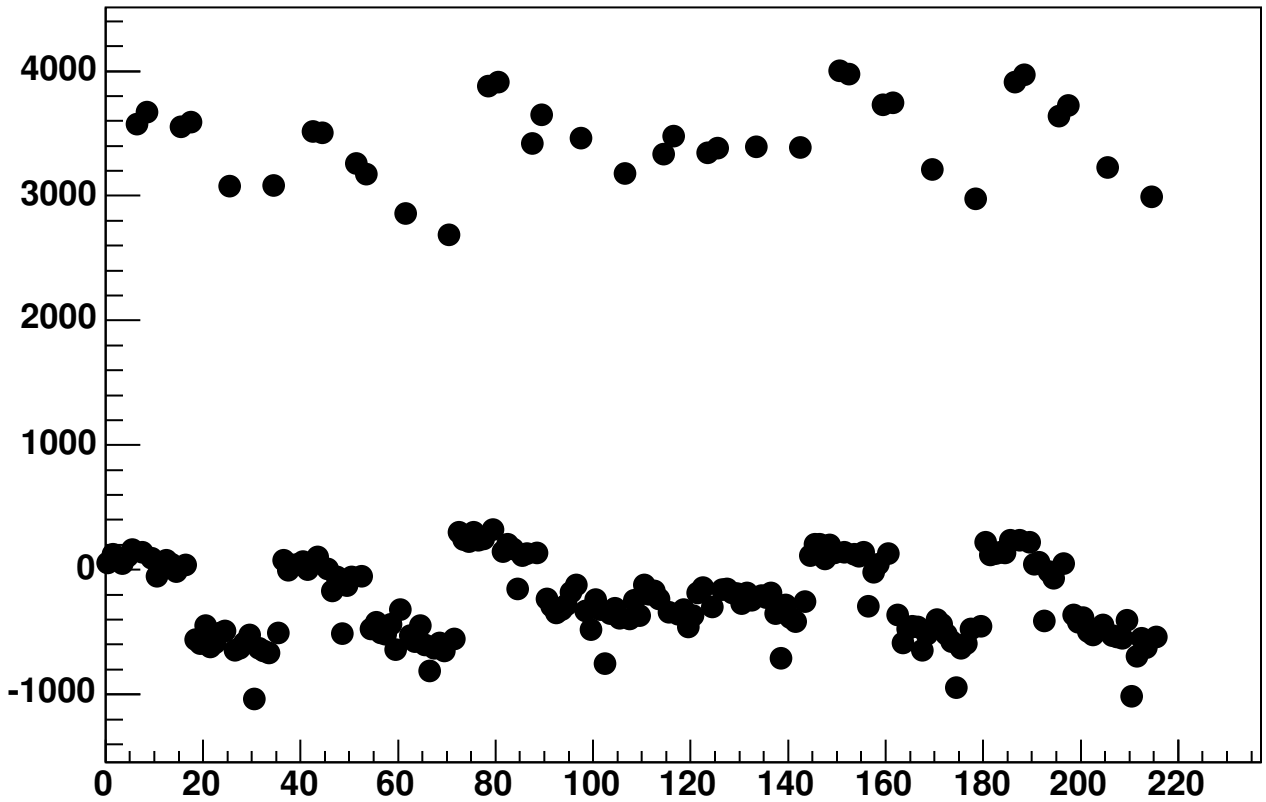
Enable 2, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



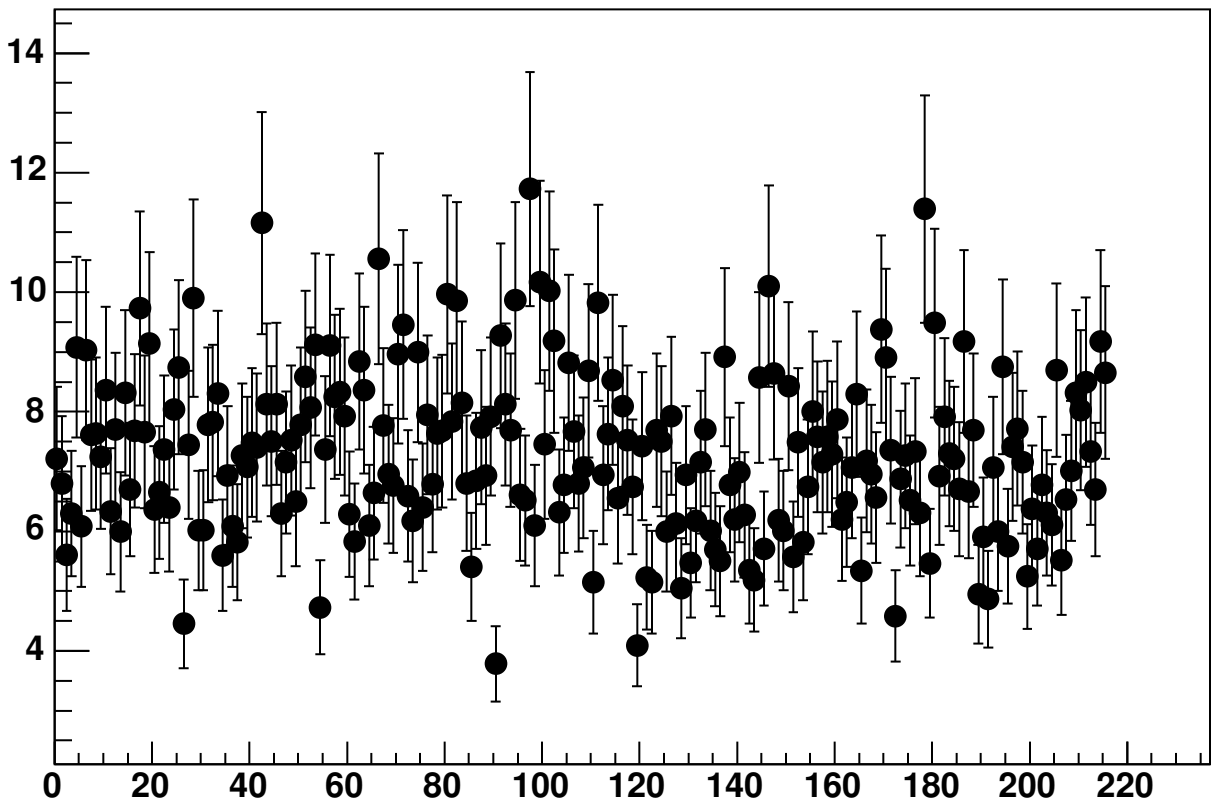
Enable 2, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



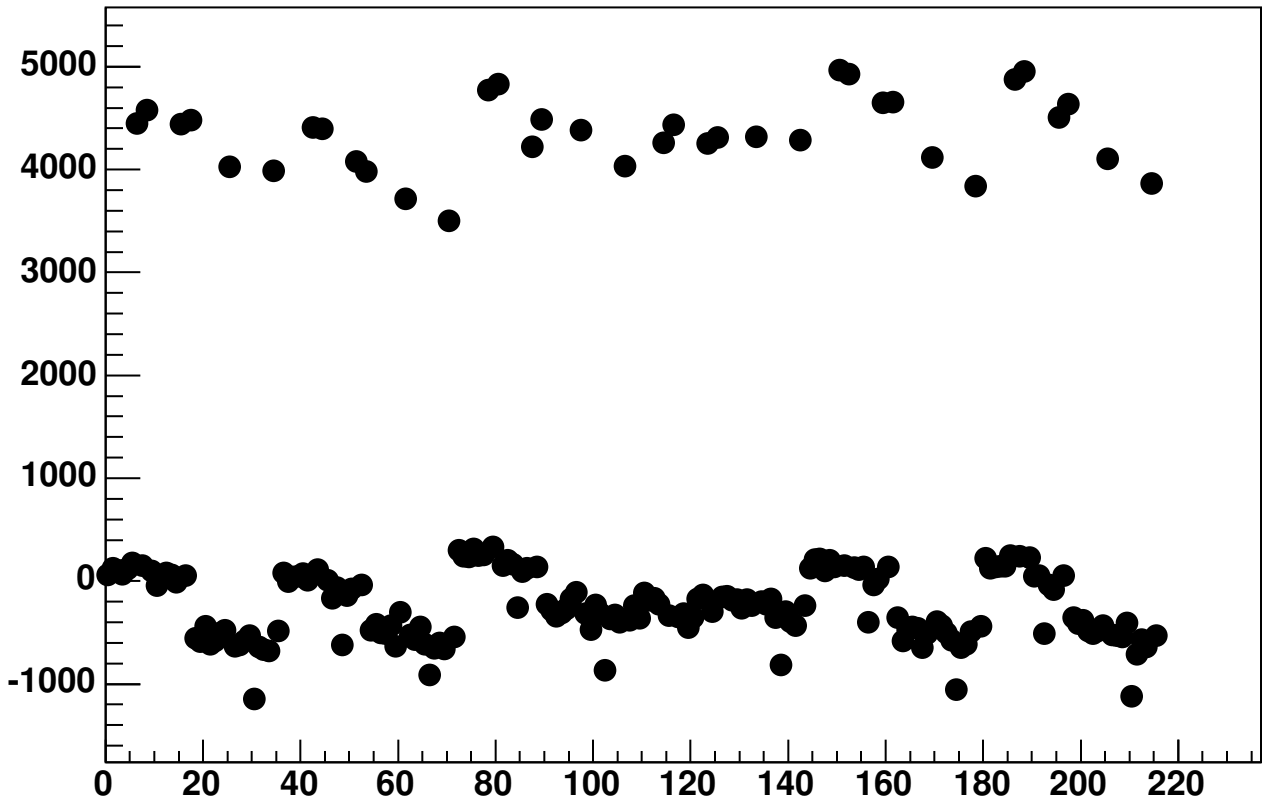
Enable 2, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



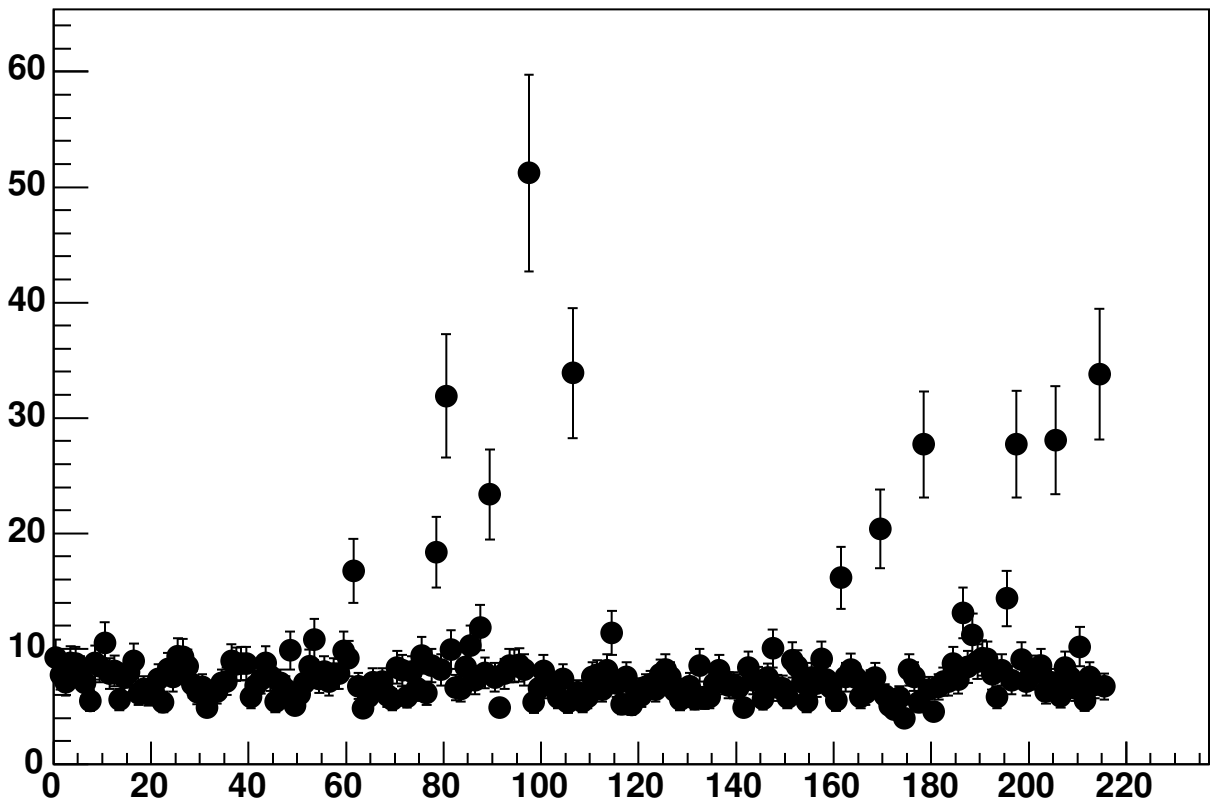
Enable 2, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



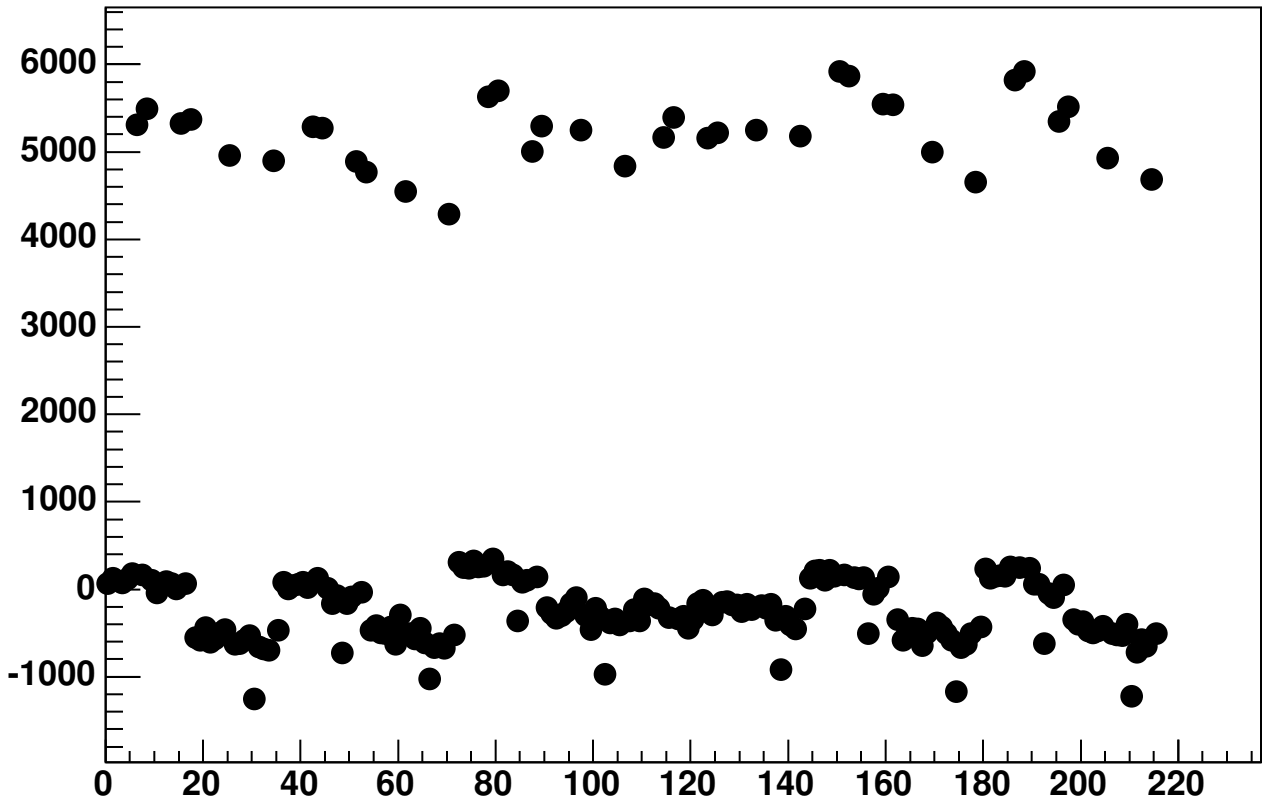
Enable 2, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



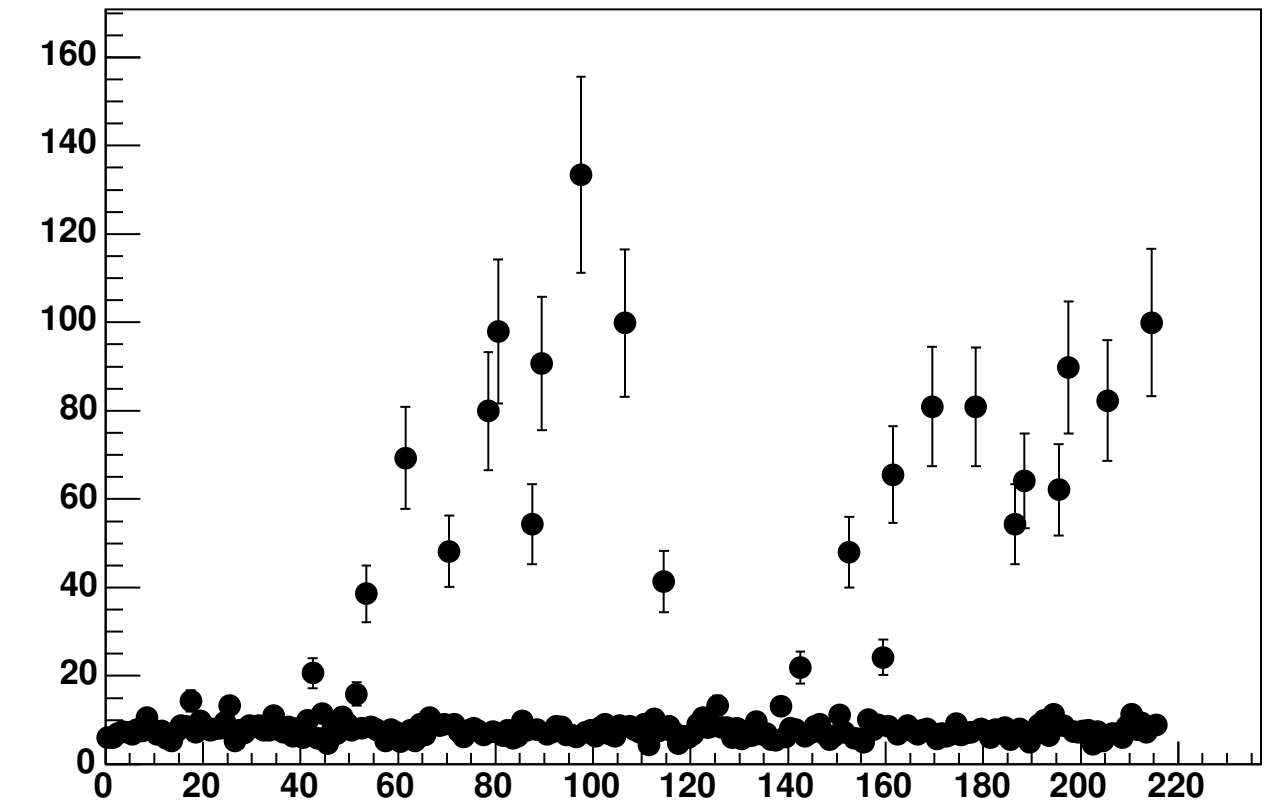
Enable 2, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



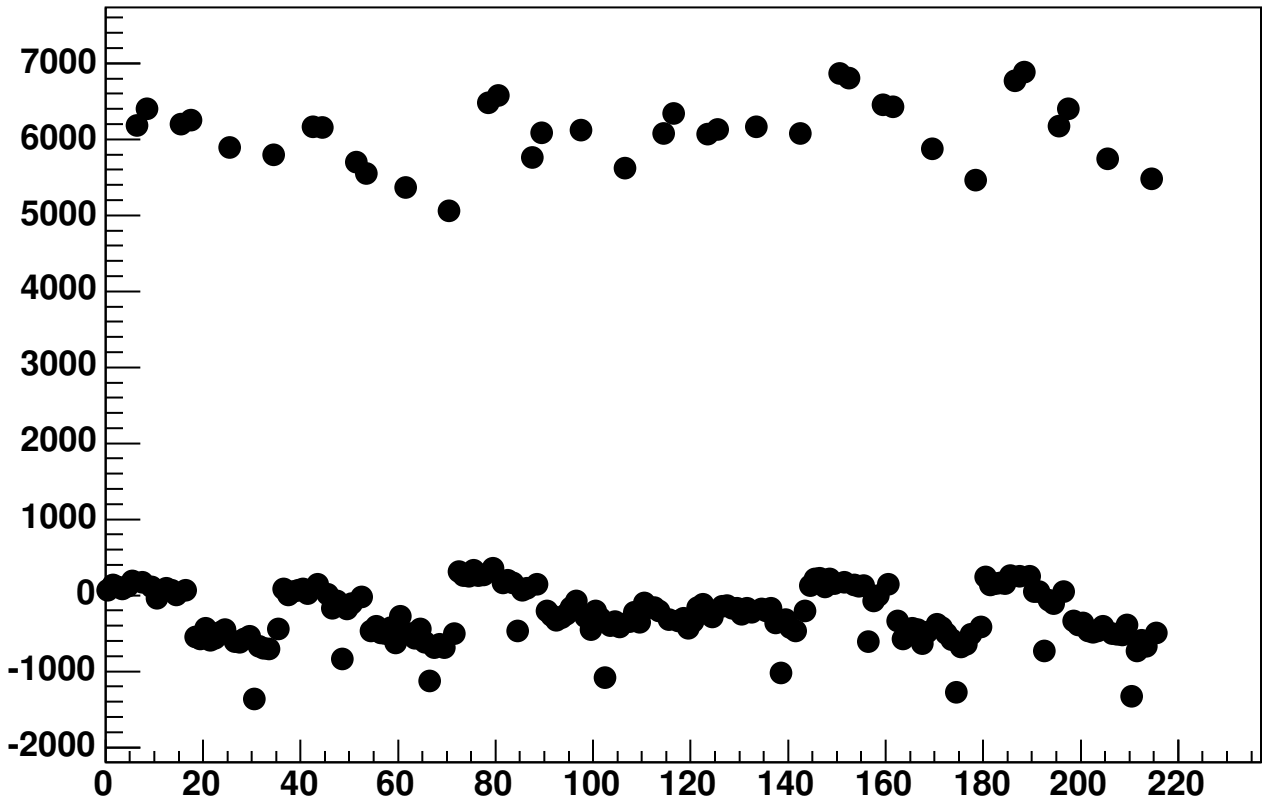
Enable 2, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



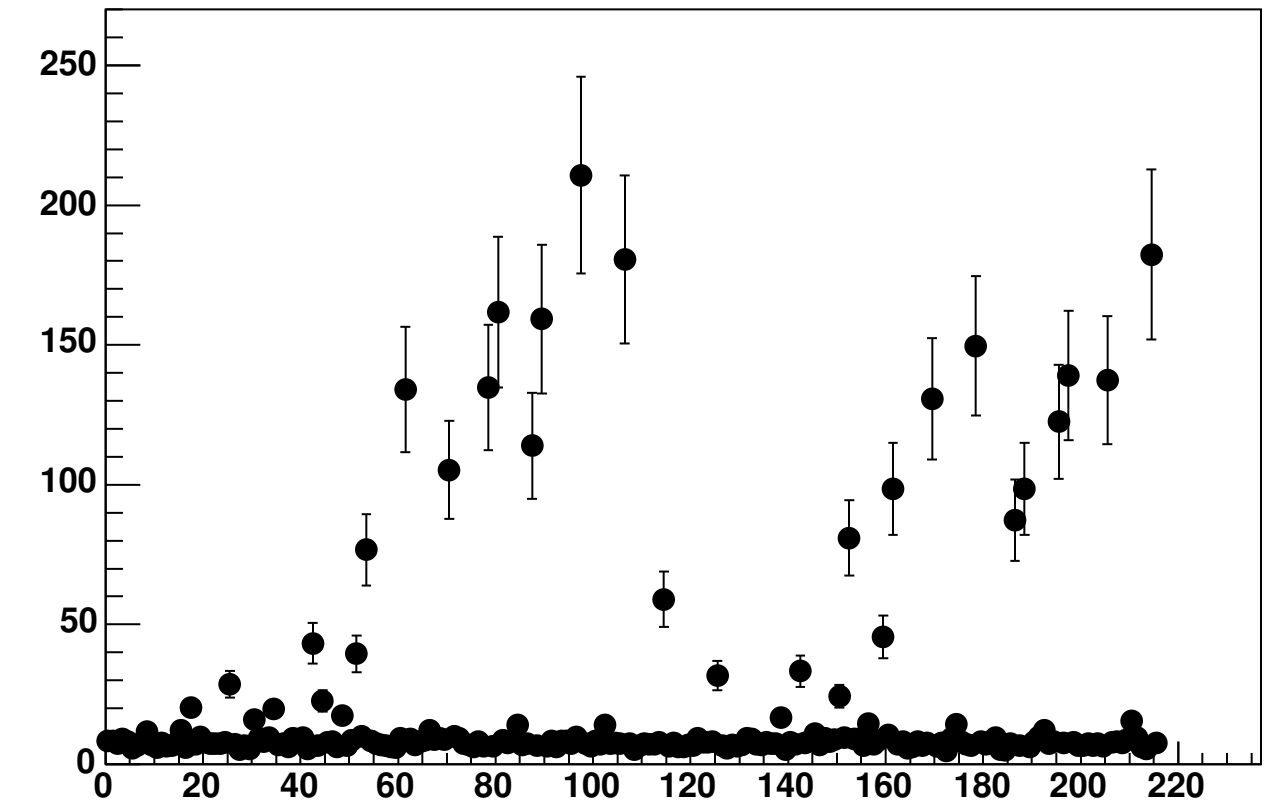
Enable 2, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



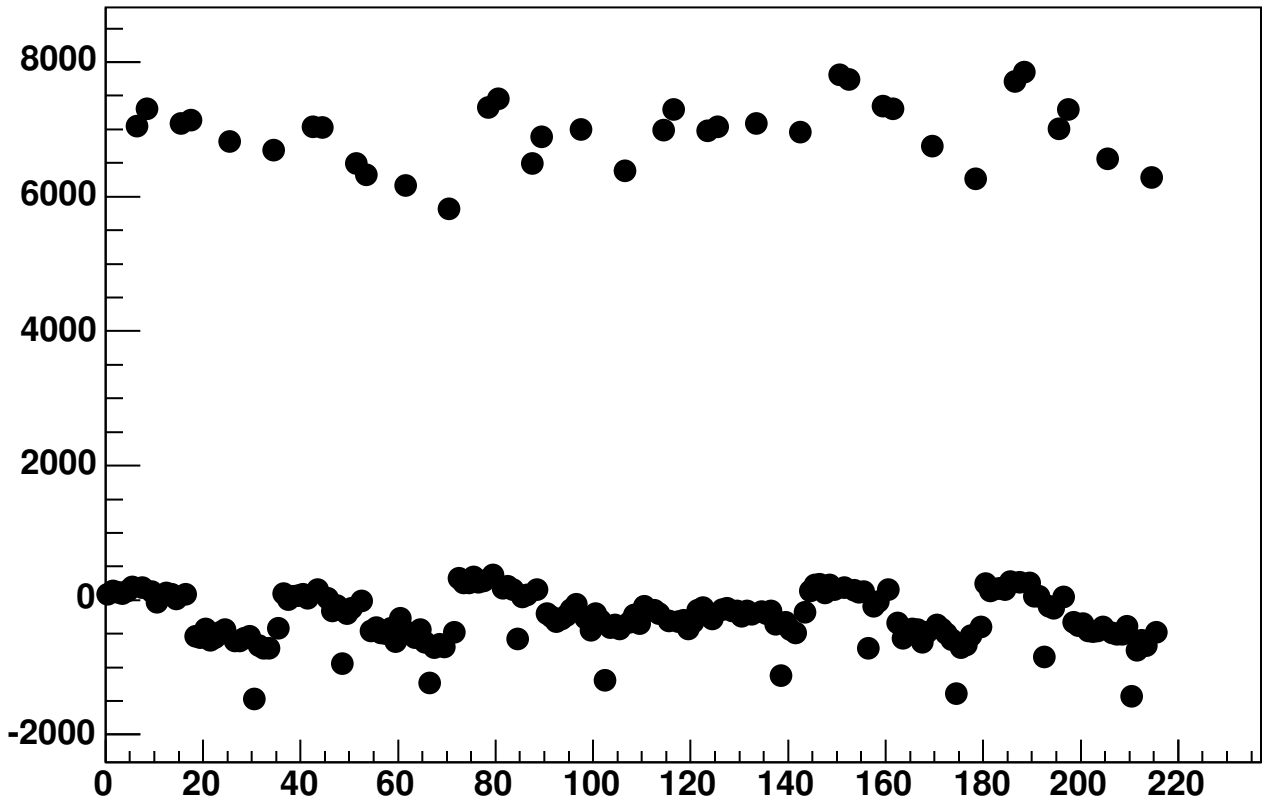
Enable 2, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



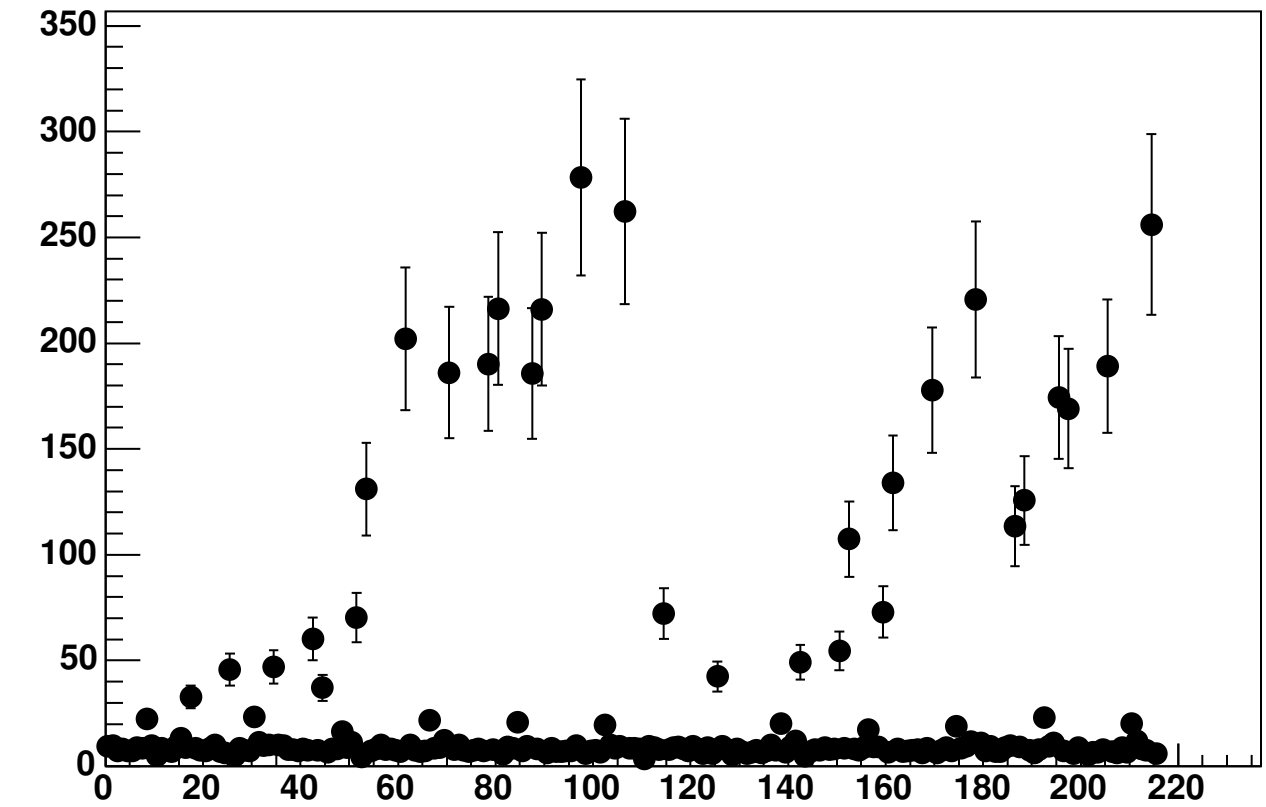
Enable 2, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



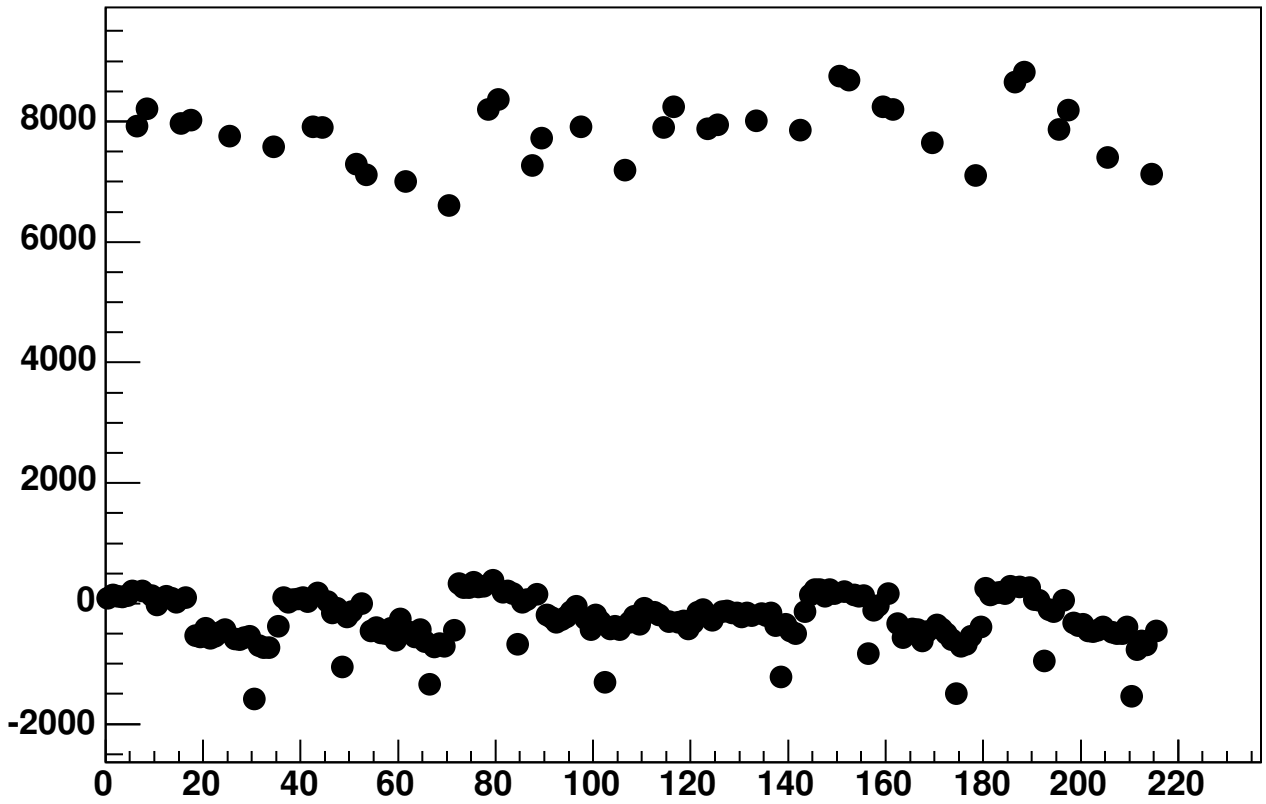
Enable 2, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



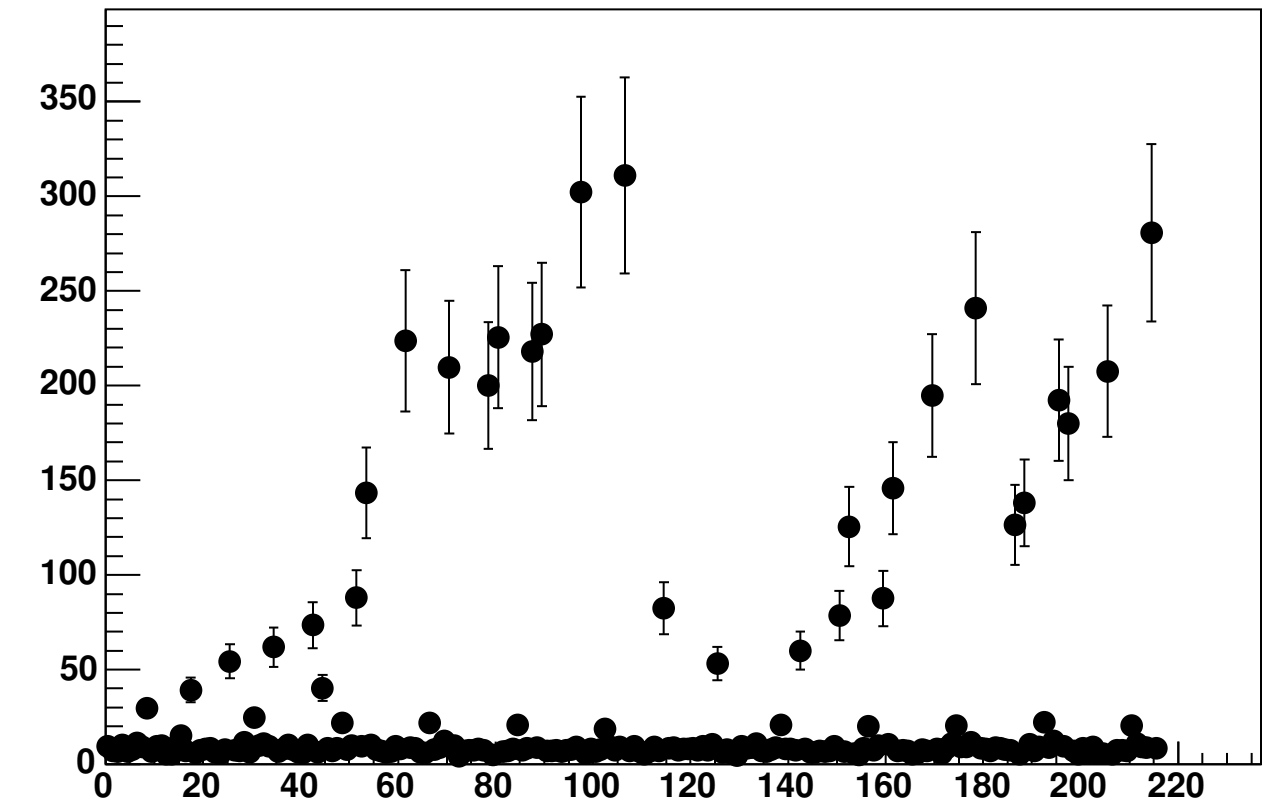
Enable 2, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



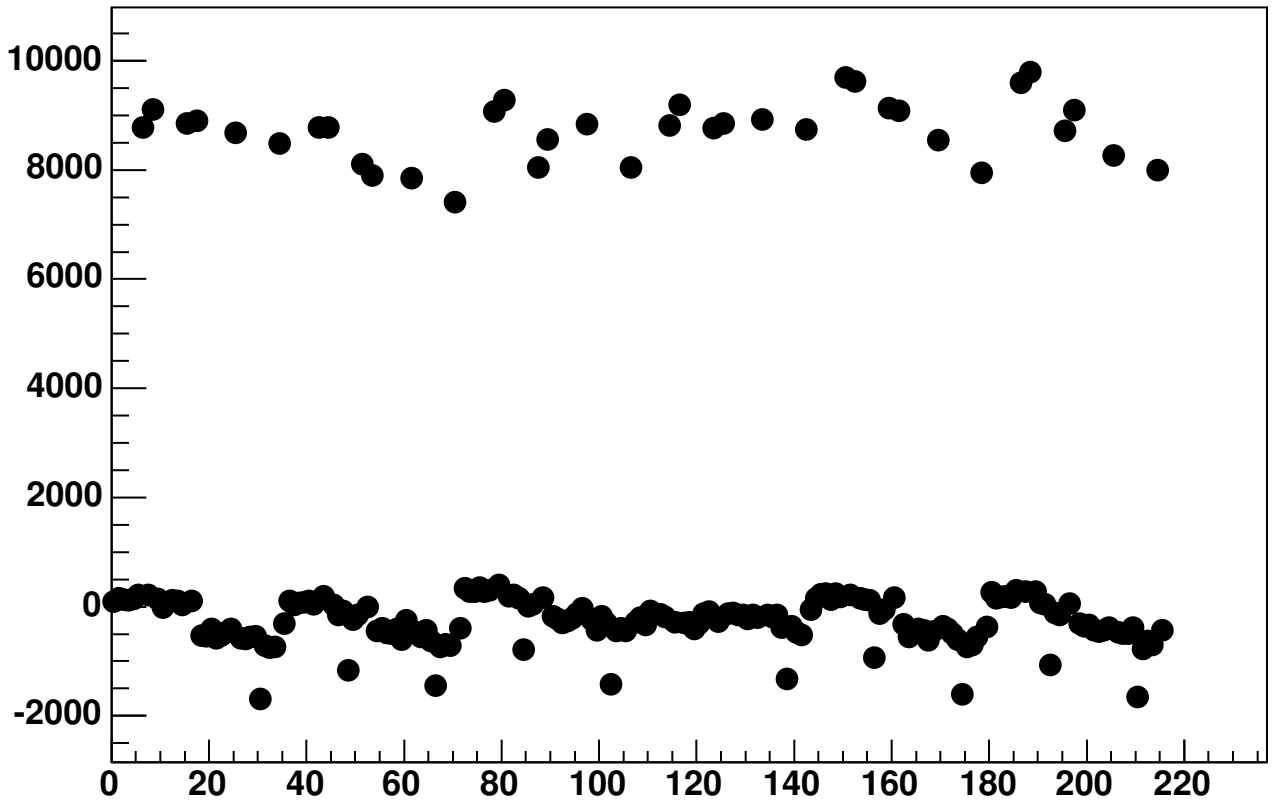
Enable 2, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



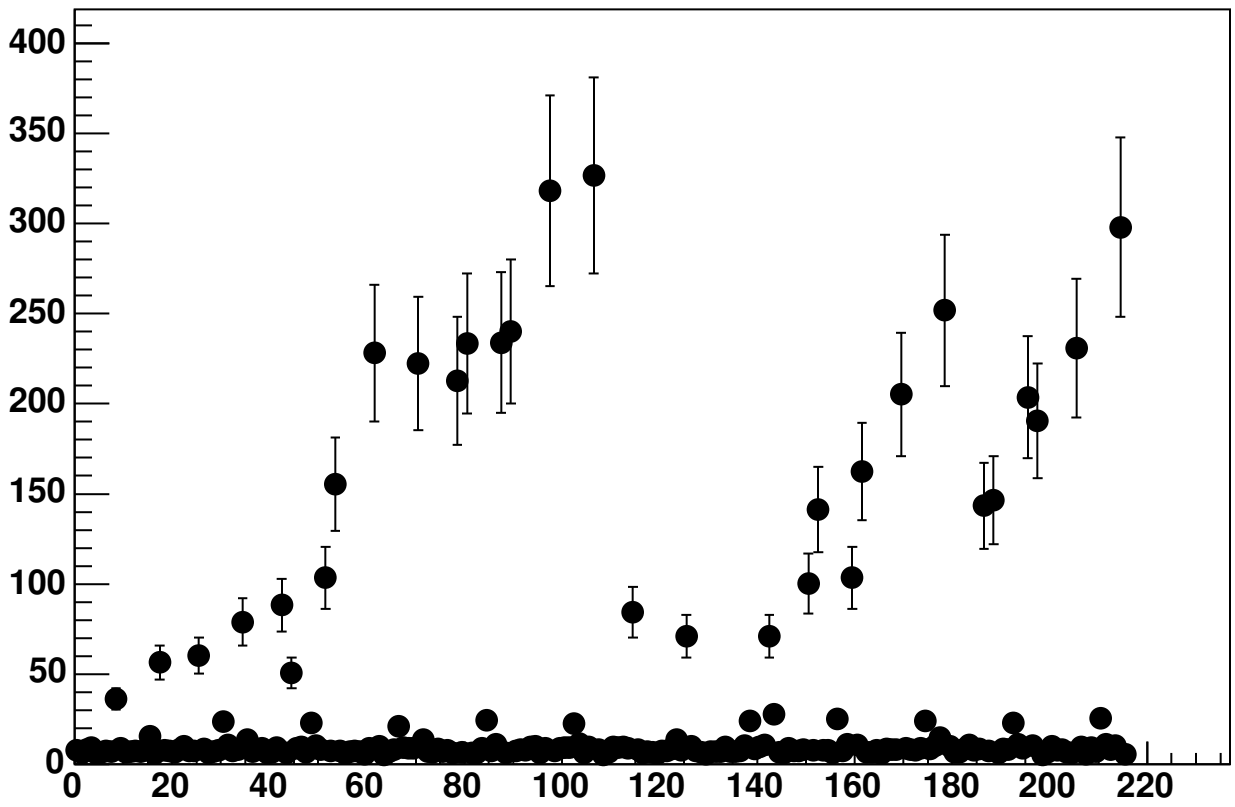
Enable 2, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



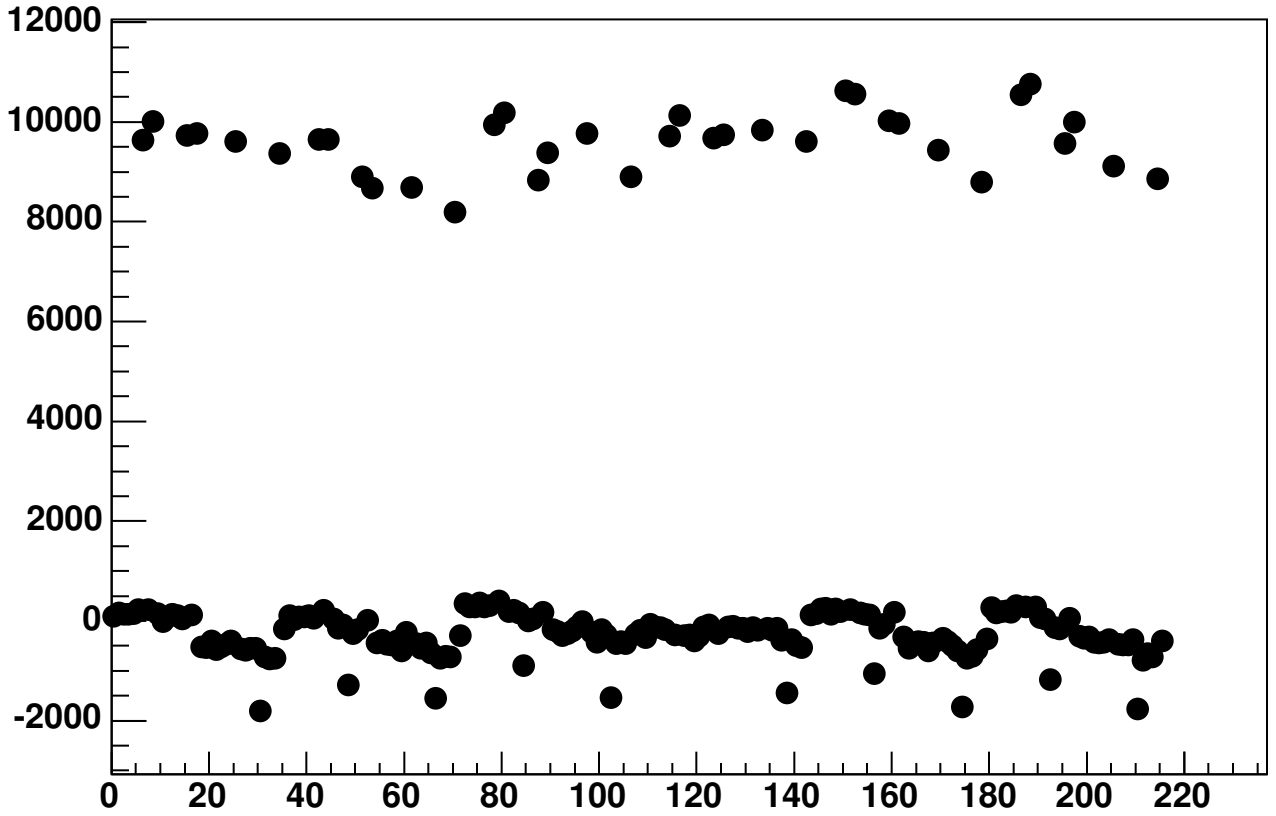
Enable 2, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



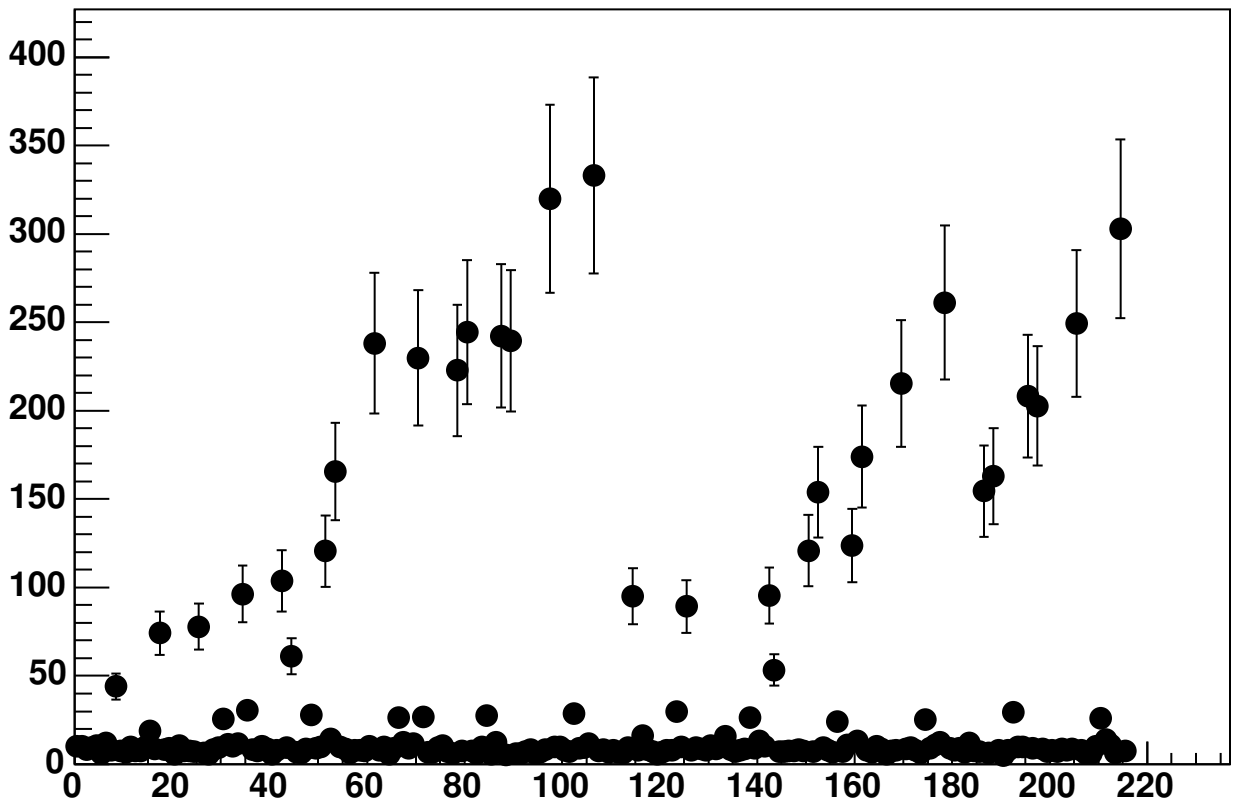
Enable 2, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



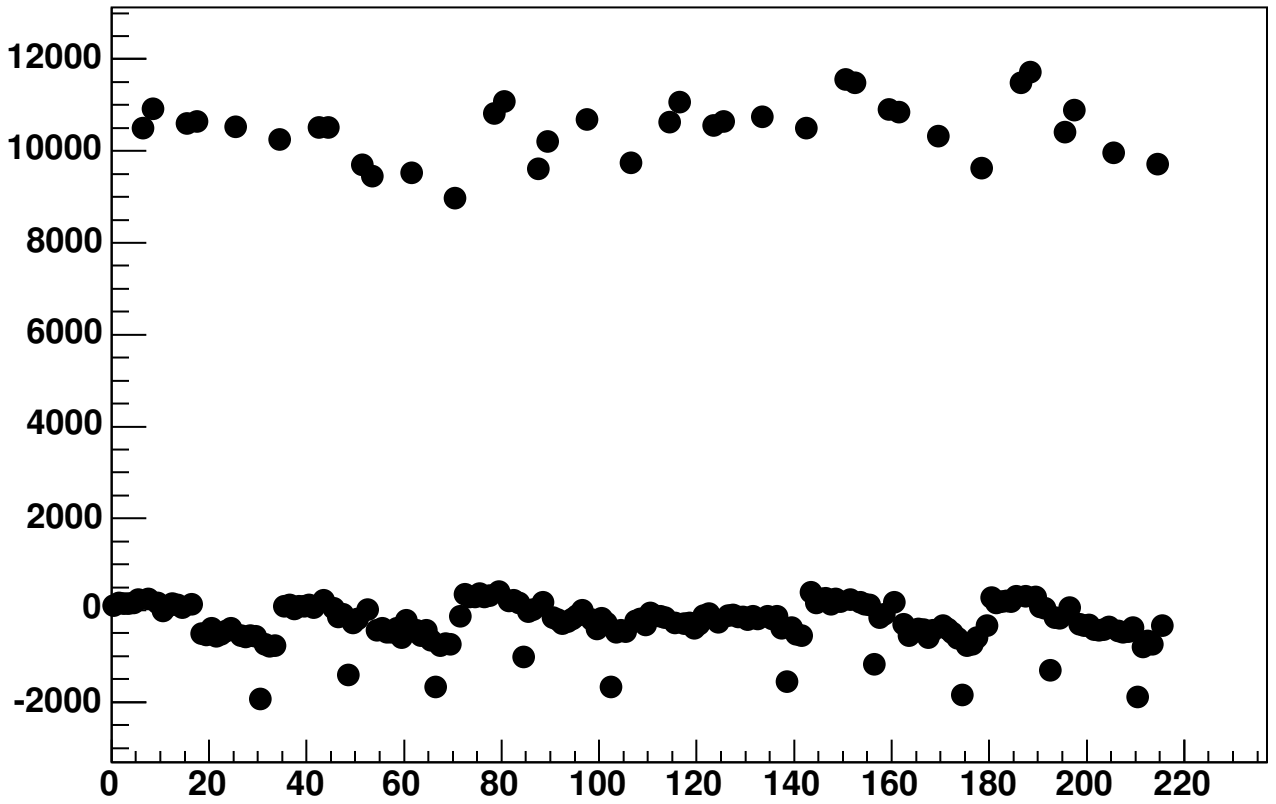
Enable 2, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



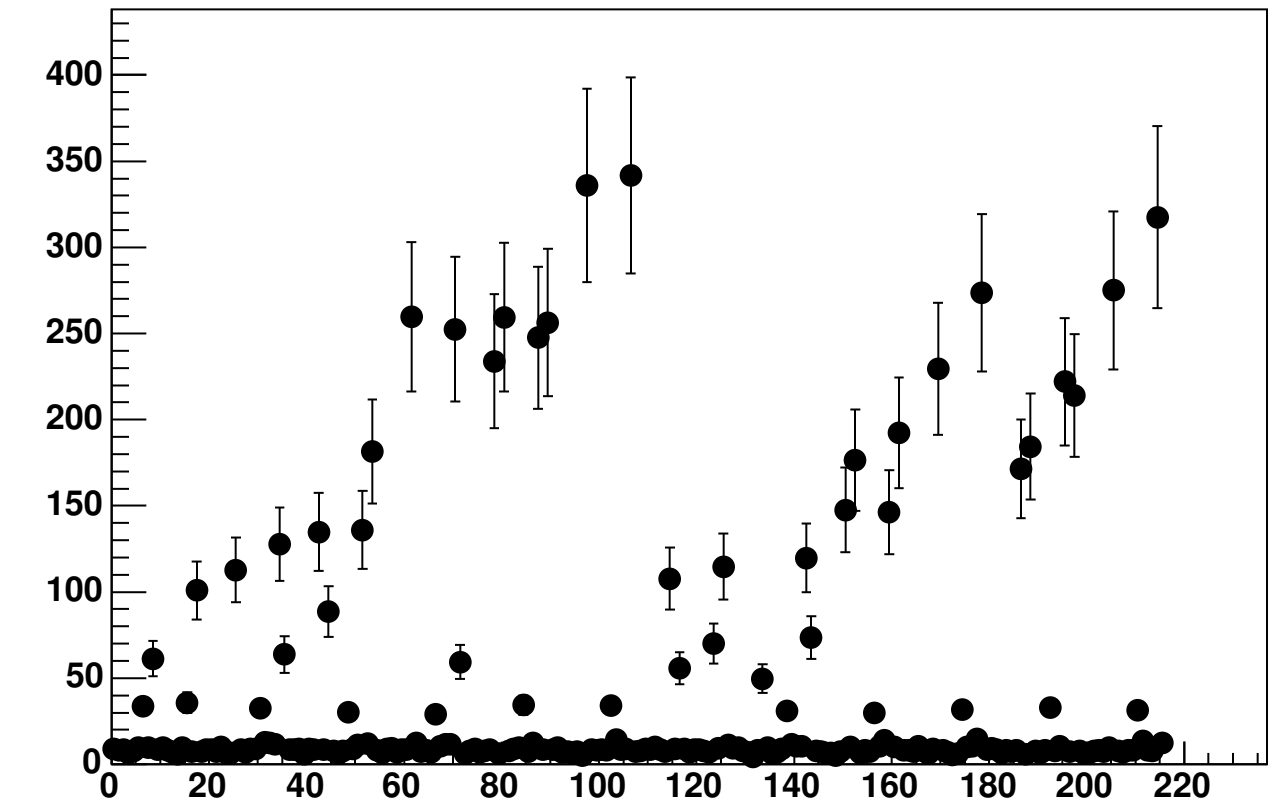
Enable 2, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



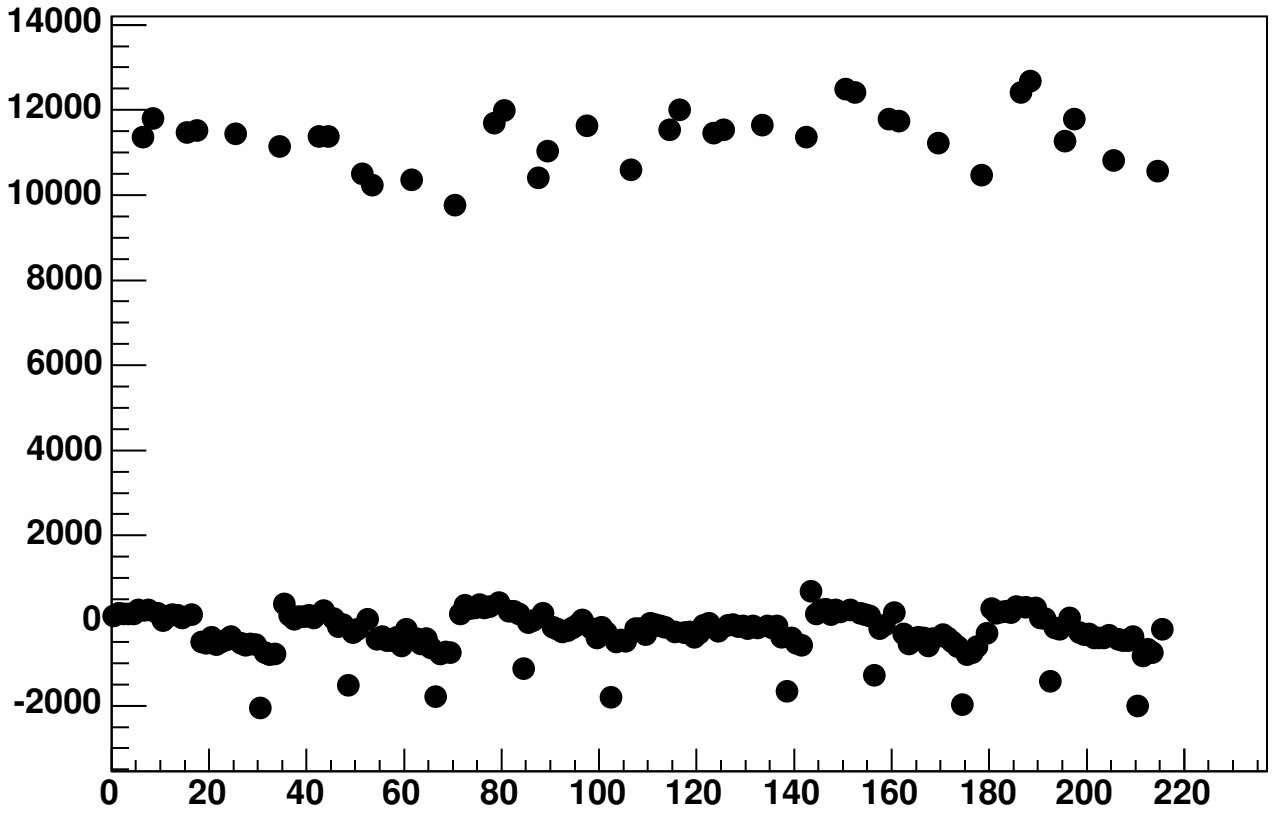
Enable 2, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



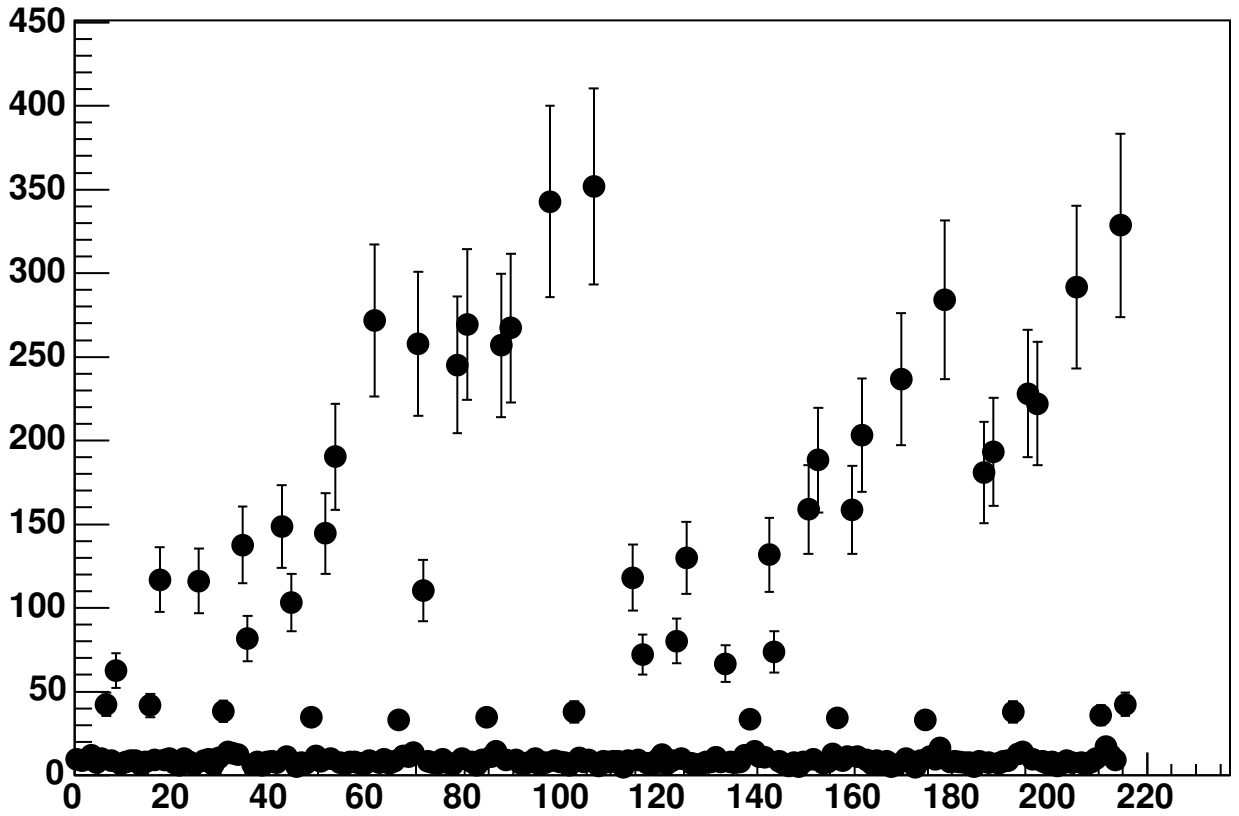
Enable 2, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



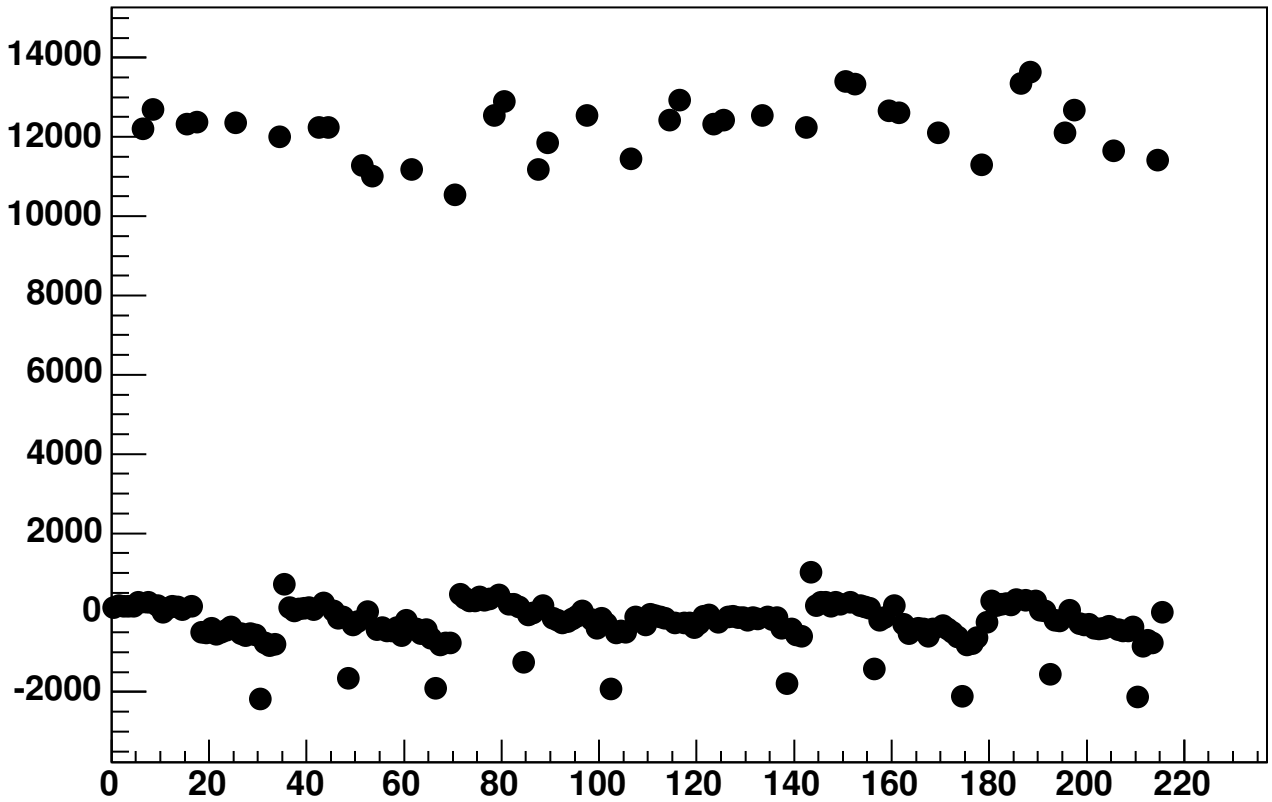
Enable 2, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



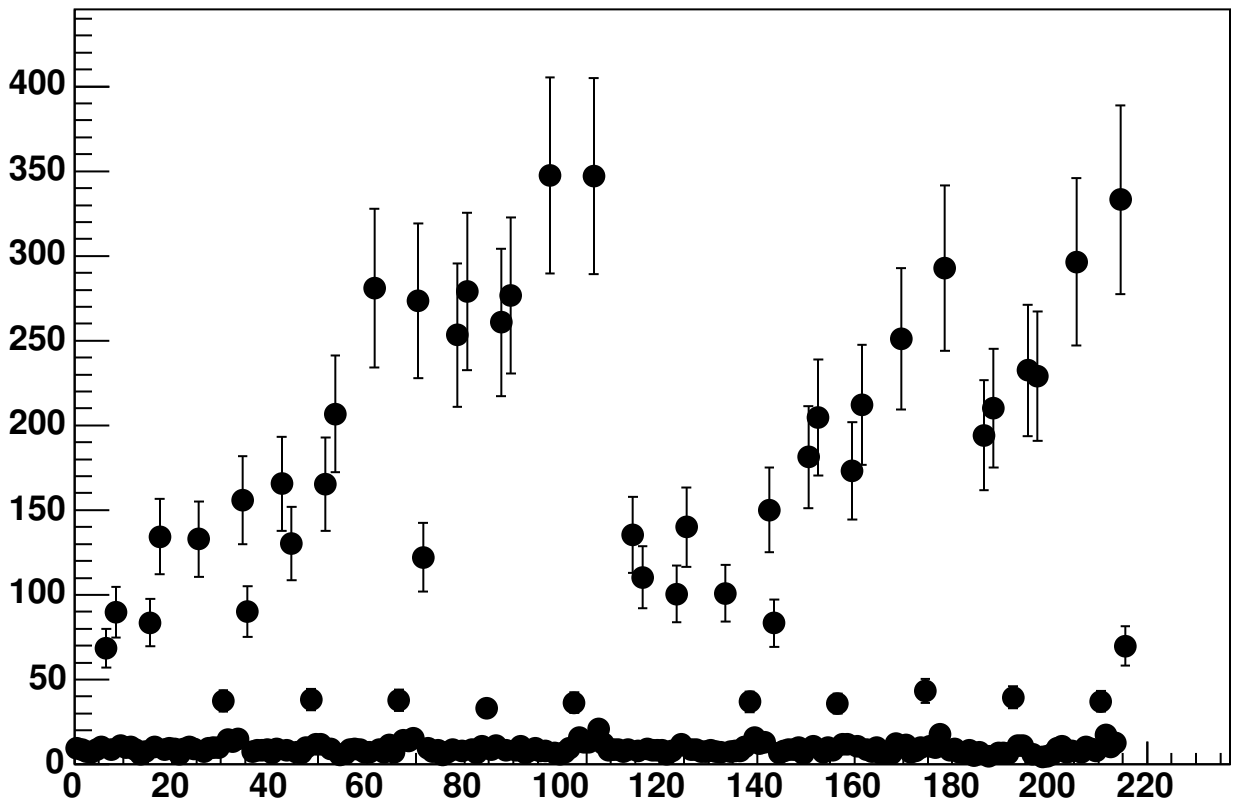
Enable 2, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



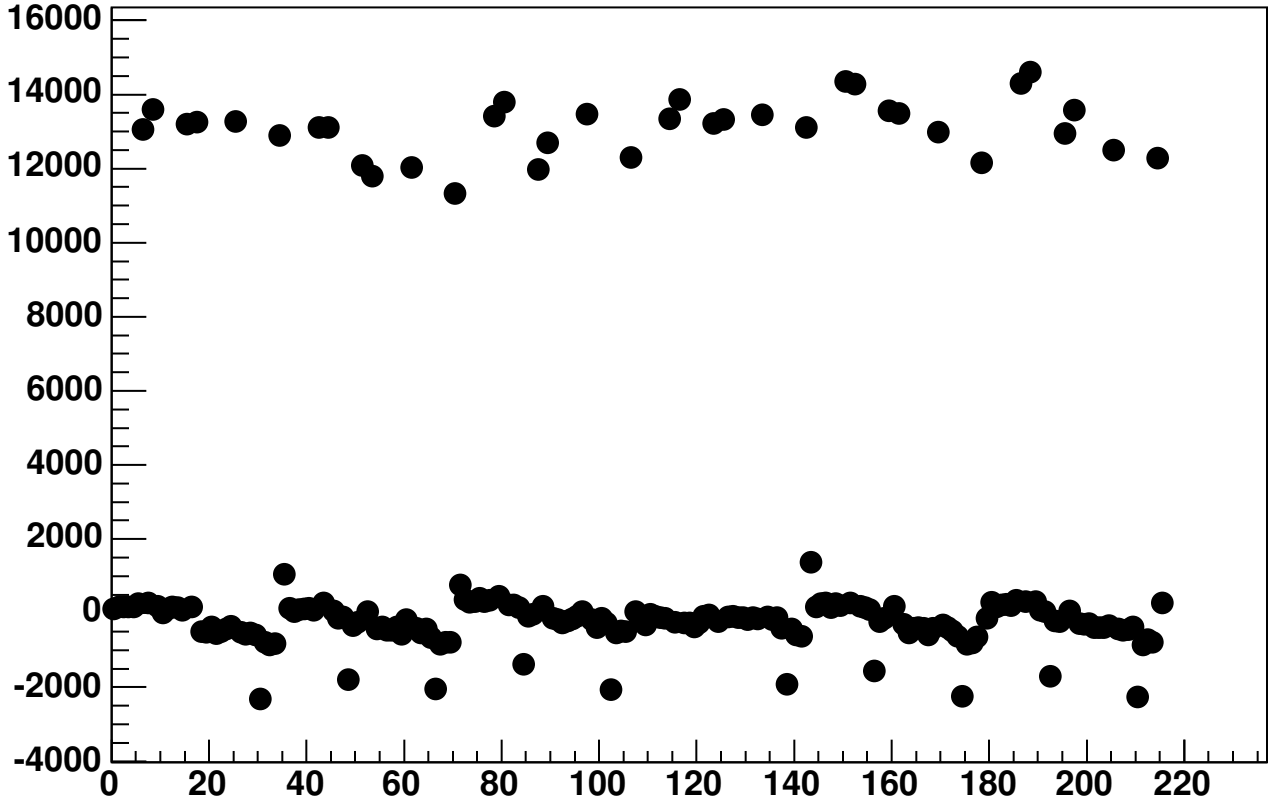
Enable 2, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



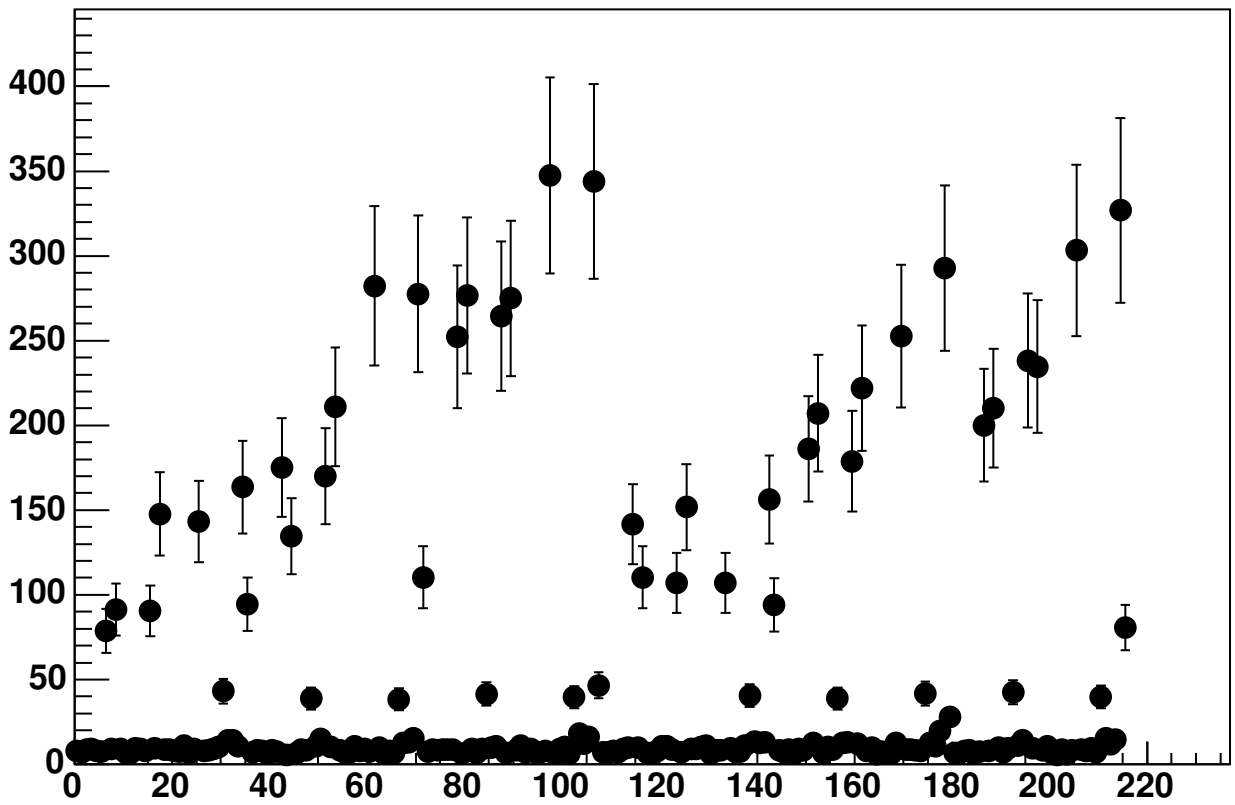
Enable 2, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



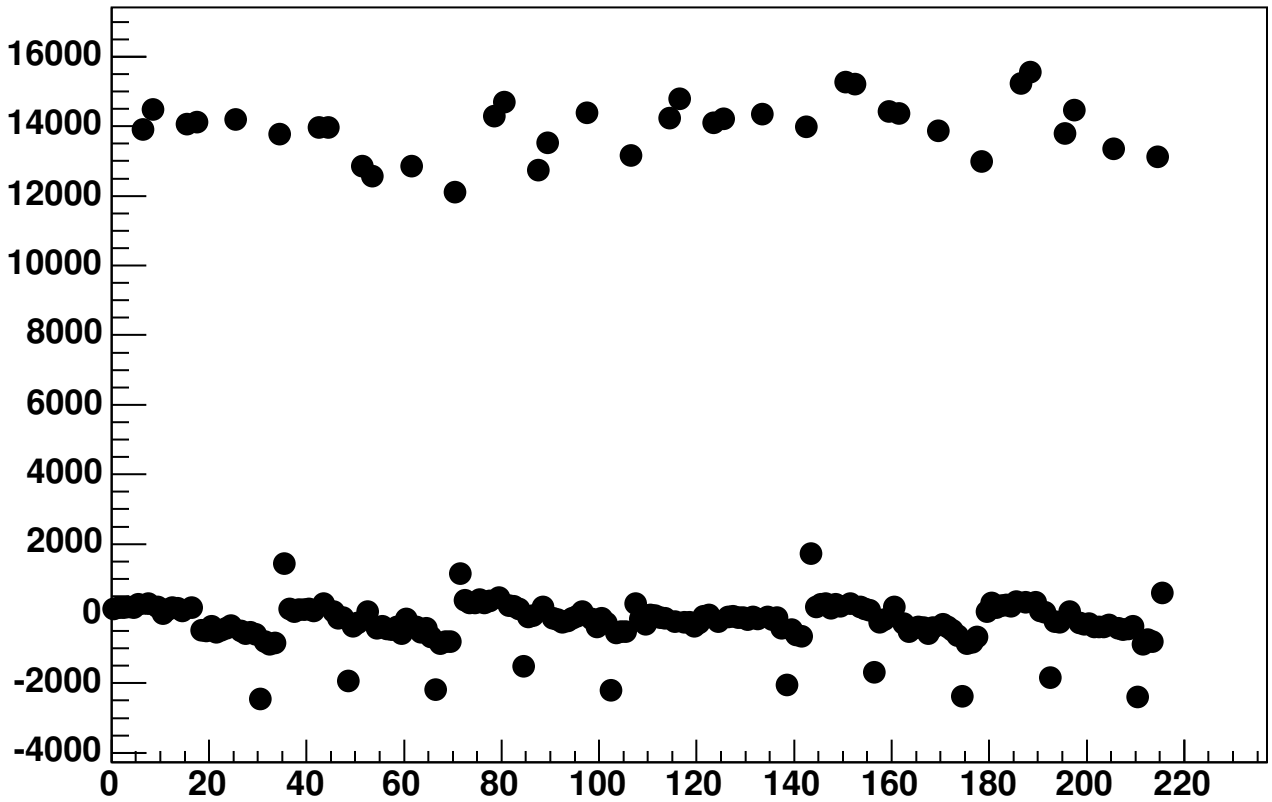
Enable 2, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



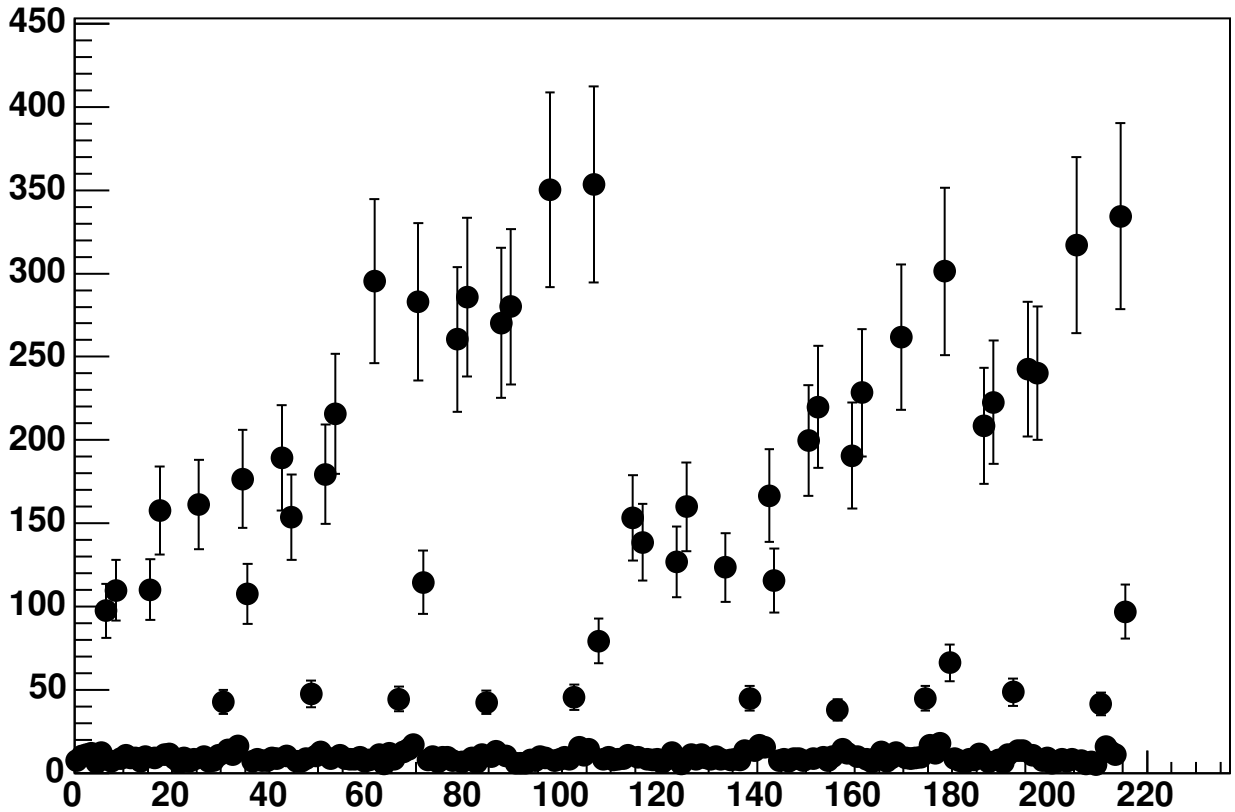
Enable 2, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



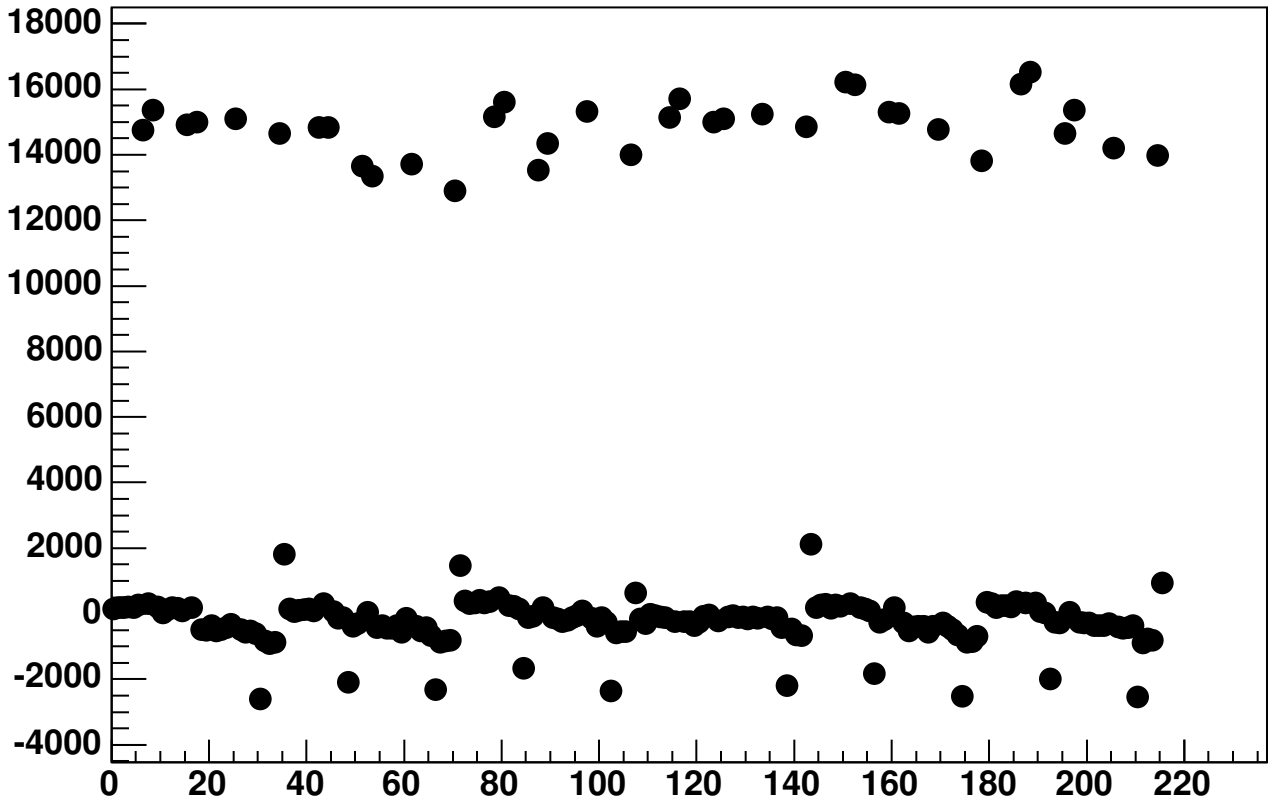
Enable 2, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



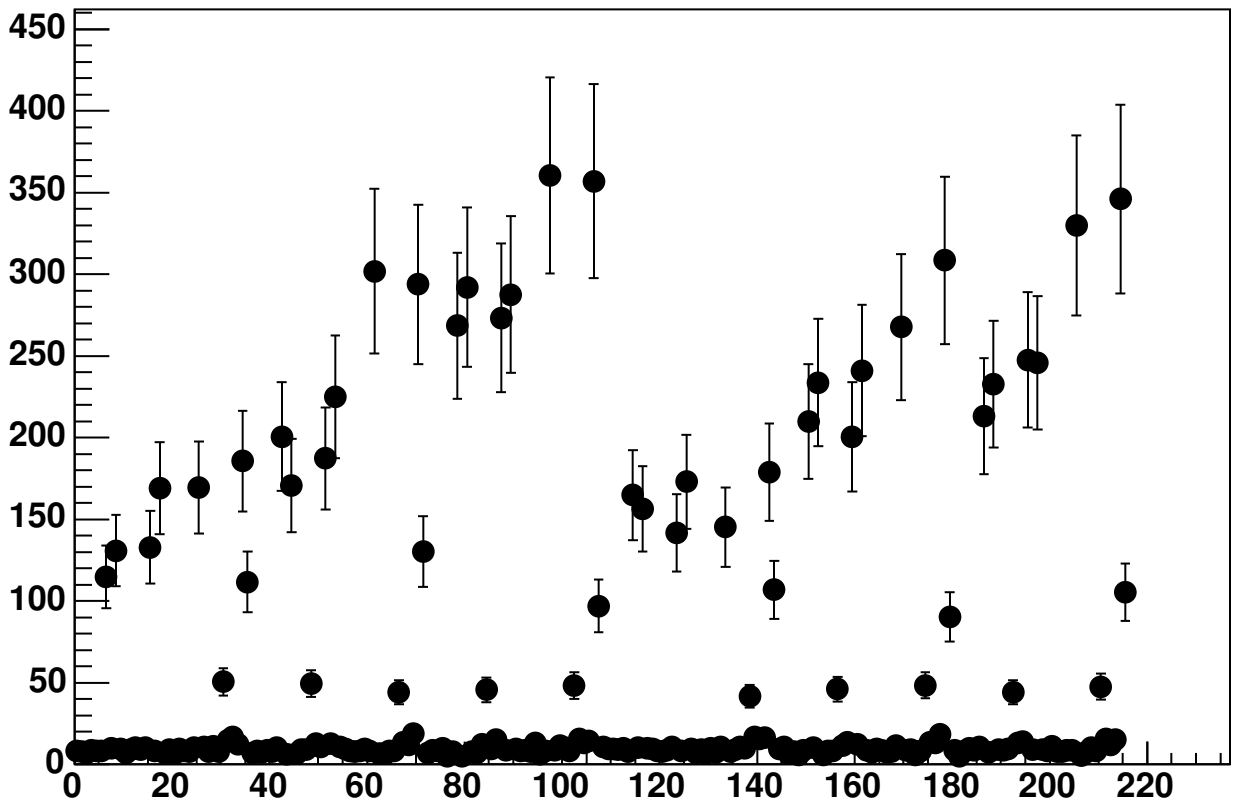
Enable 2, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



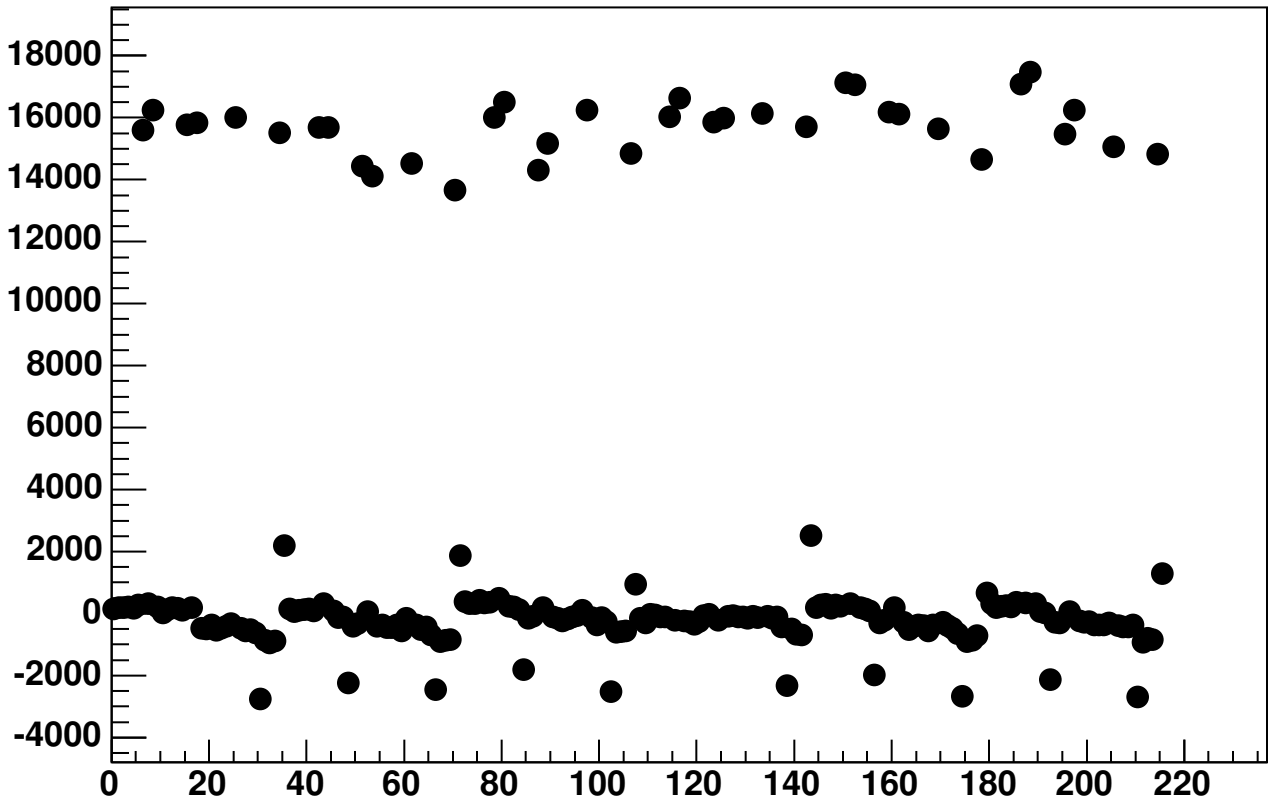
Enable 2, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



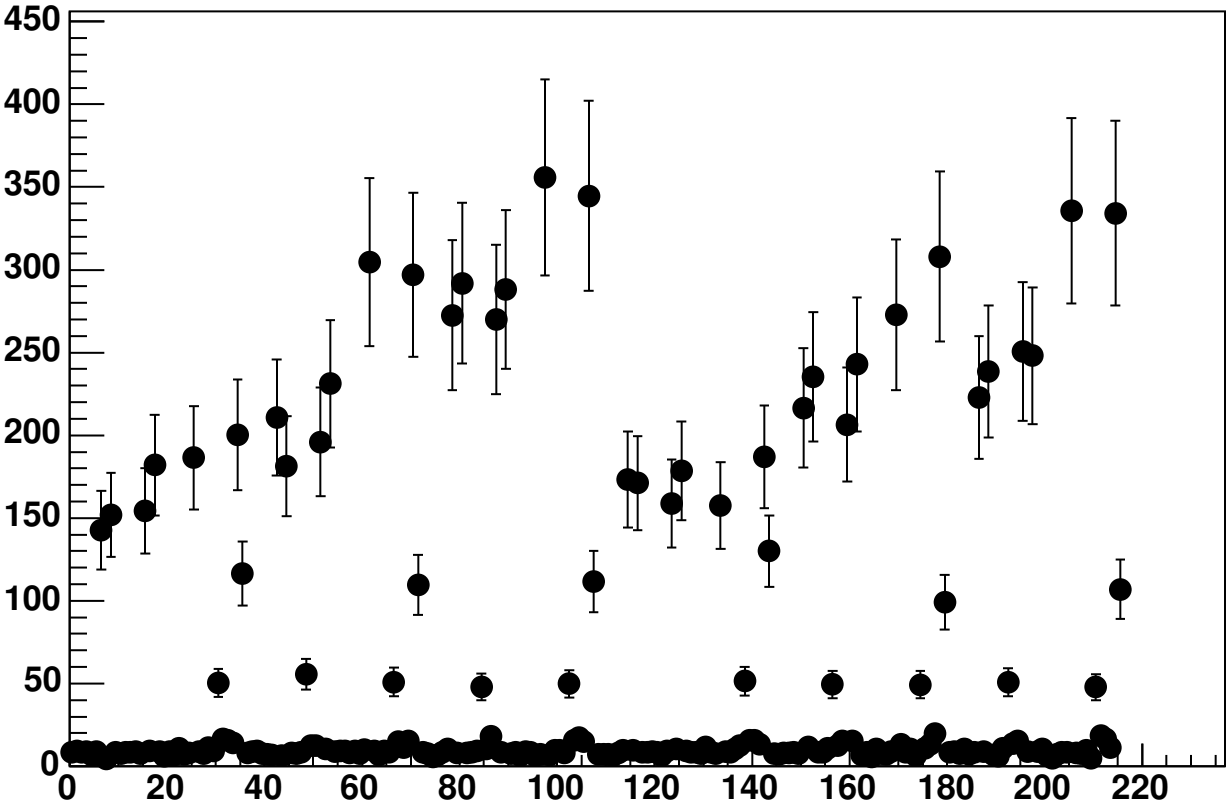
Enable 2, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



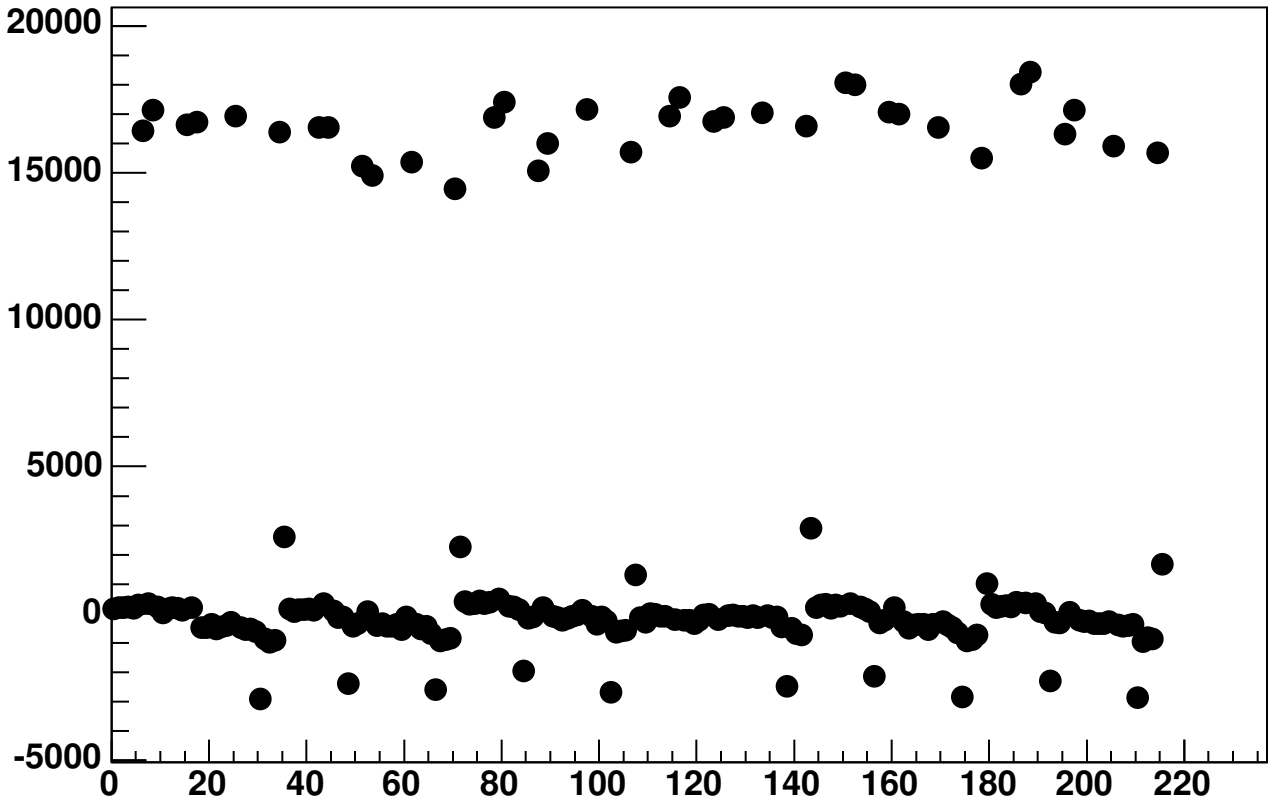
Enable 2, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



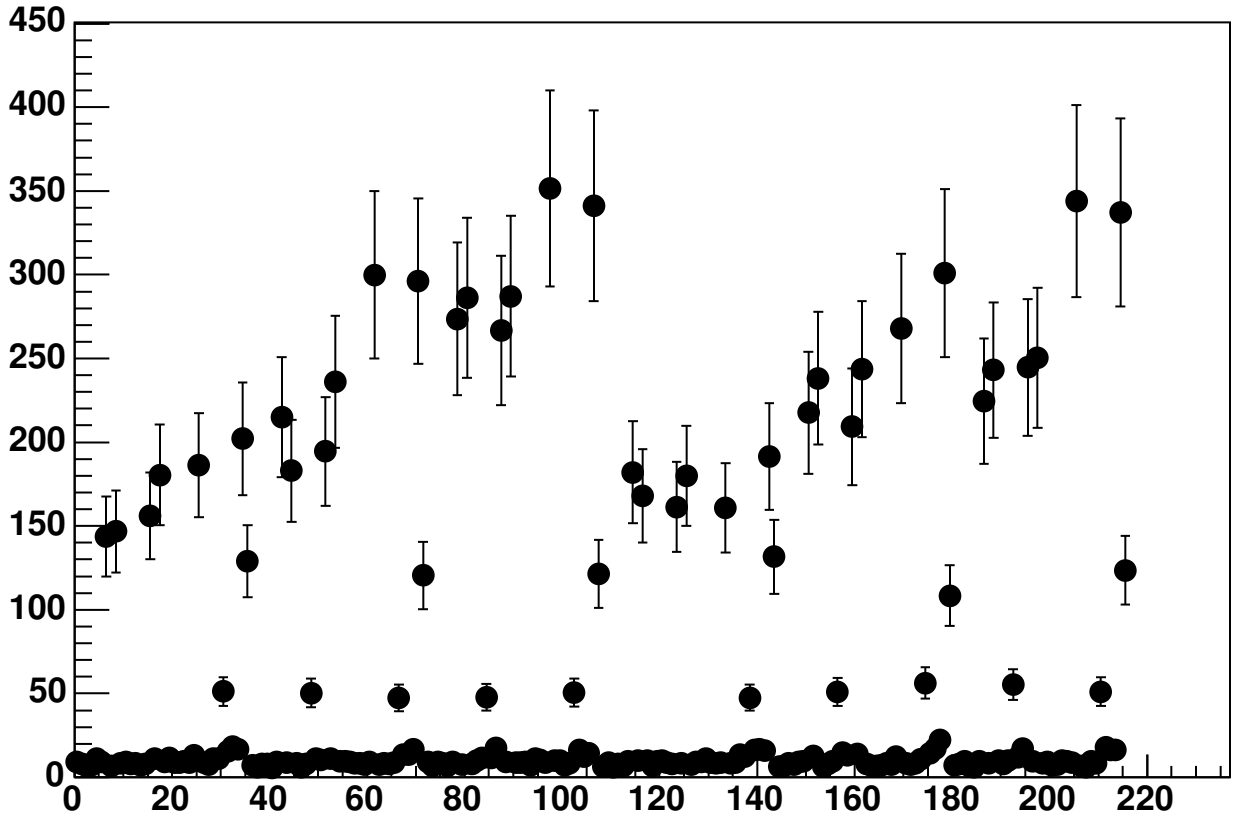
Enable 2, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



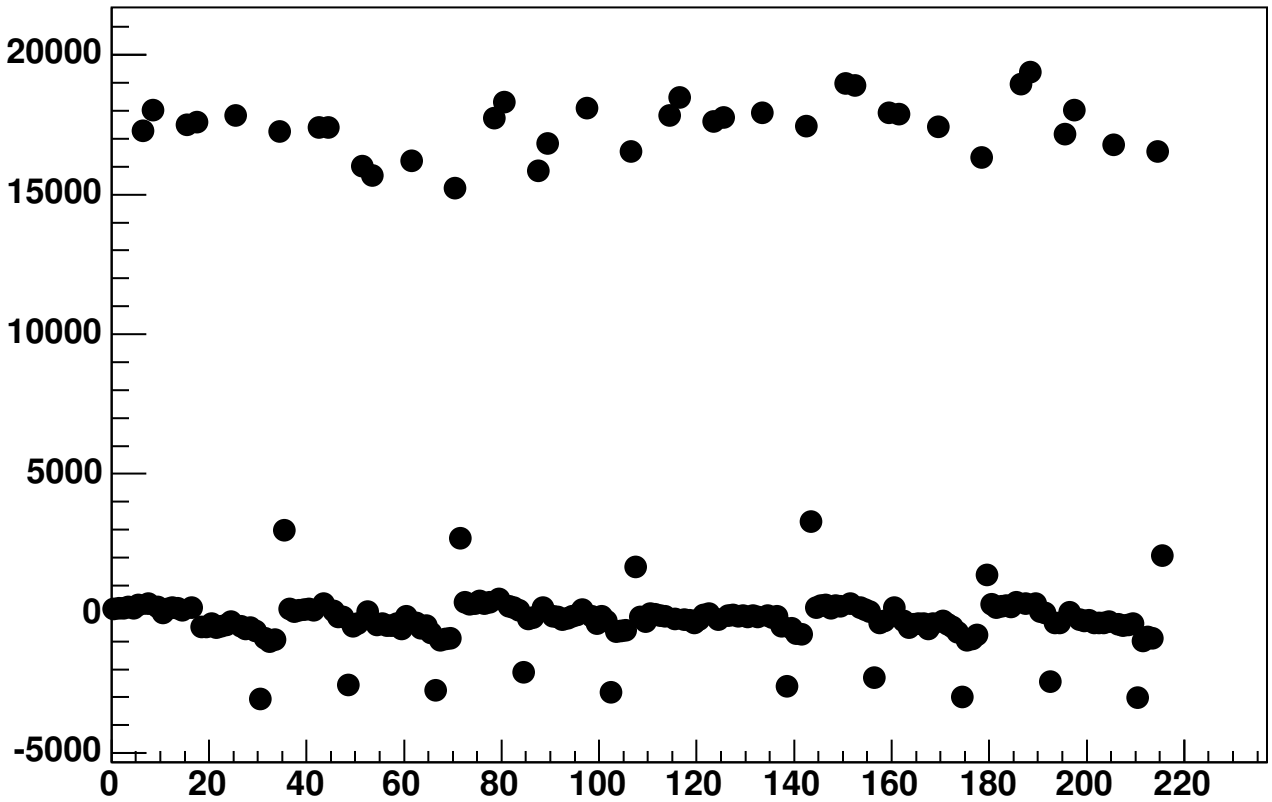
Enable 2, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



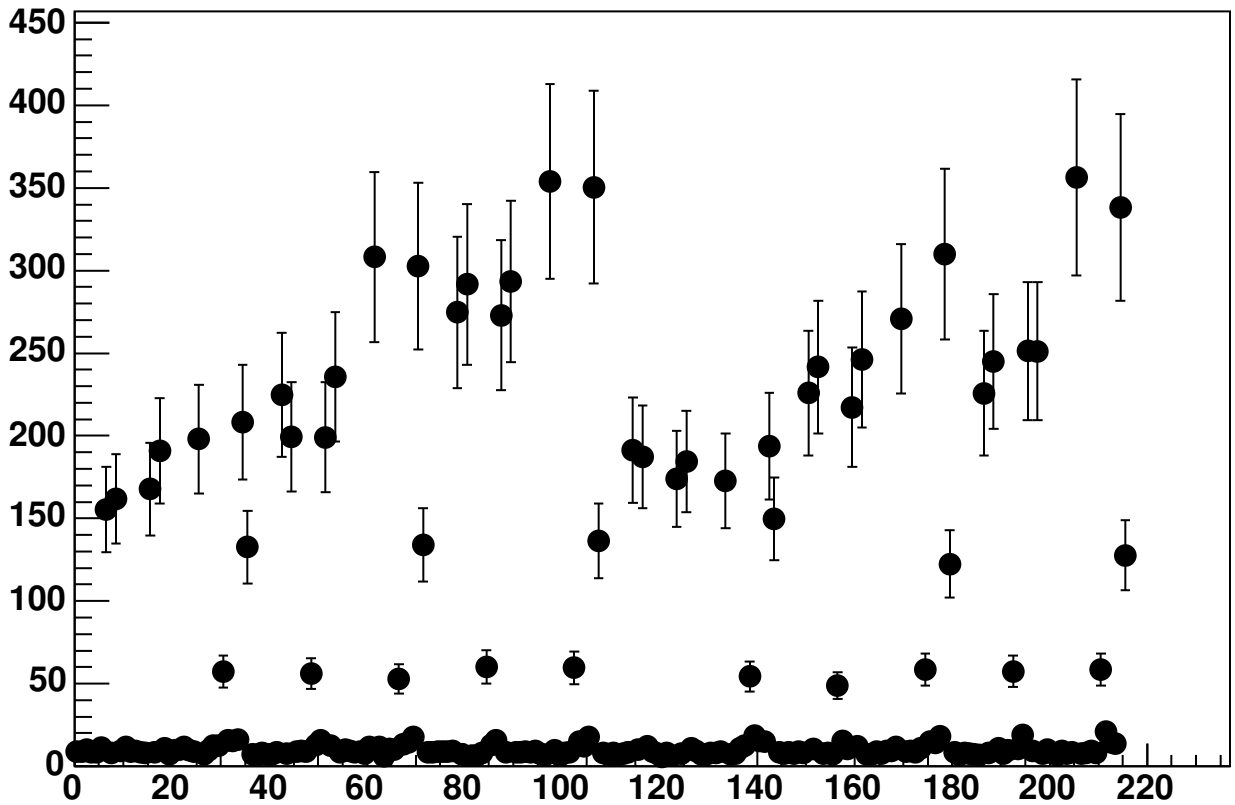
Enable 2, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



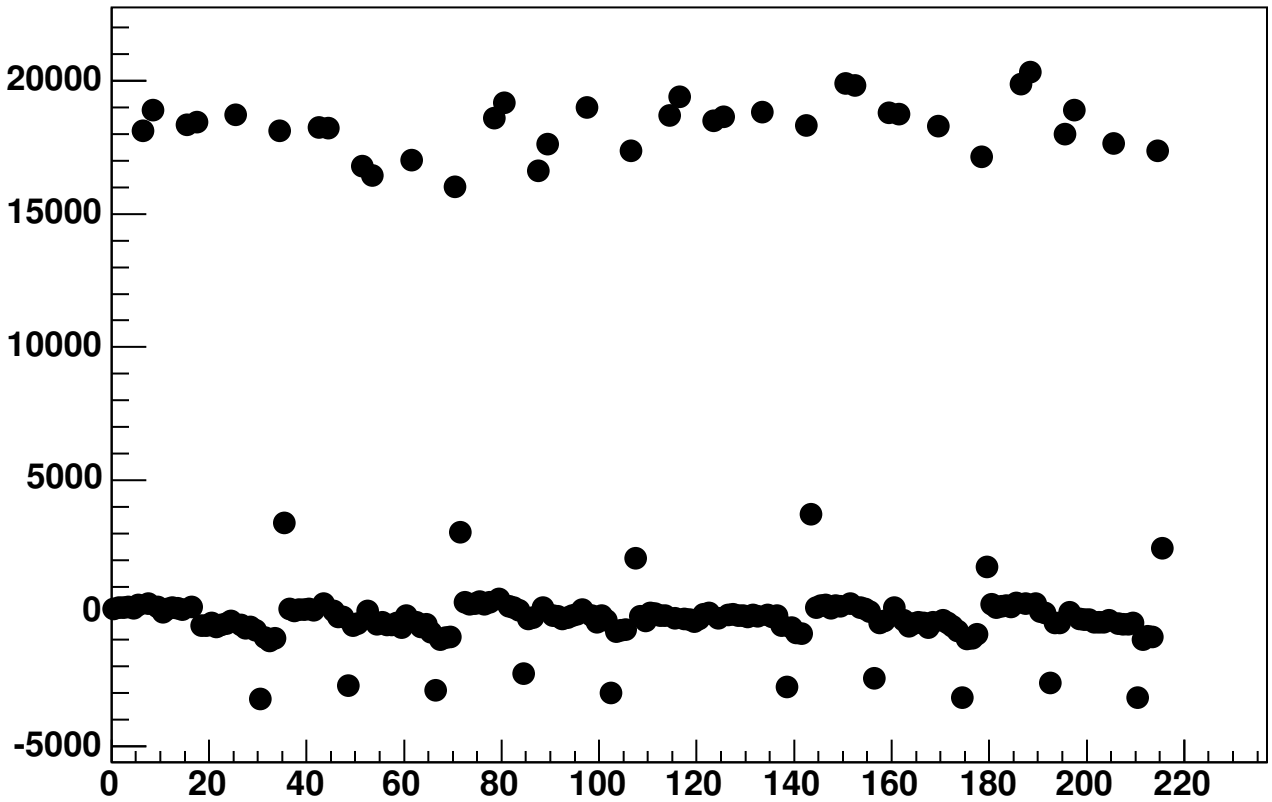
Enable 2, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



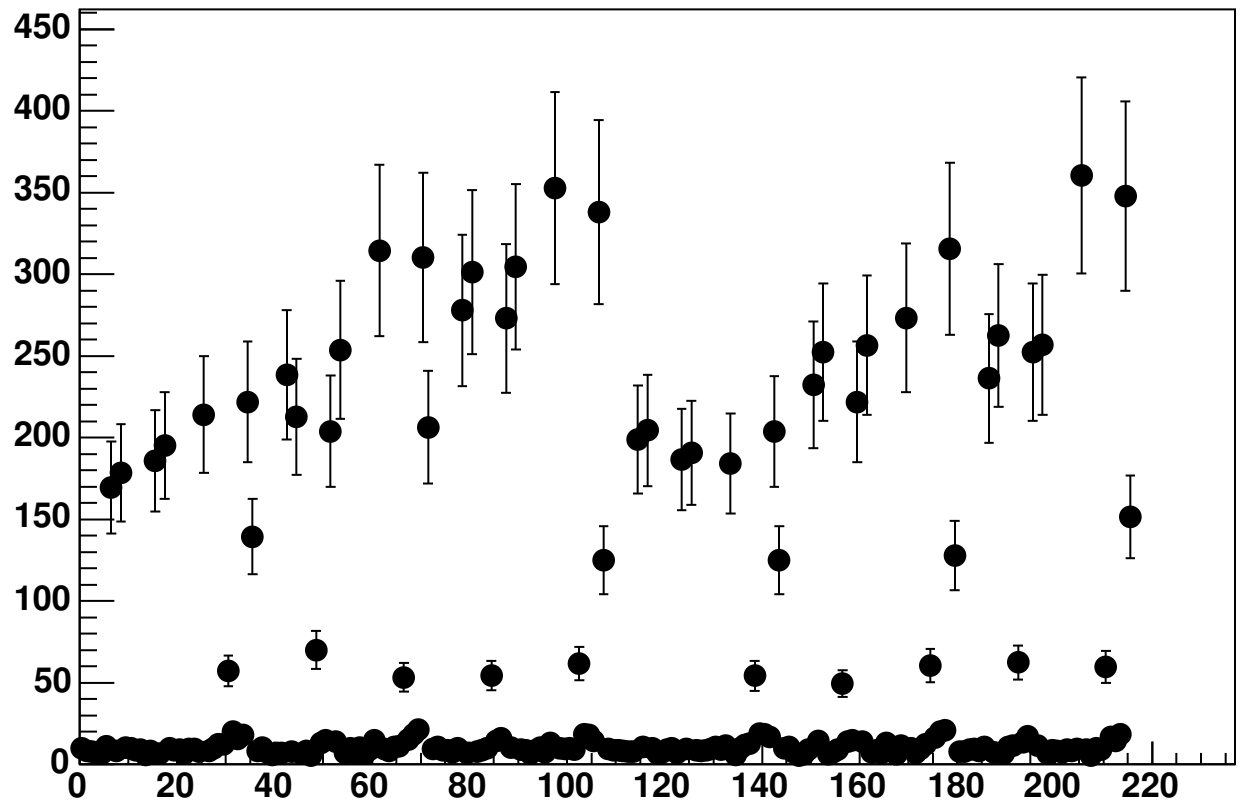
Enable 2, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



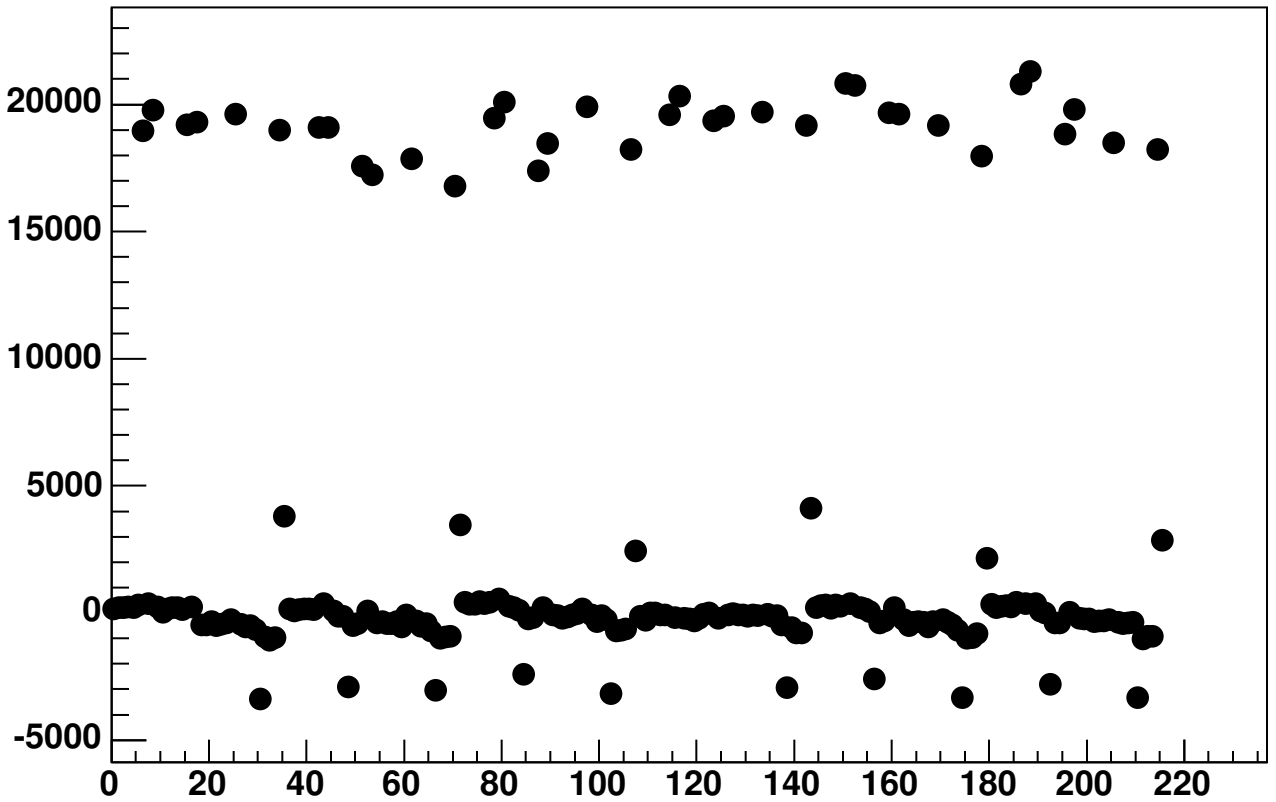
Enable 2, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



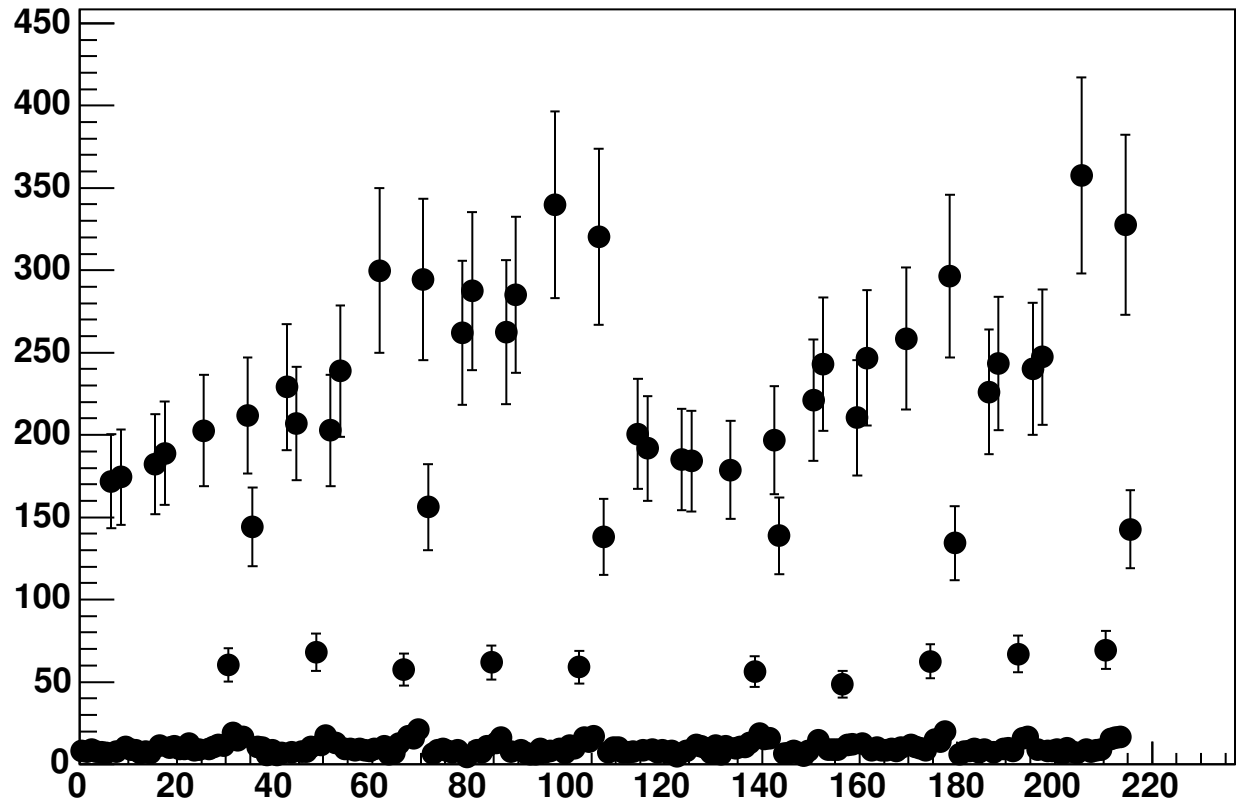
Enable 2, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



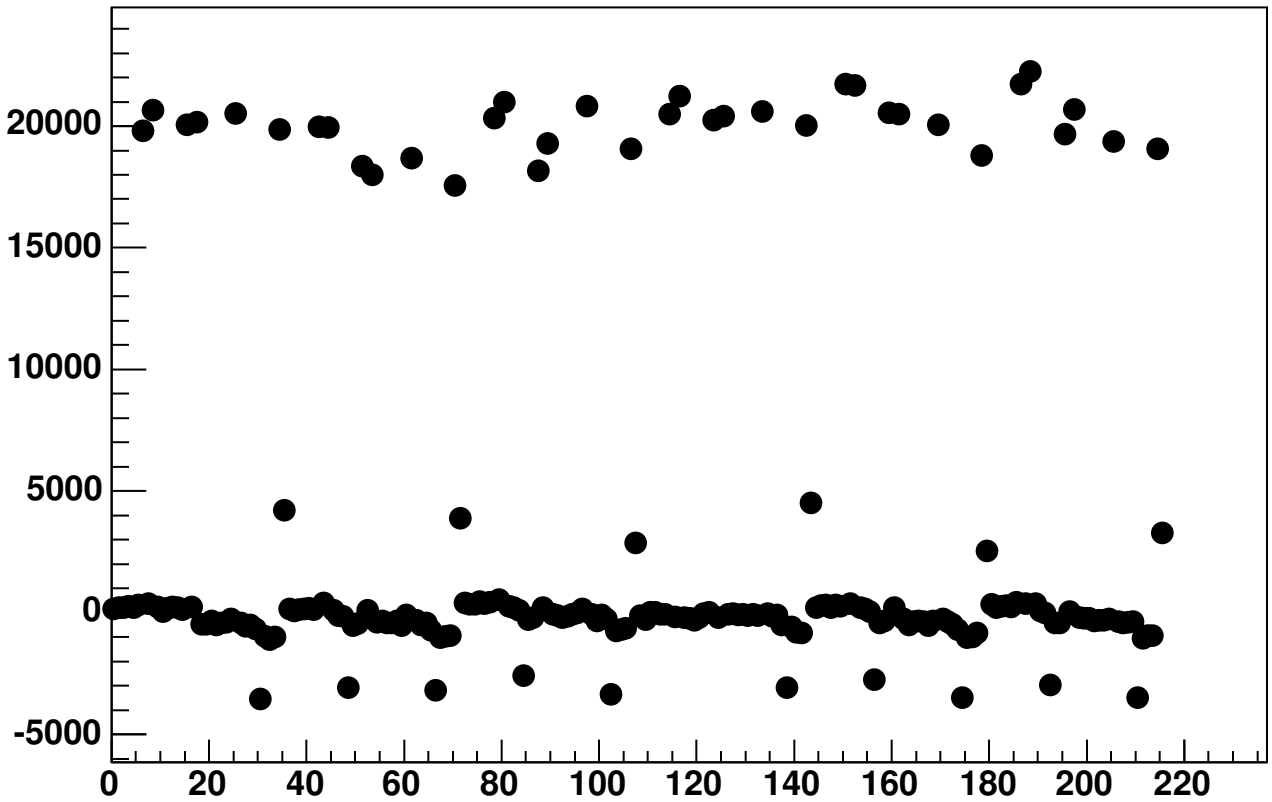
Enable 2, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



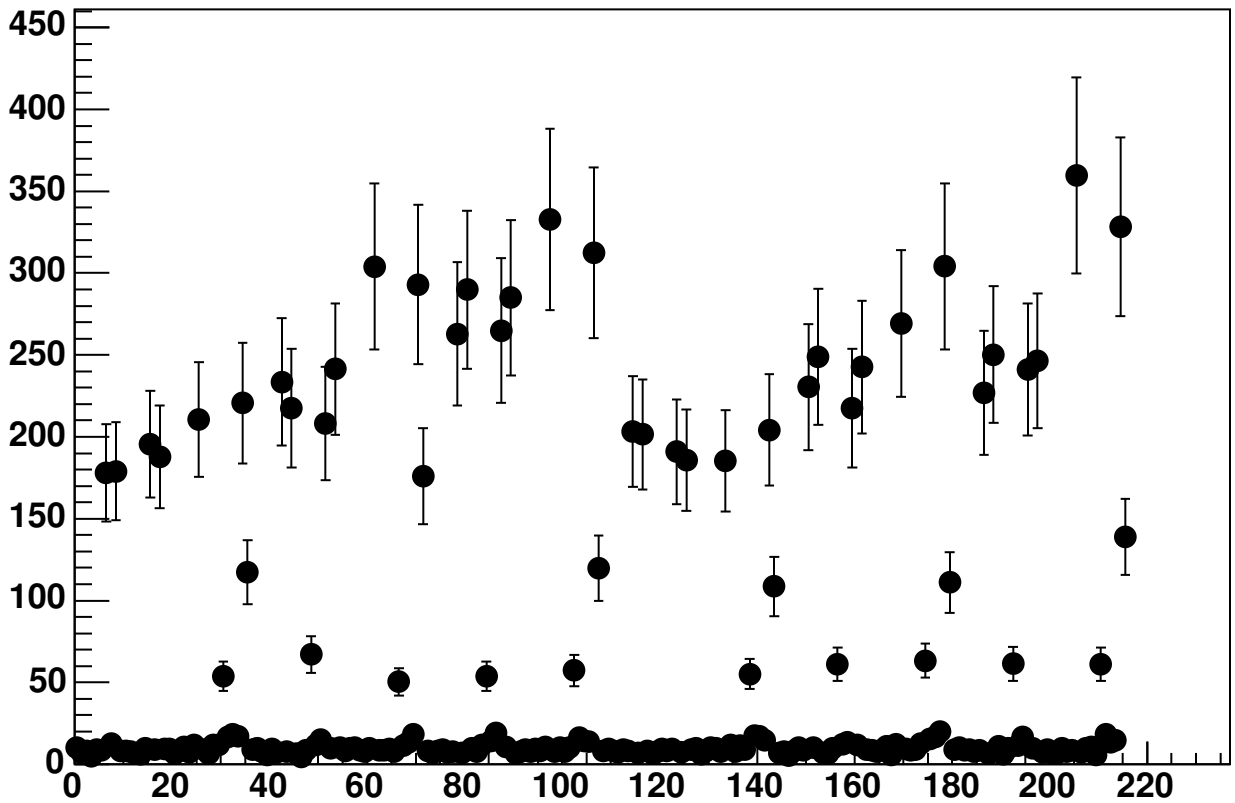
Enable 2, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



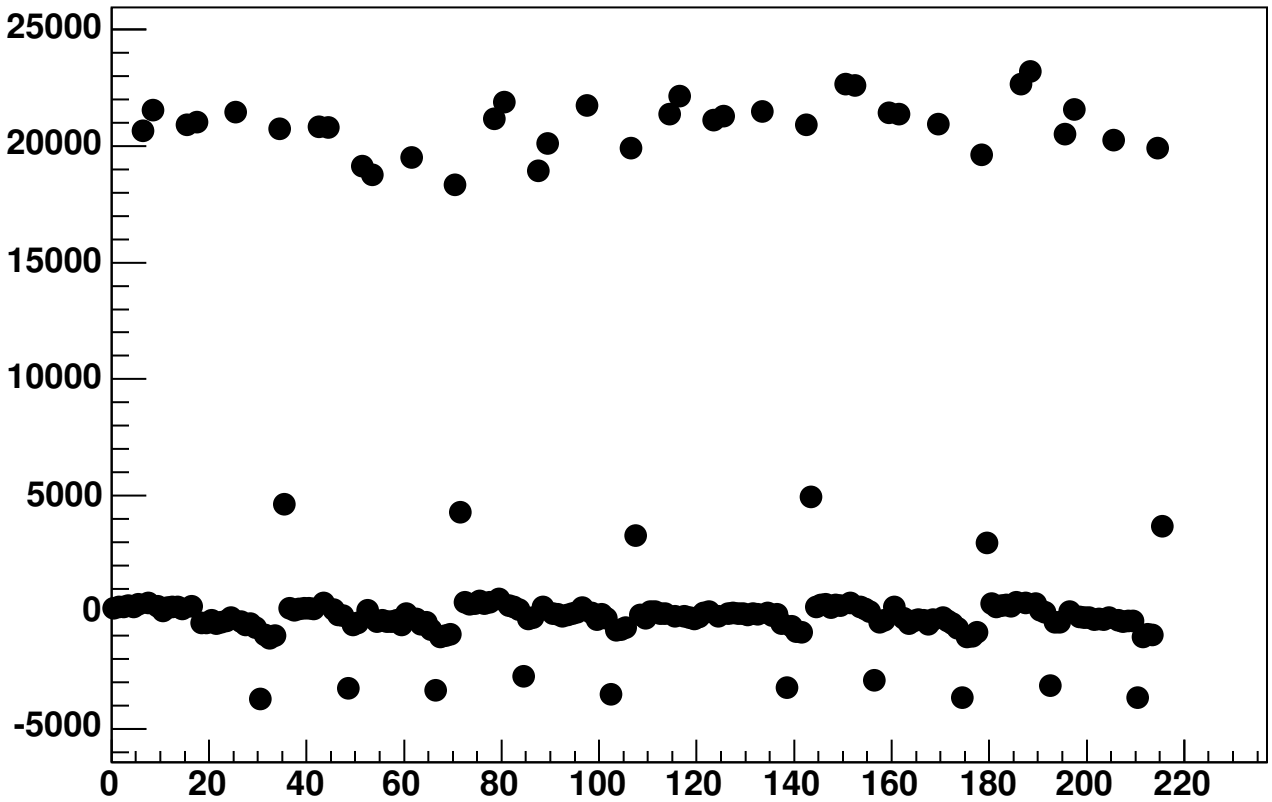
Enable 2, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



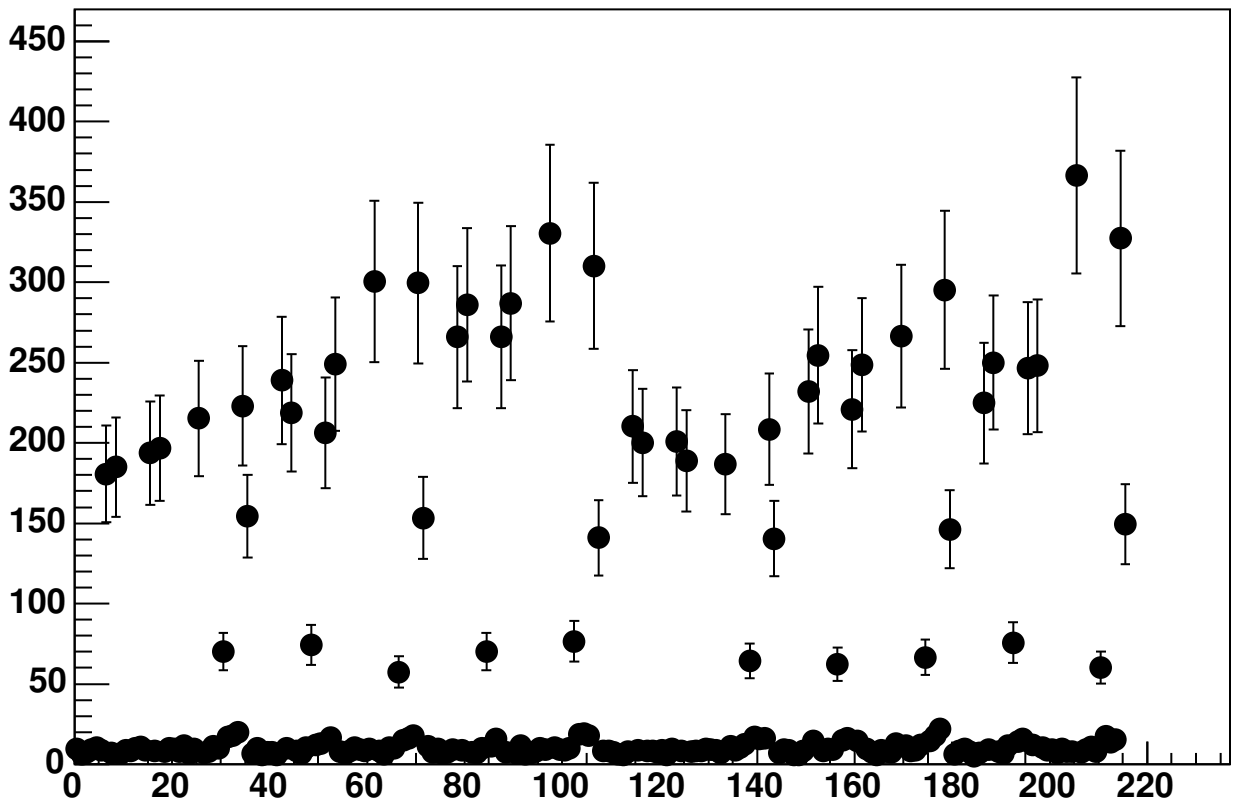
Enable 2, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



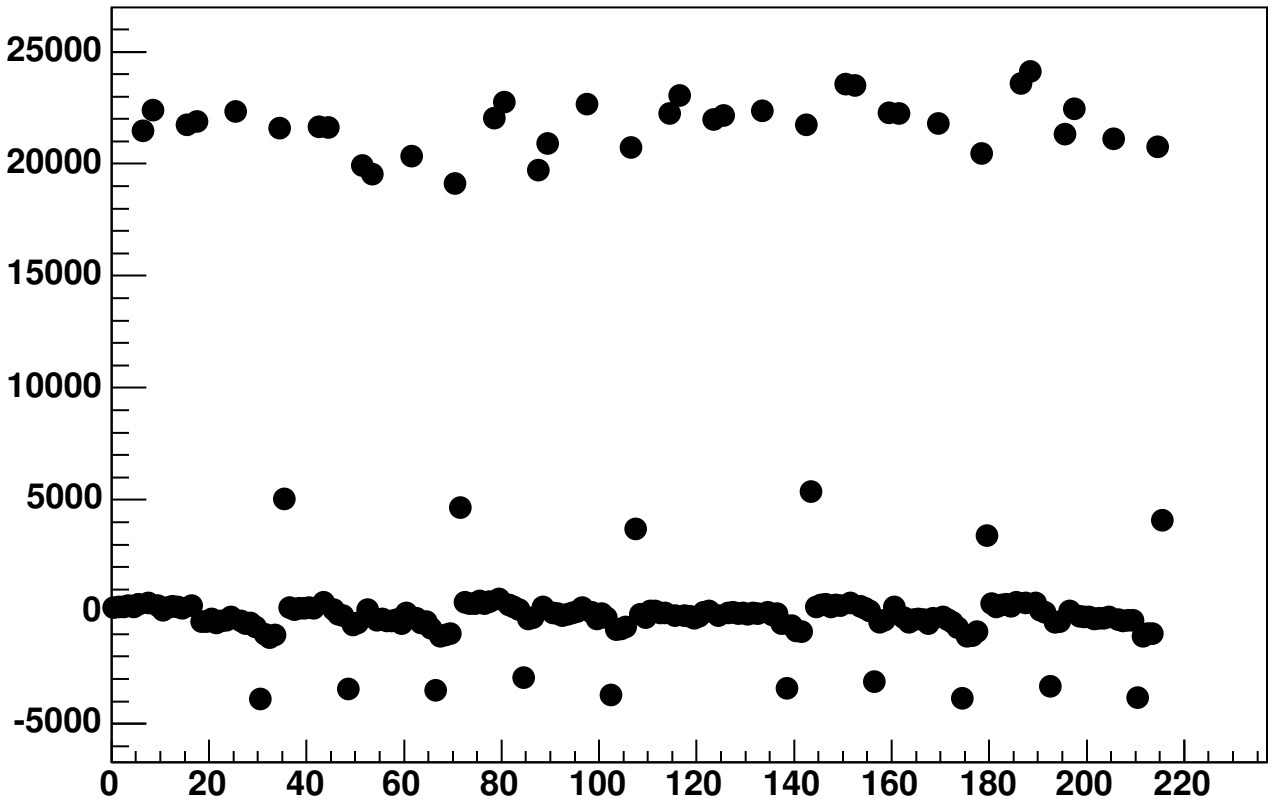
Enable 2, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



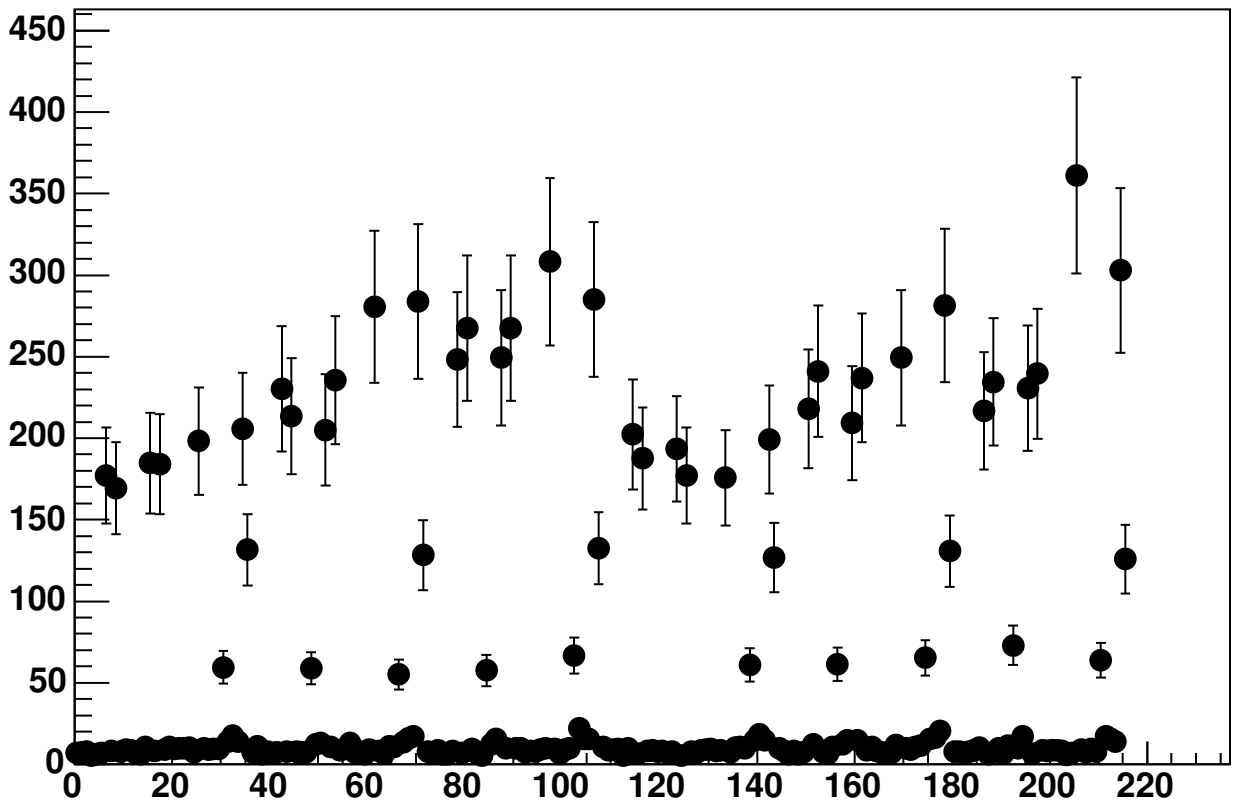
Enable 2, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



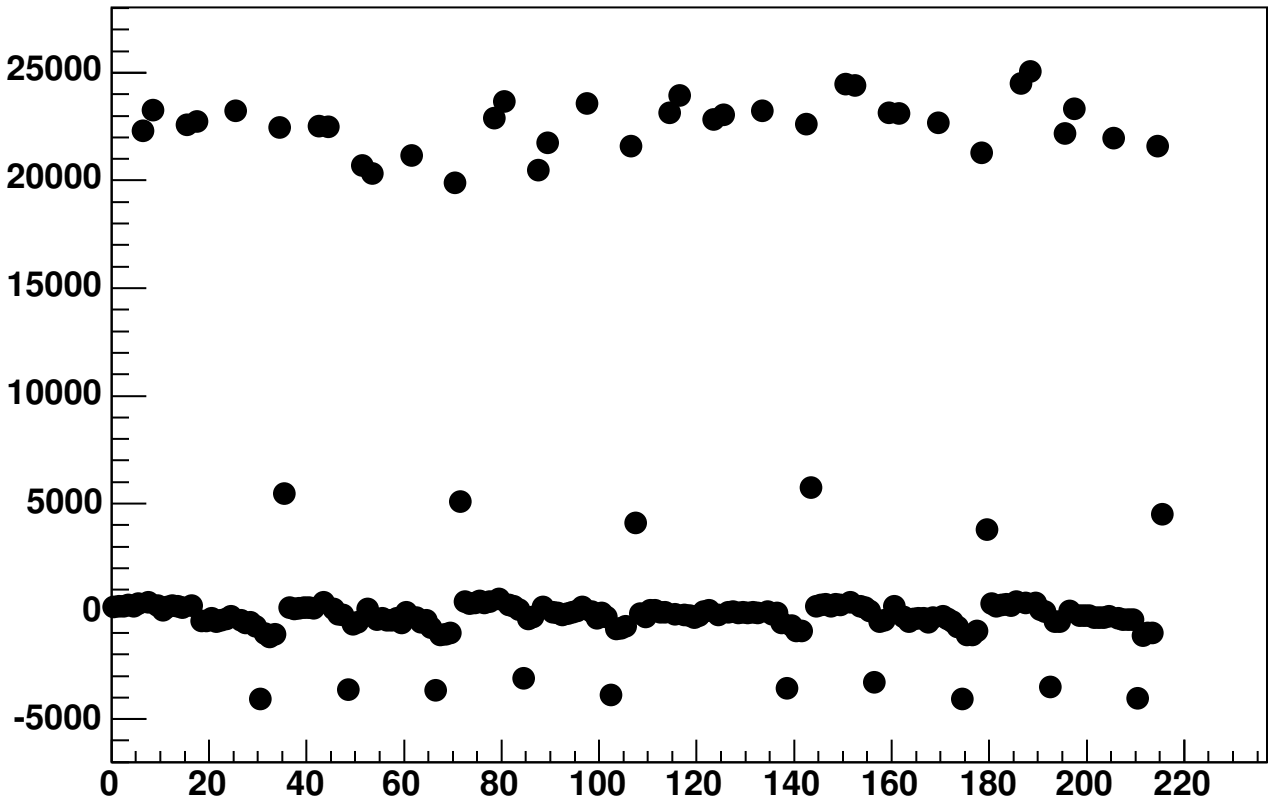
Enable 2, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



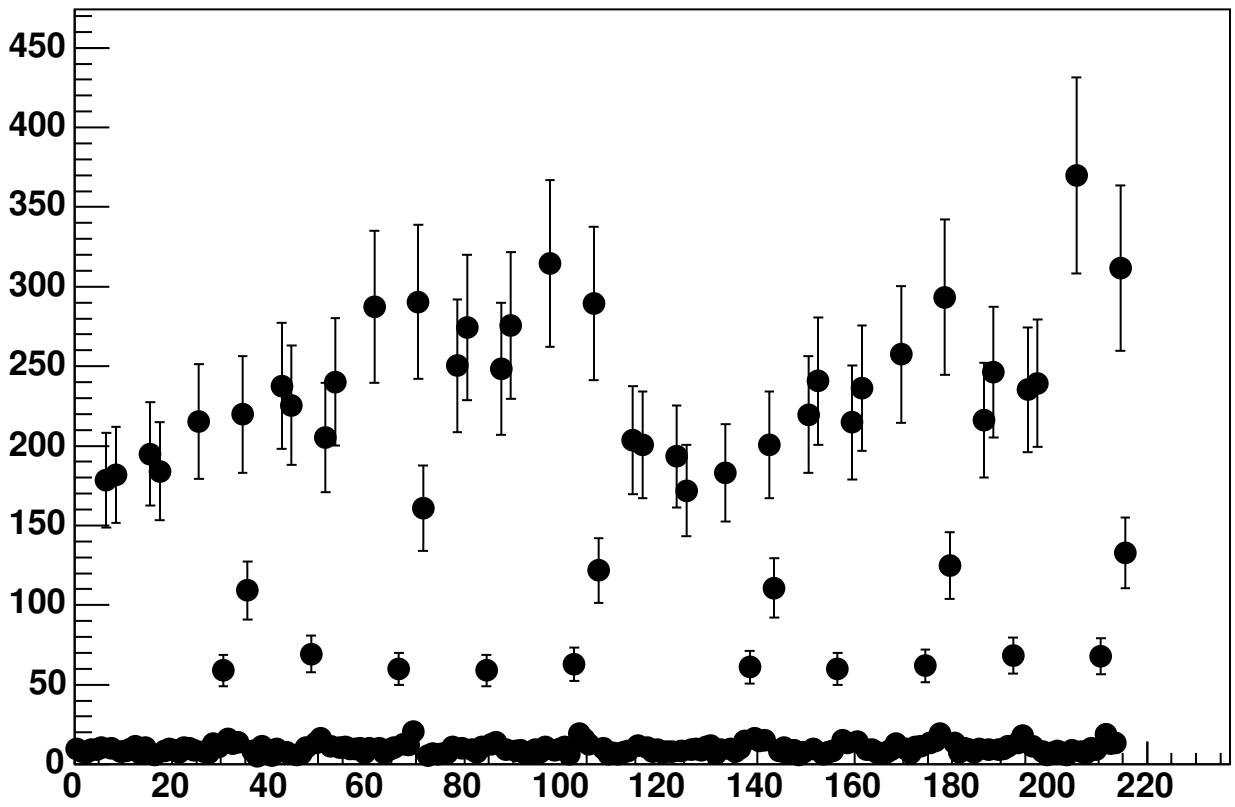
Enable 2, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



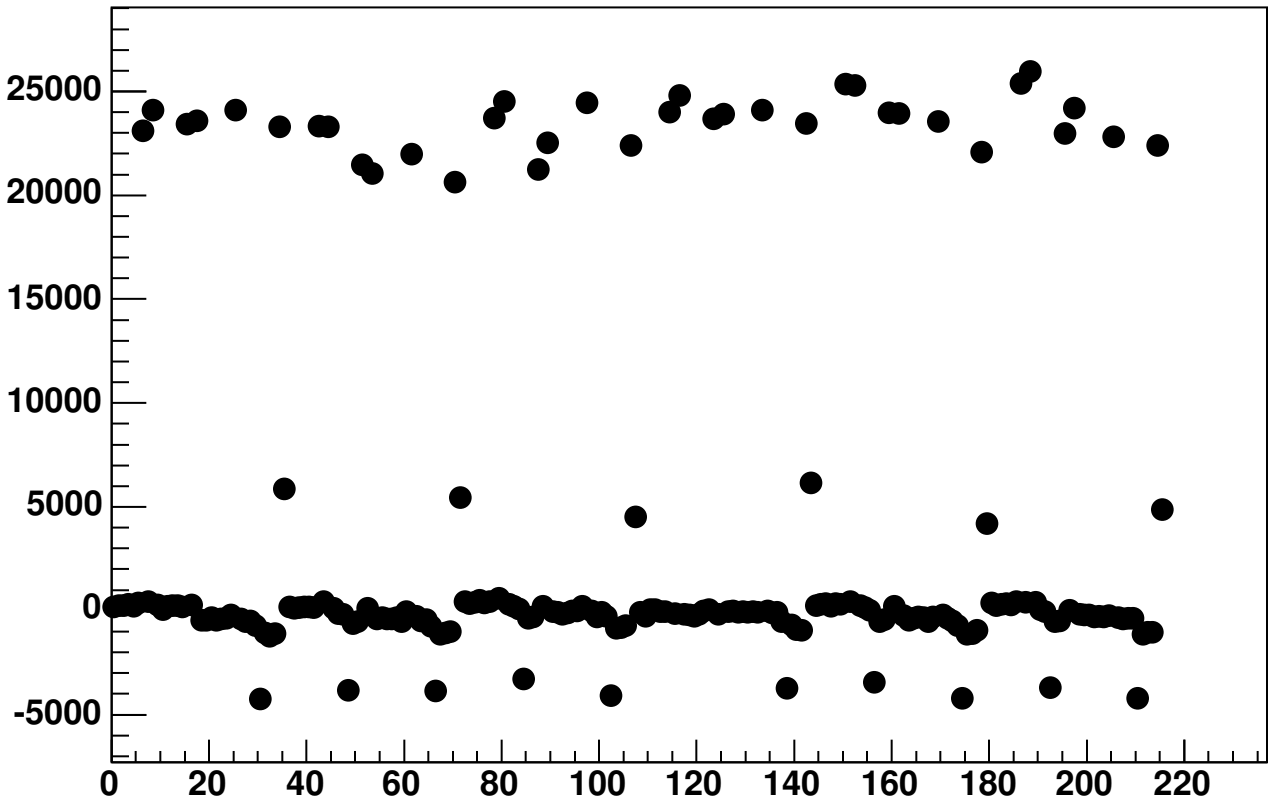
Enable 2, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



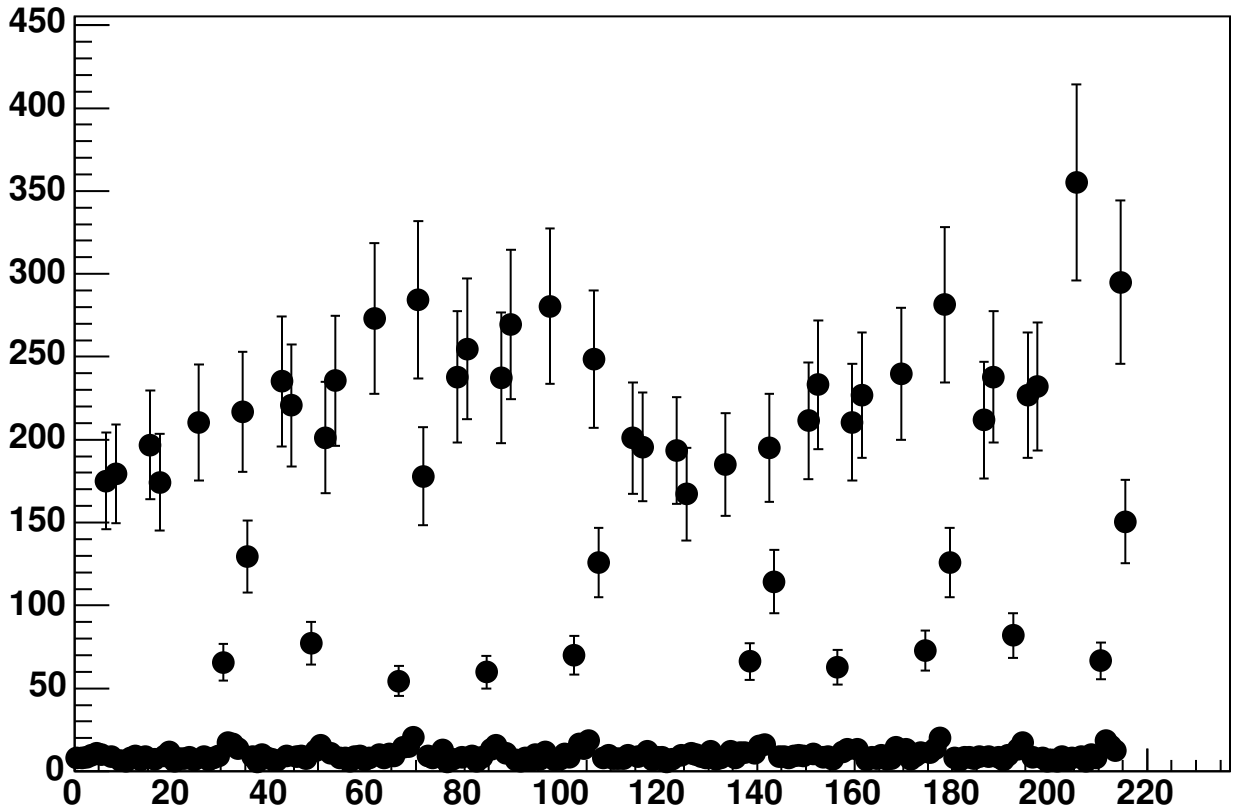
Enable 2, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



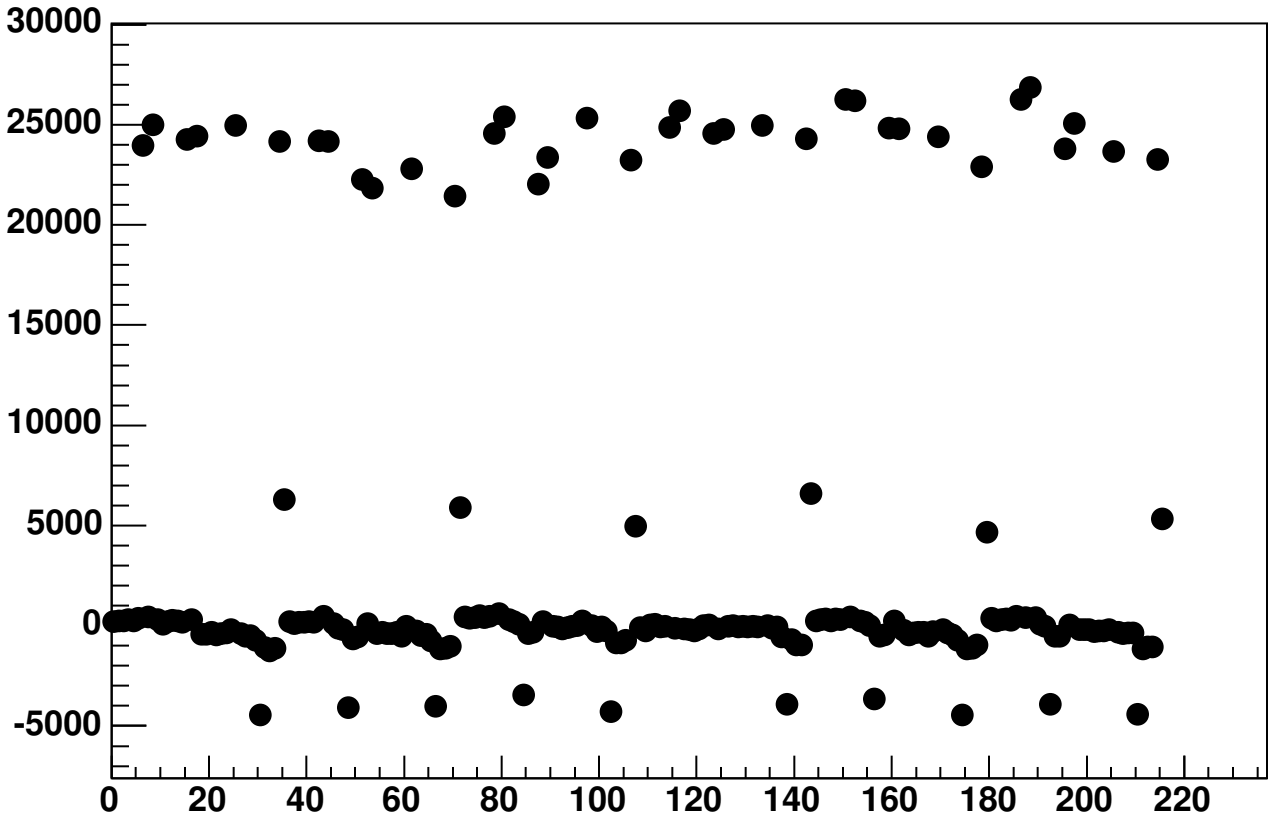
Enable 2, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



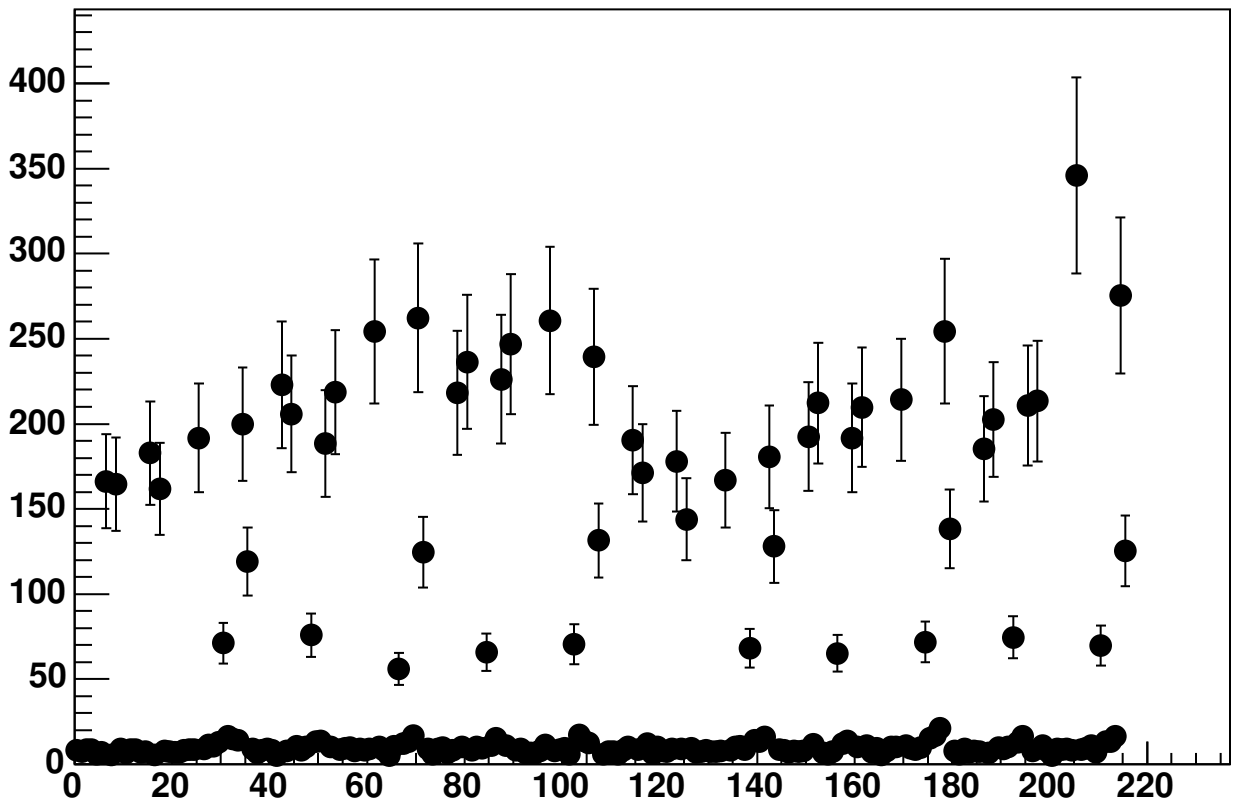
Enable 2, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



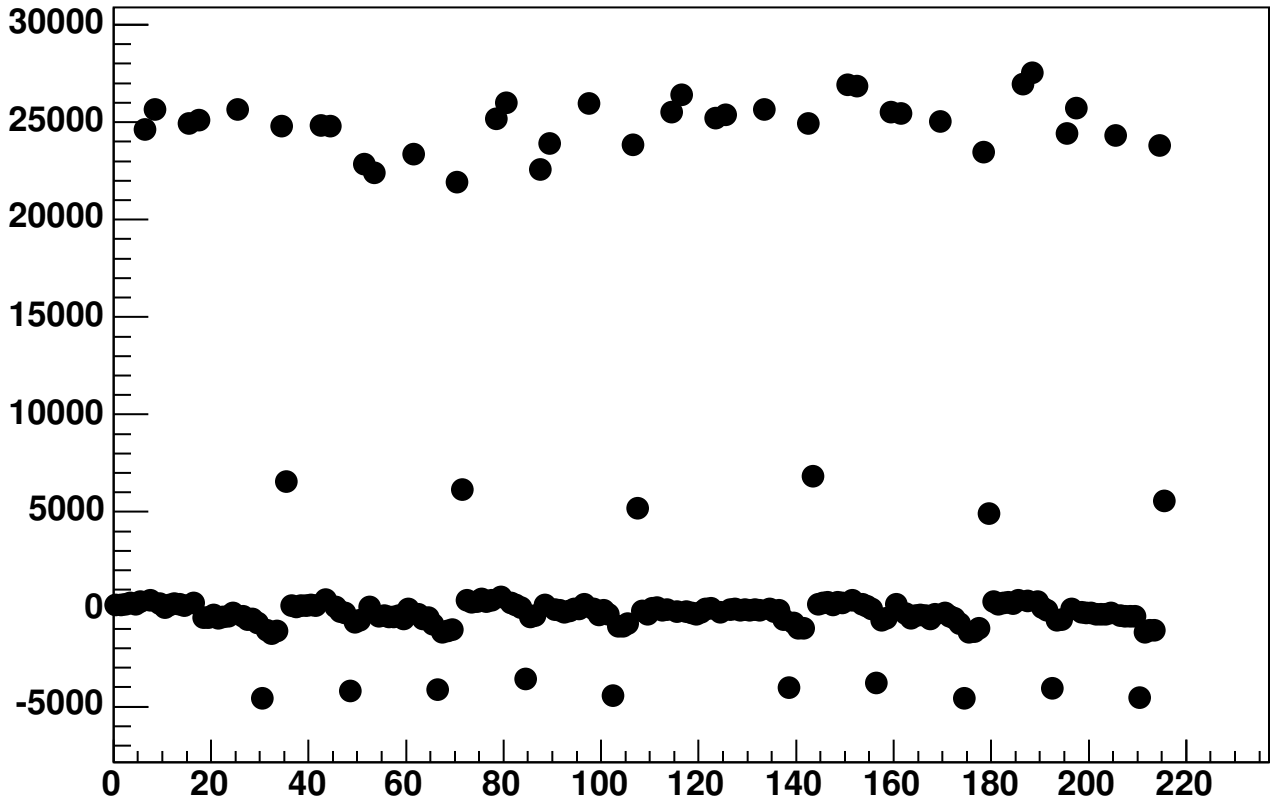
Enable 2, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



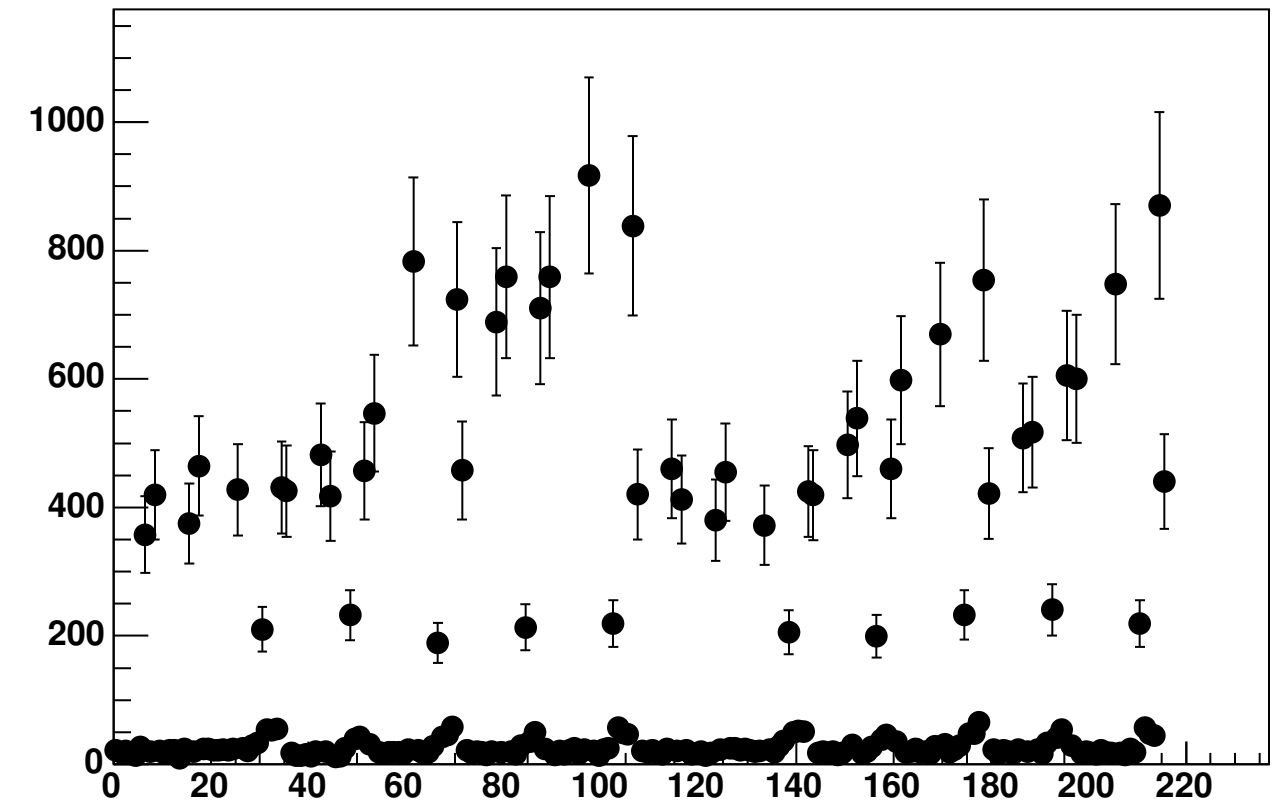
Enable 2, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



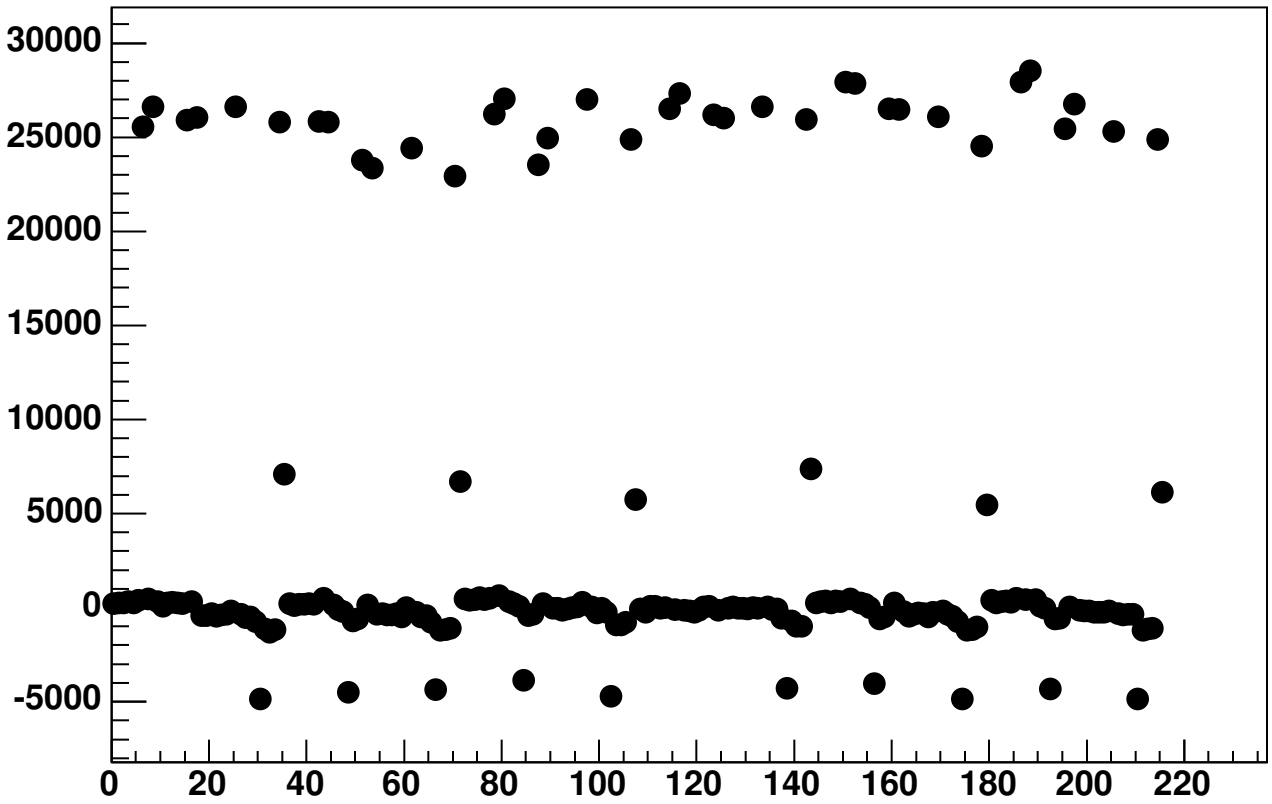
Enable 2, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



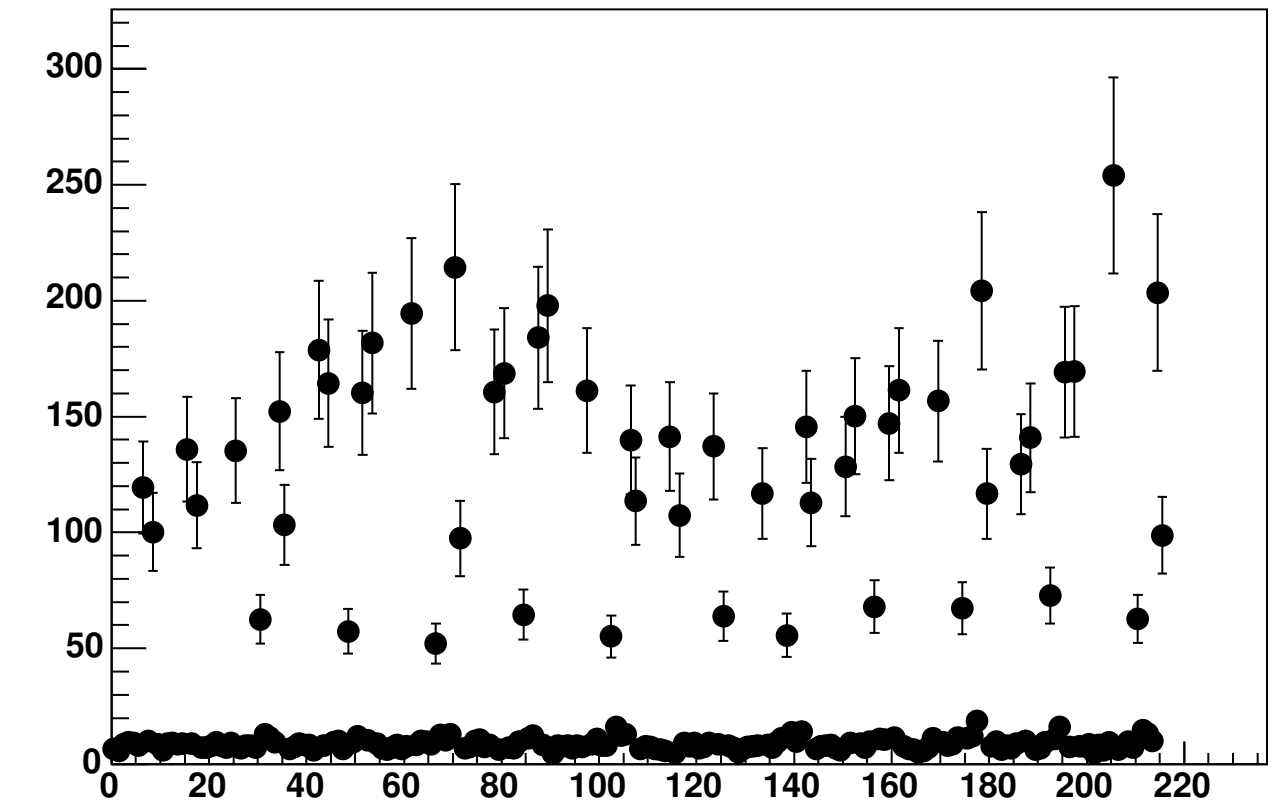
Enable 2, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



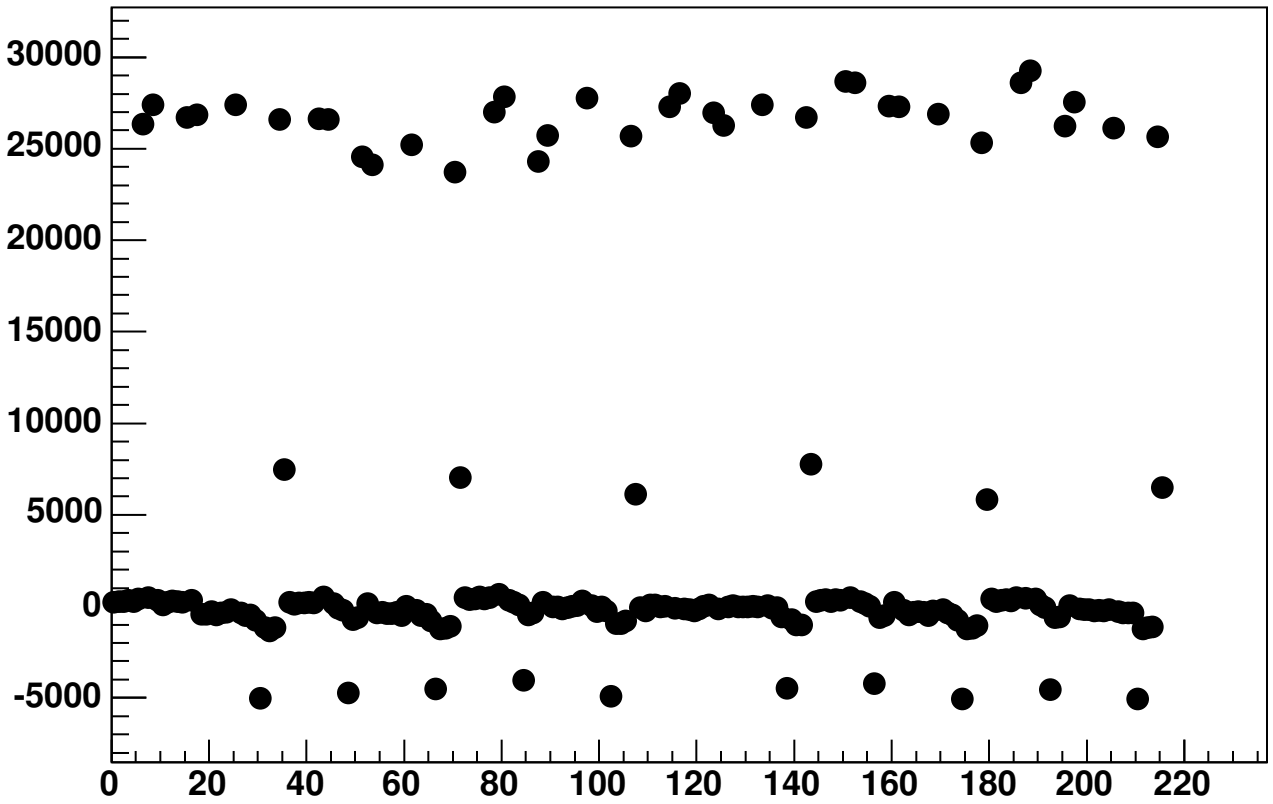
Enable 2, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



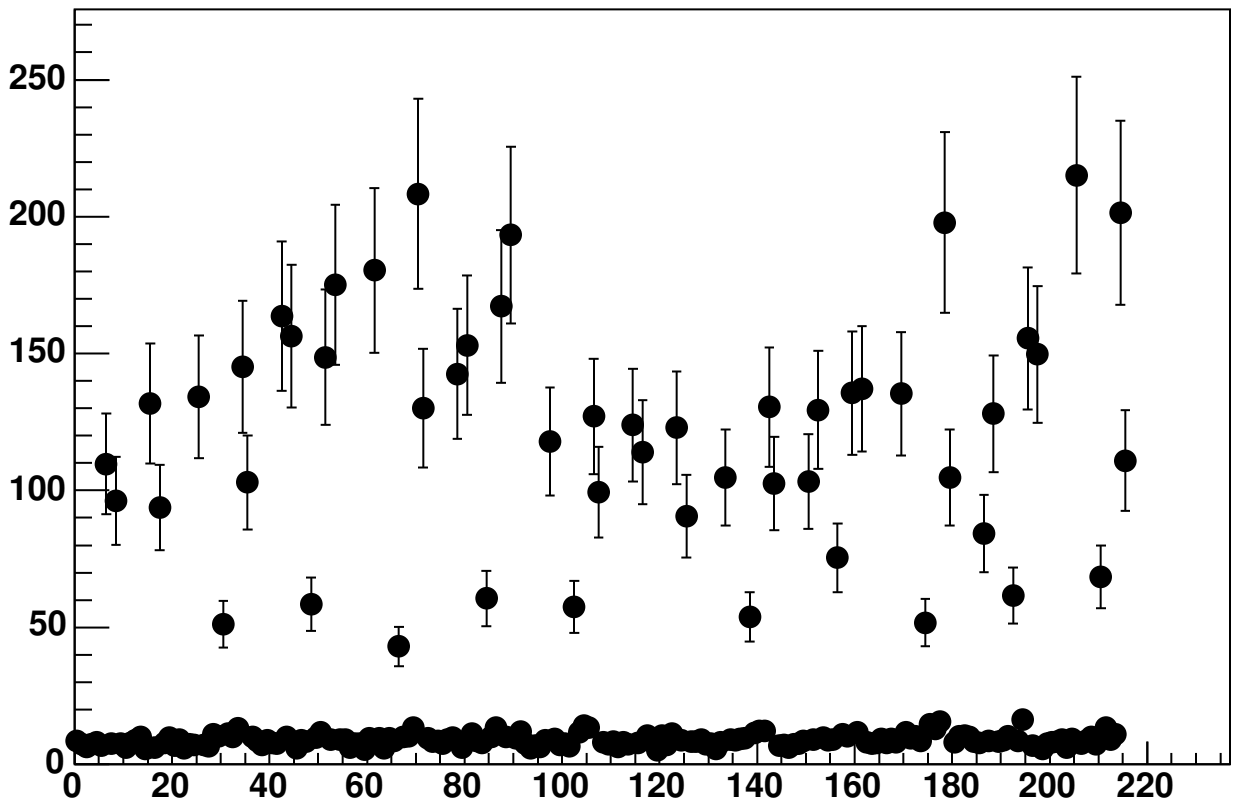
Enable 2, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



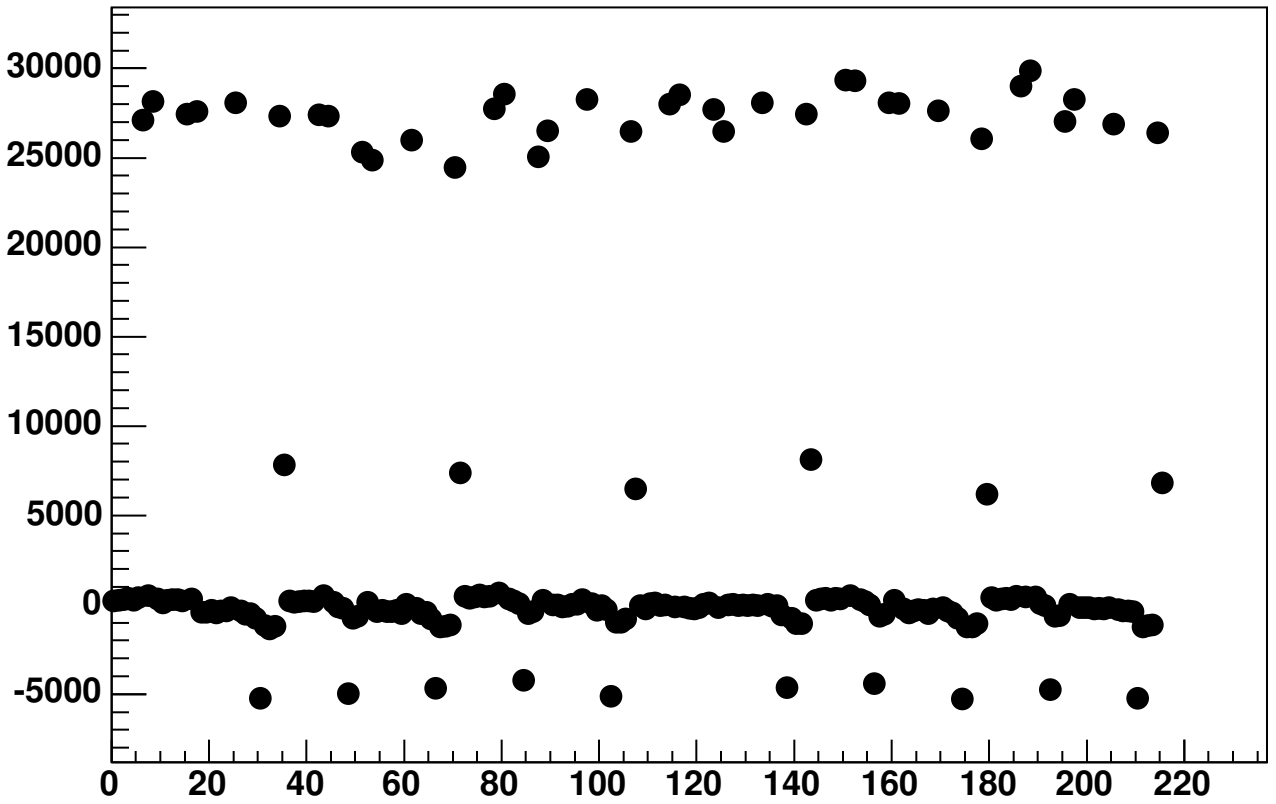
Enable 2, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



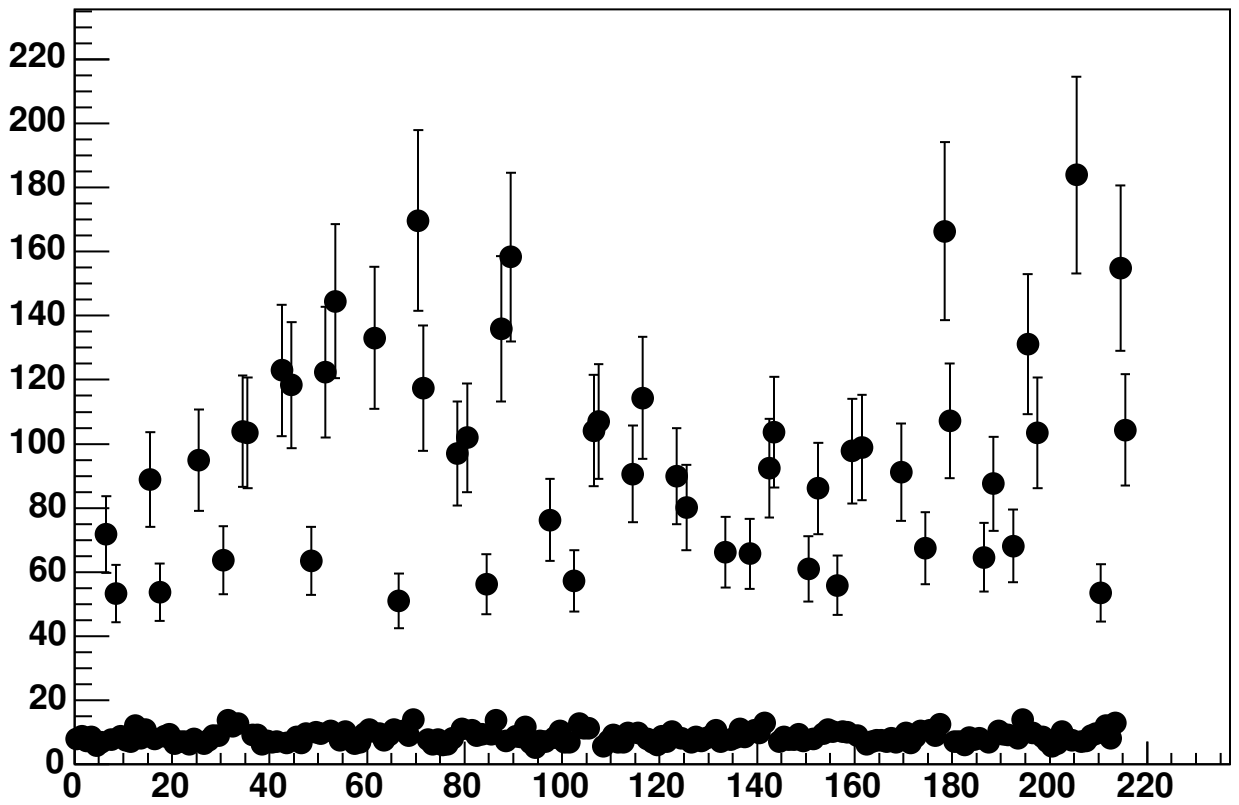
Enable 2, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



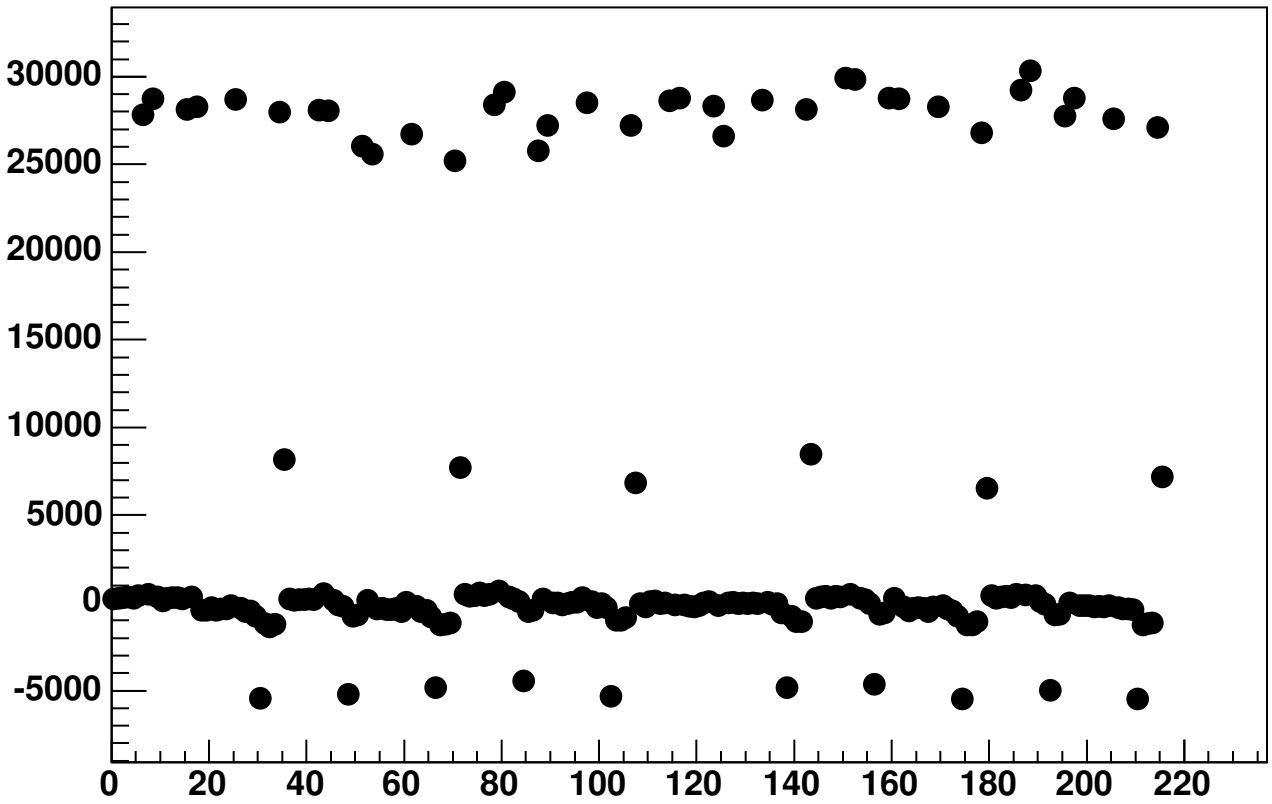
Enable 2, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



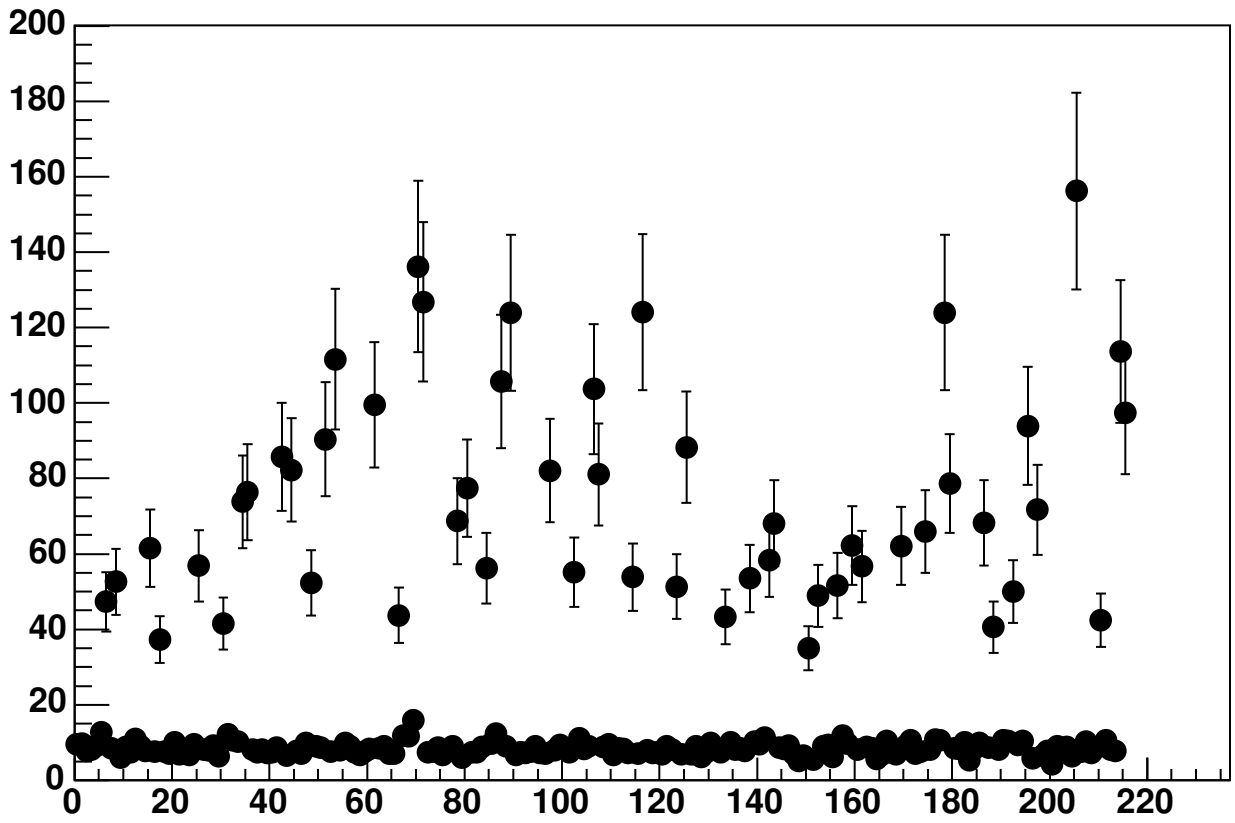
Enable 2, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



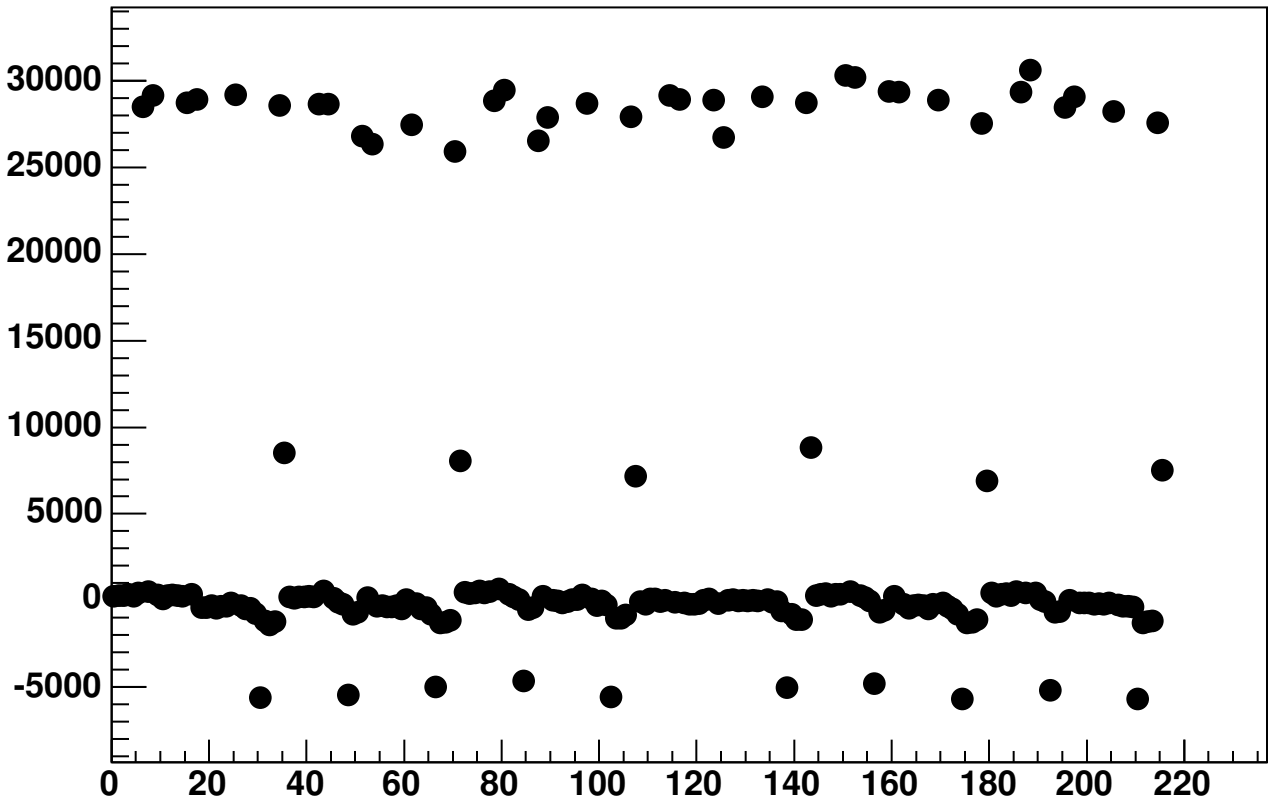
Enable 2, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



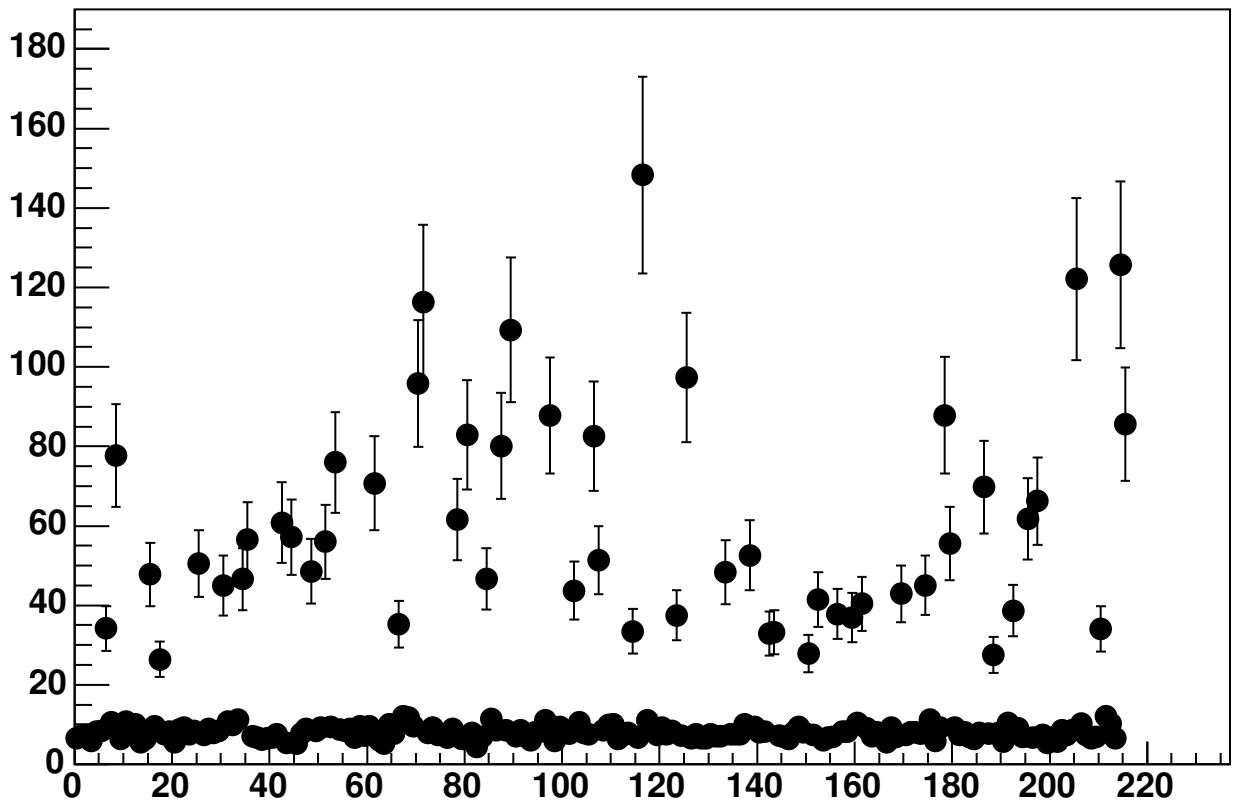
Enable 2, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



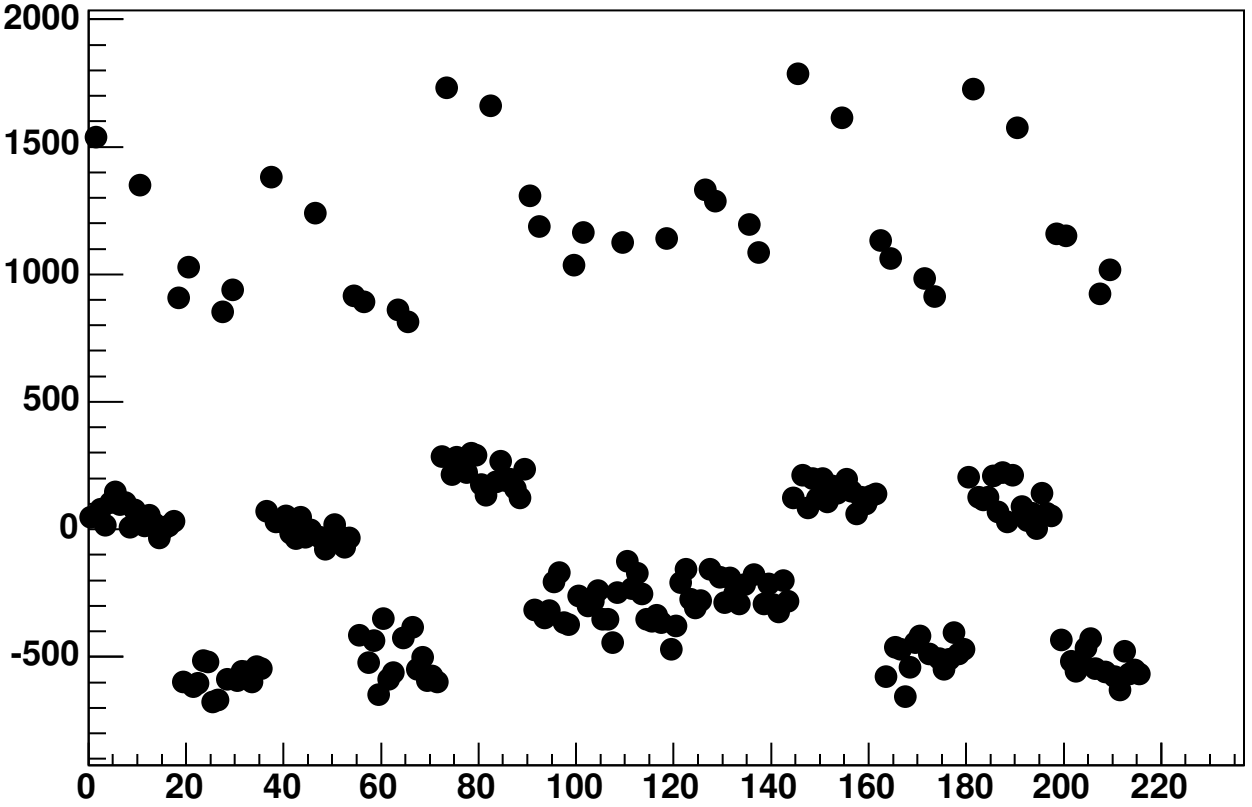
Enable 2, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



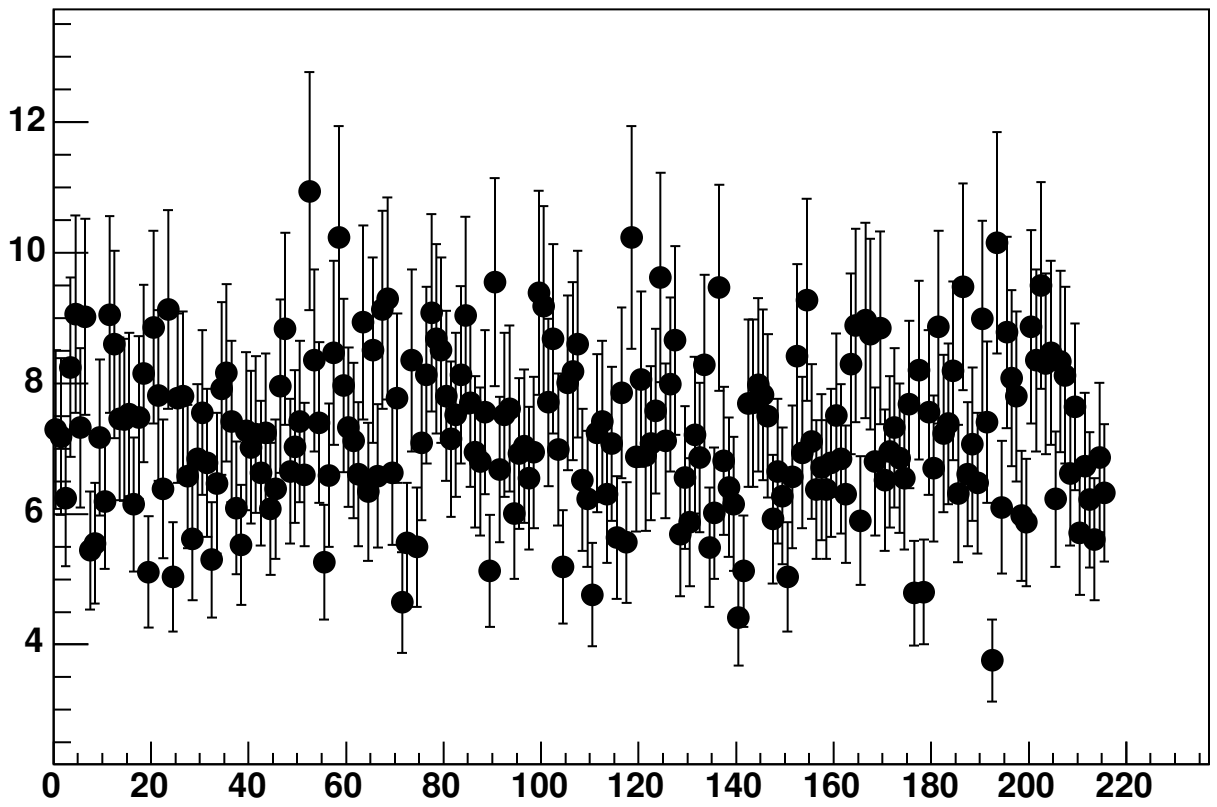
Enable 2, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



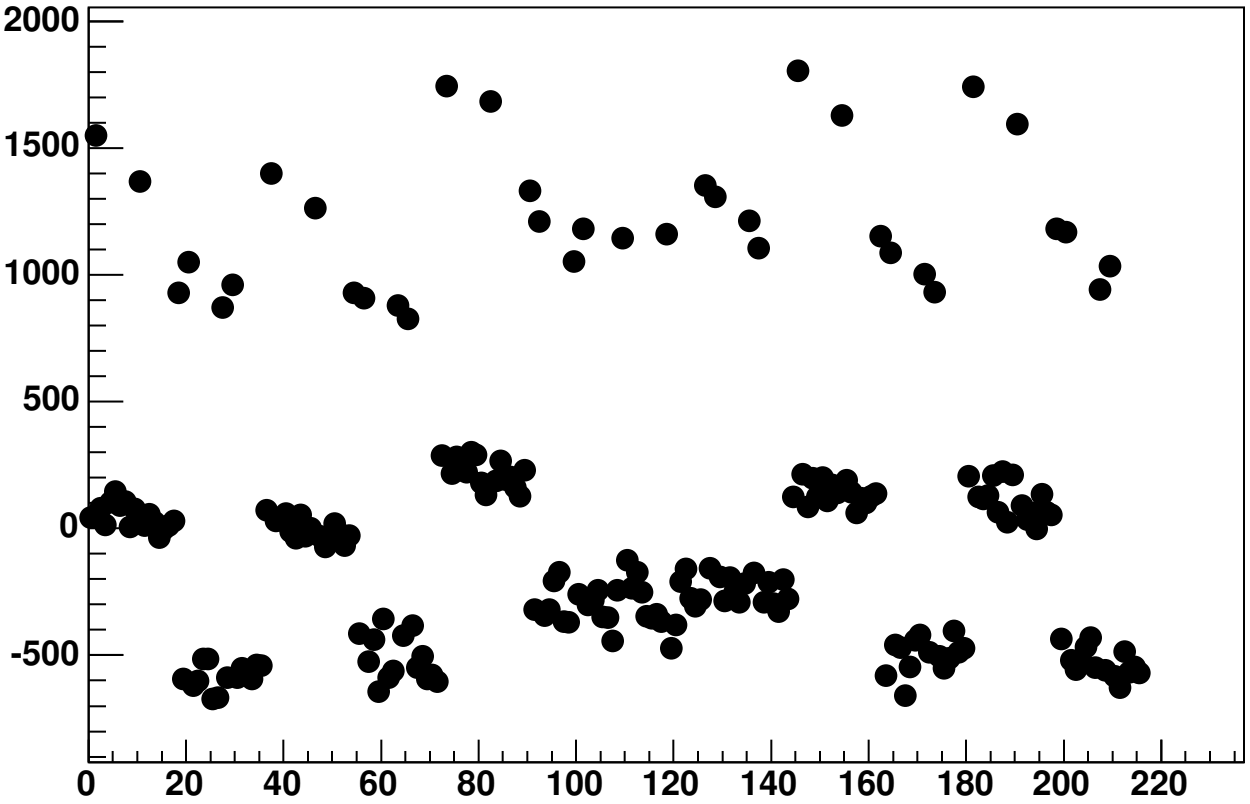
Enable 3, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



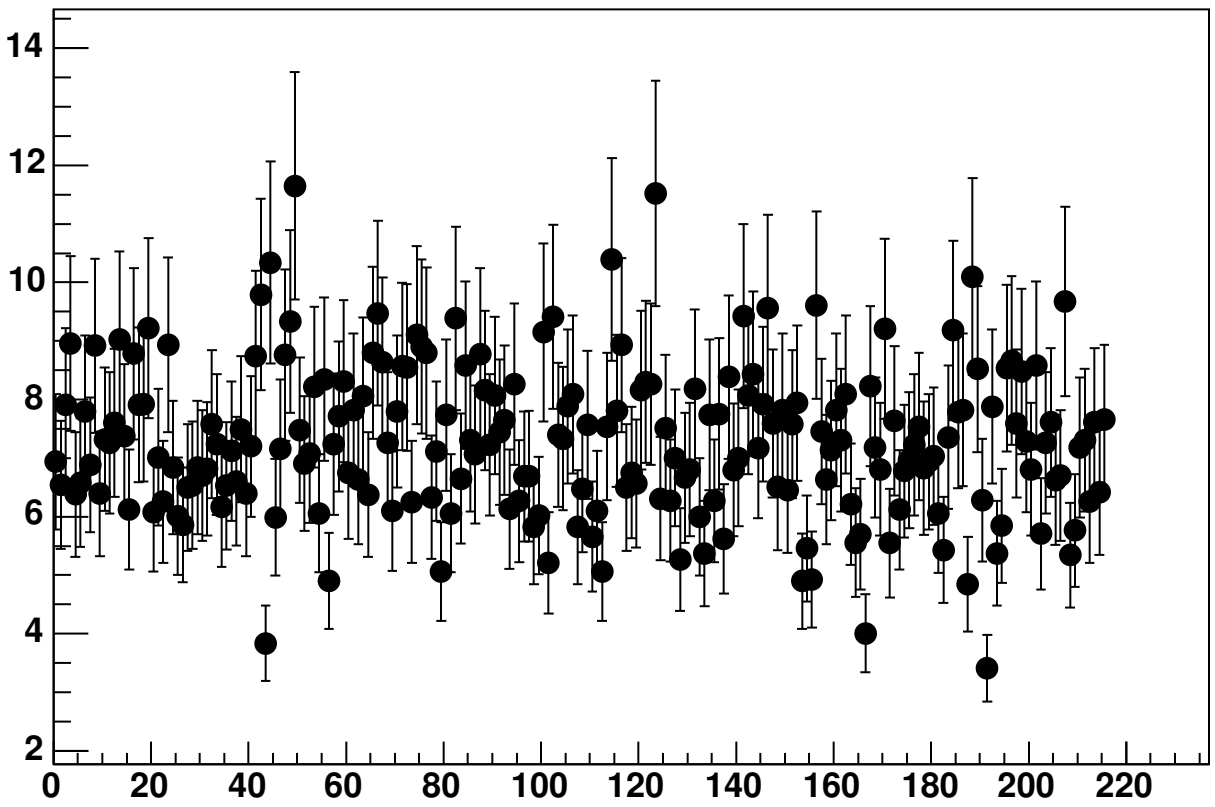
Enable 3, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



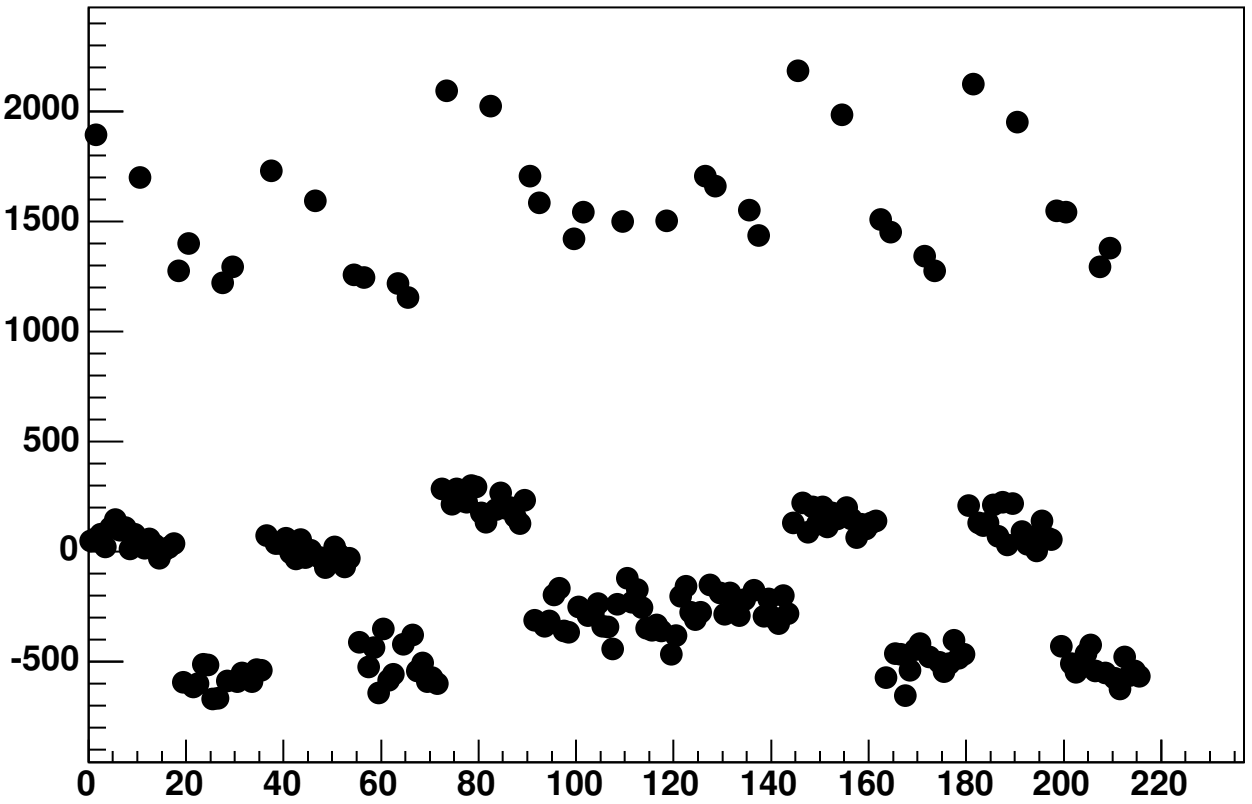
Enable 3, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



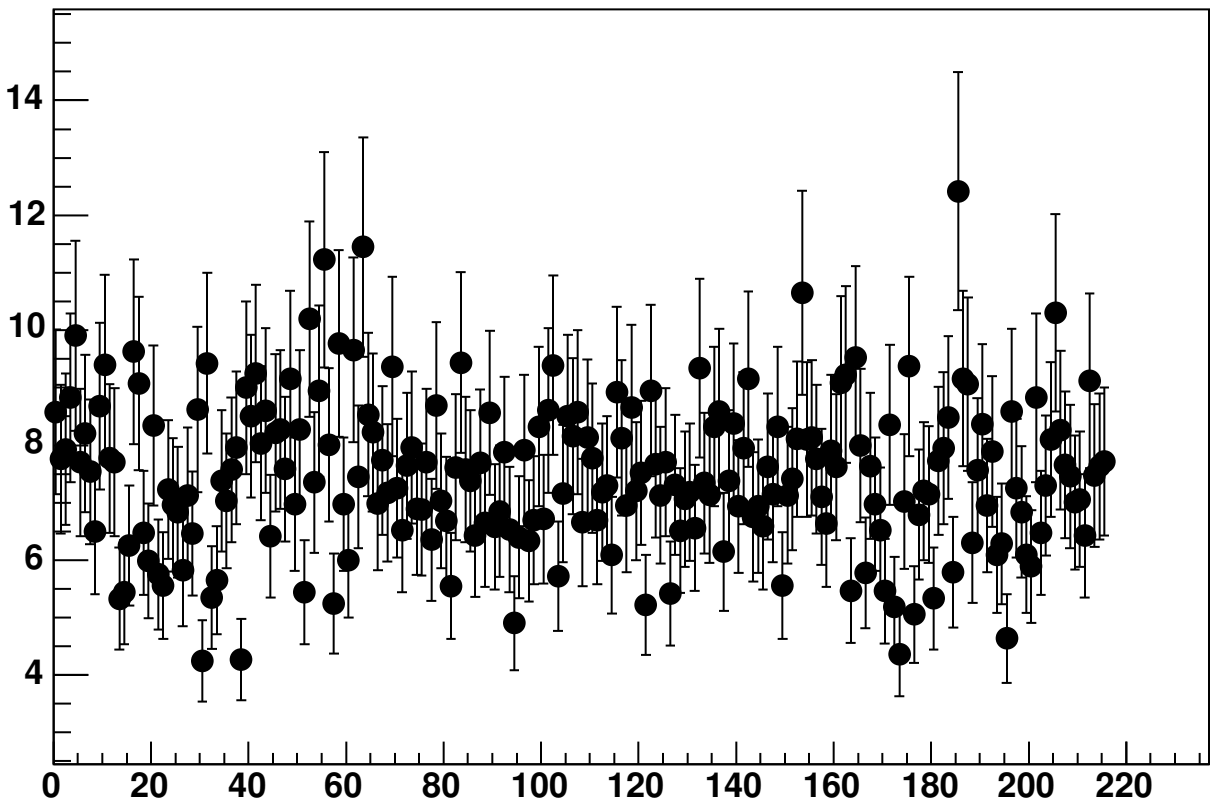
Enable 3, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



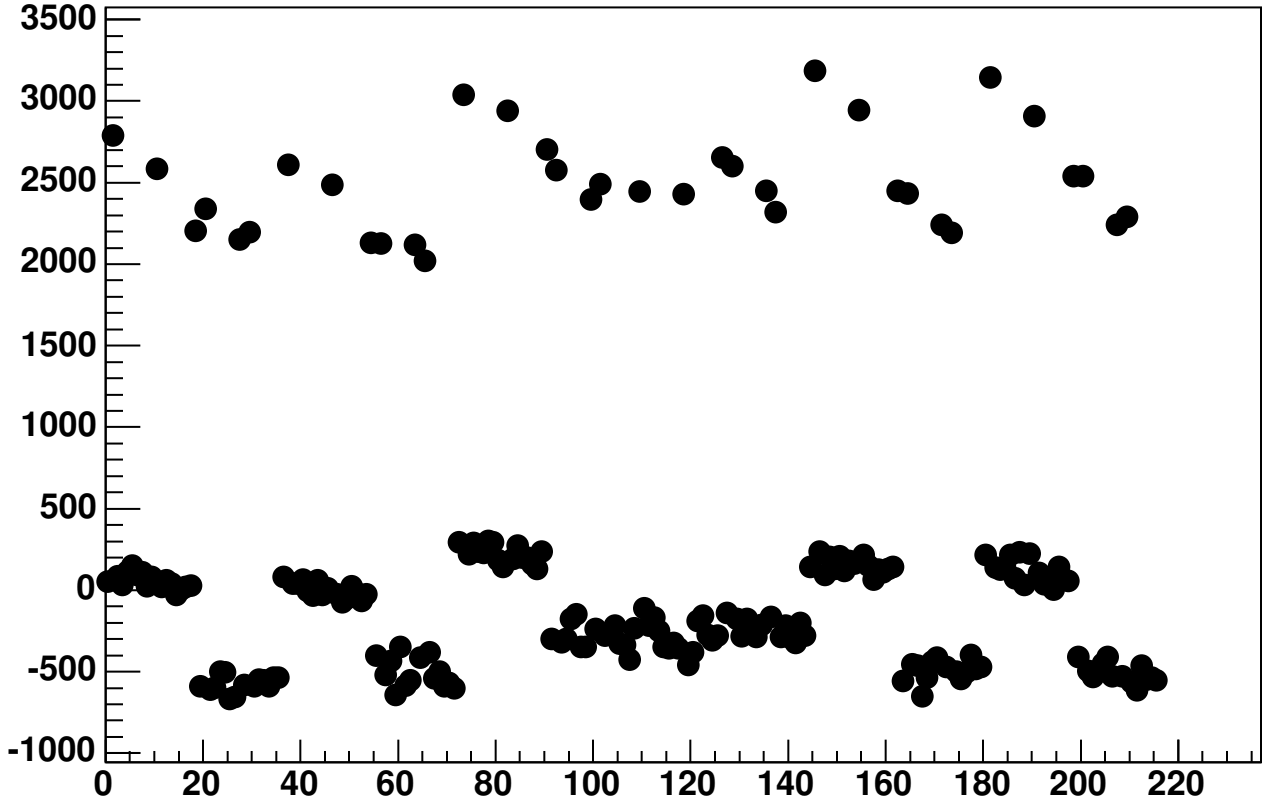
Enable 3, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



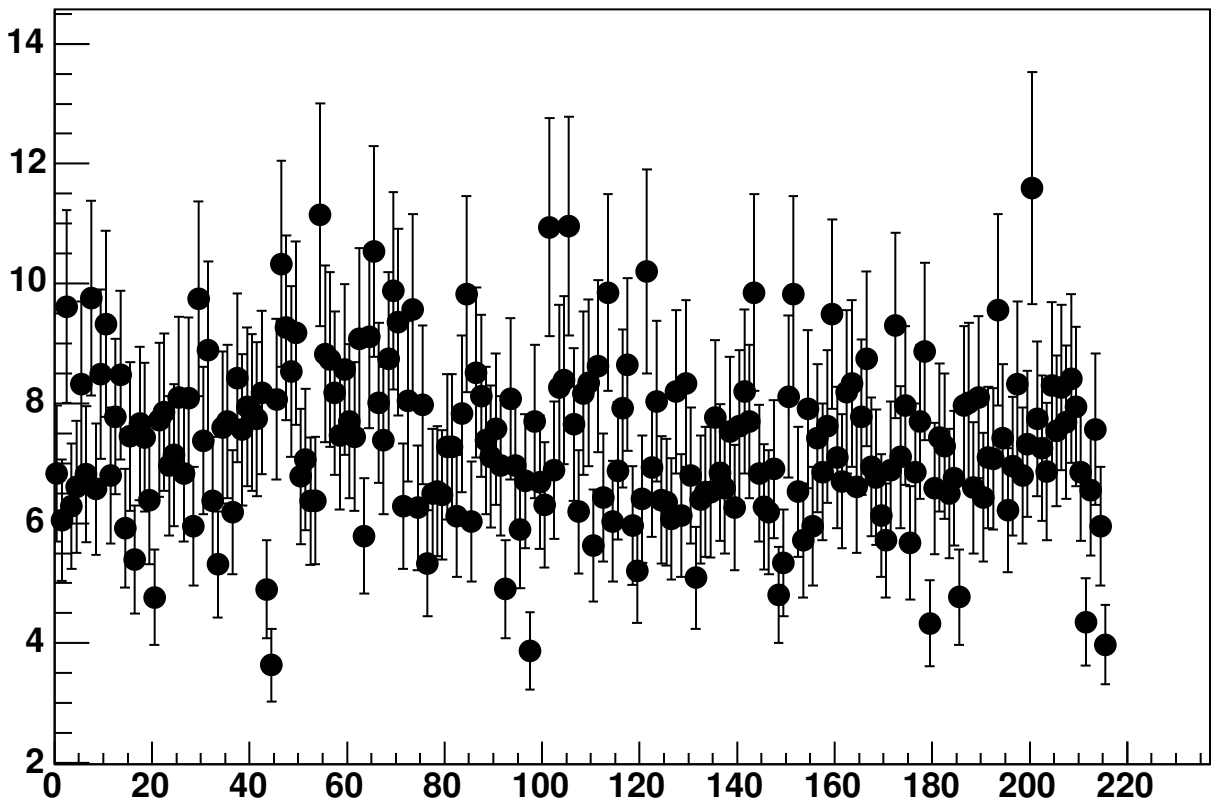
Enable 3, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



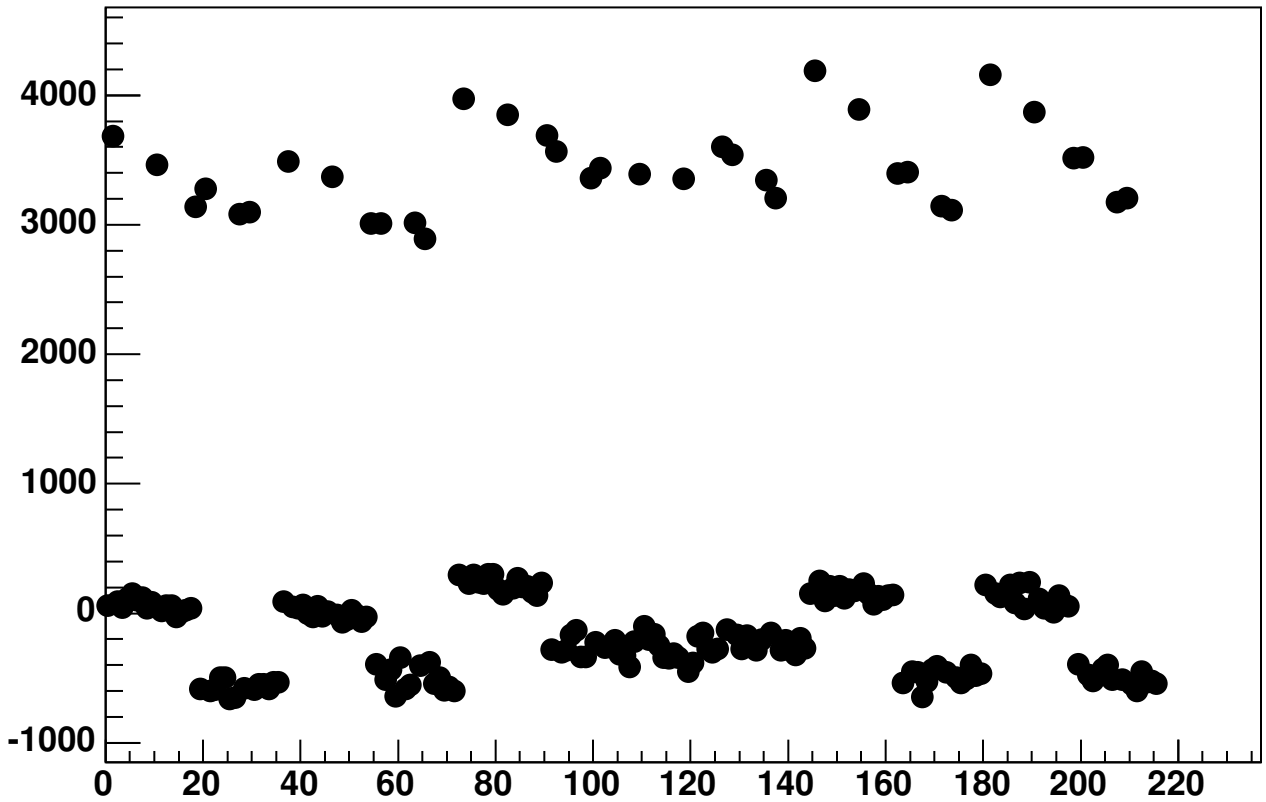
Enable 3, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



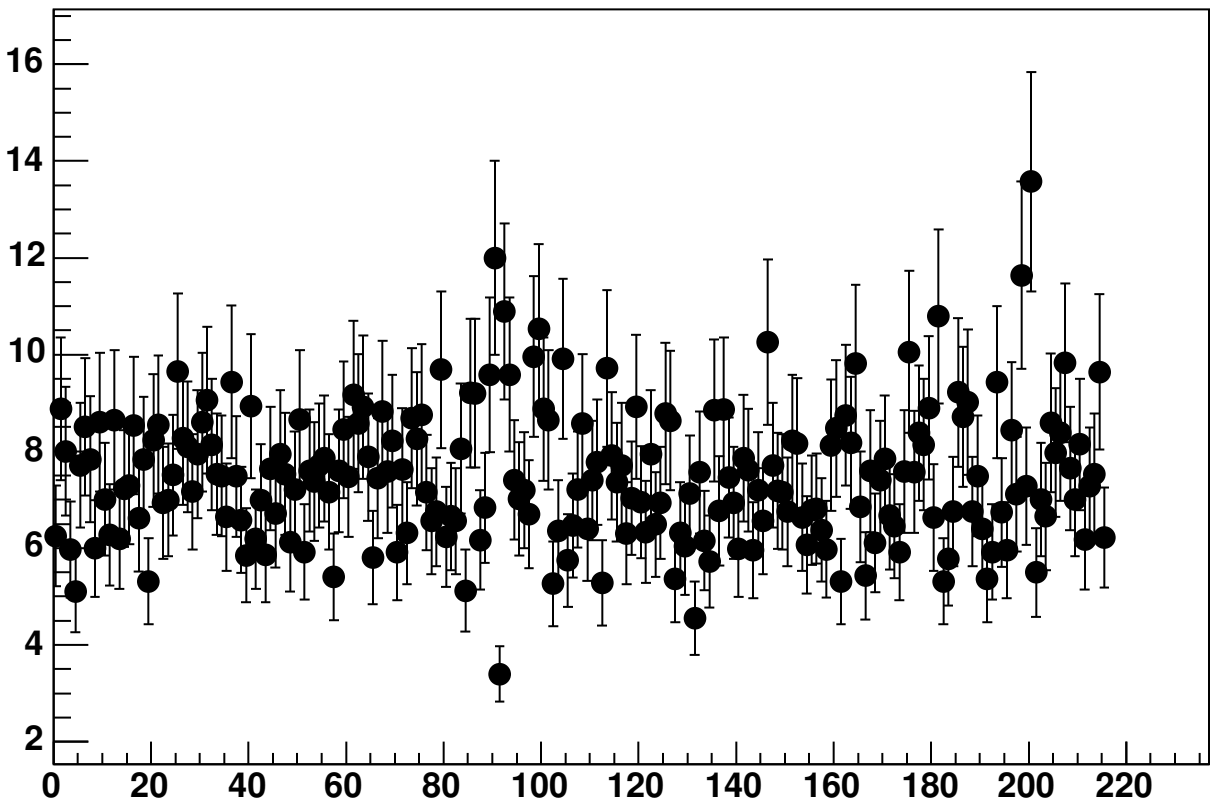
Enable 3, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



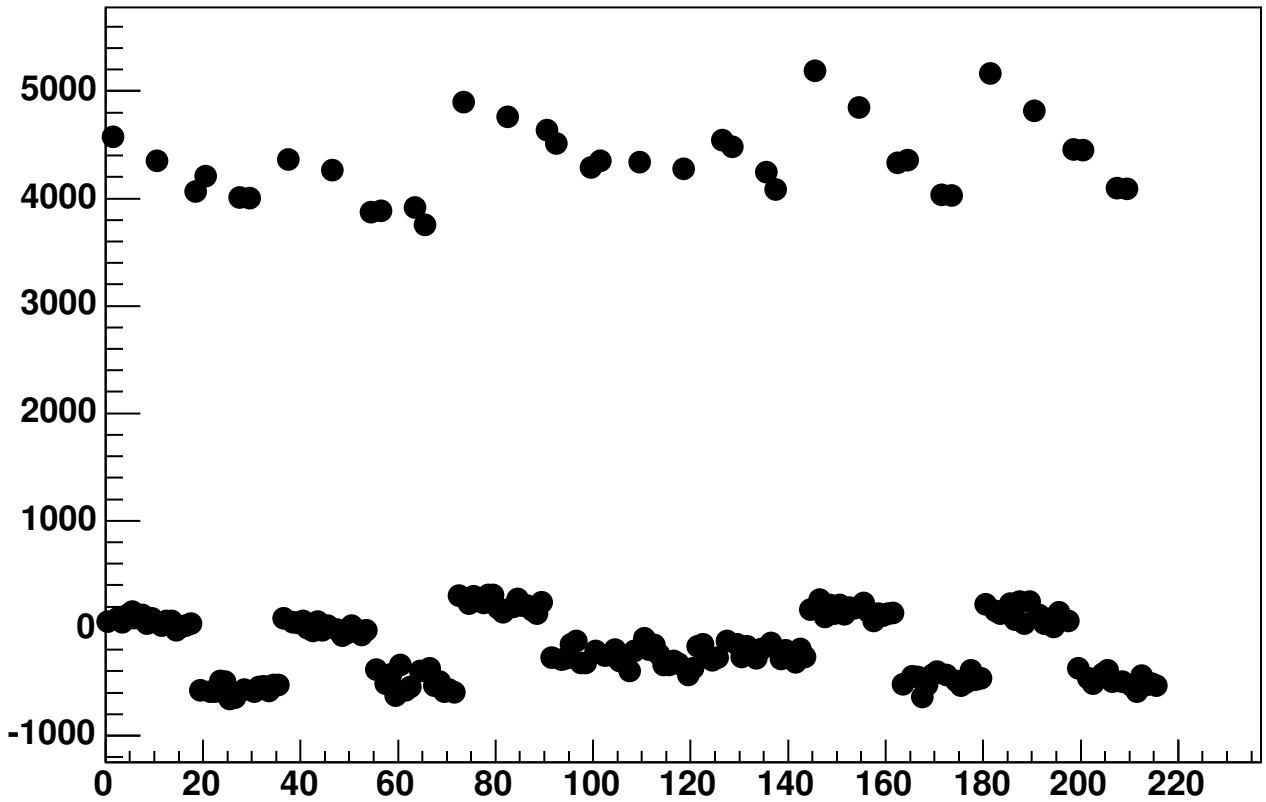
Enable 3, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



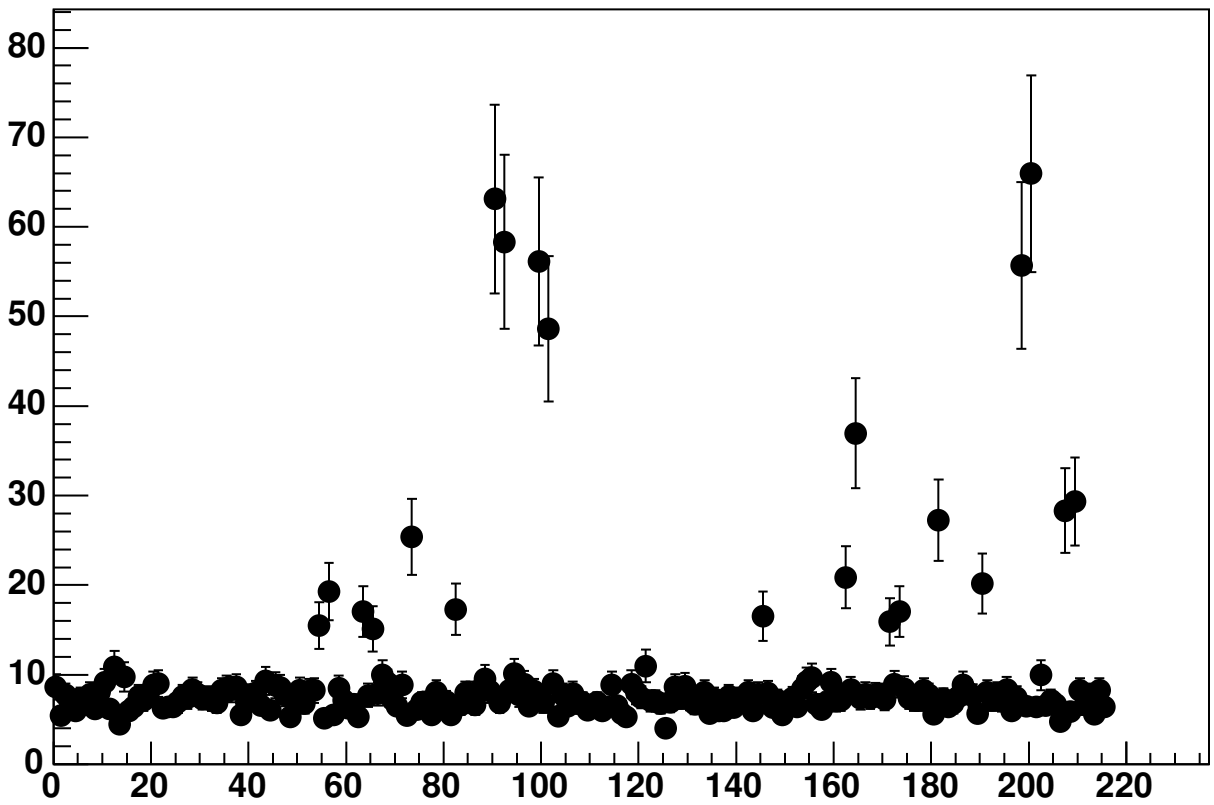
Enable 3, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



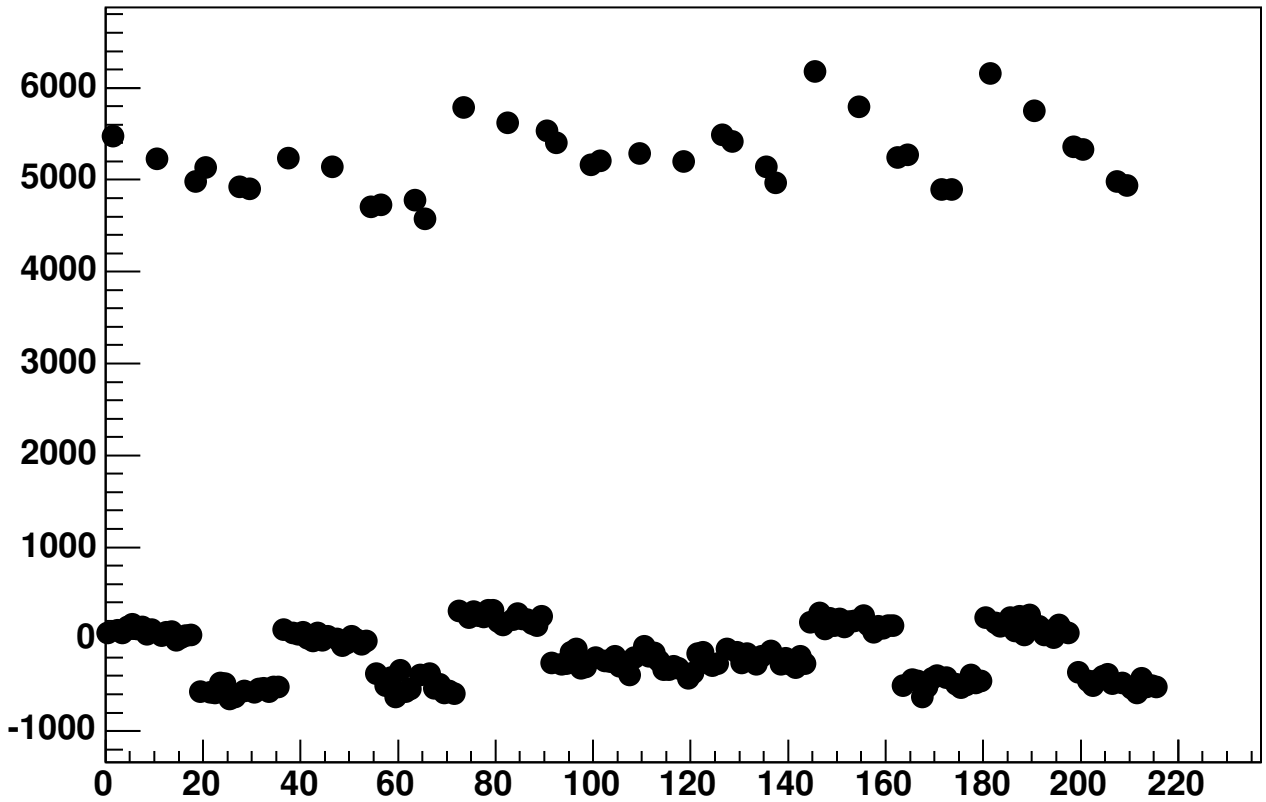
Enable 3, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



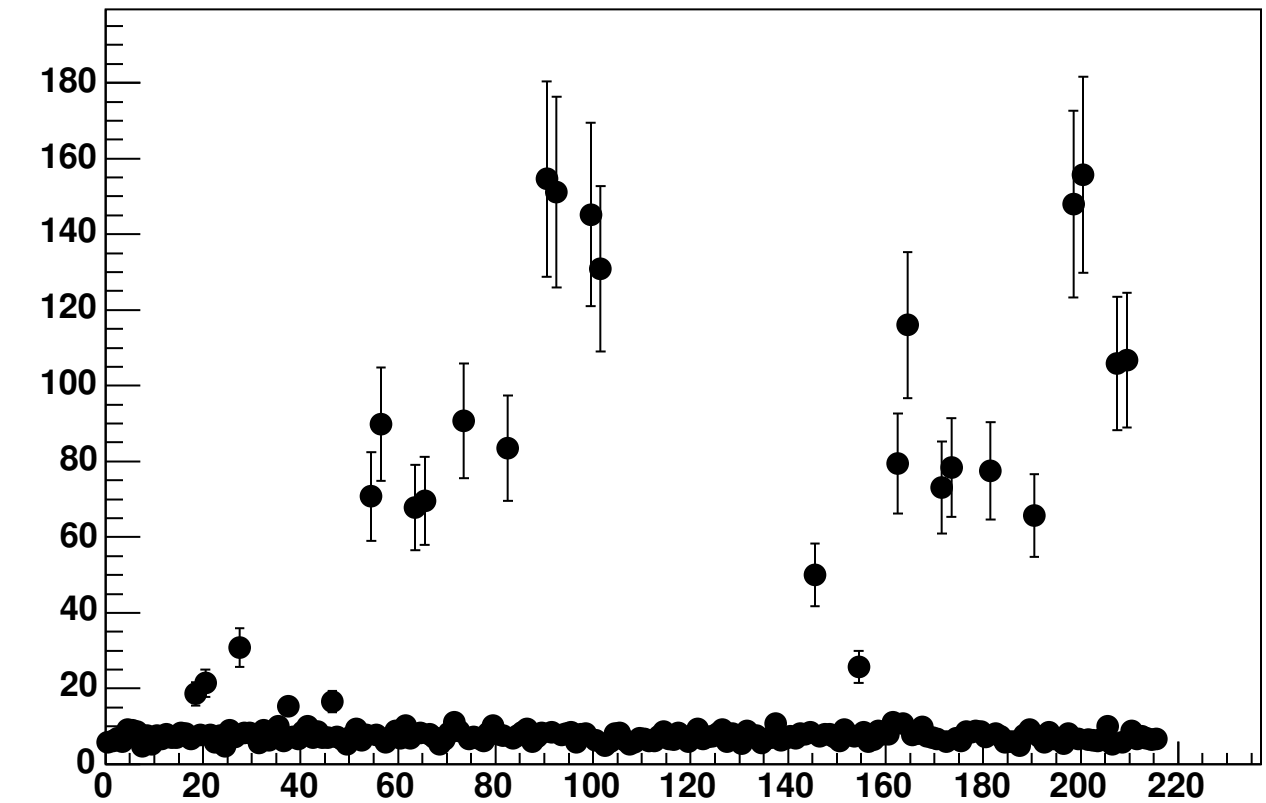
Enable 3, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



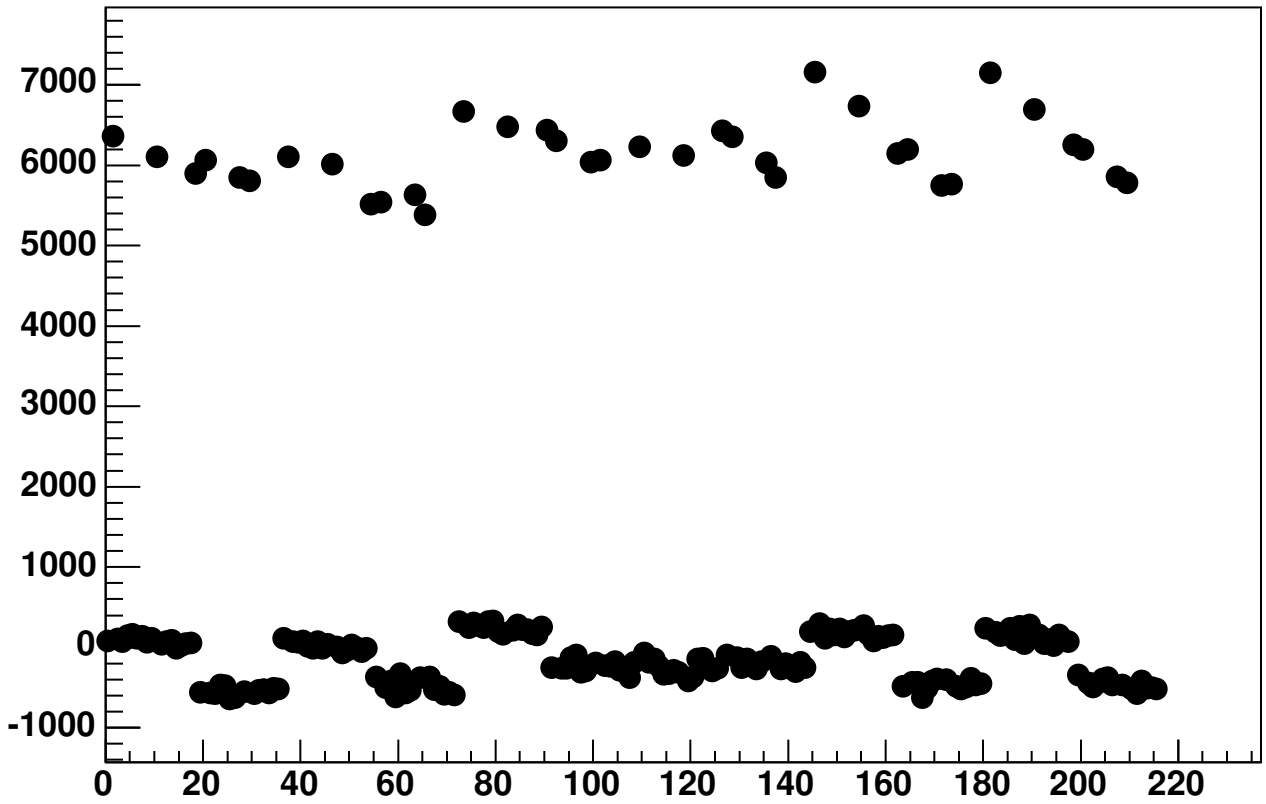
Enable 3, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



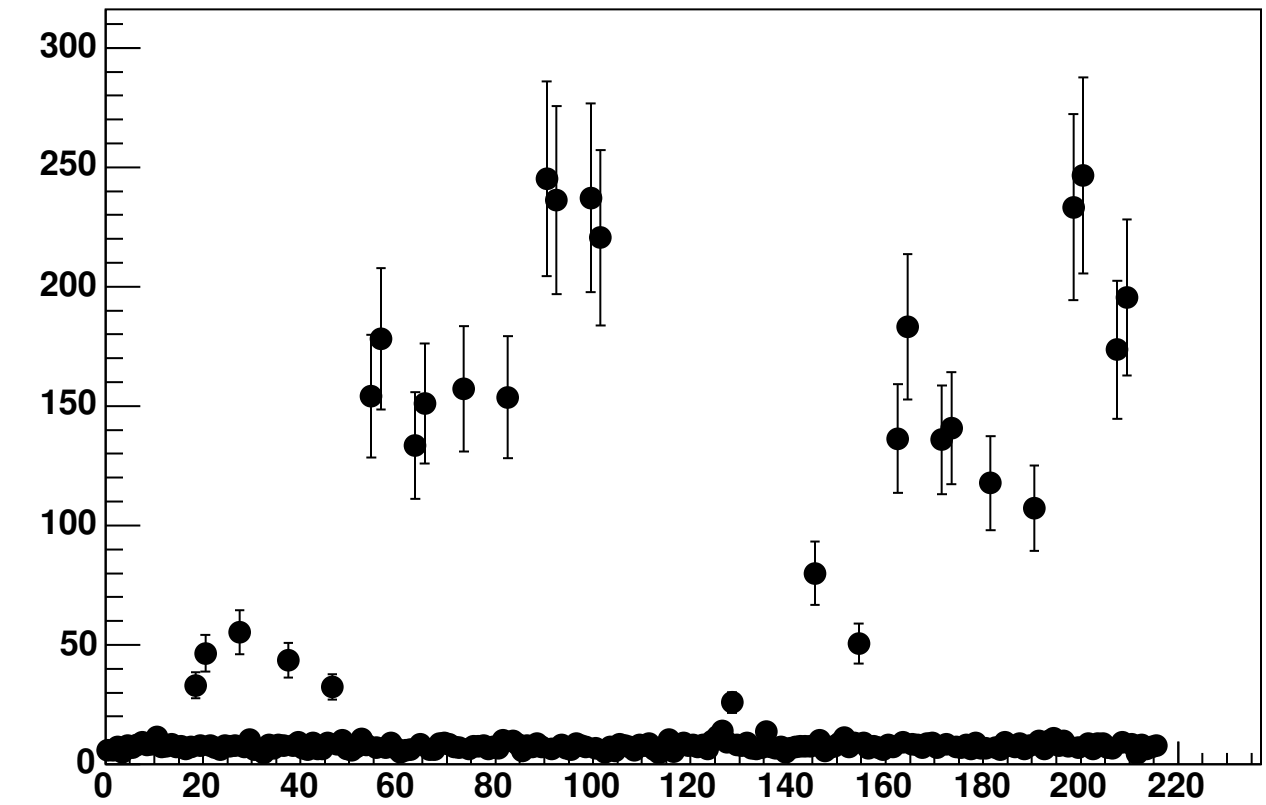
Enable 3, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



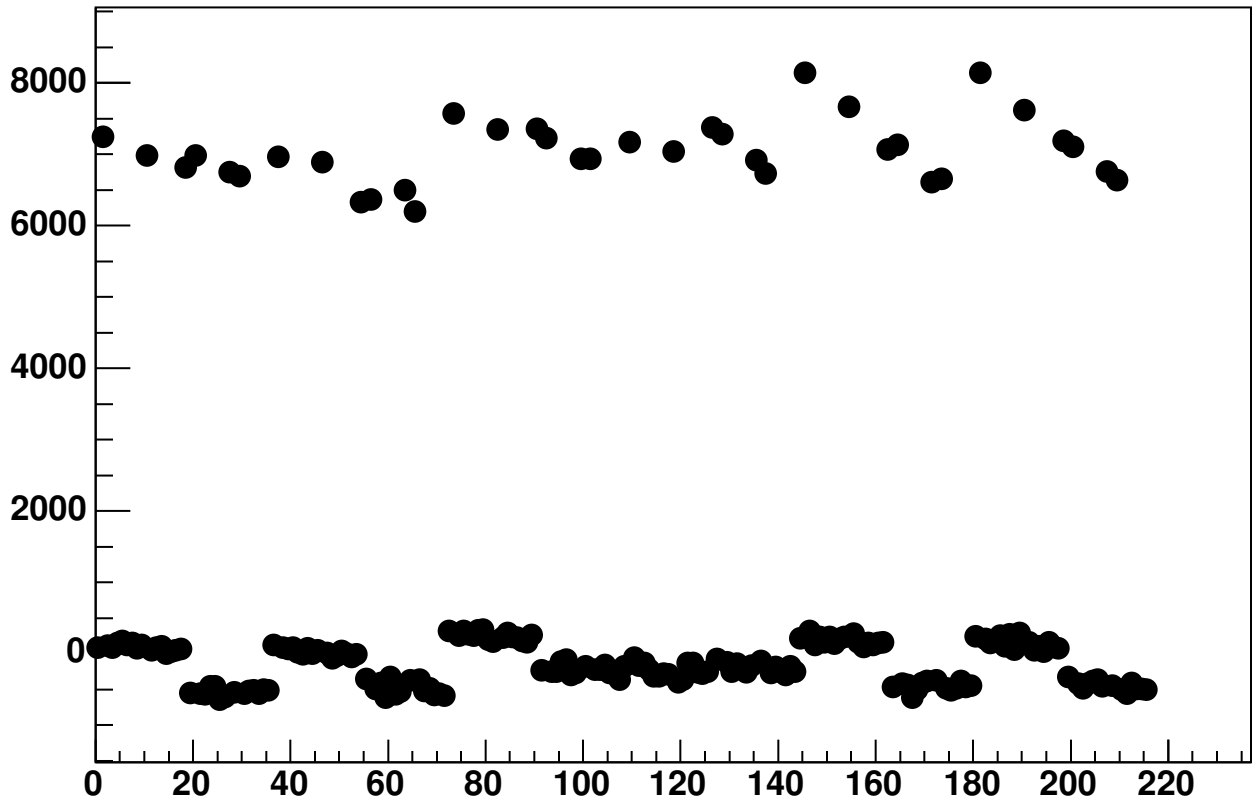
Enable 3, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



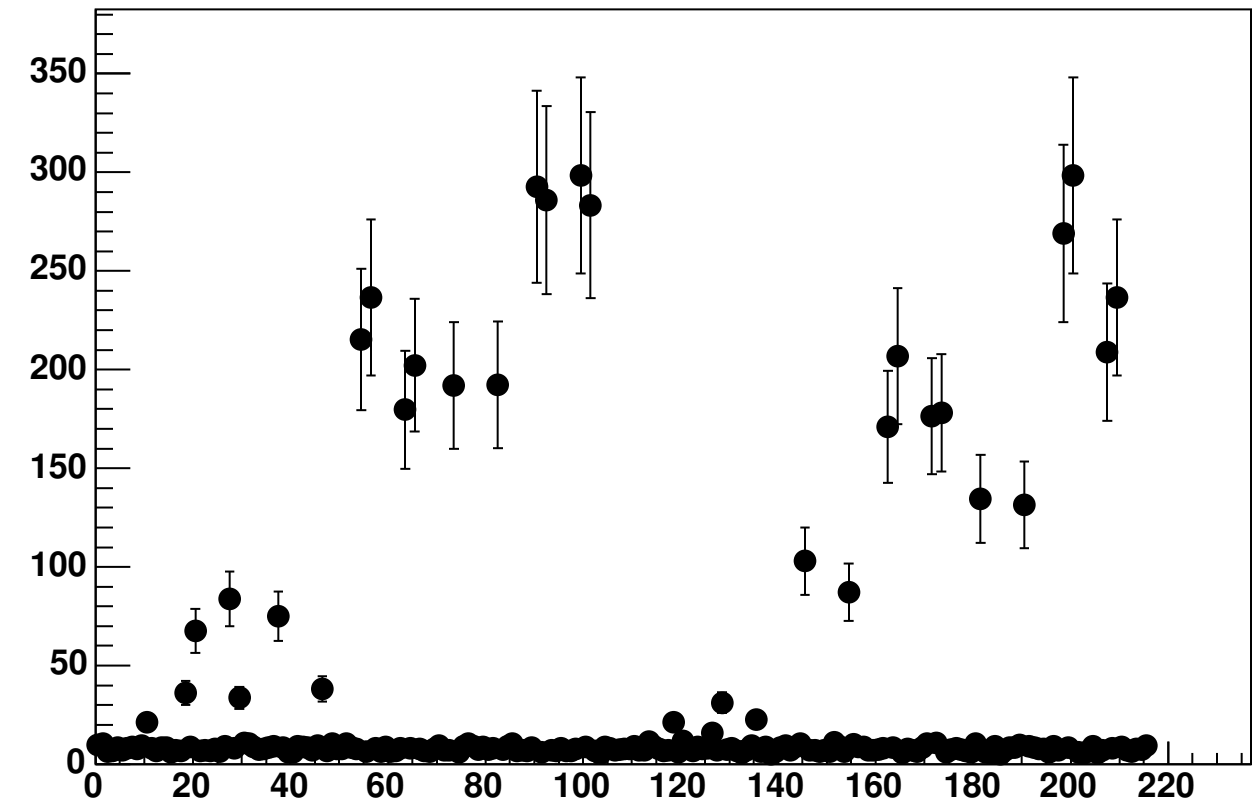
Enable 3, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



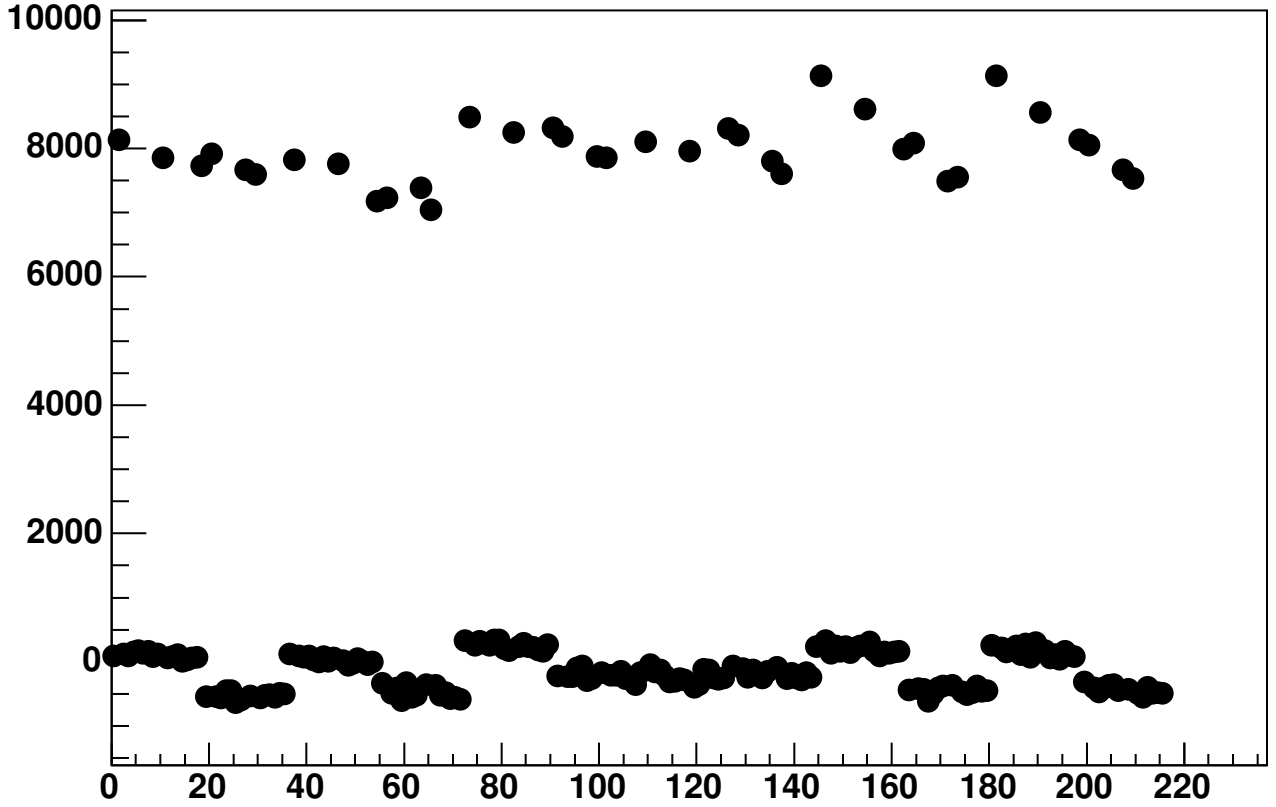
Enable 3, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



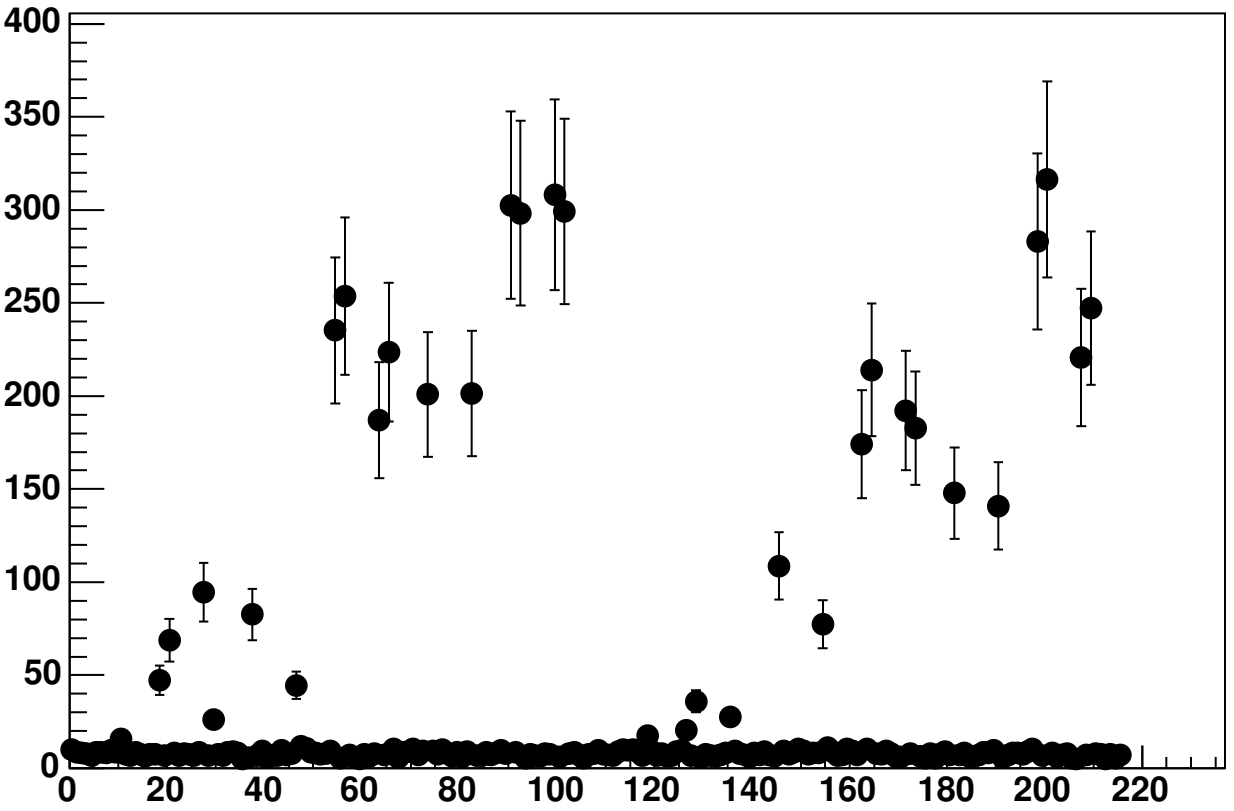
Enable 3, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



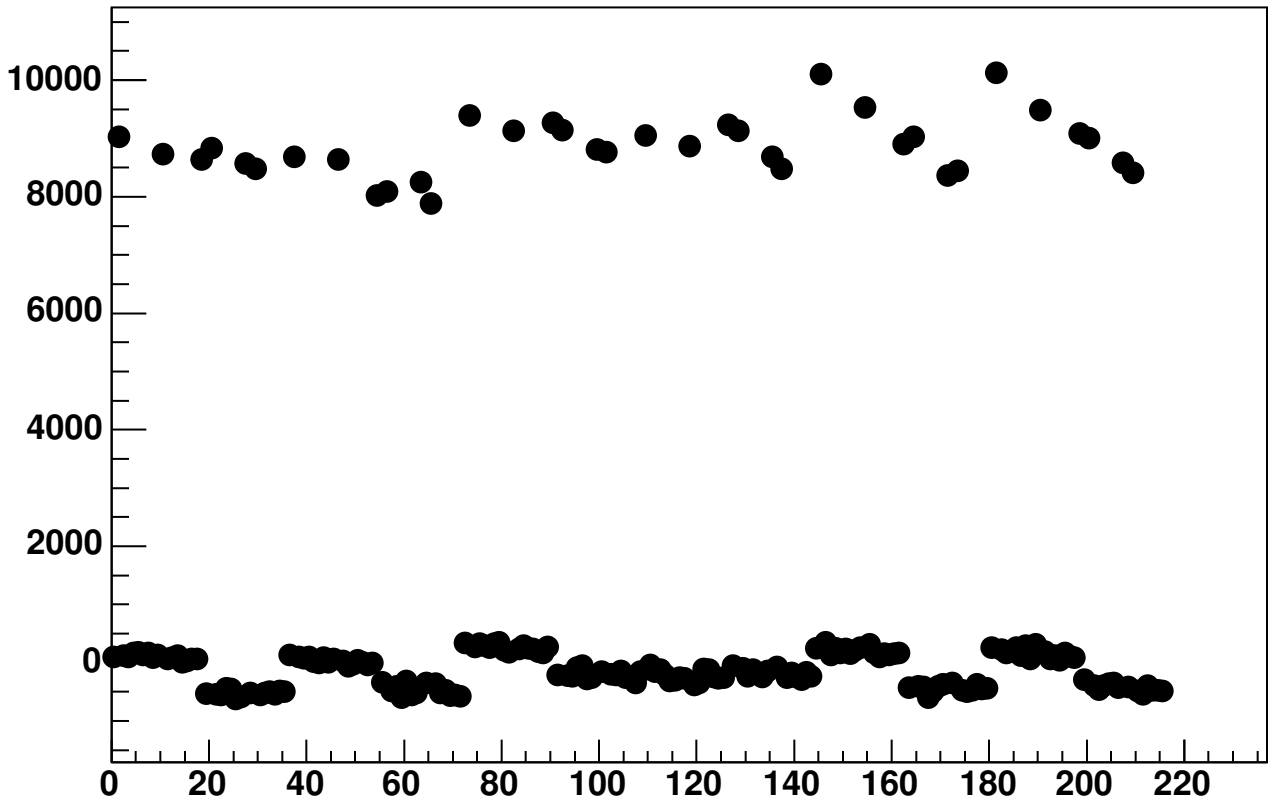
Enable 3, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



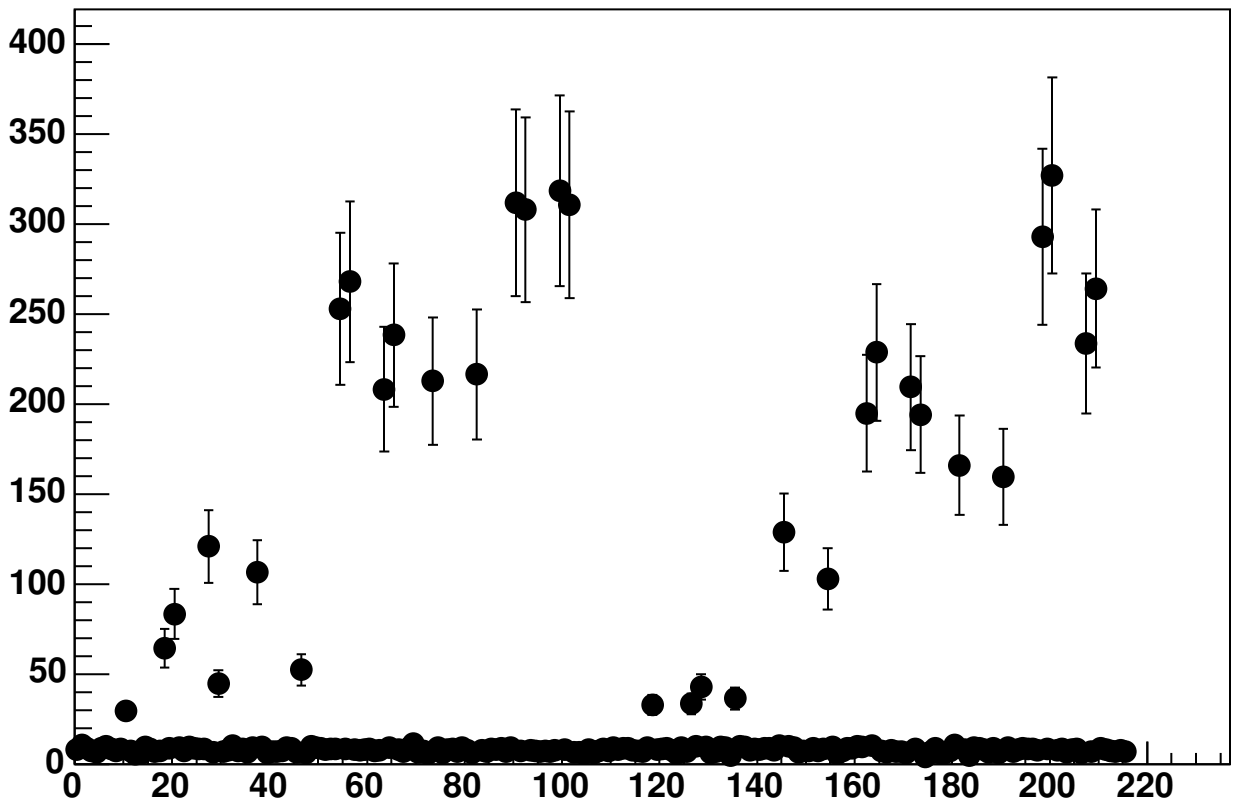
Enable 3, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



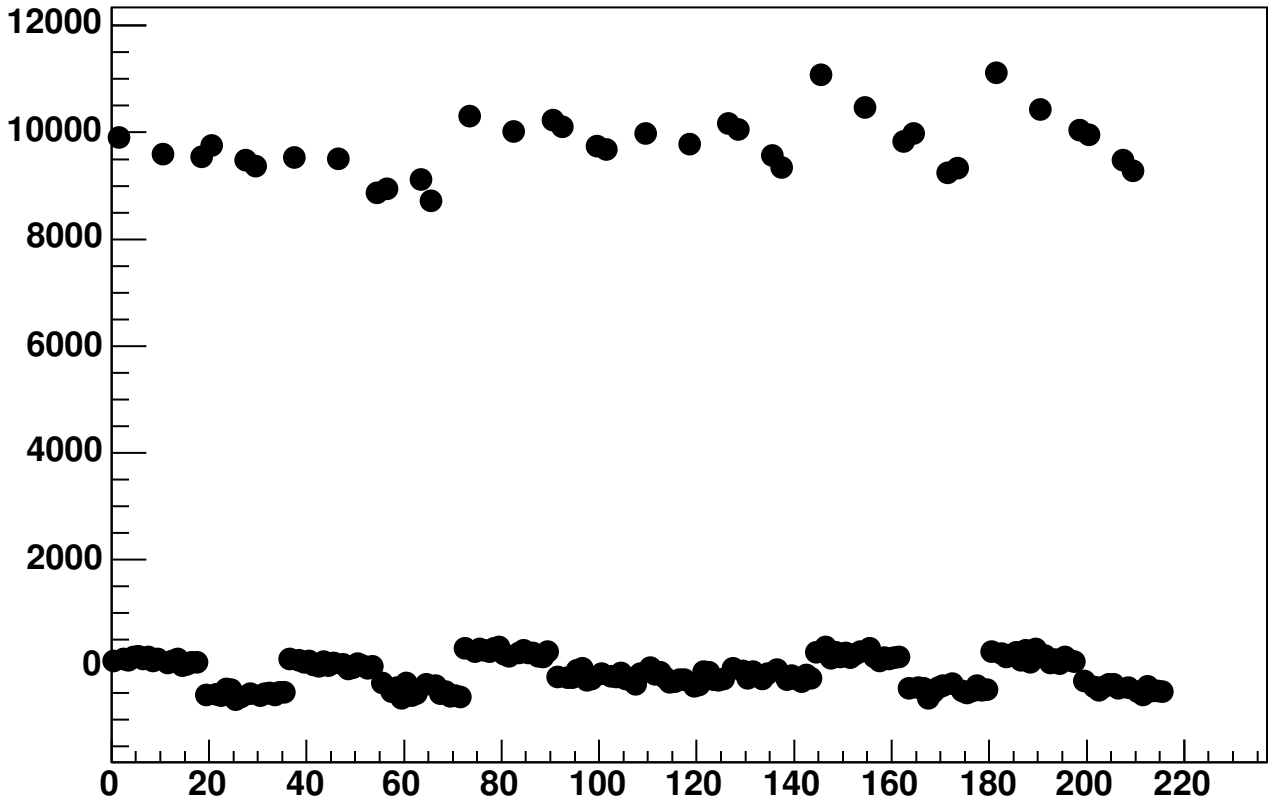
Enable 3, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



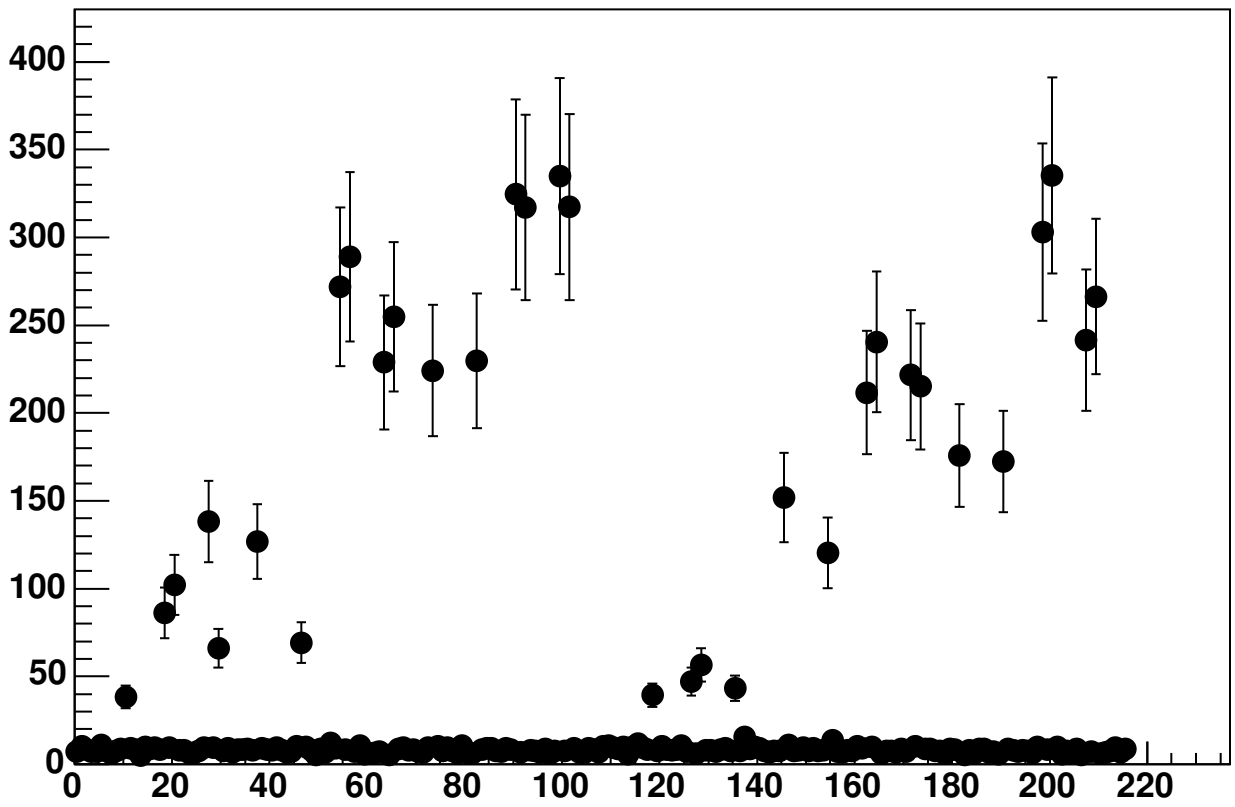
Enable 3, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



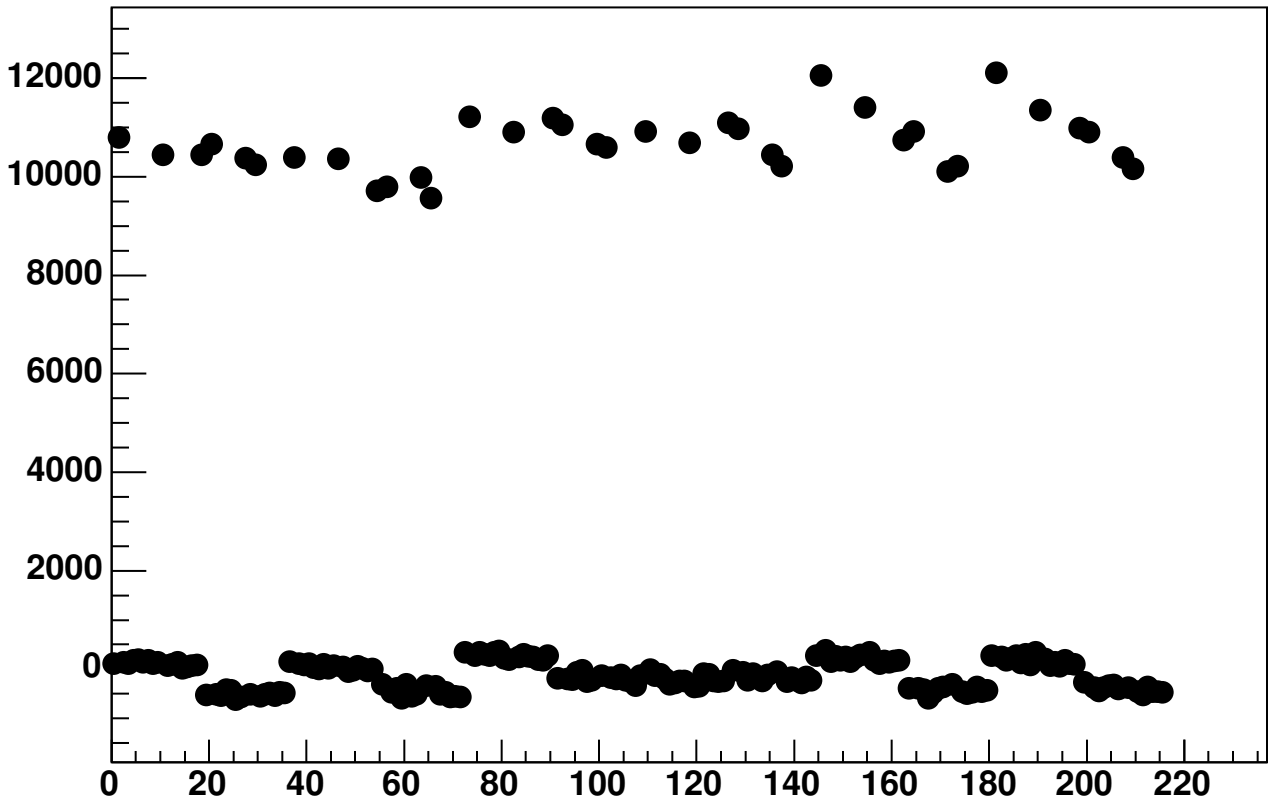
Enable 3, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



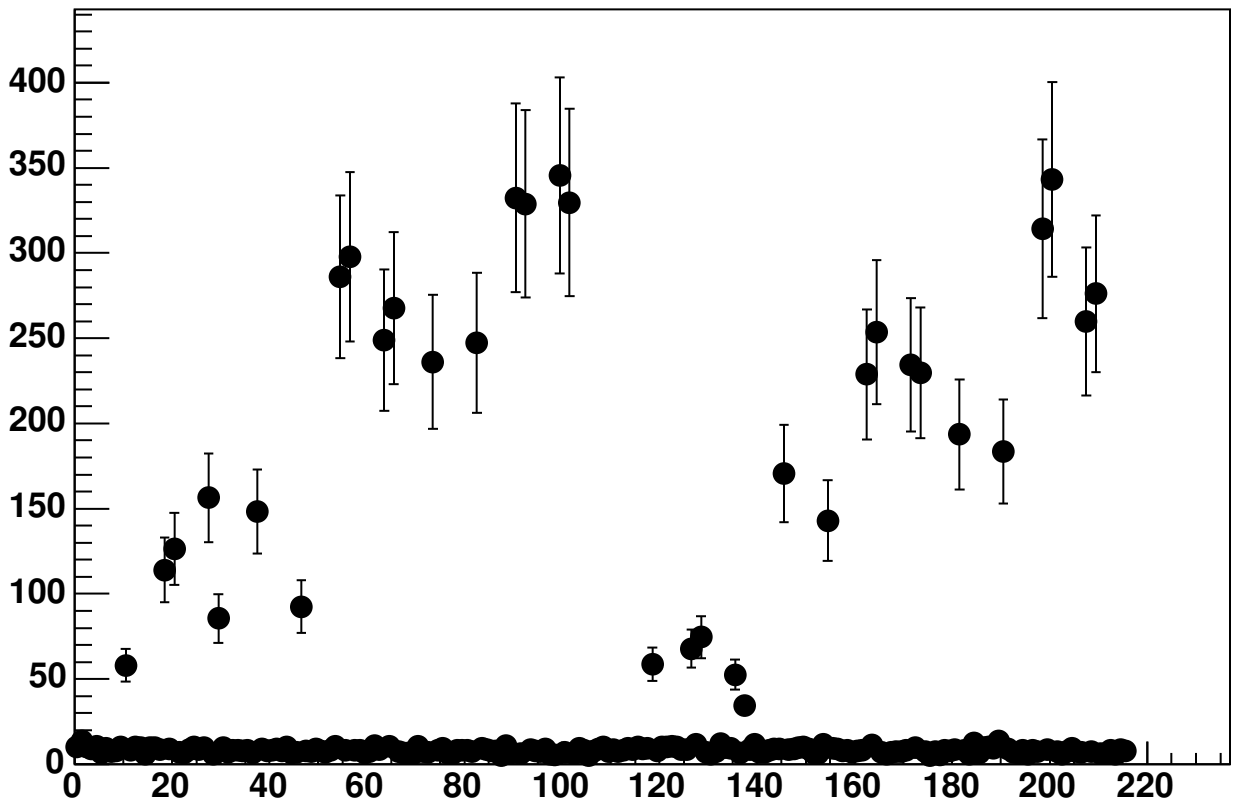
Enable 3, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



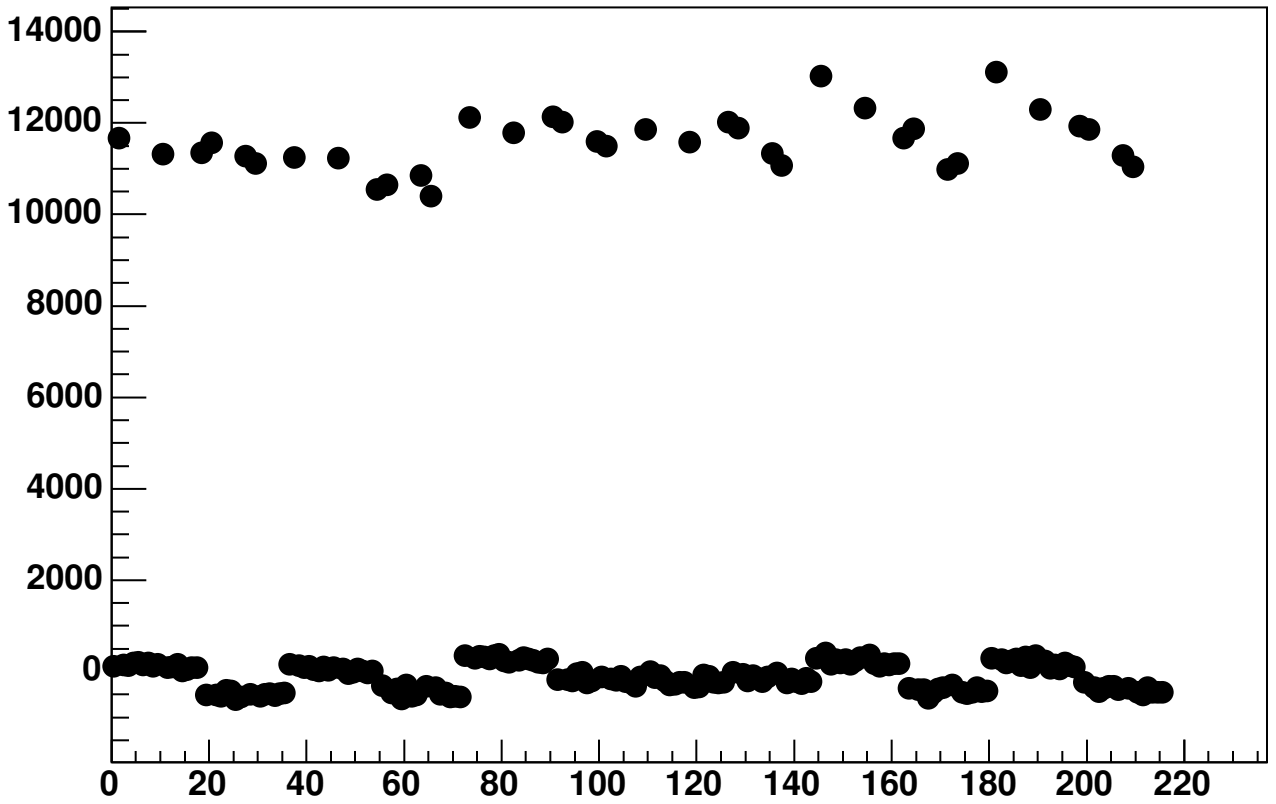
Enable 3, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



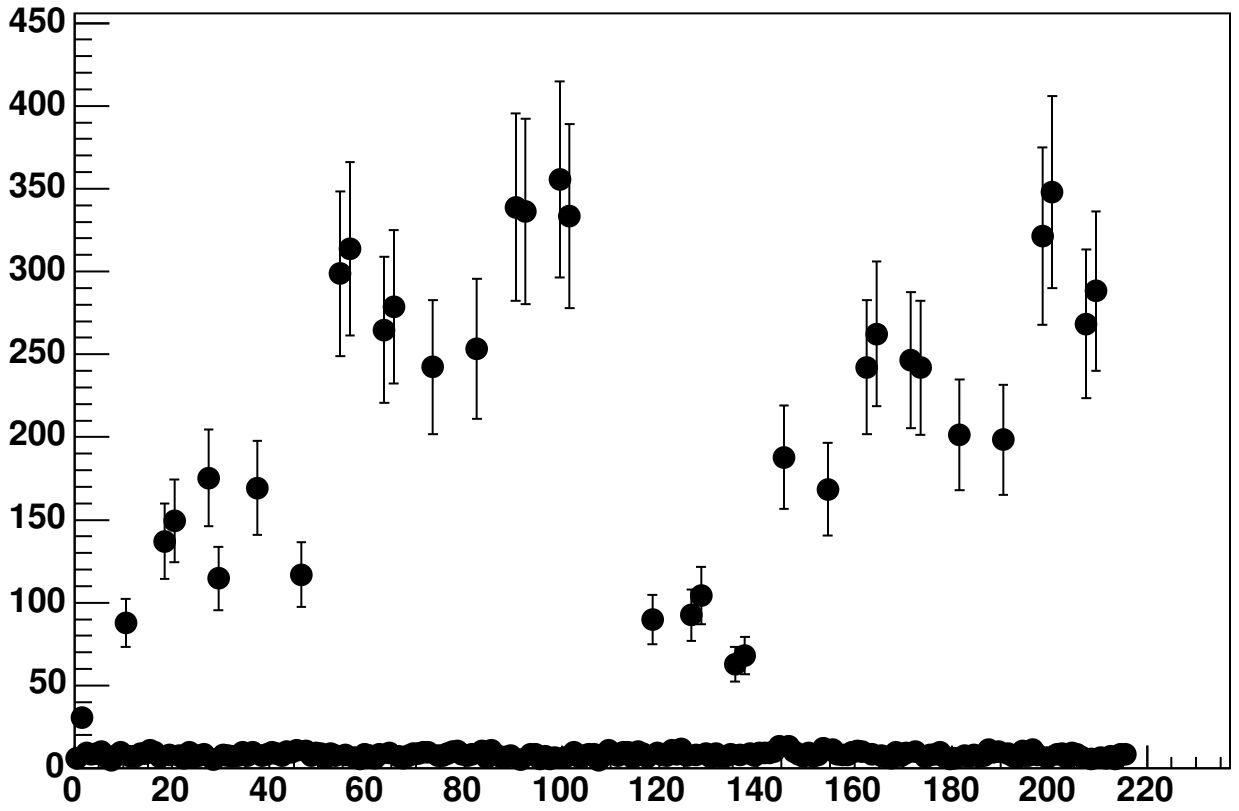
Enable 3, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



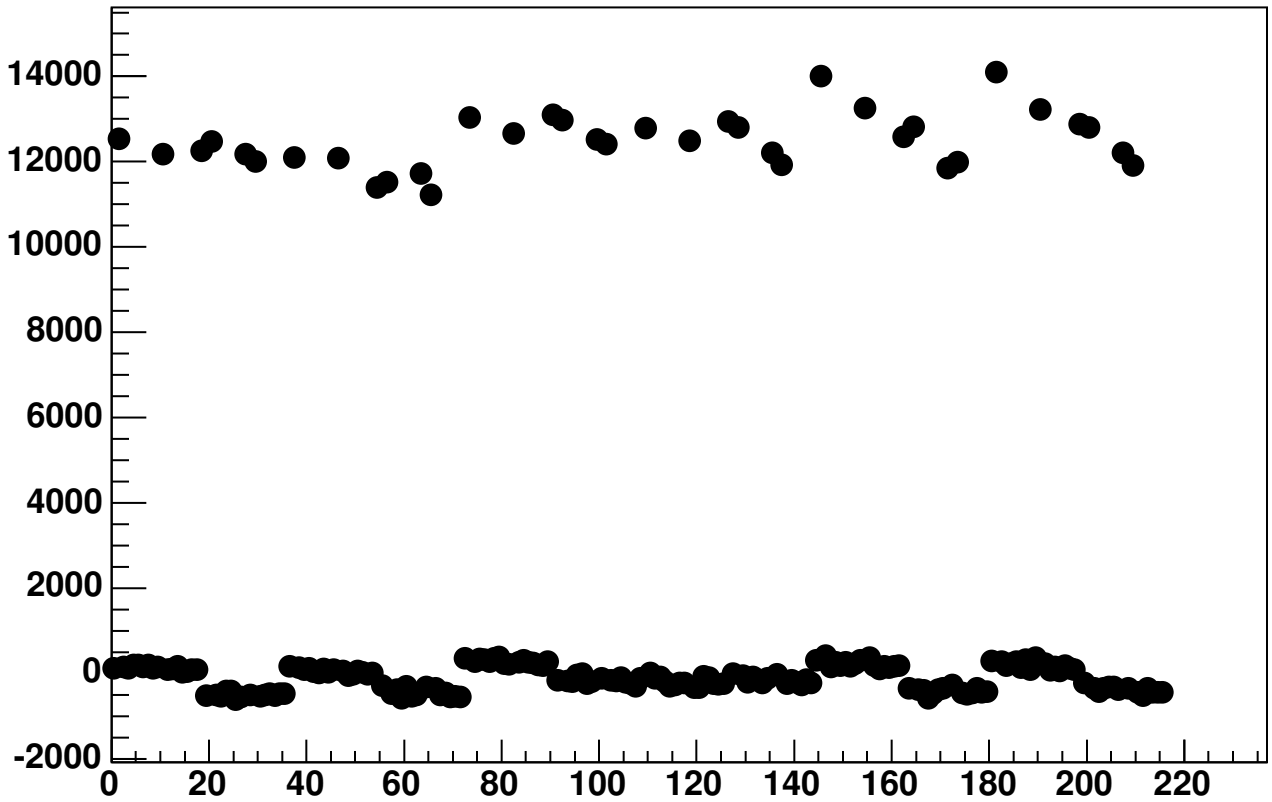
Enable 3, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



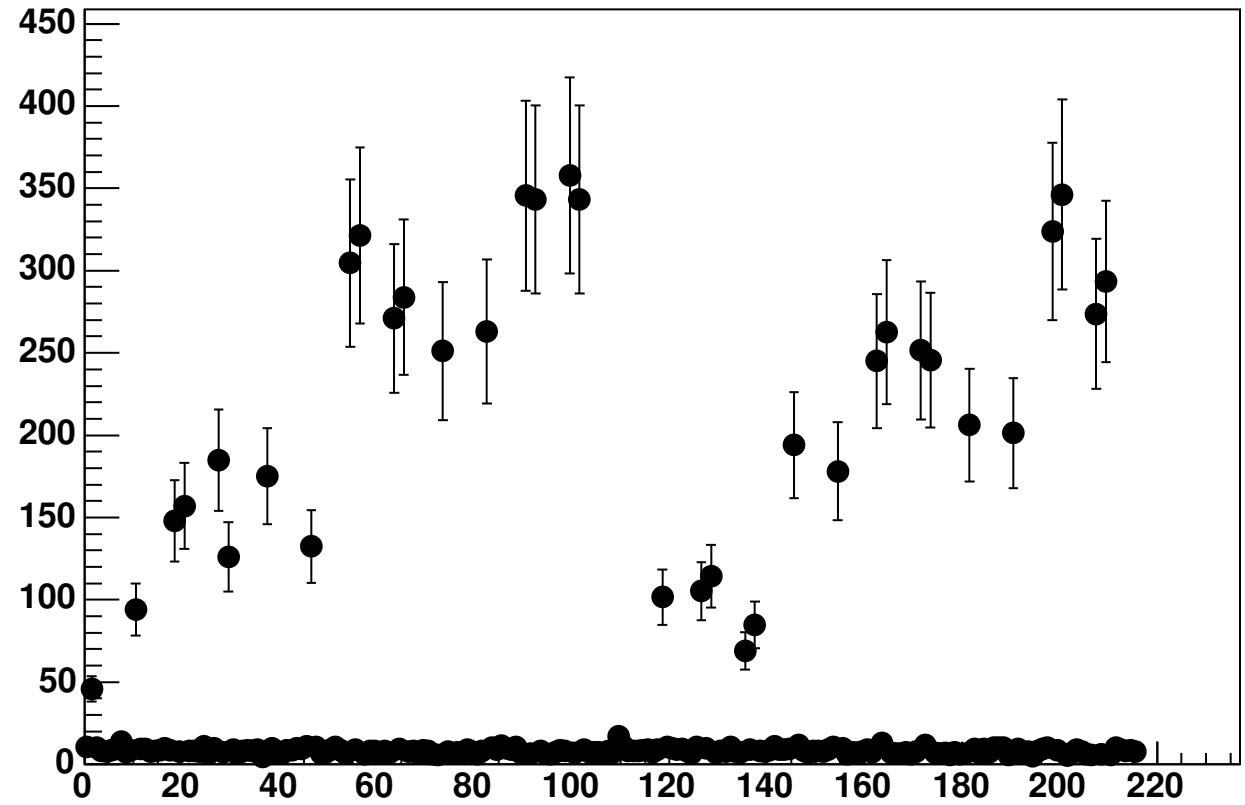
Enable 3, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



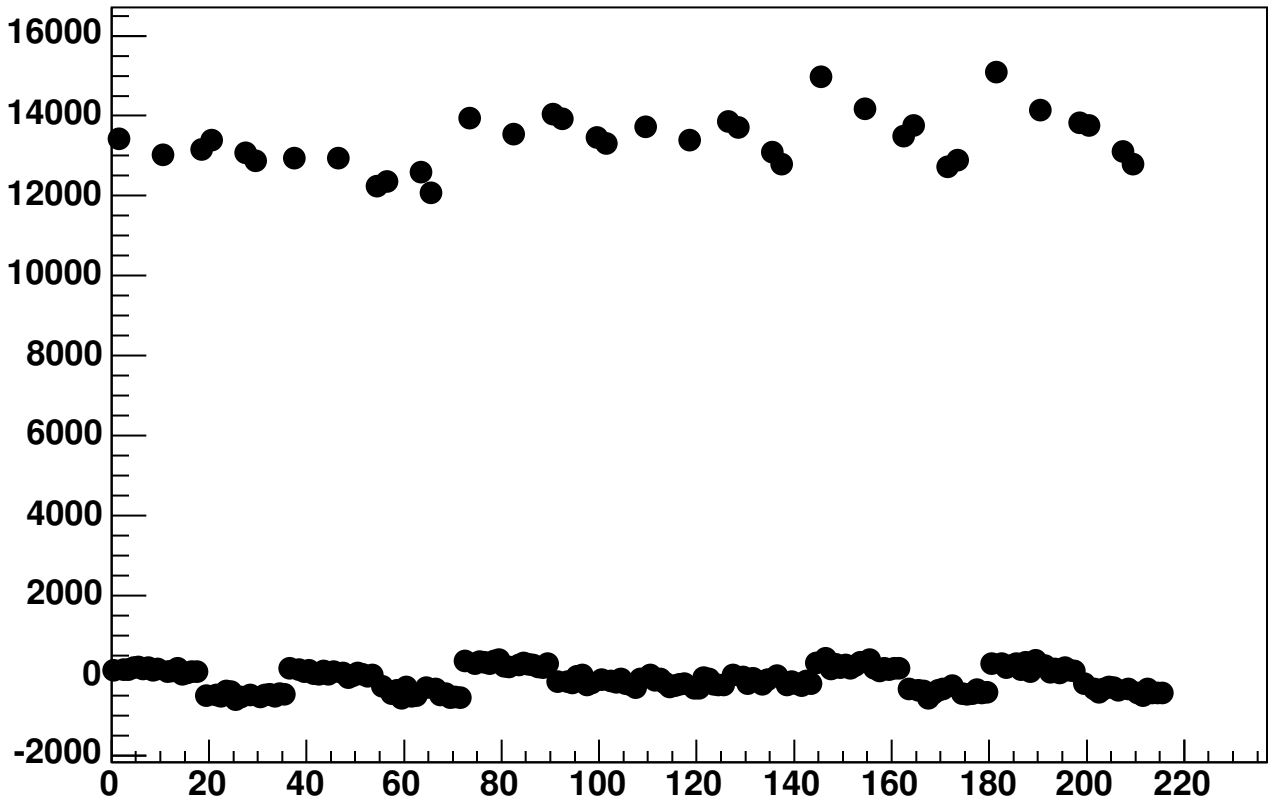
Enable 3, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



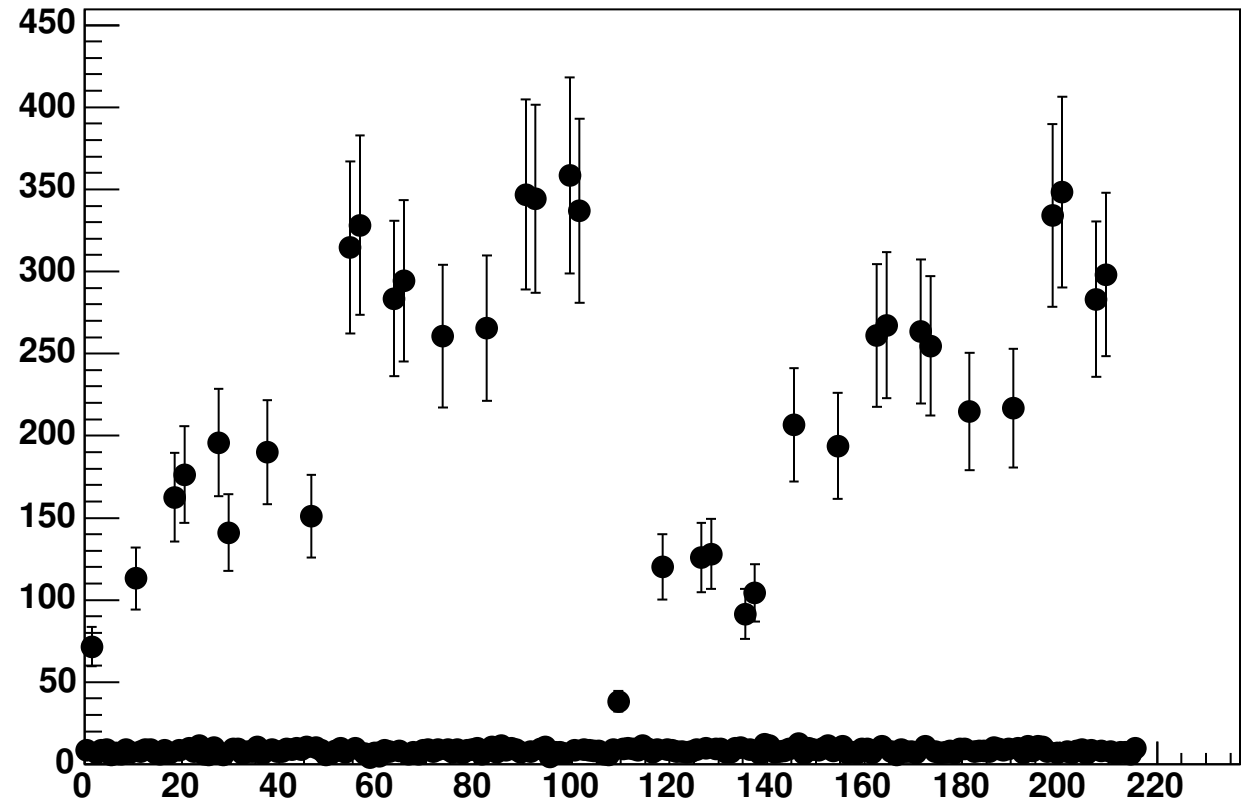
Enable 3, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



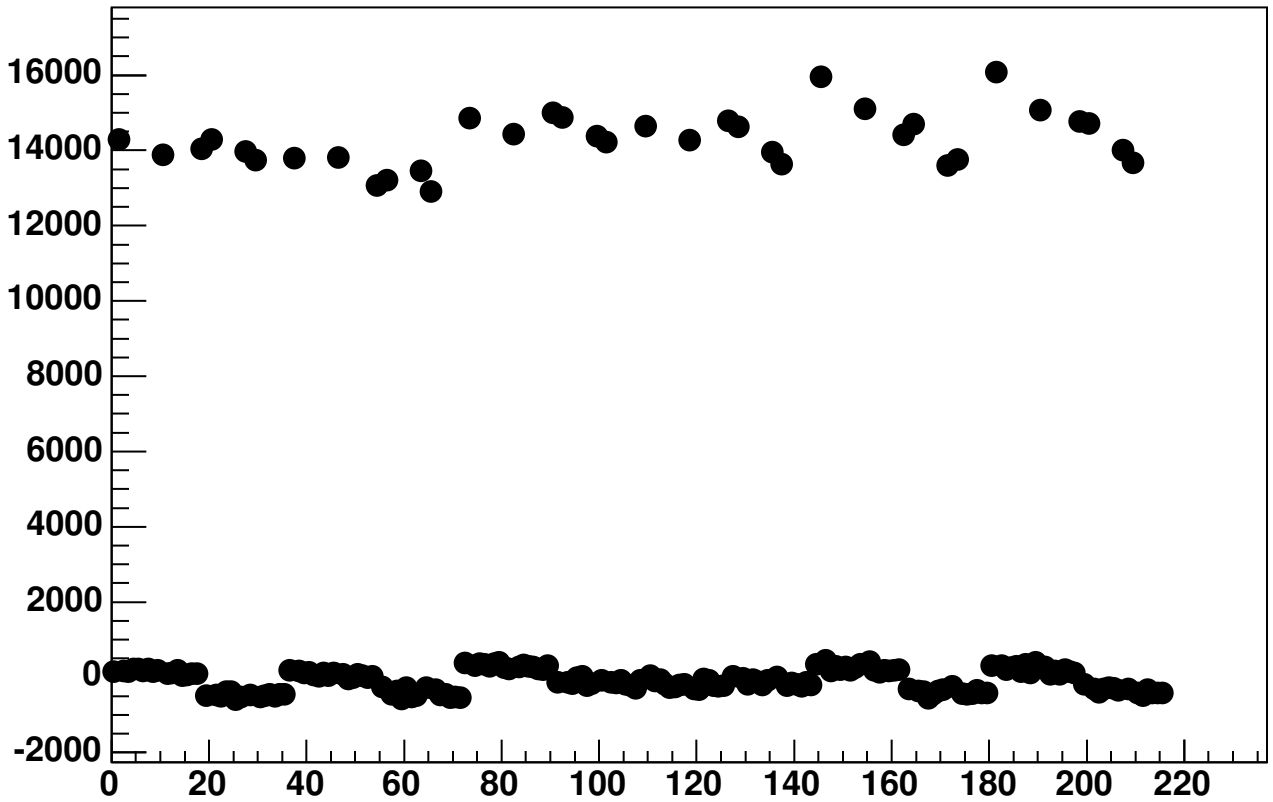
Enable 3, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



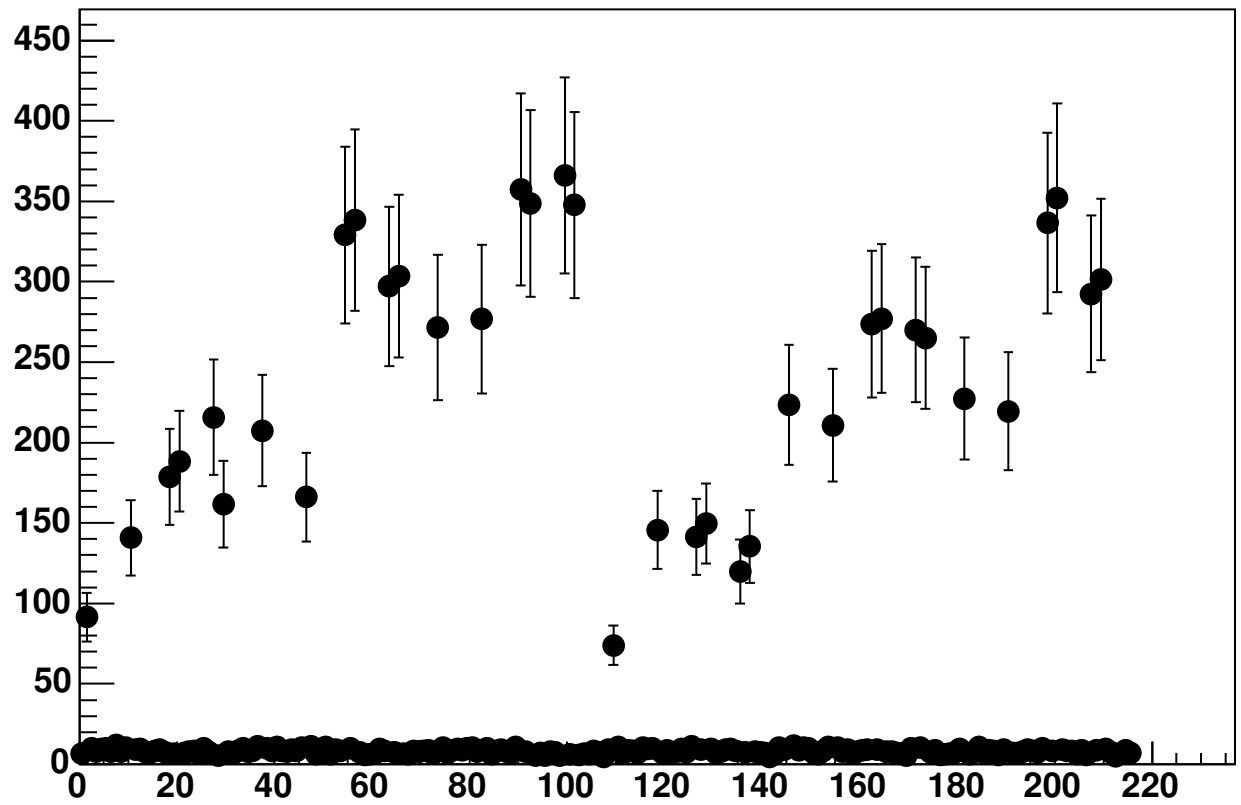
Enable 3, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



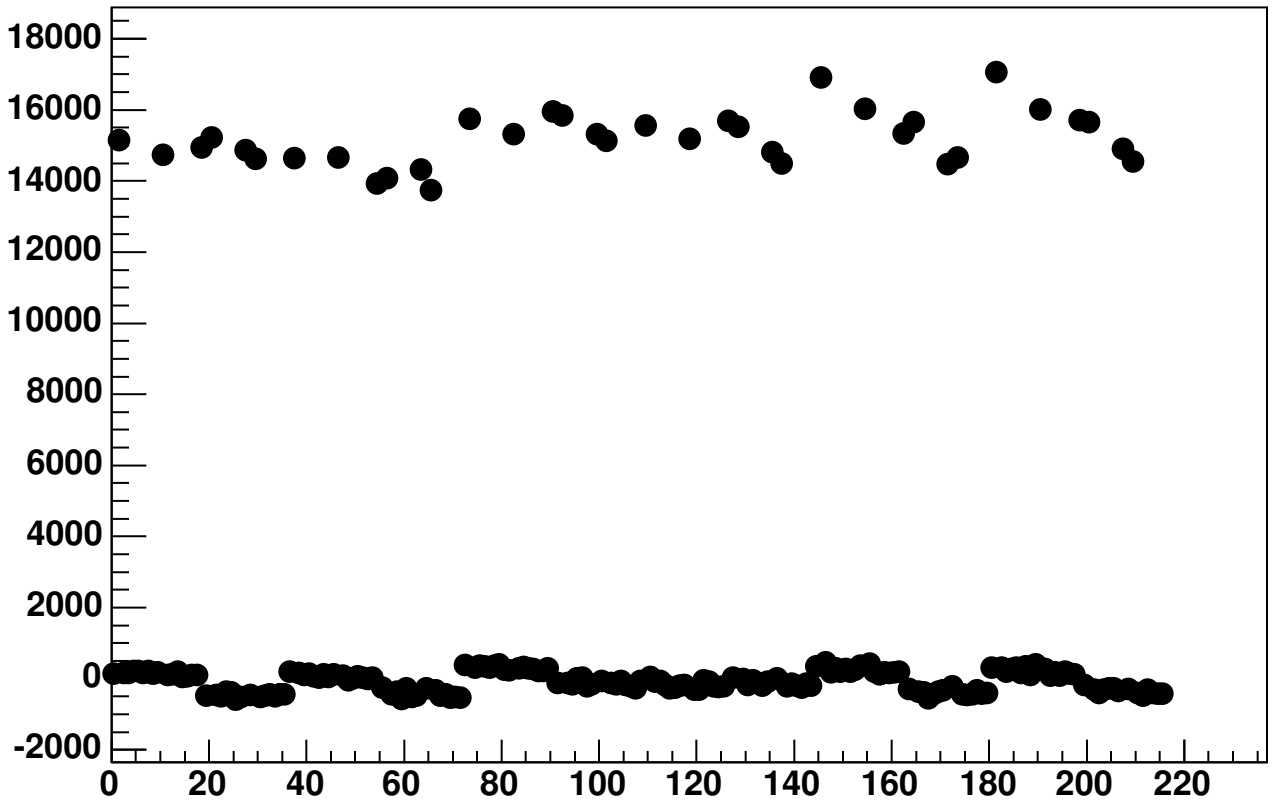
Enable 3, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



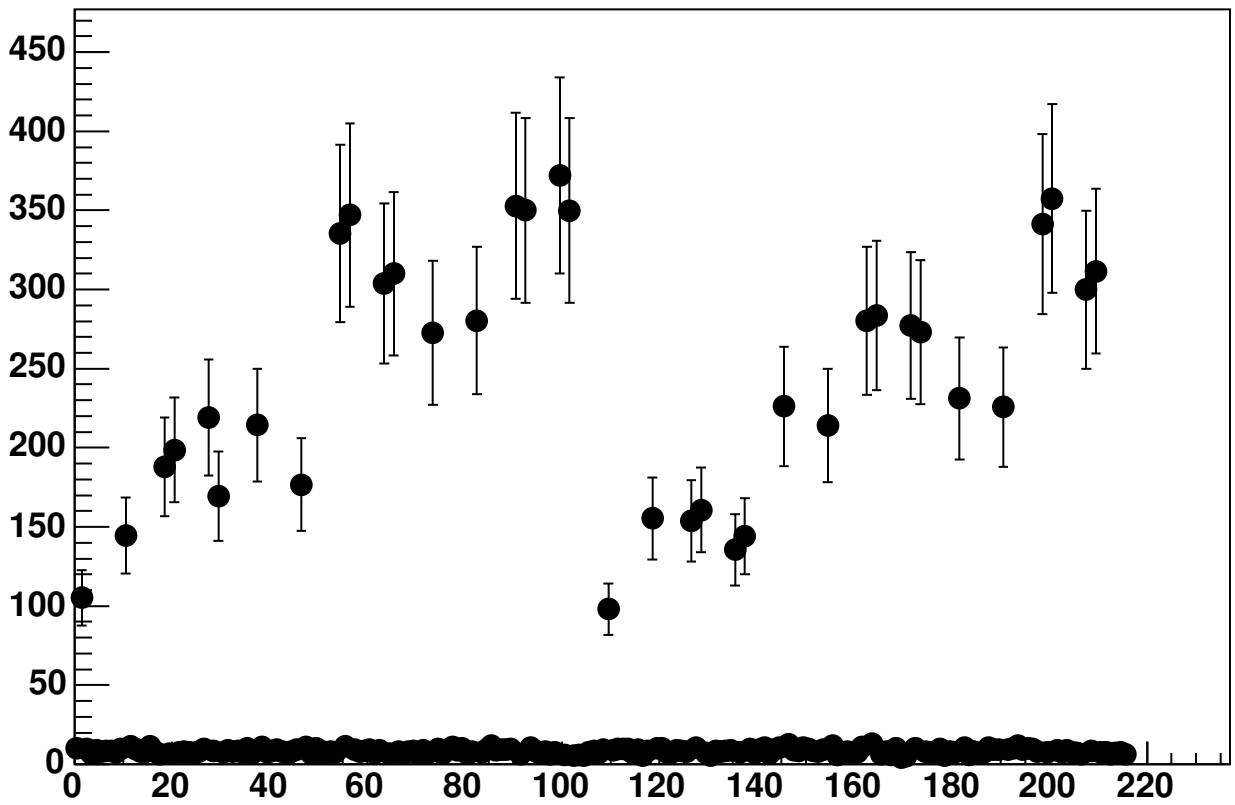
Enable 3, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



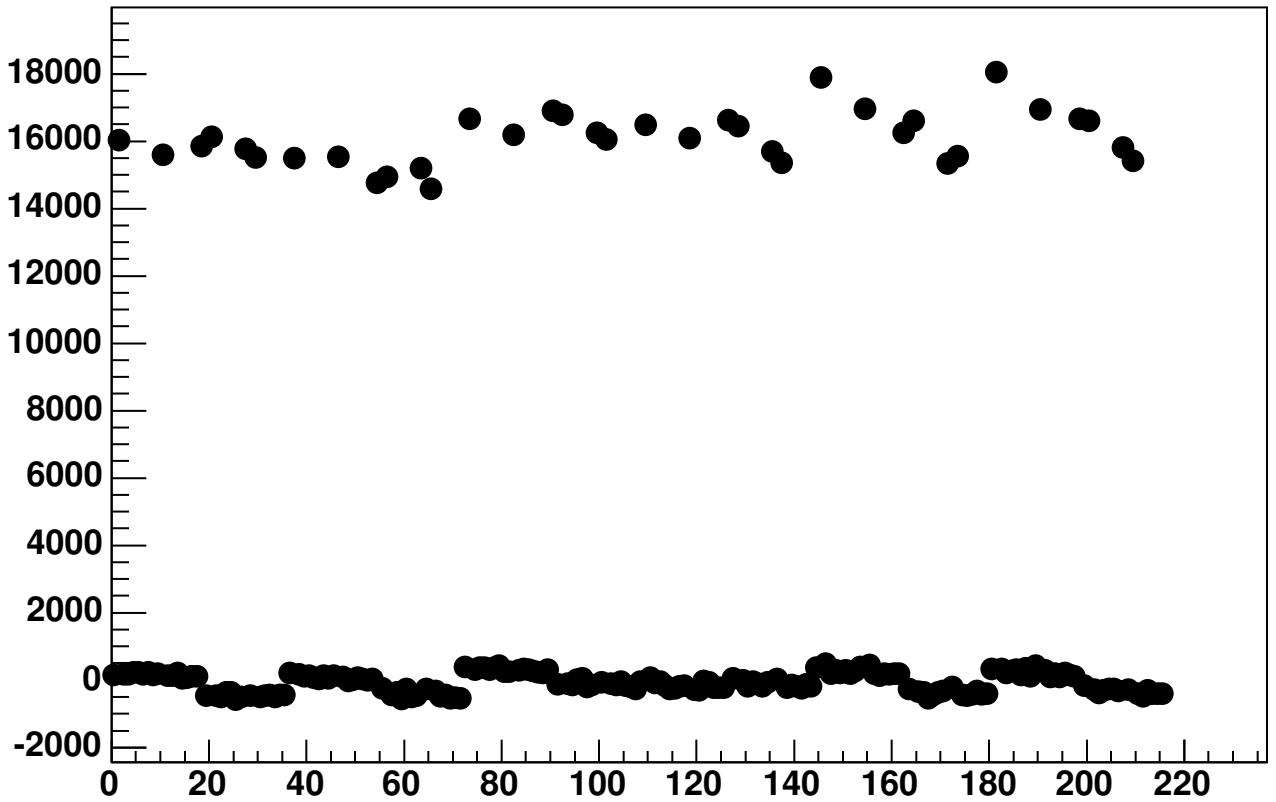
Enable 3, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



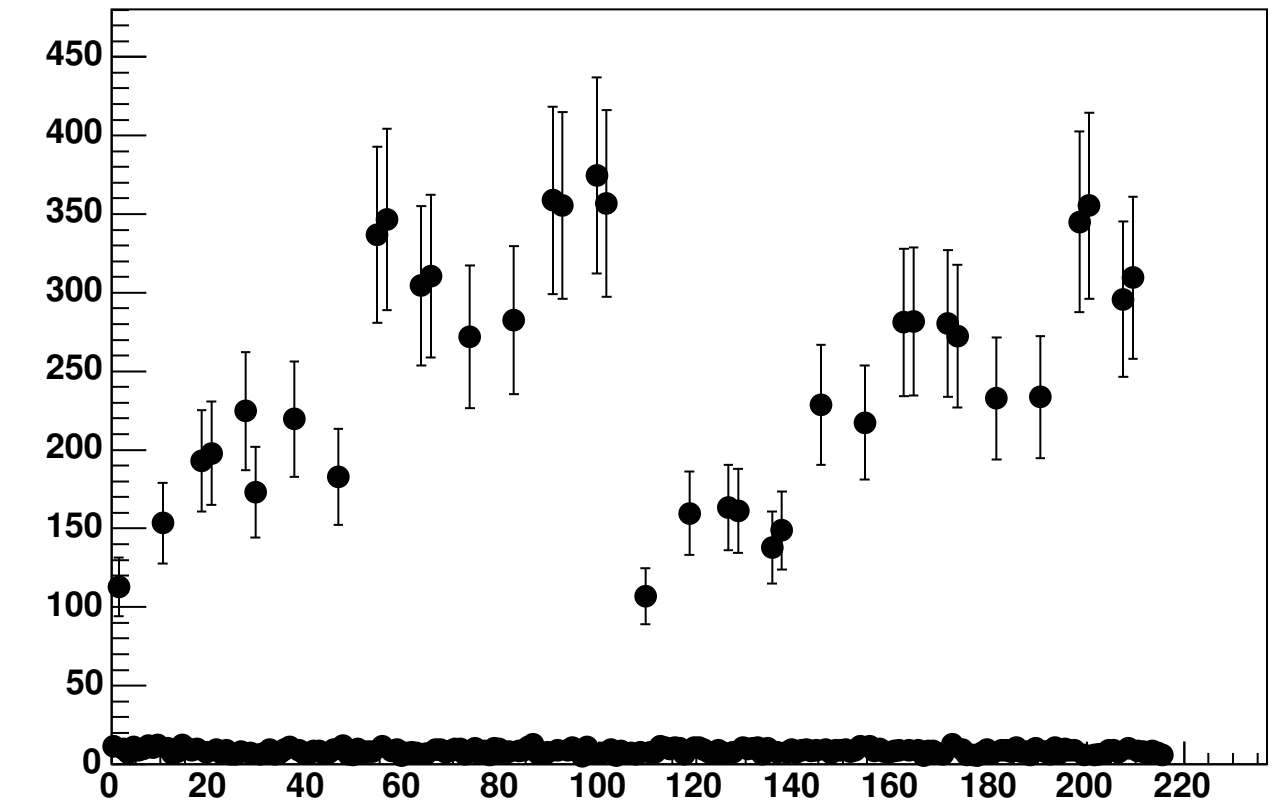
Enable 3, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



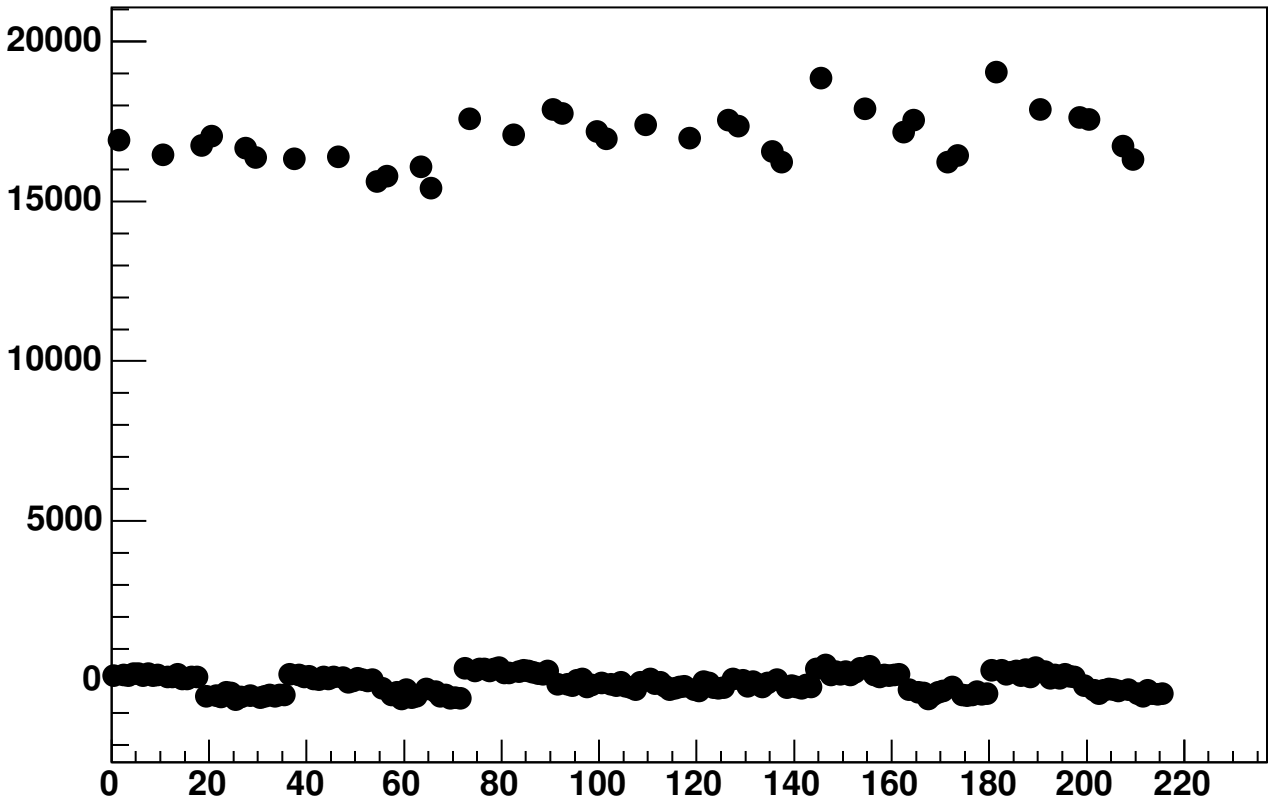
Enable 3, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



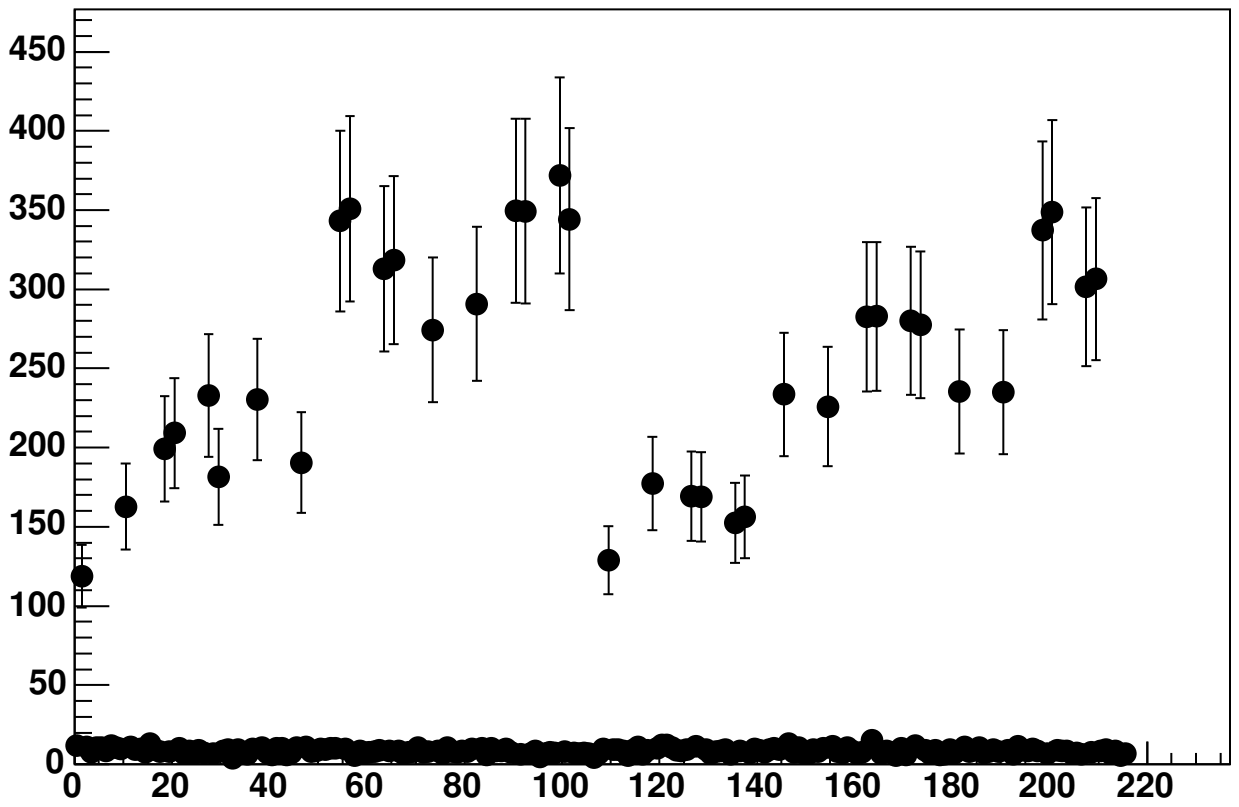
Enable 3, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



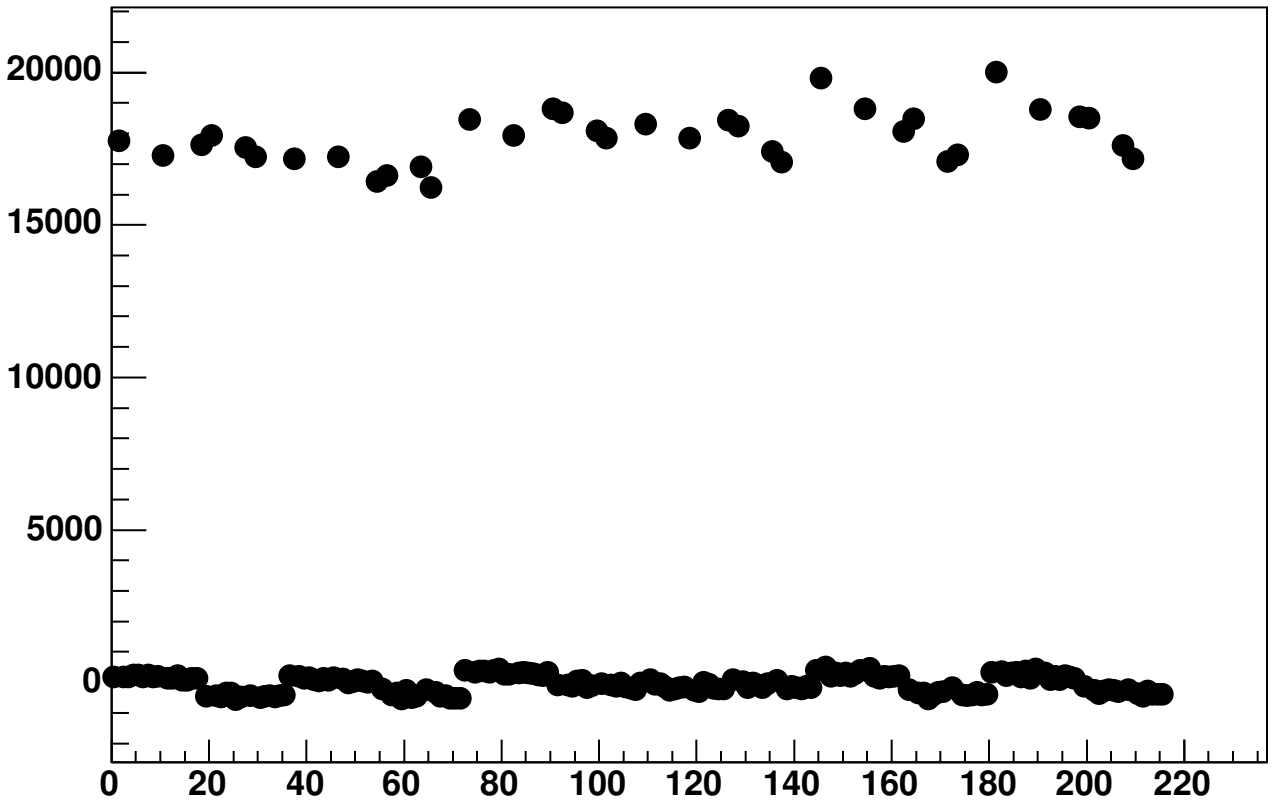
Enable 3, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



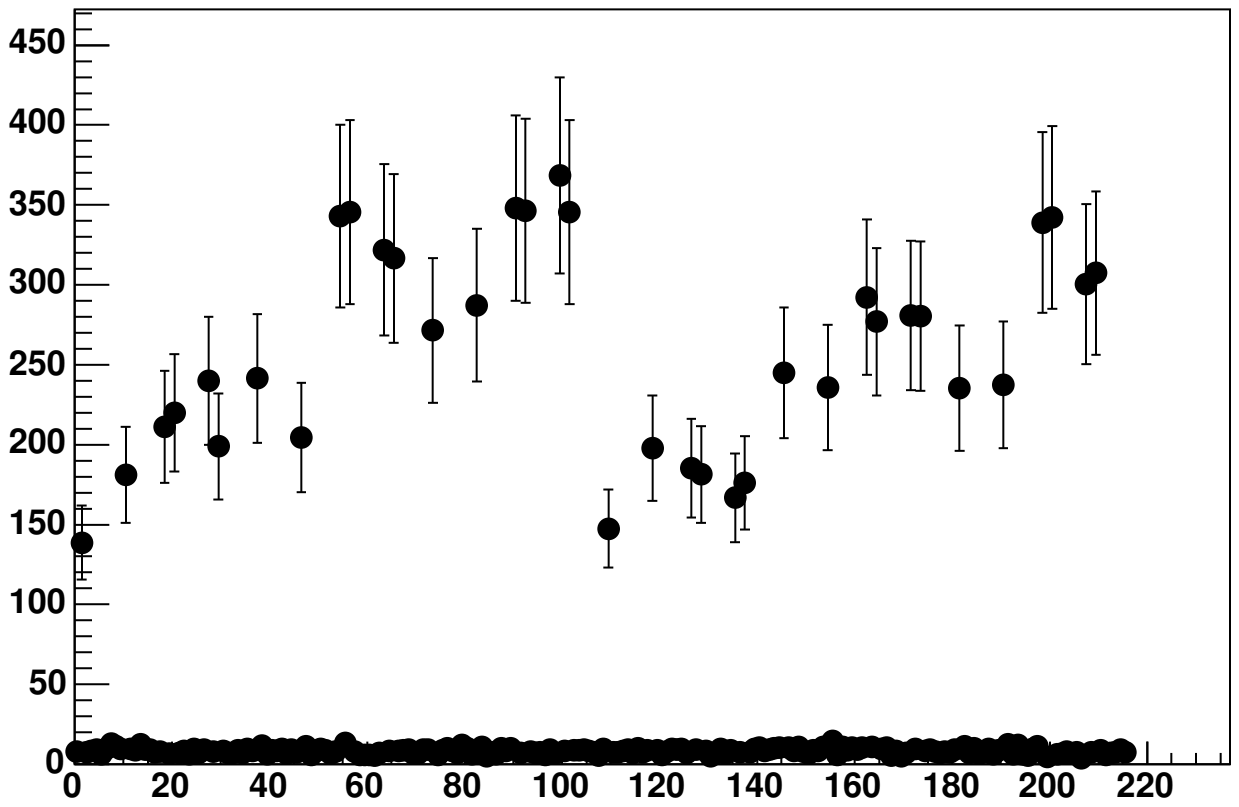
Enable 3, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



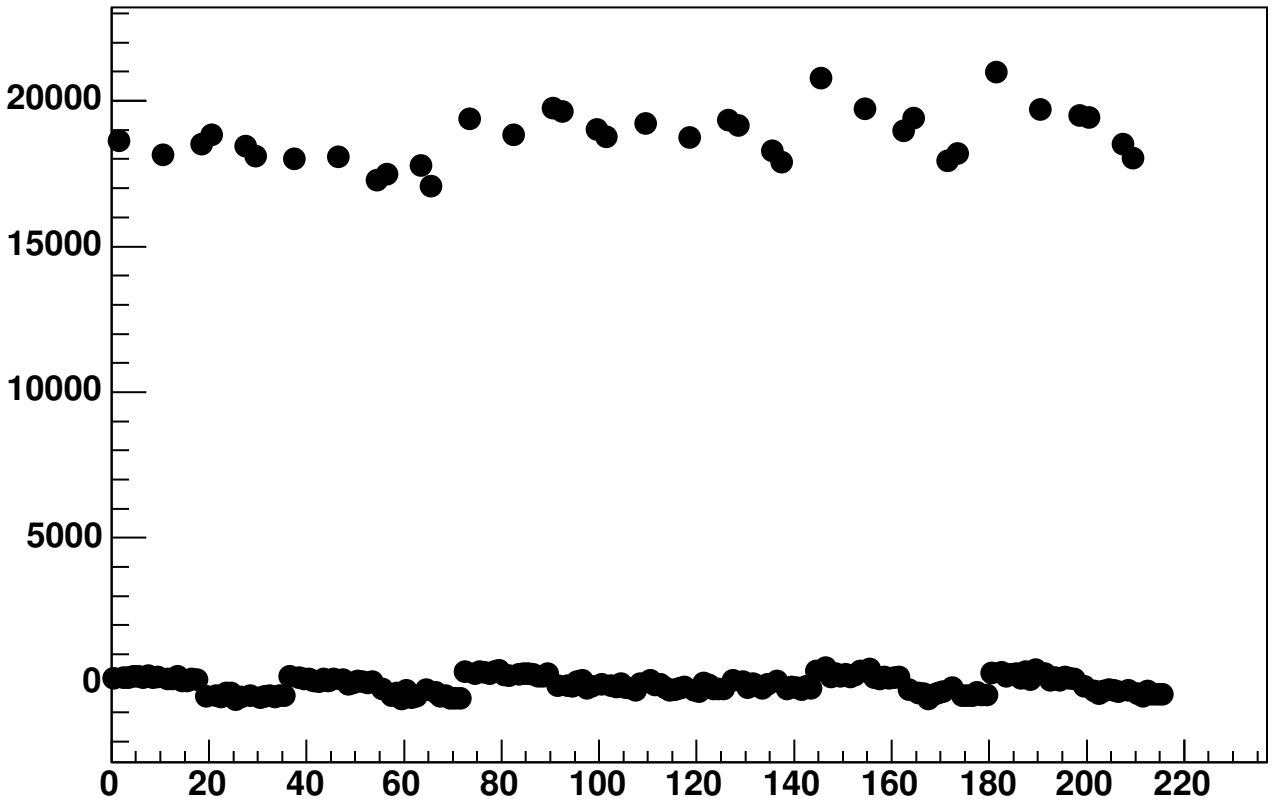
Enable 3, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



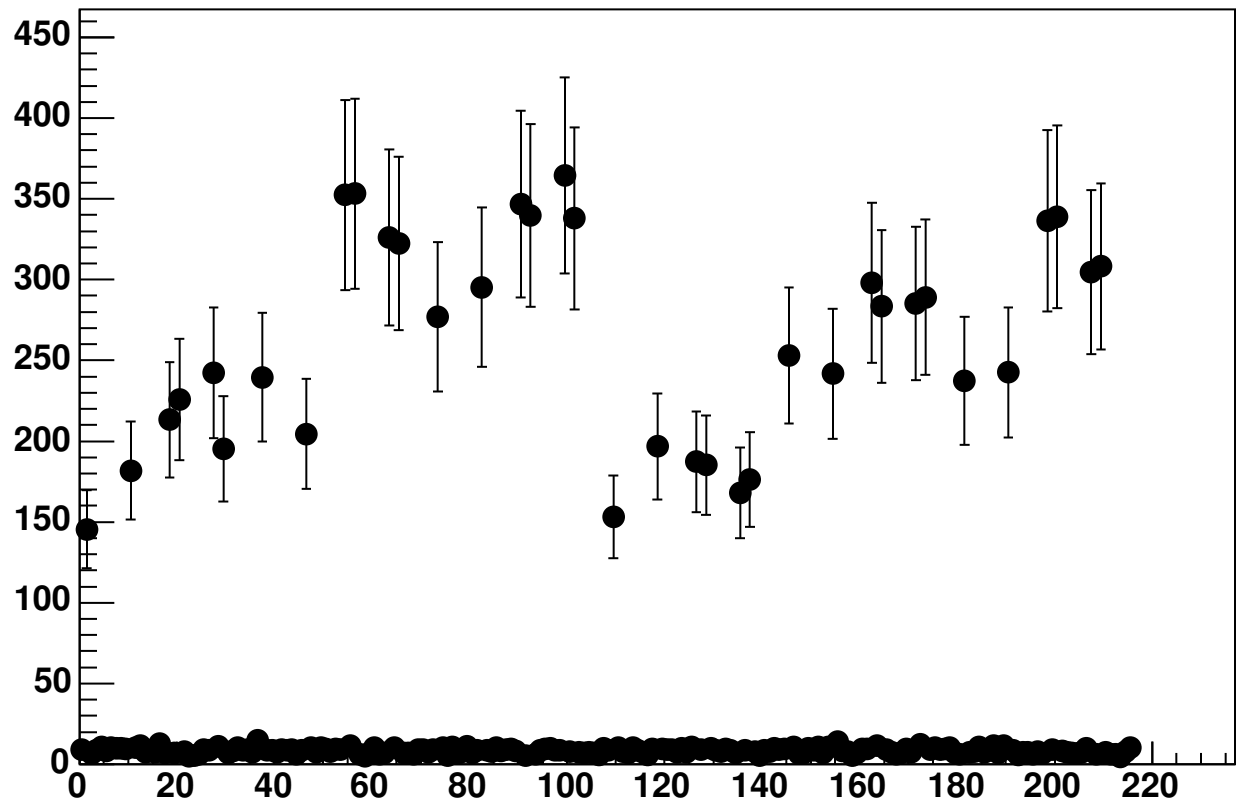
Enable 3, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



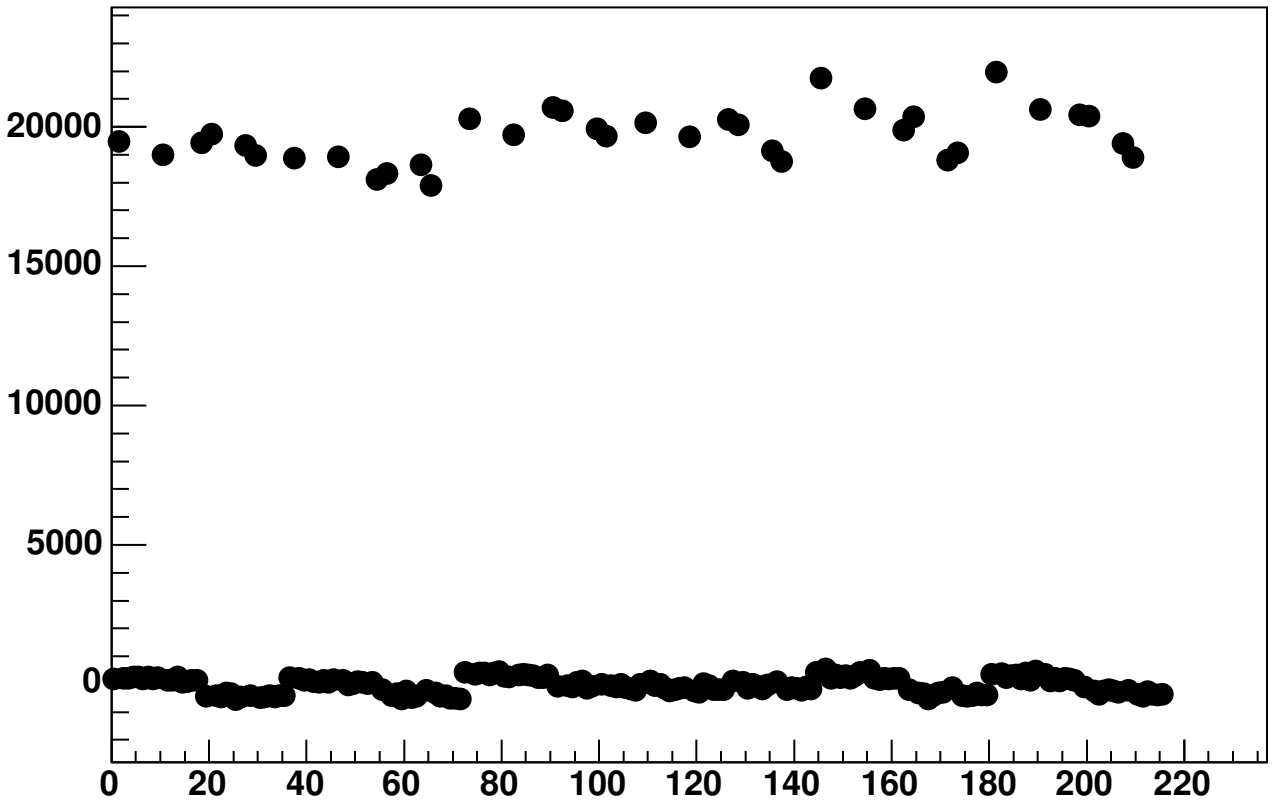
Enable 3, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



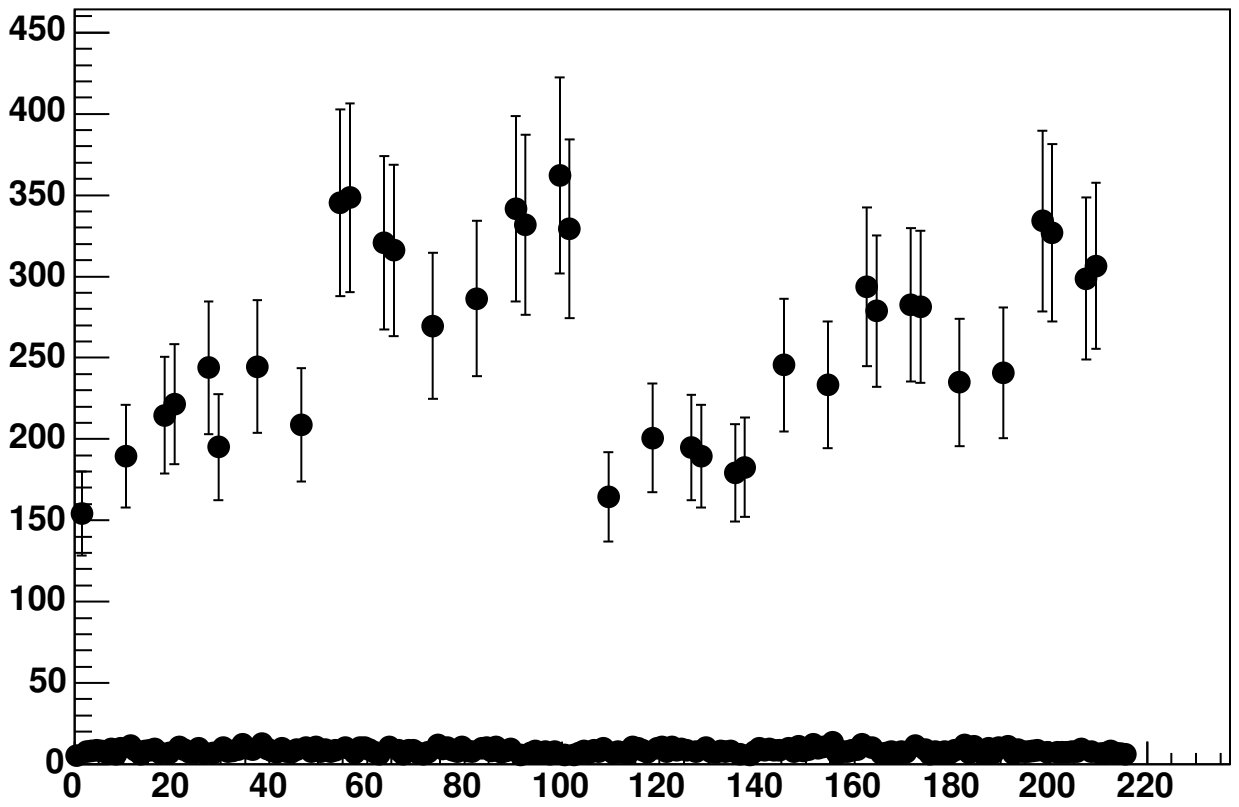
Enable 3, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



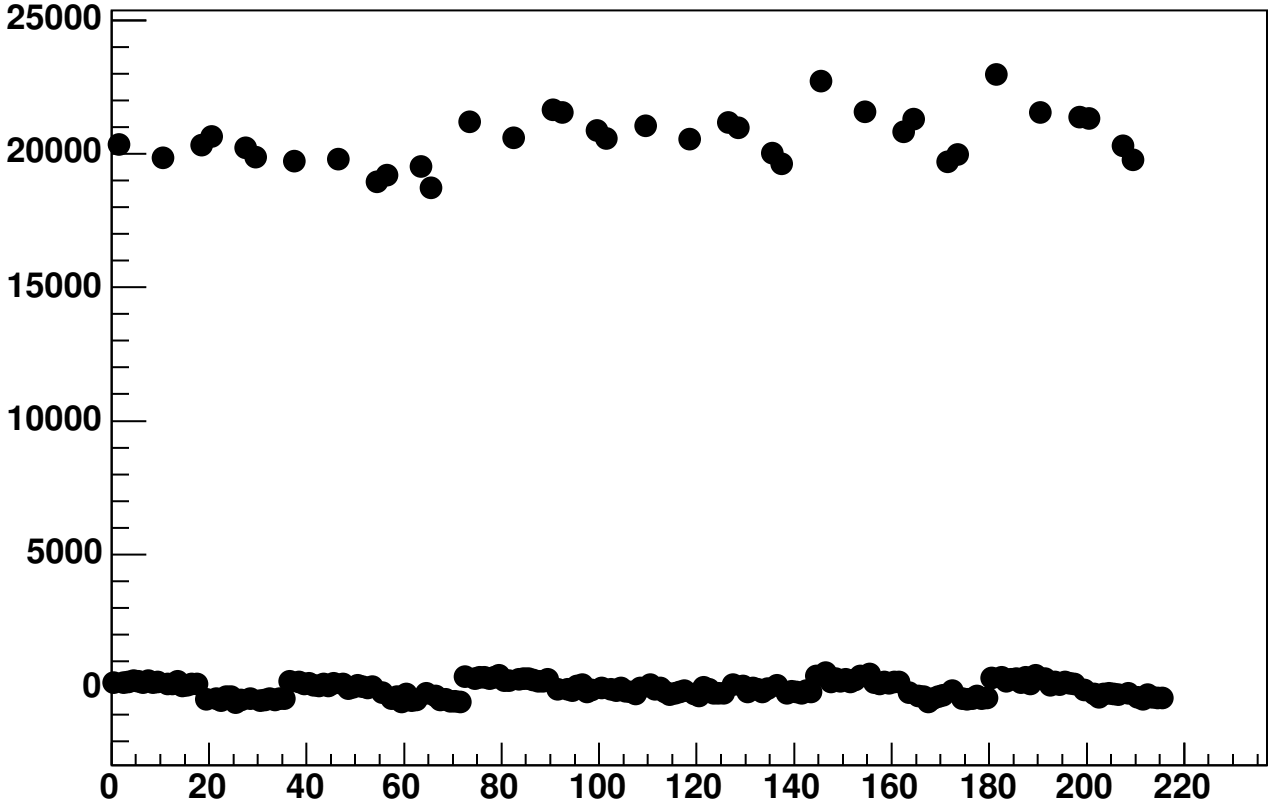
Enable 3, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



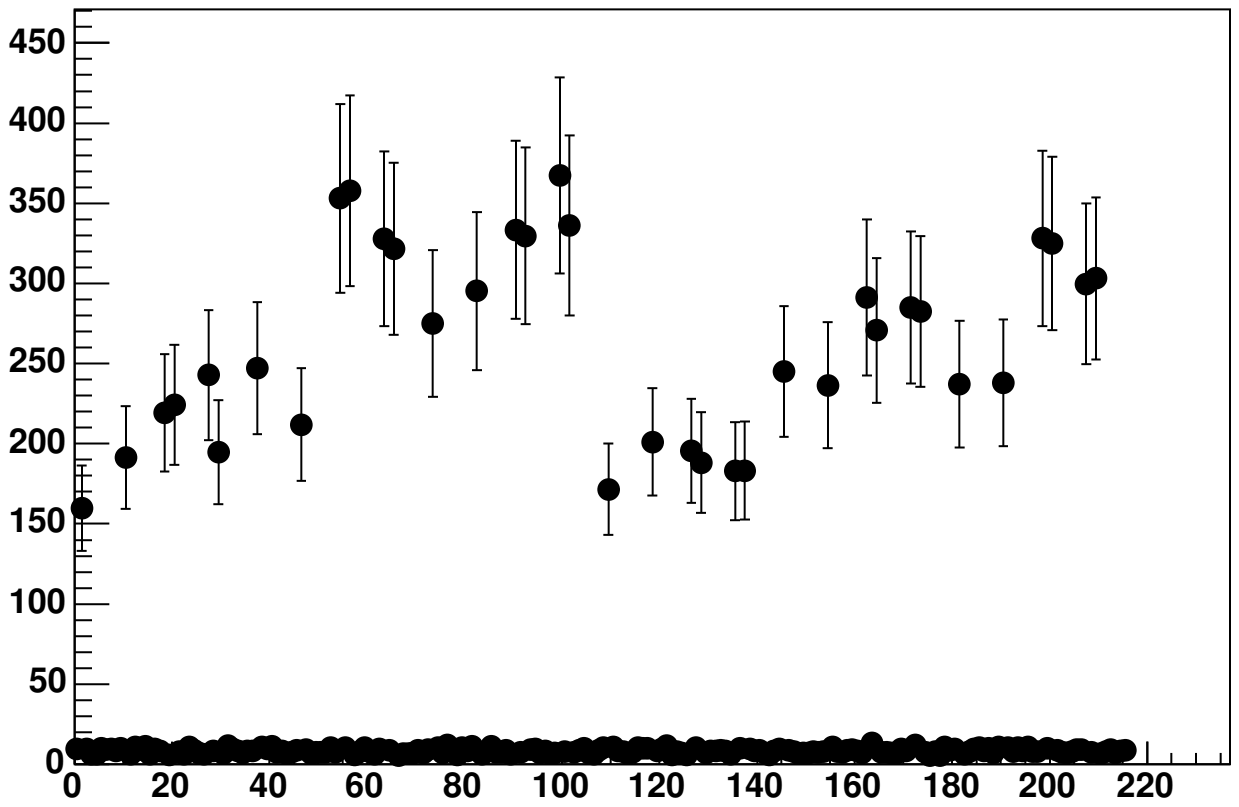
Enable 3, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



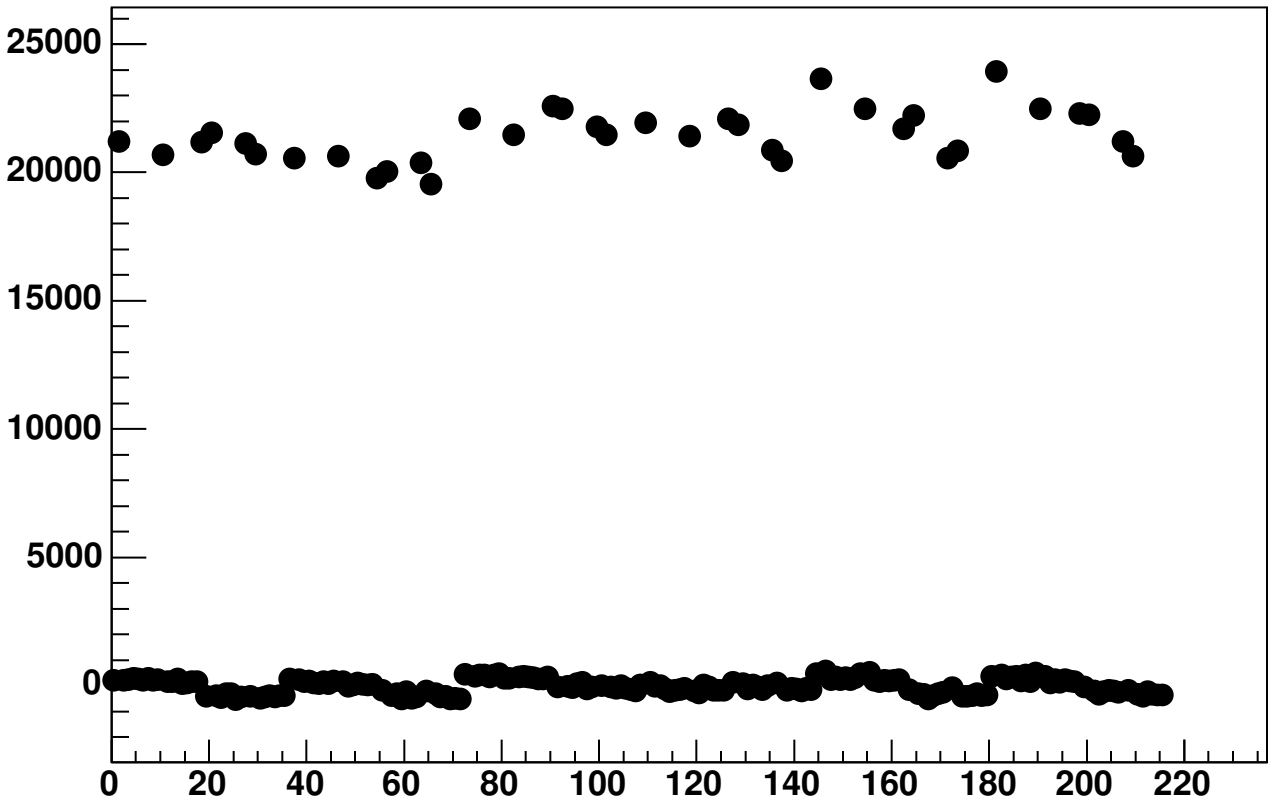
Enable 3, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



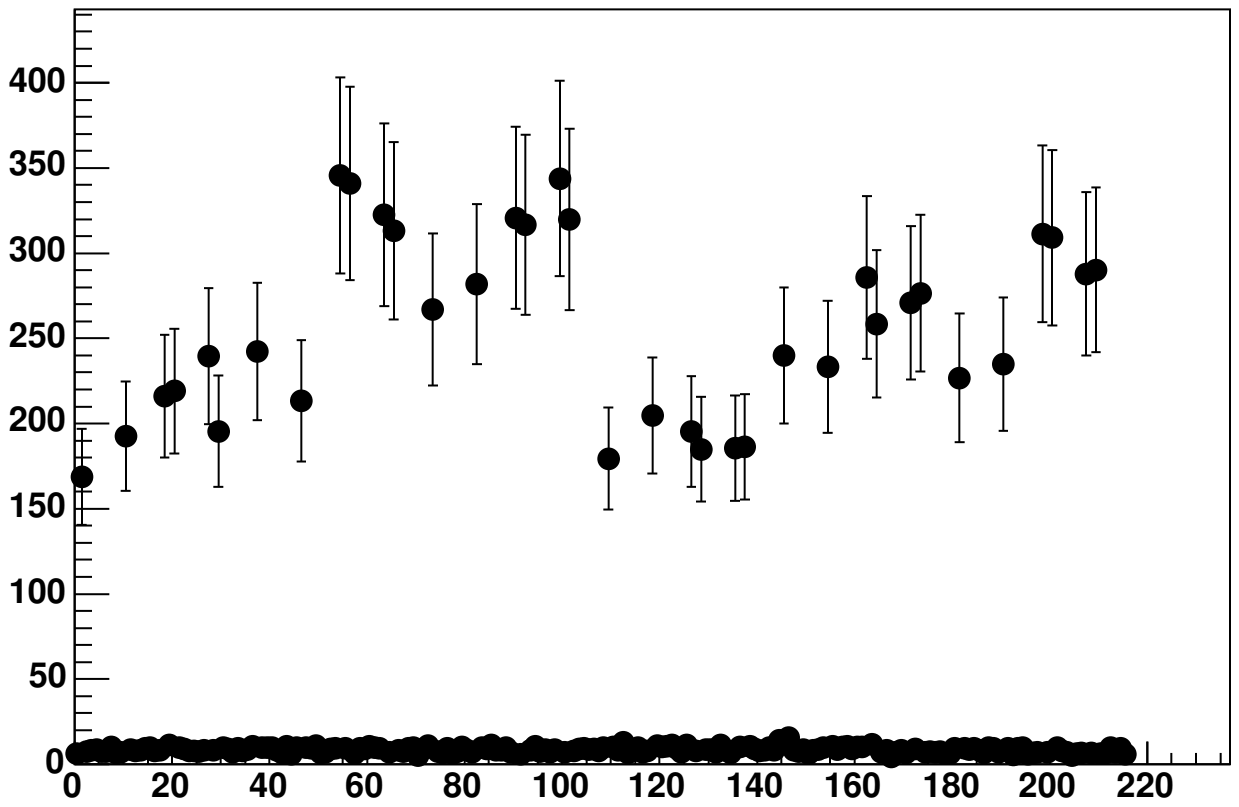
Enable 3, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



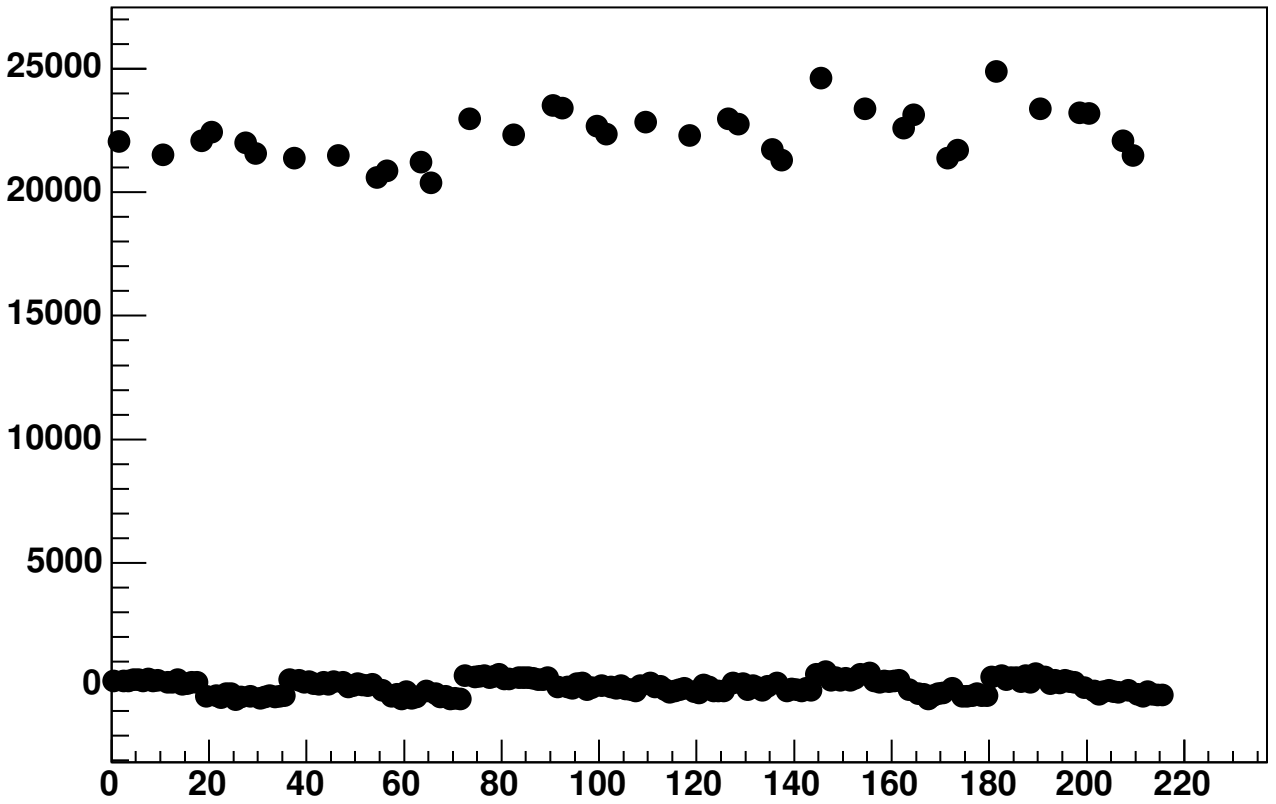
Enable 3, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



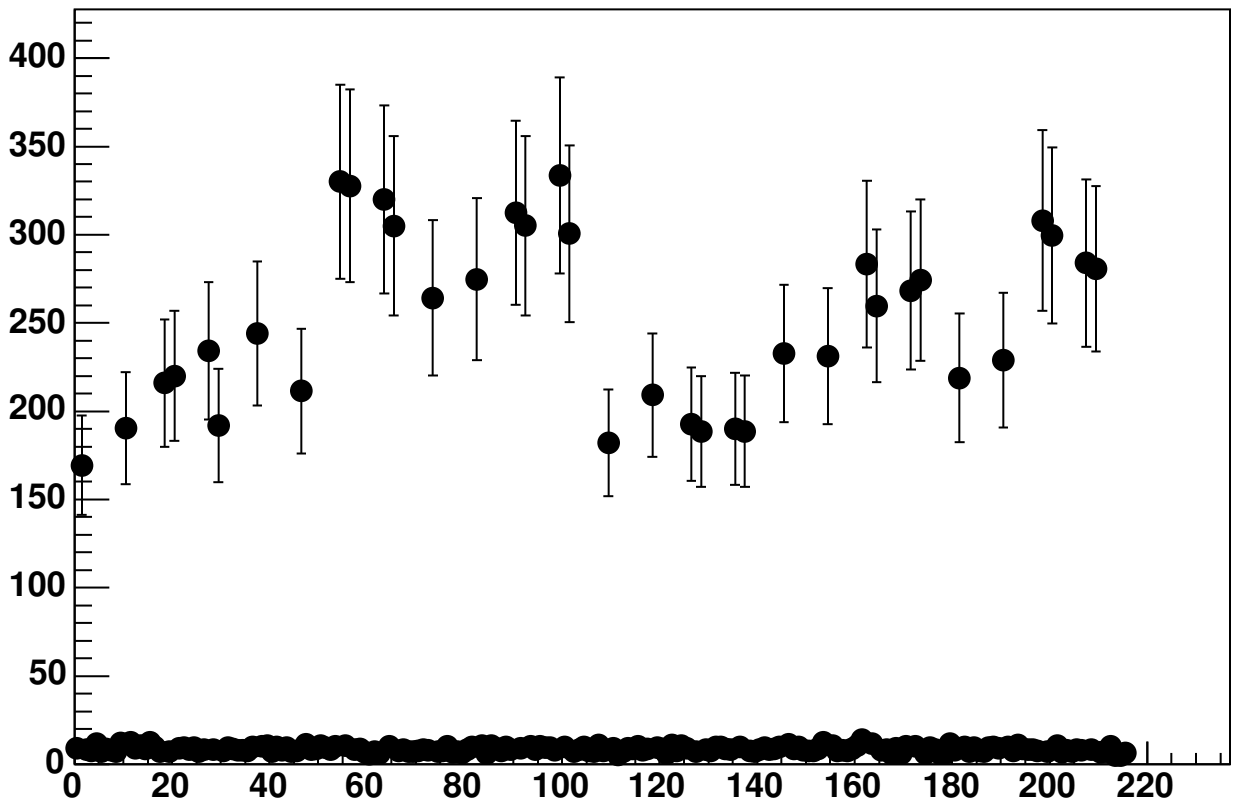
Enable 3, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



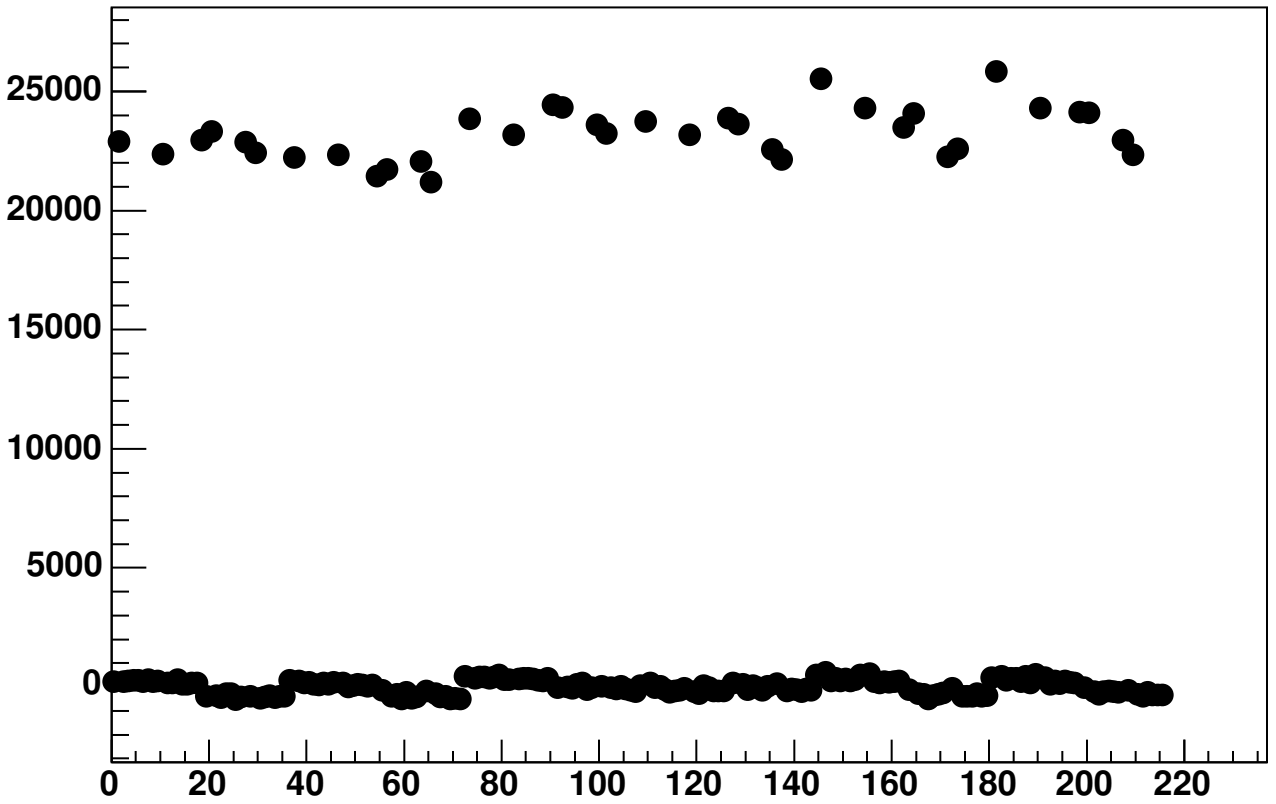
Enable 3, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



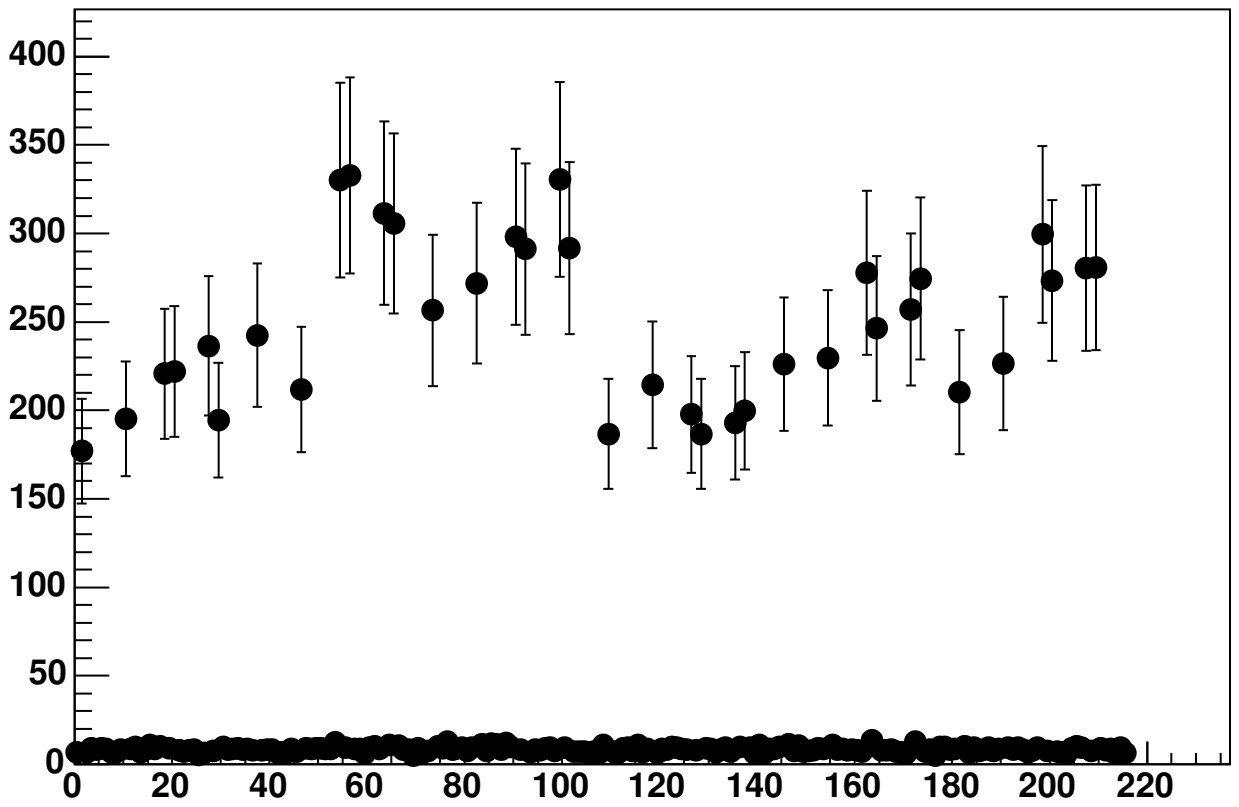
Enable 3, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



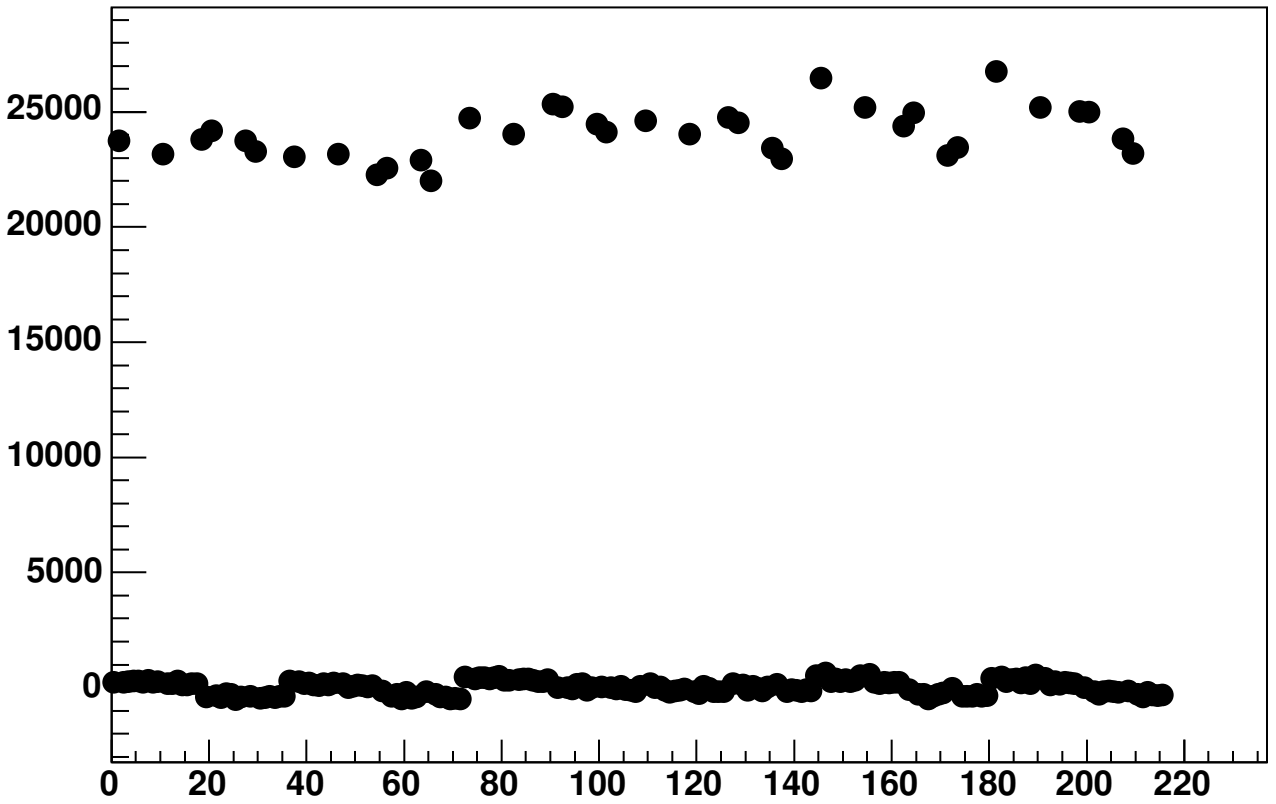
Enable 3, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



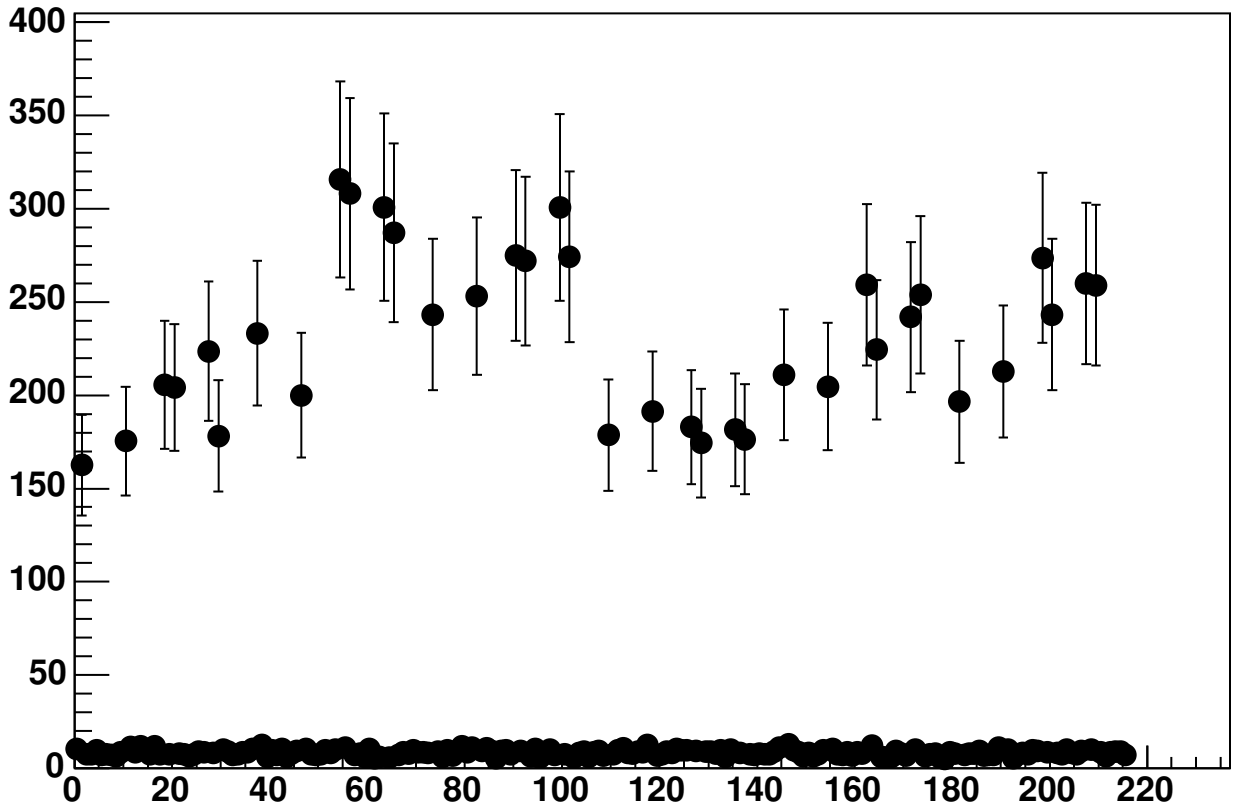
Enable 3, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



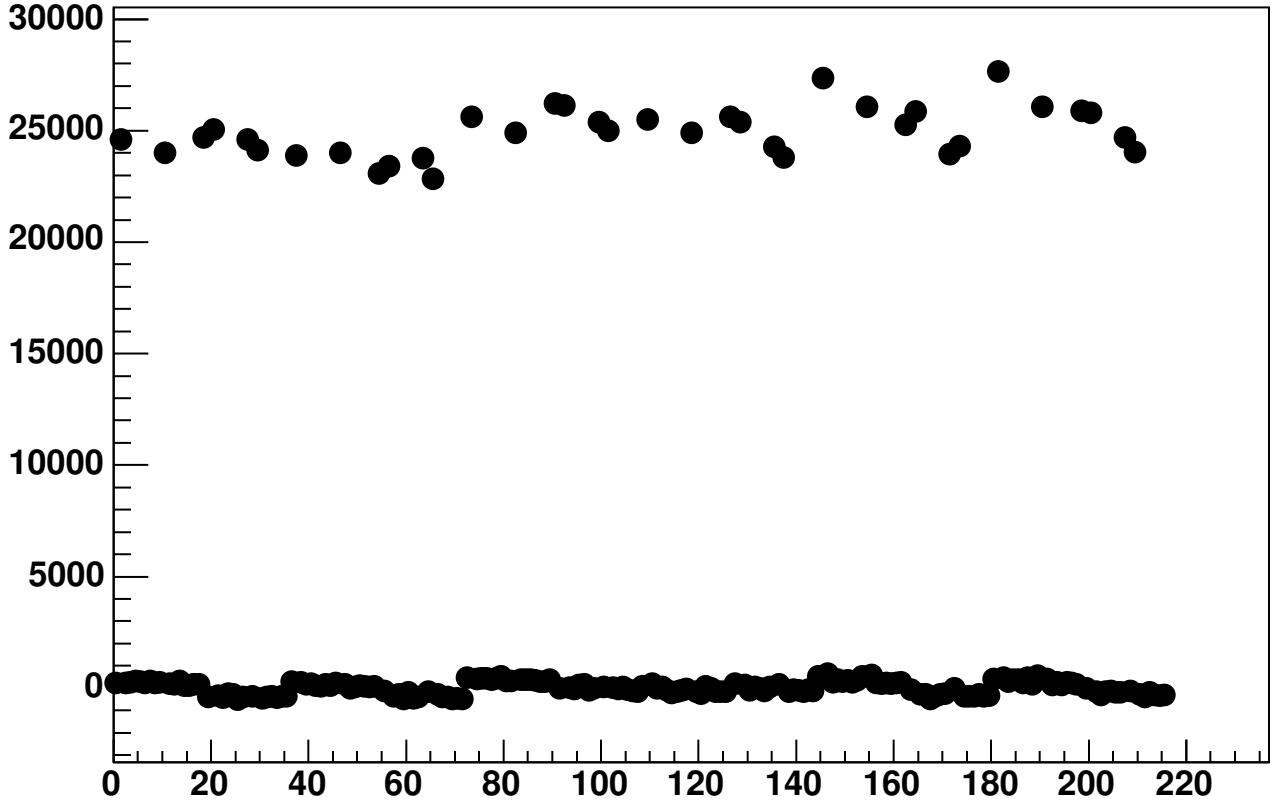
Enable 3, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



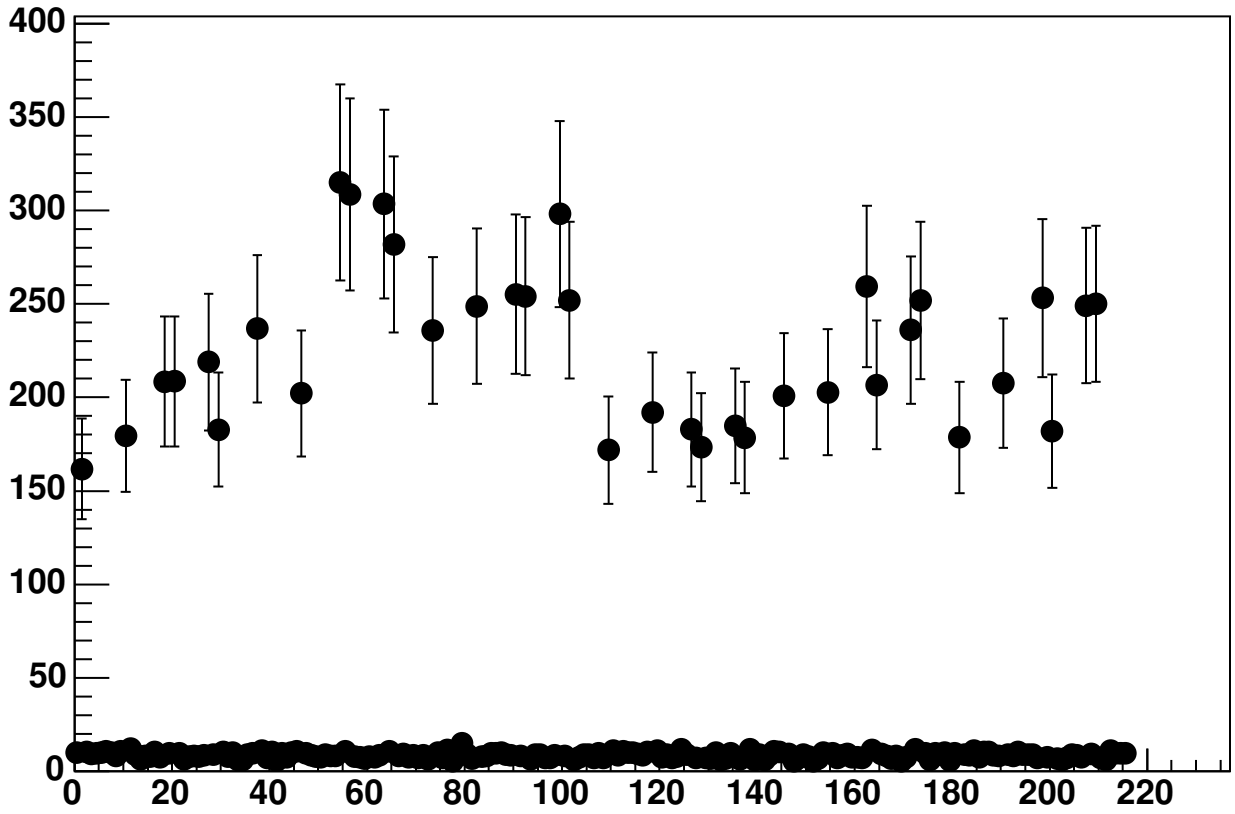
Enable 3, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



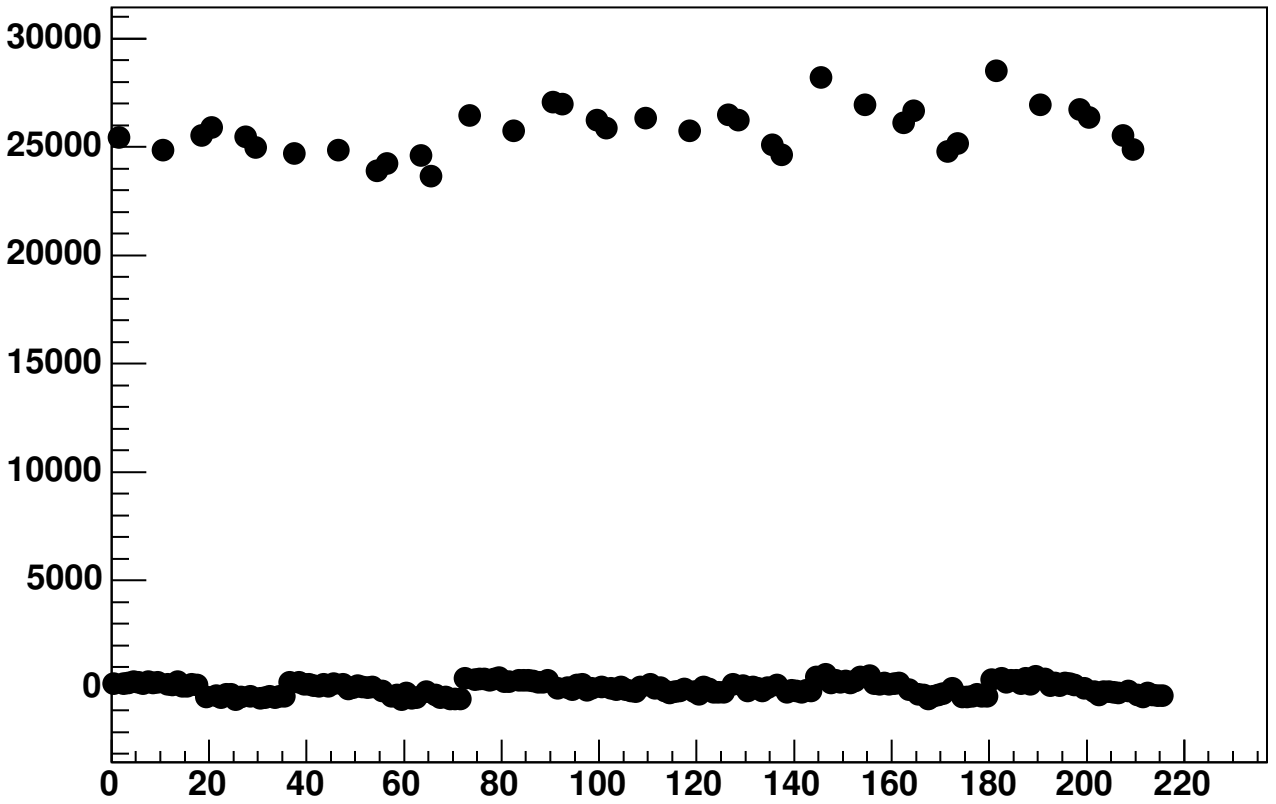
Enable 3, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



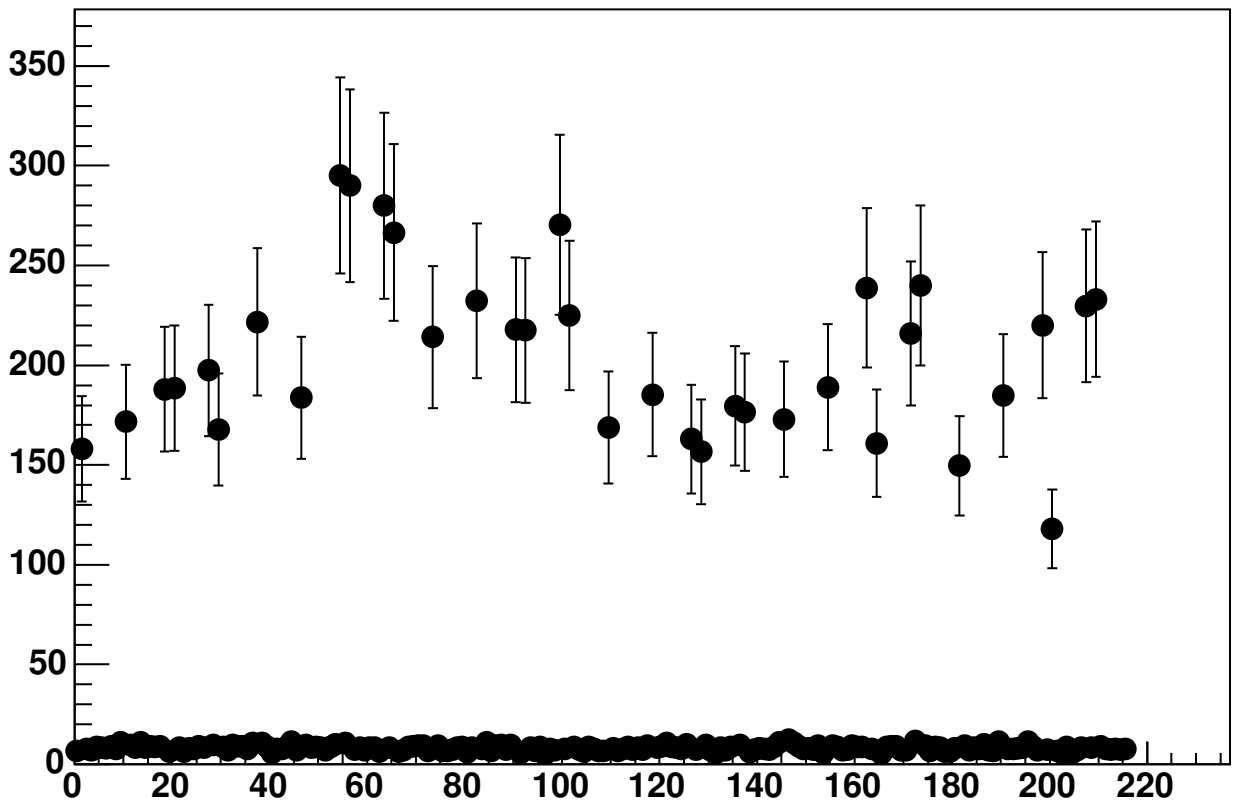
Enable 3, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



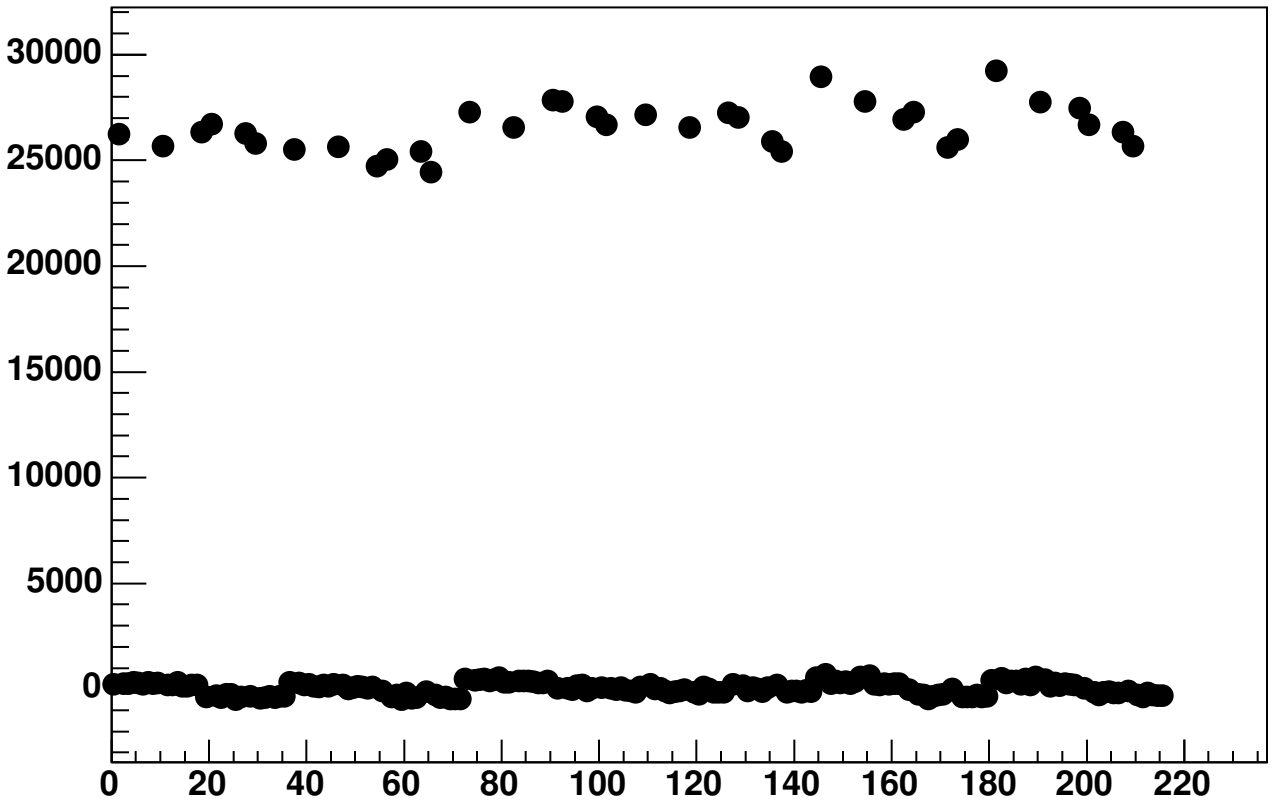
Enable 3, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



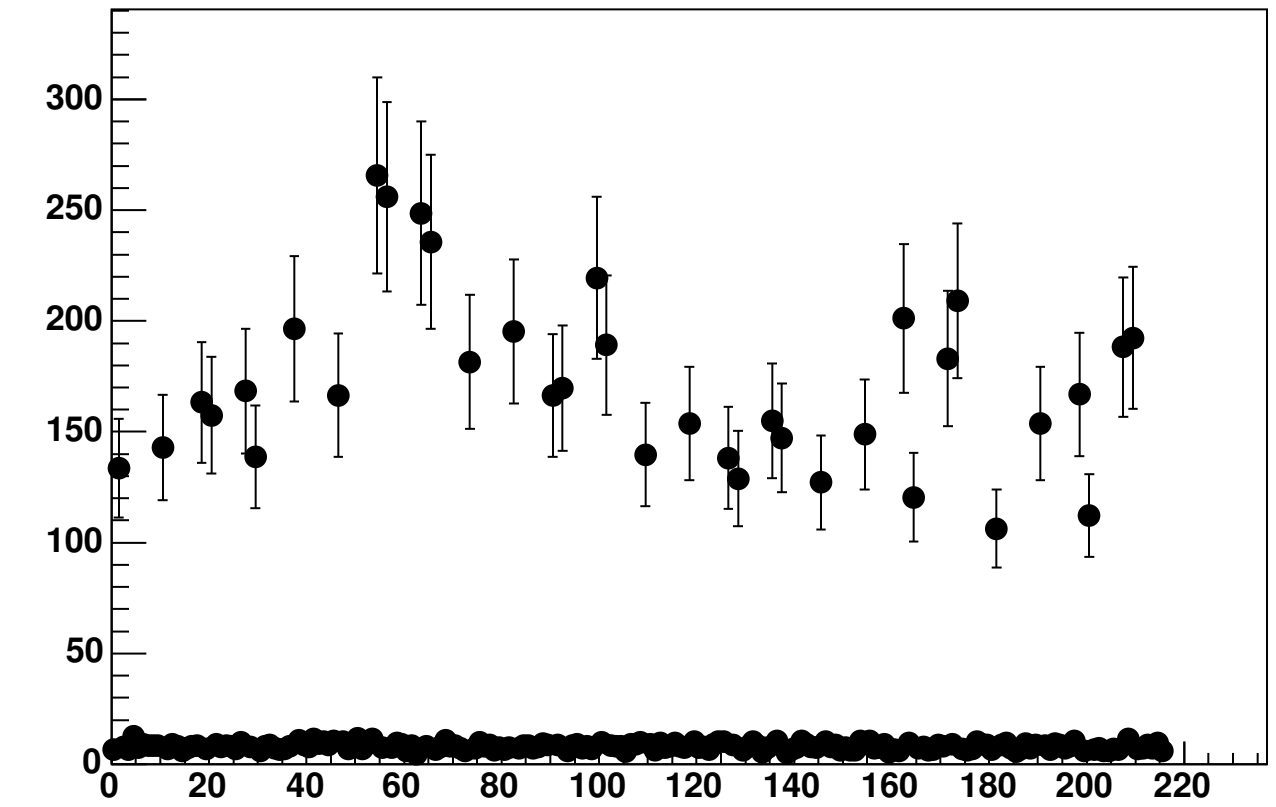
Enable 3, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



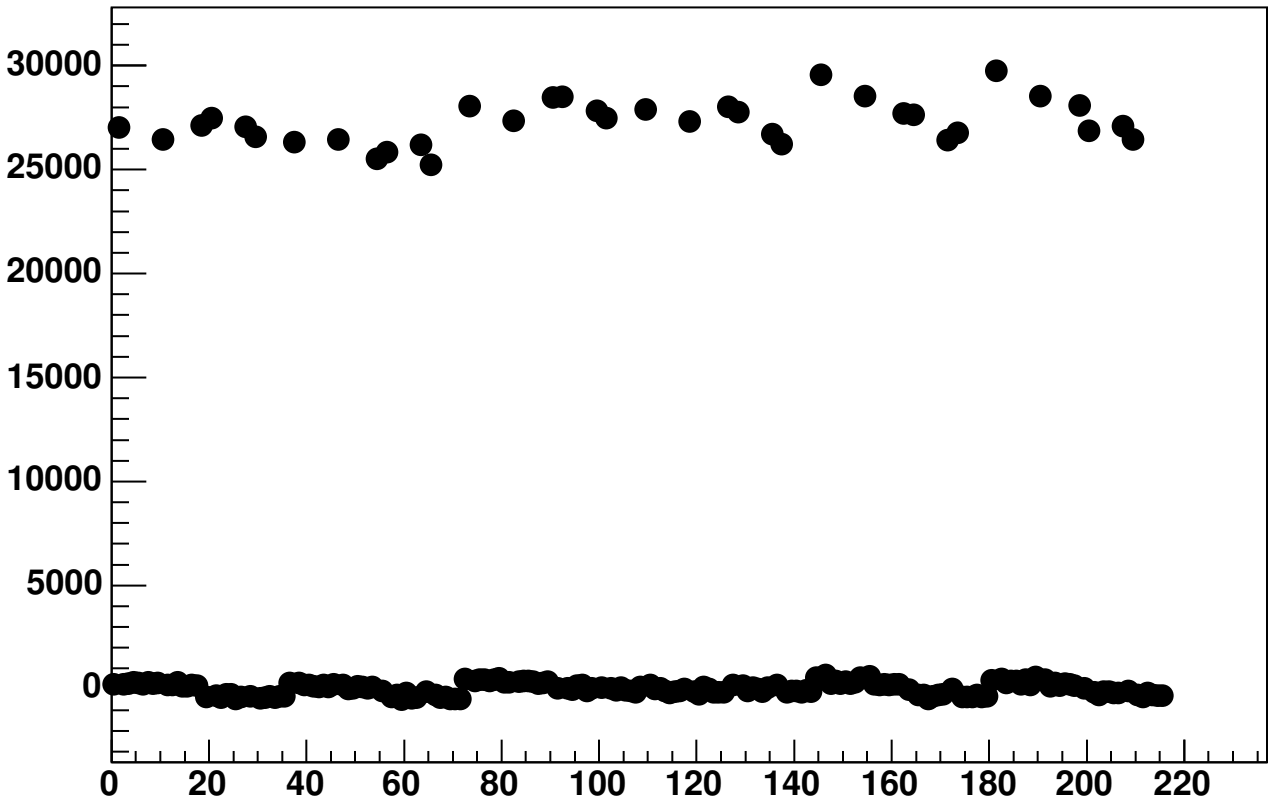
Enable 3, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



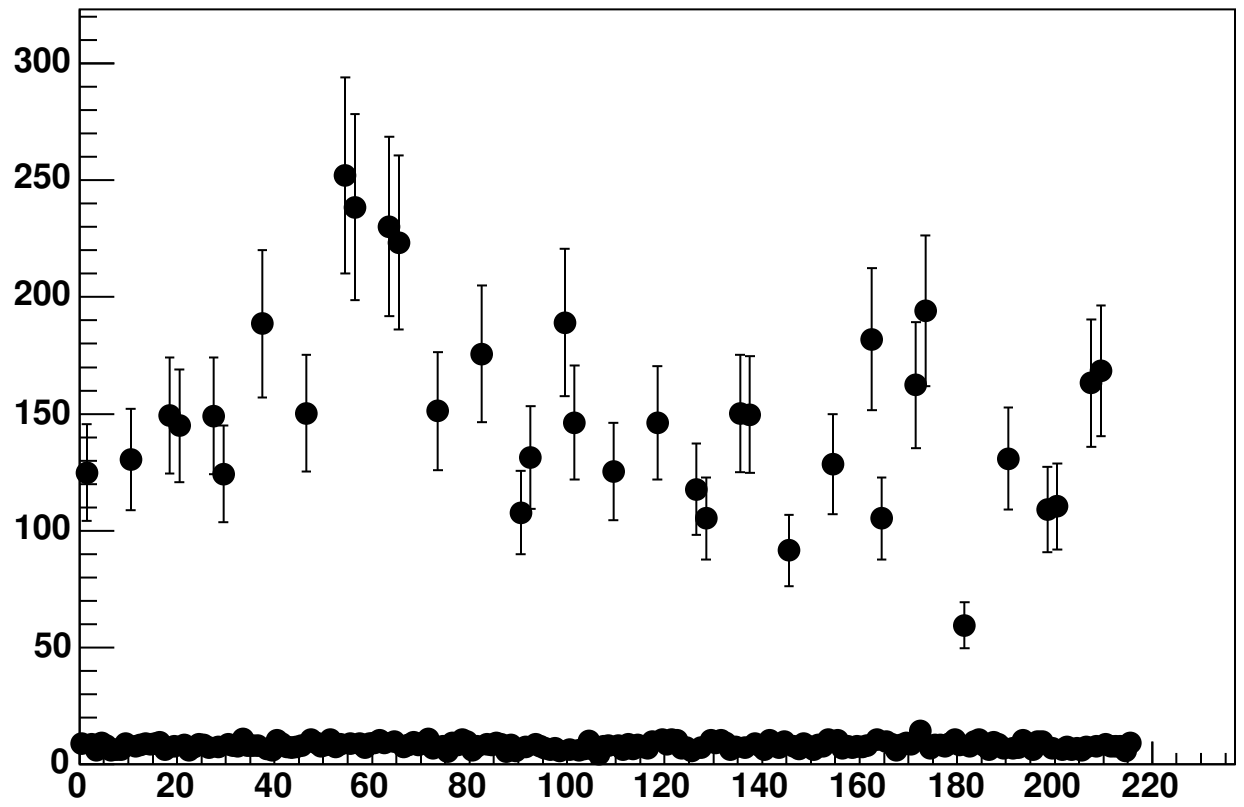
Enable 3, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



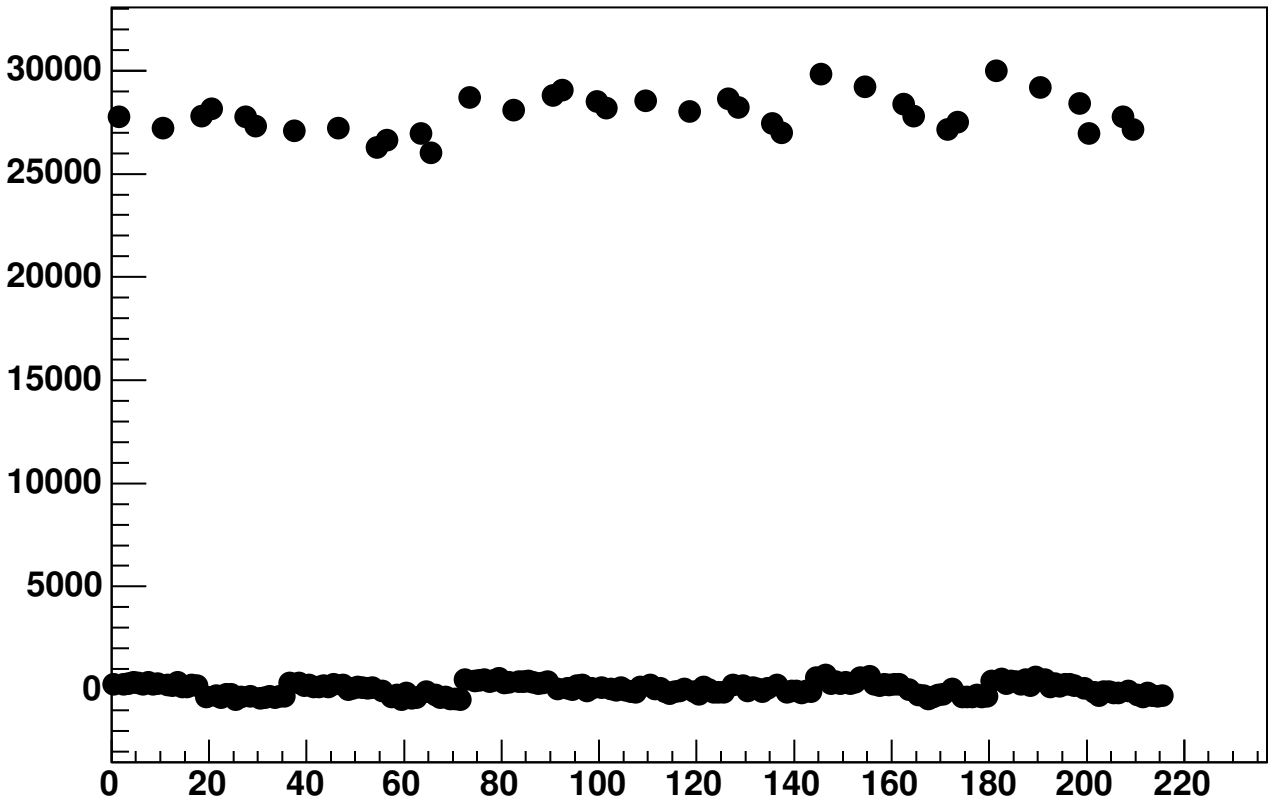
Enable 3, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



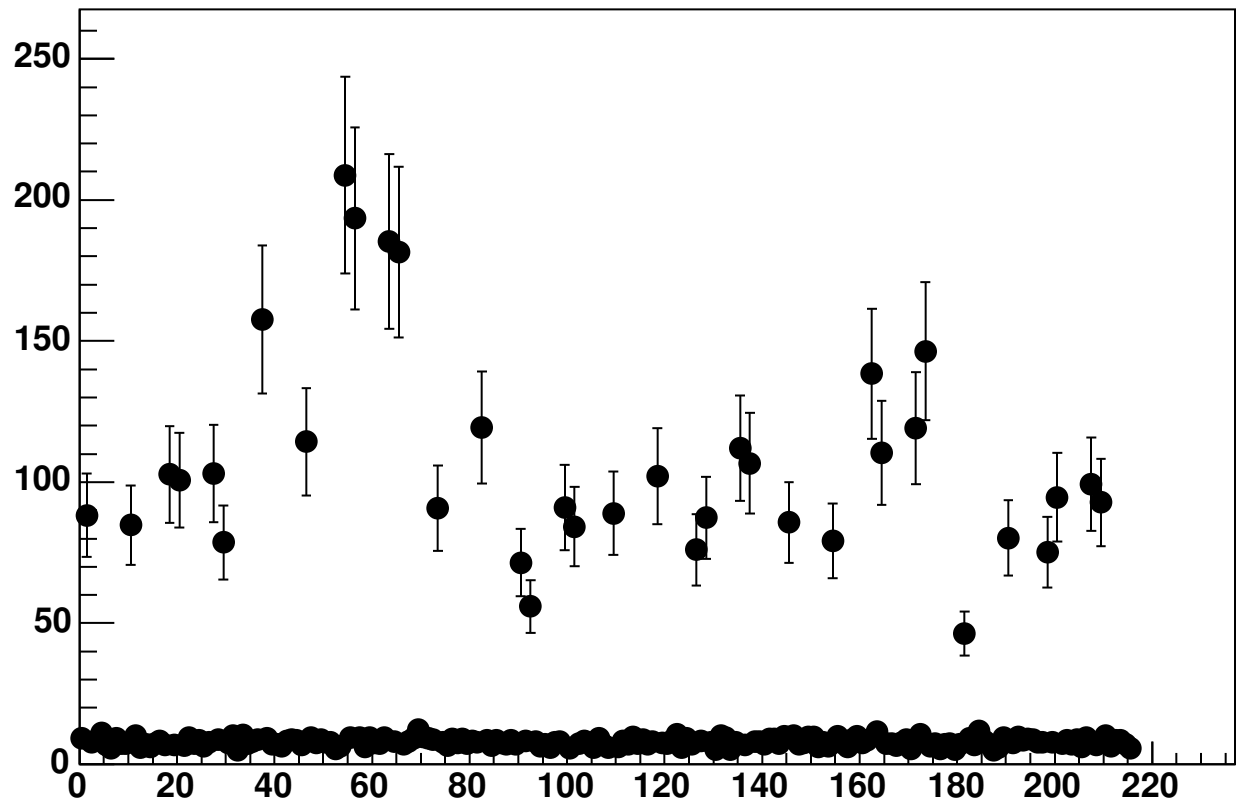
Enable 3, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



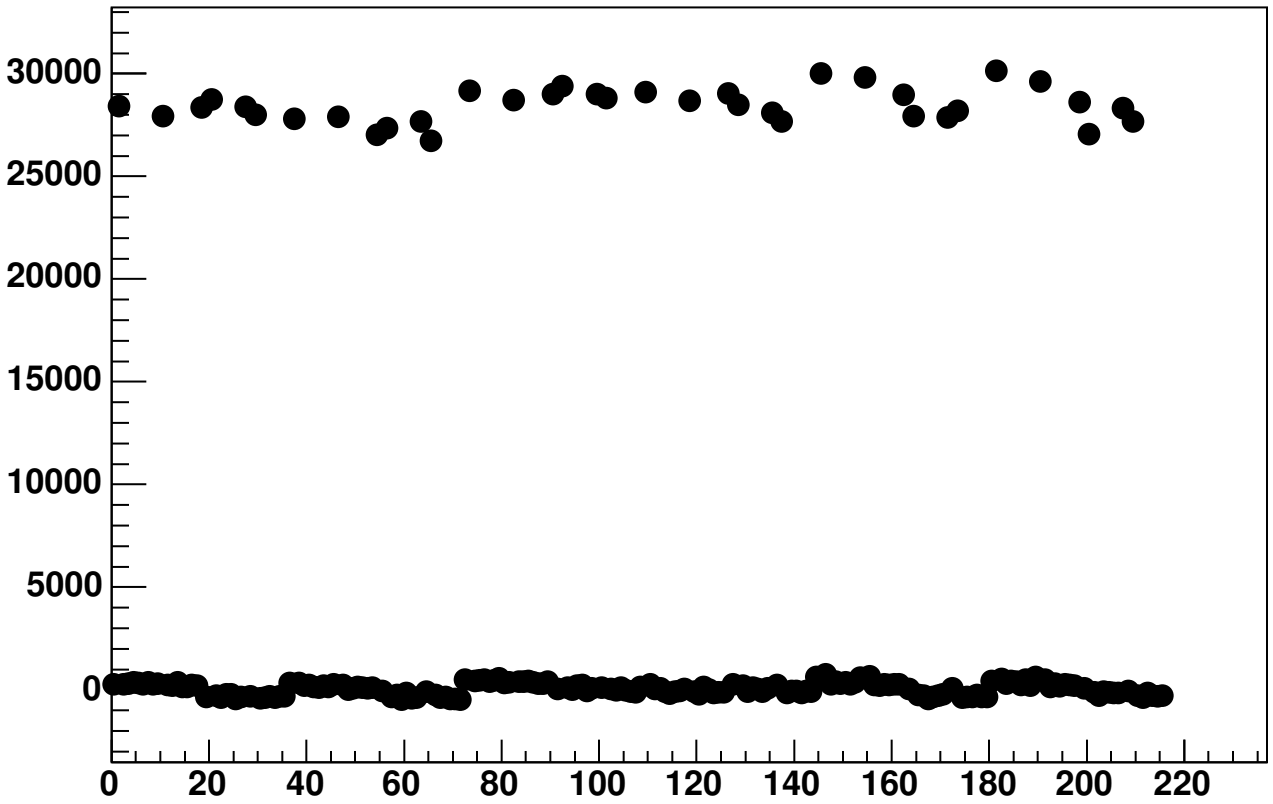
Enable 3, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



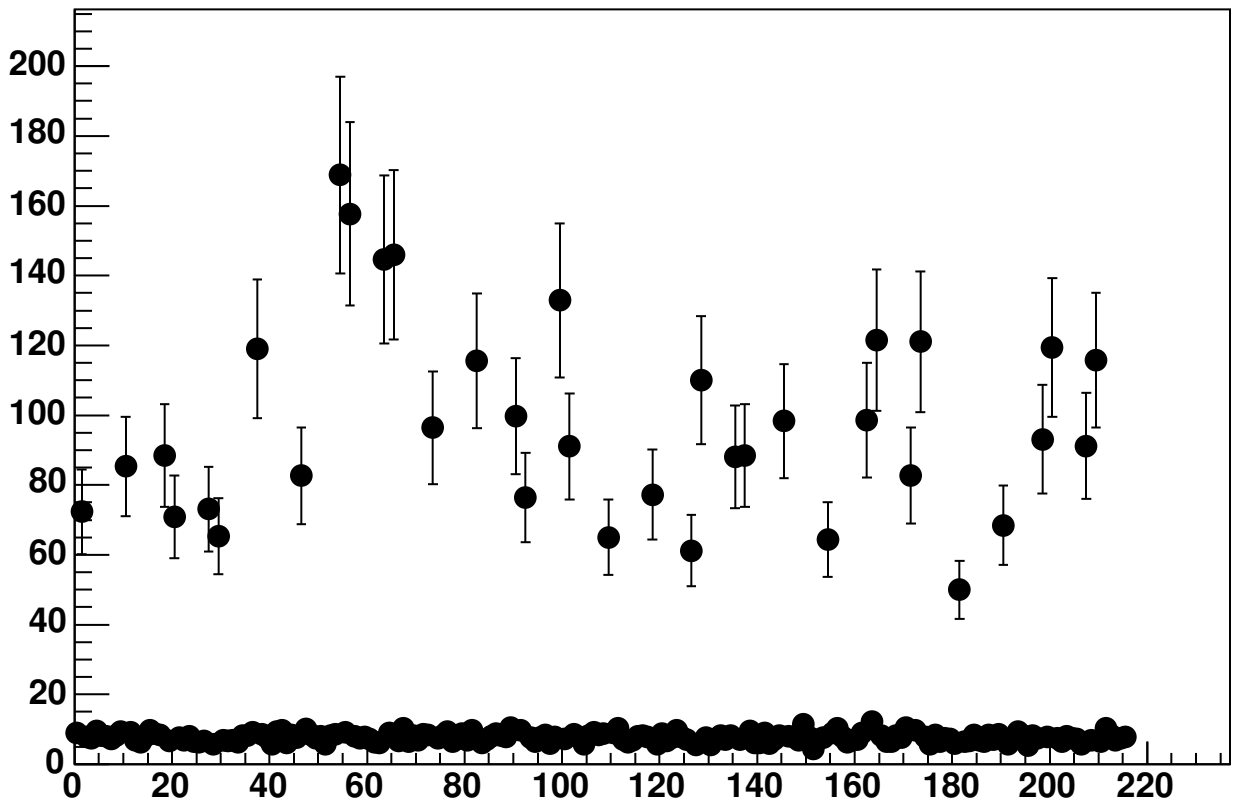
Enable 3, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



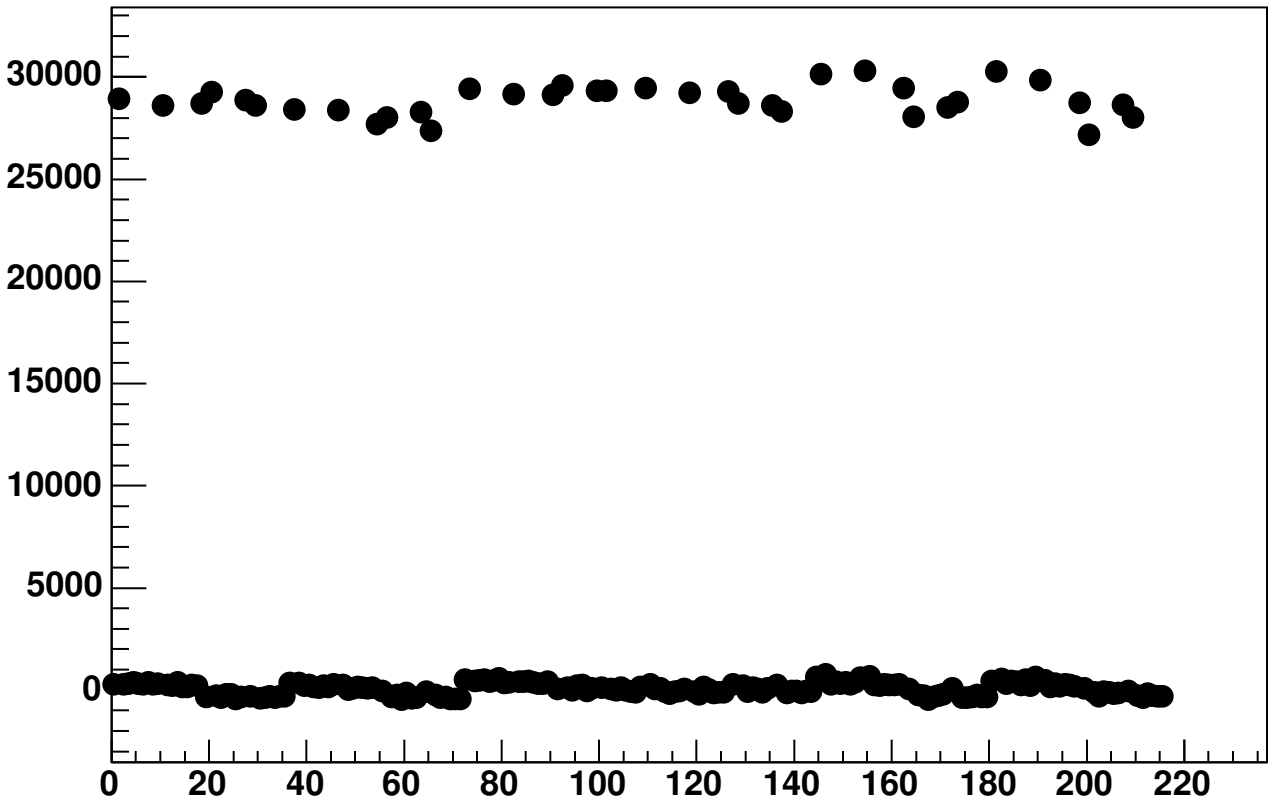
Enable 3, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



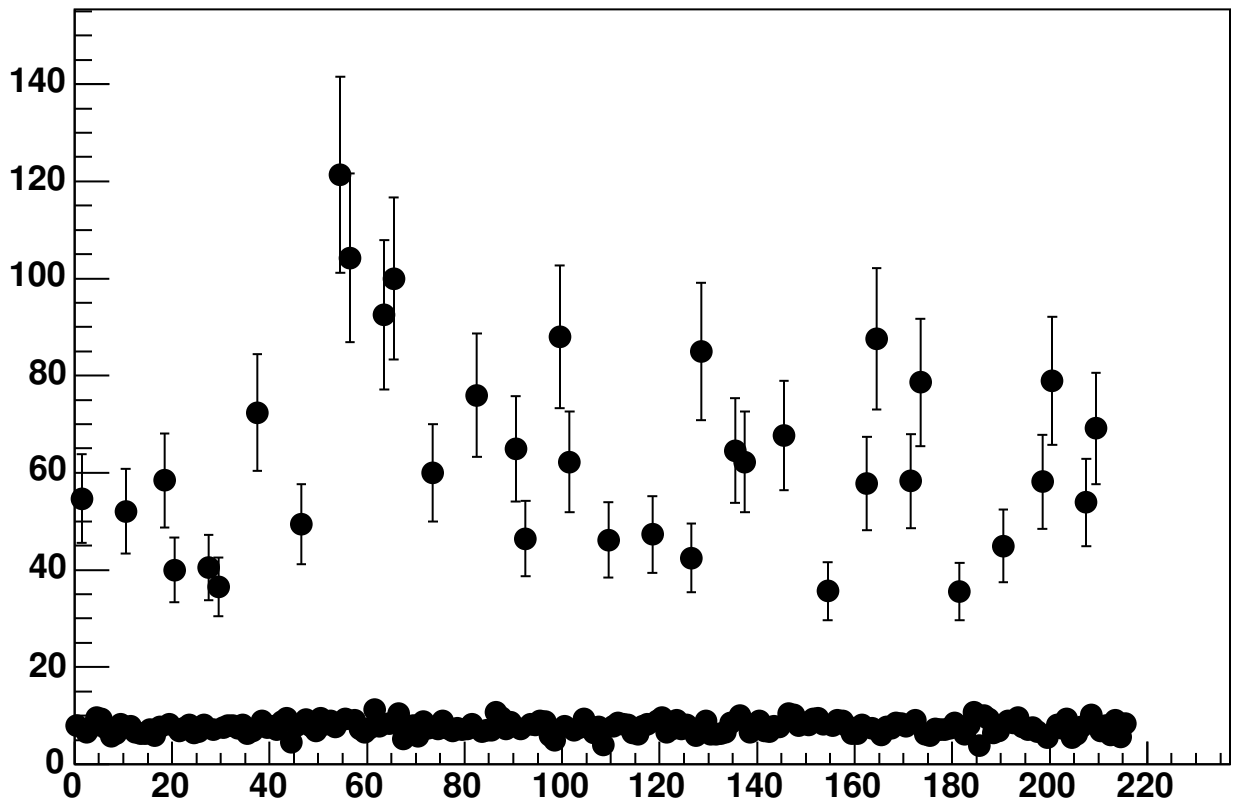
Enable 3, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



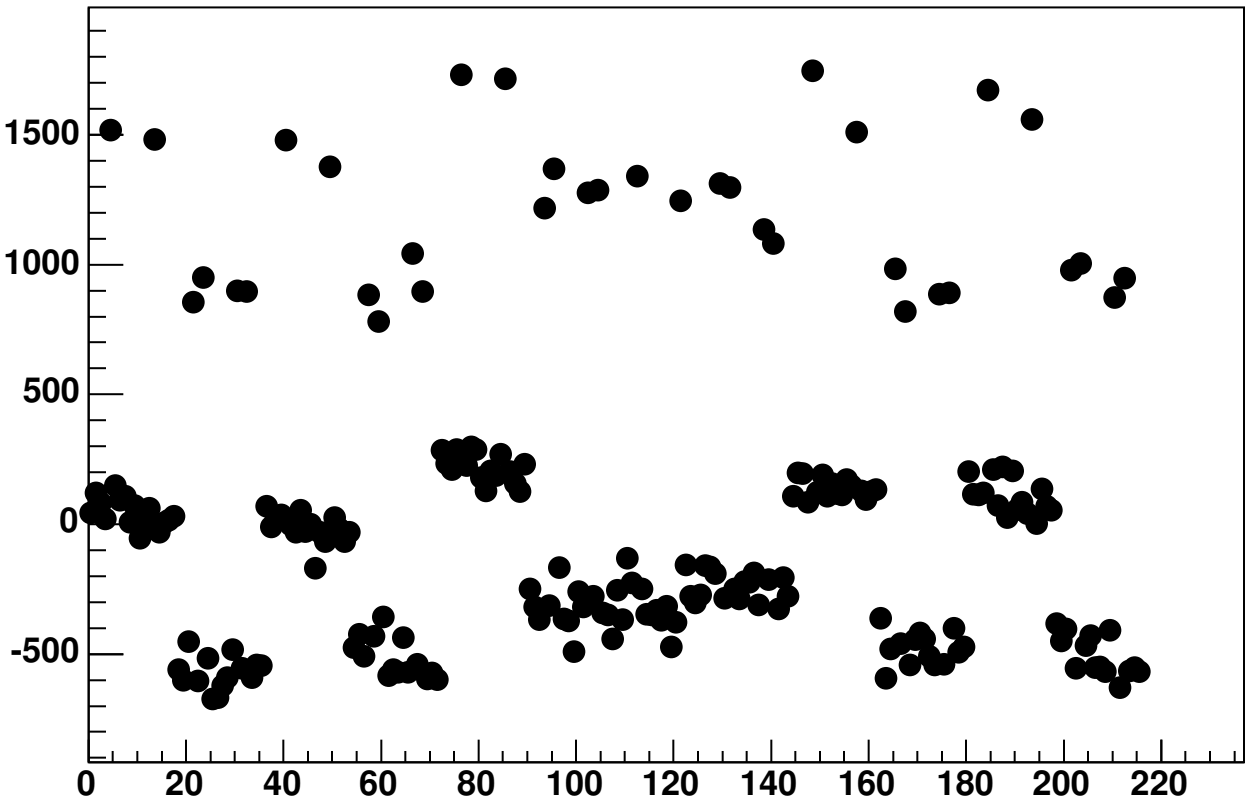
Enable 3, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



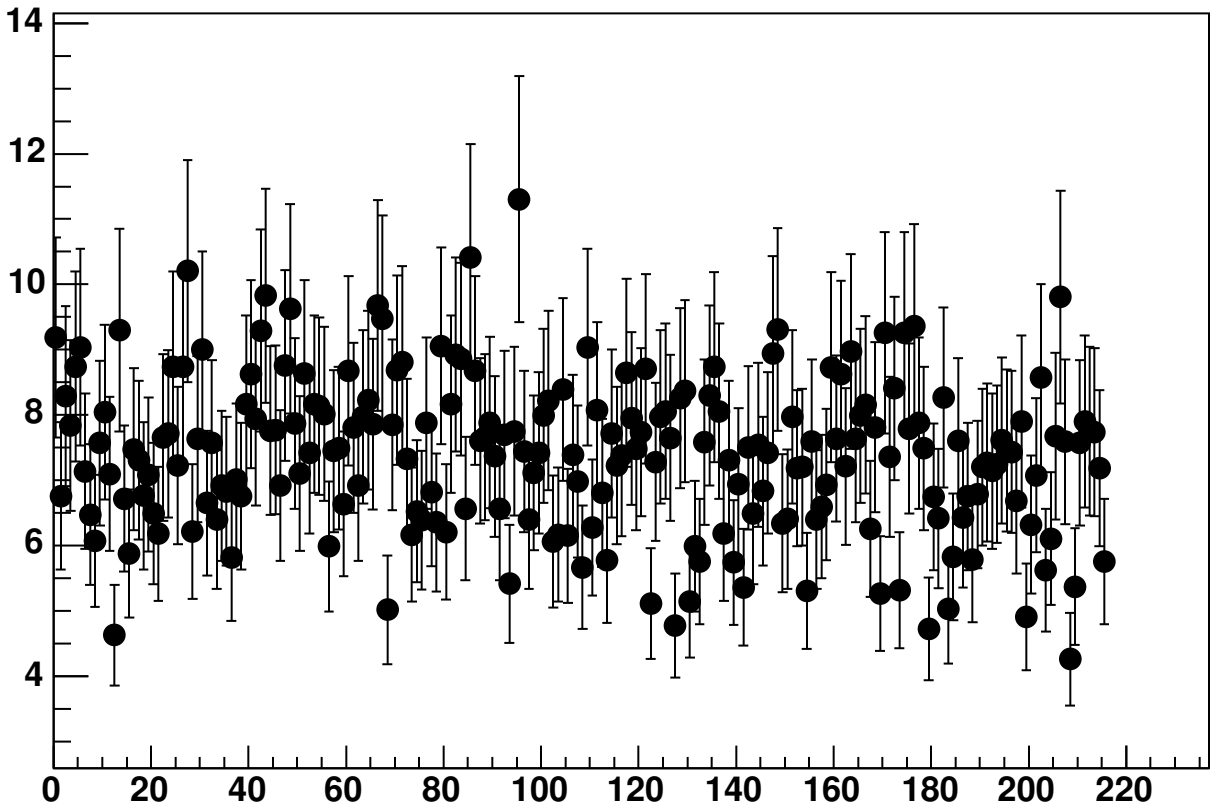
Enable 3, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



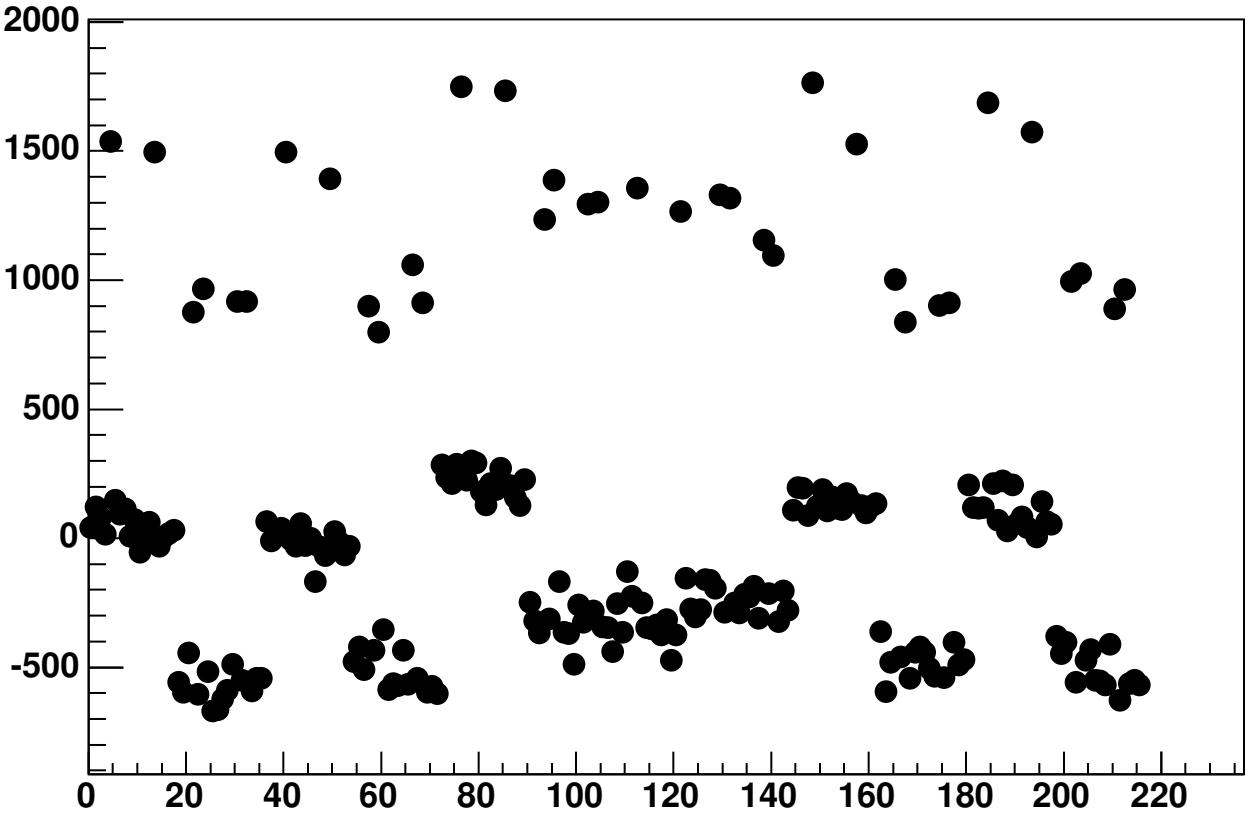
Enable 4, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



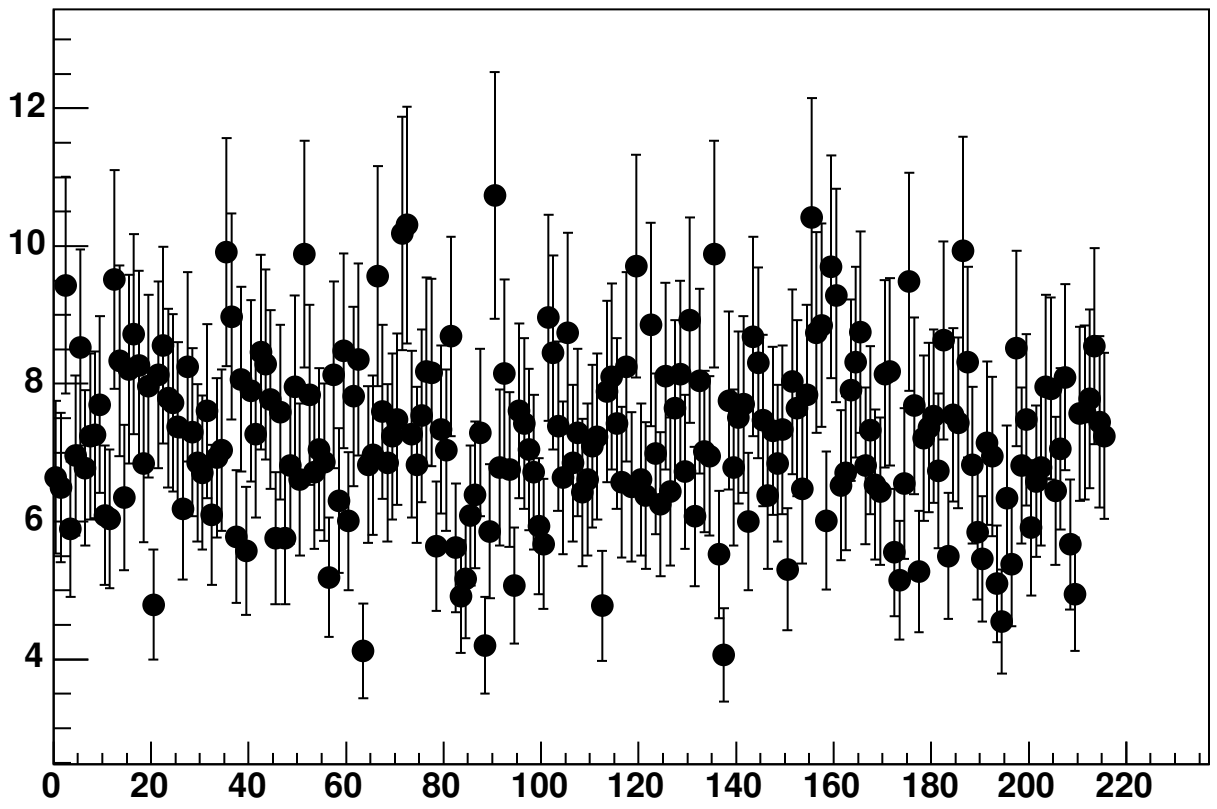
Enable 4, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



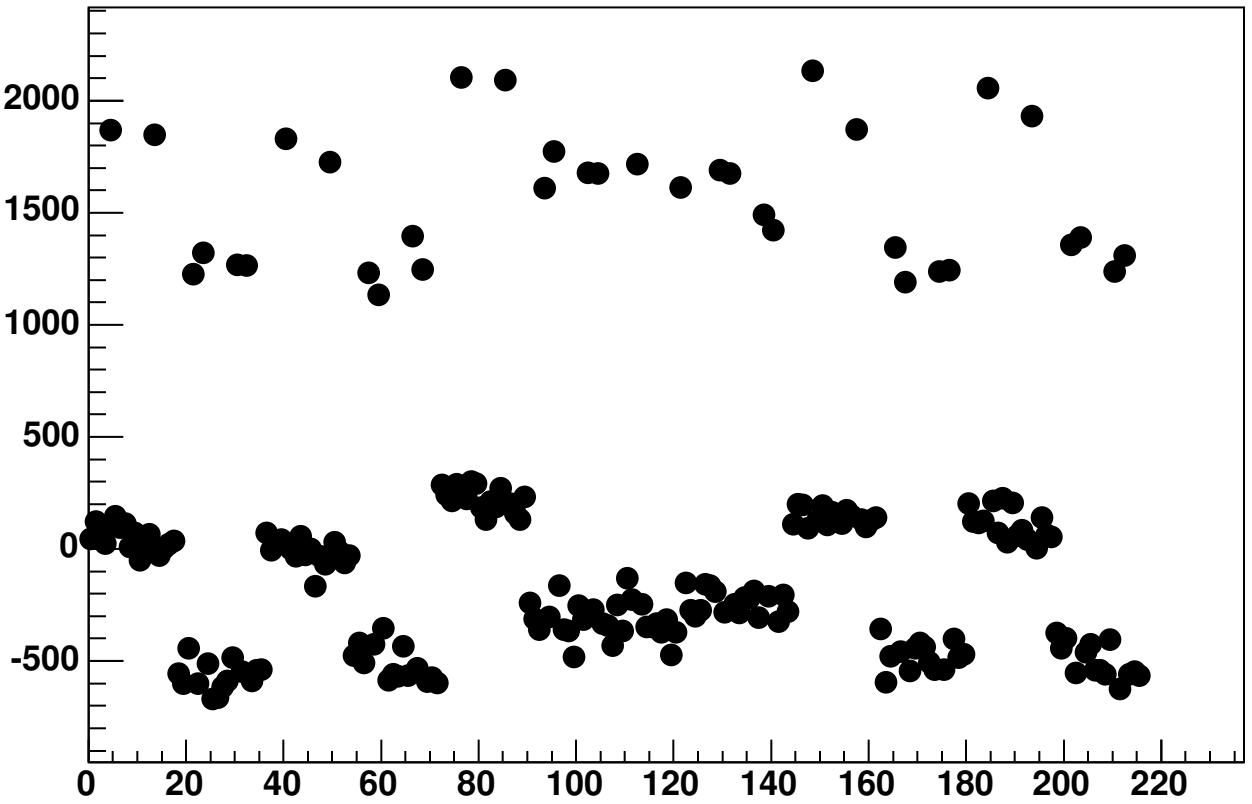
Enable 4, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



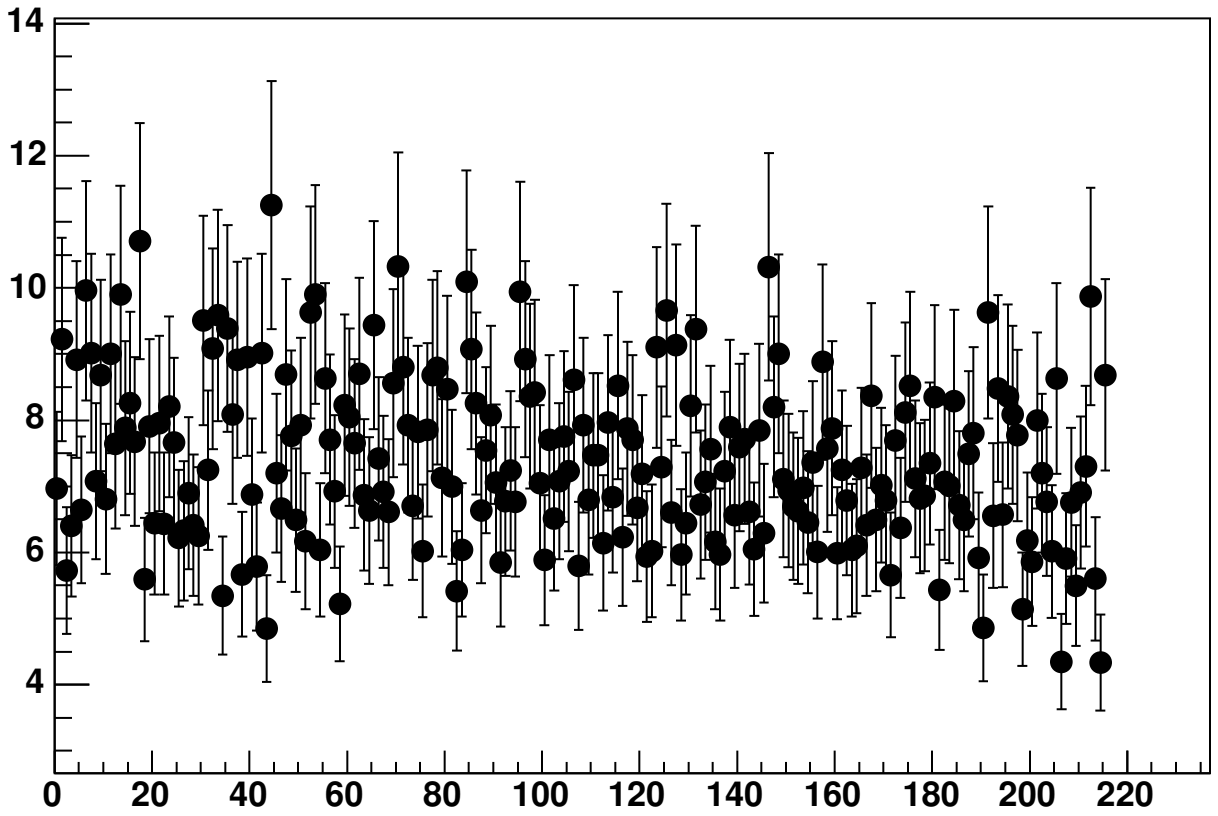
Enable 4, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



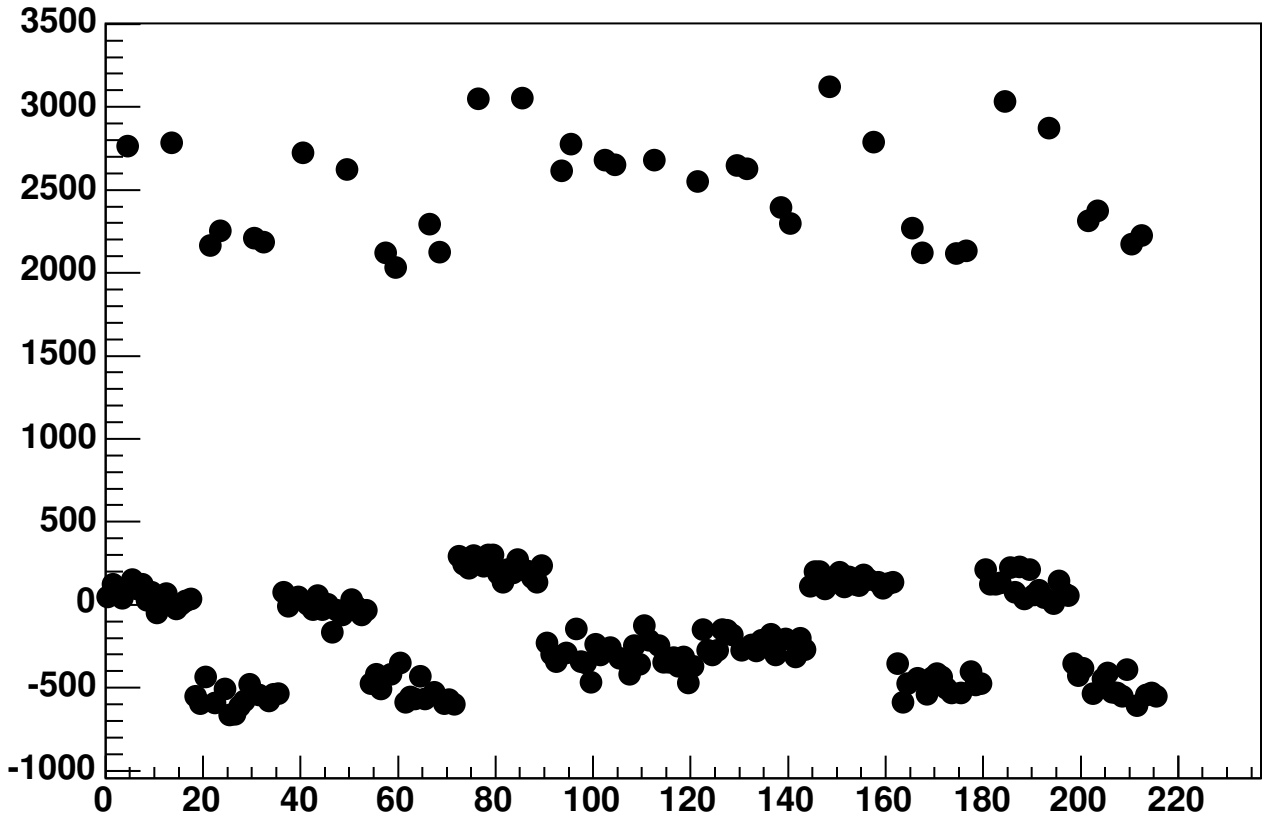
Enable 4, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



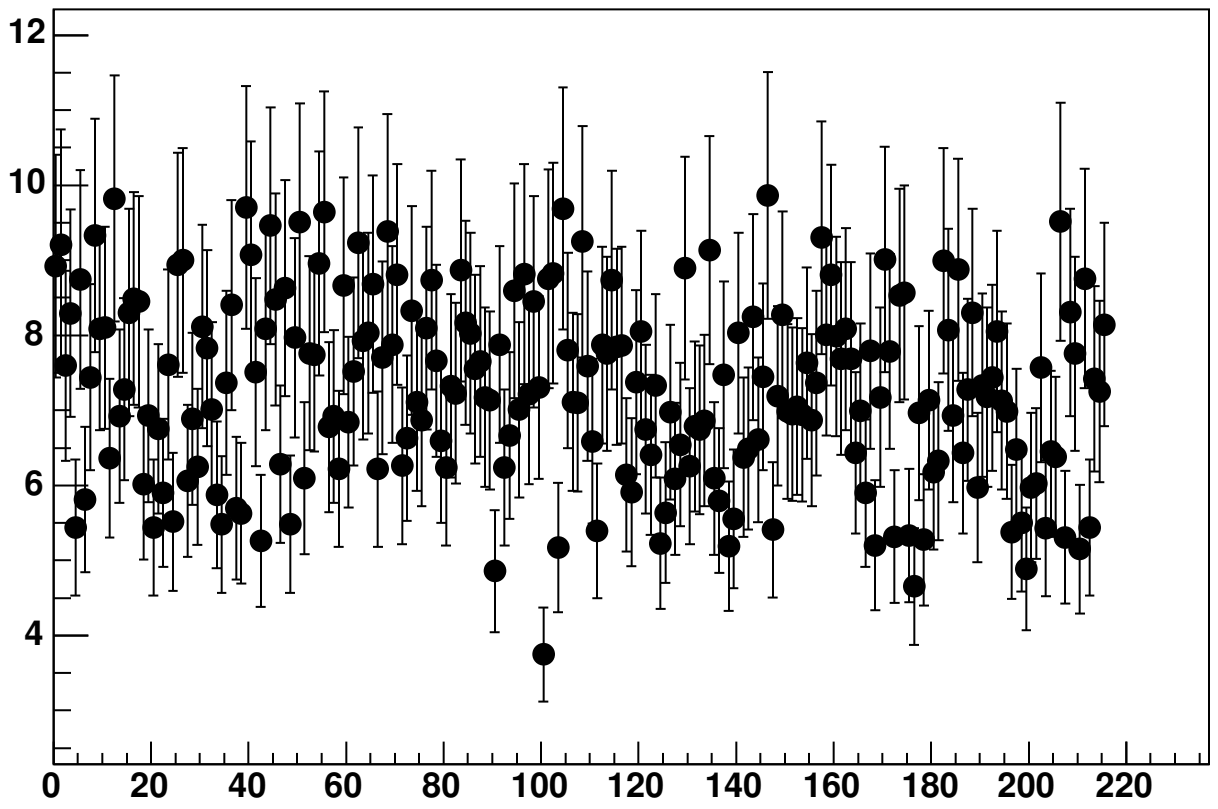
Enable 4, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



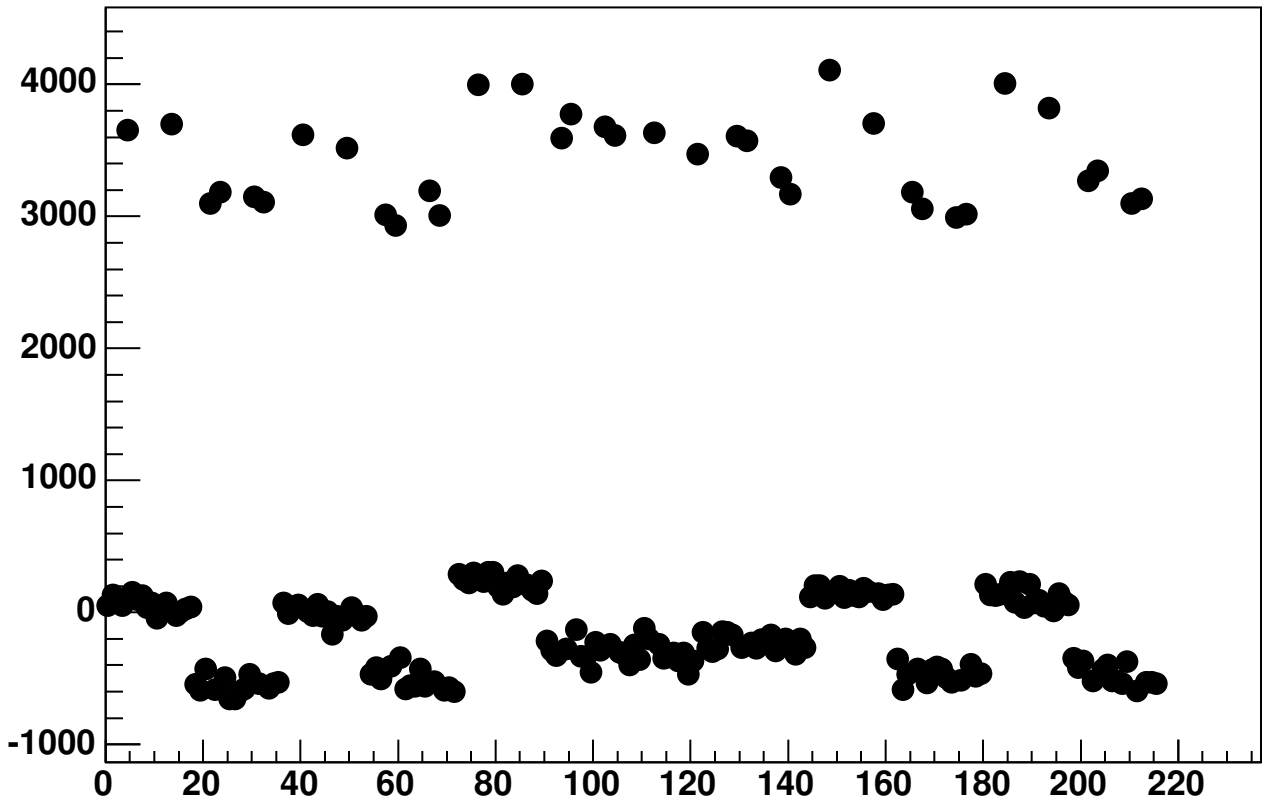
Enable 4, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



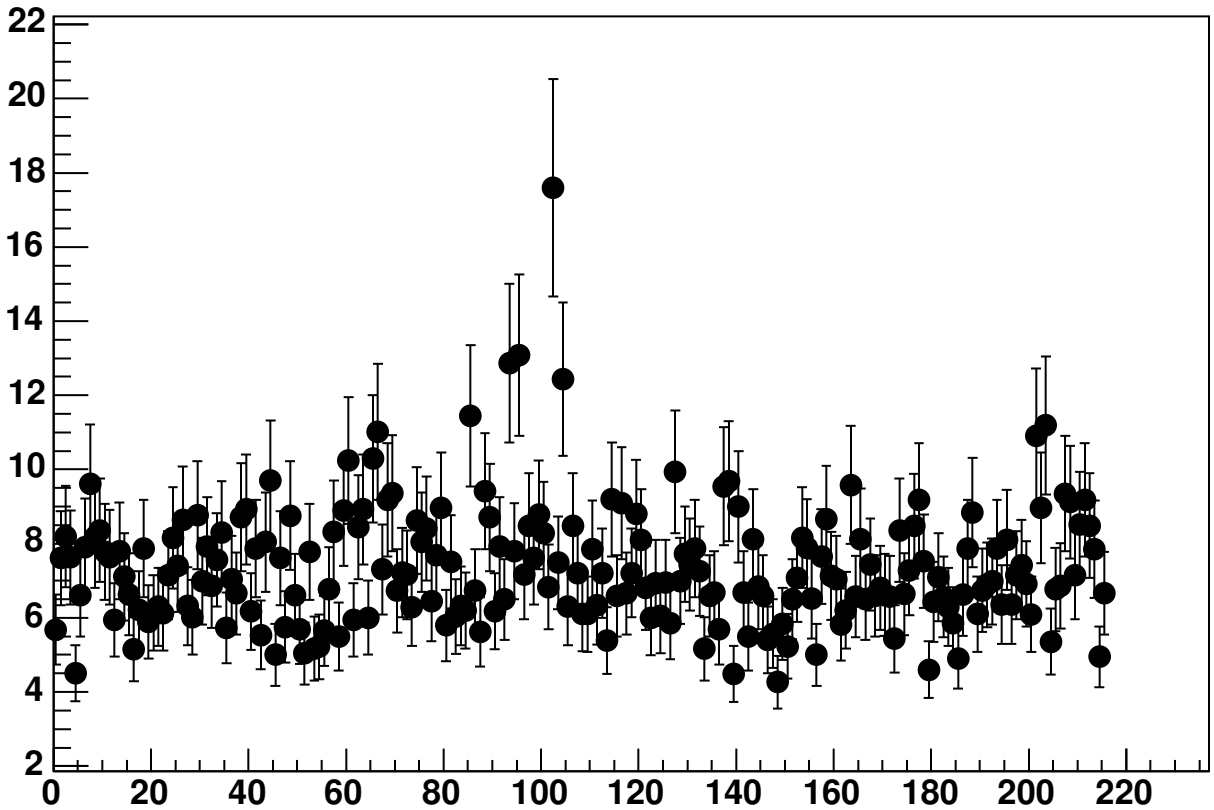
Enable 4, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



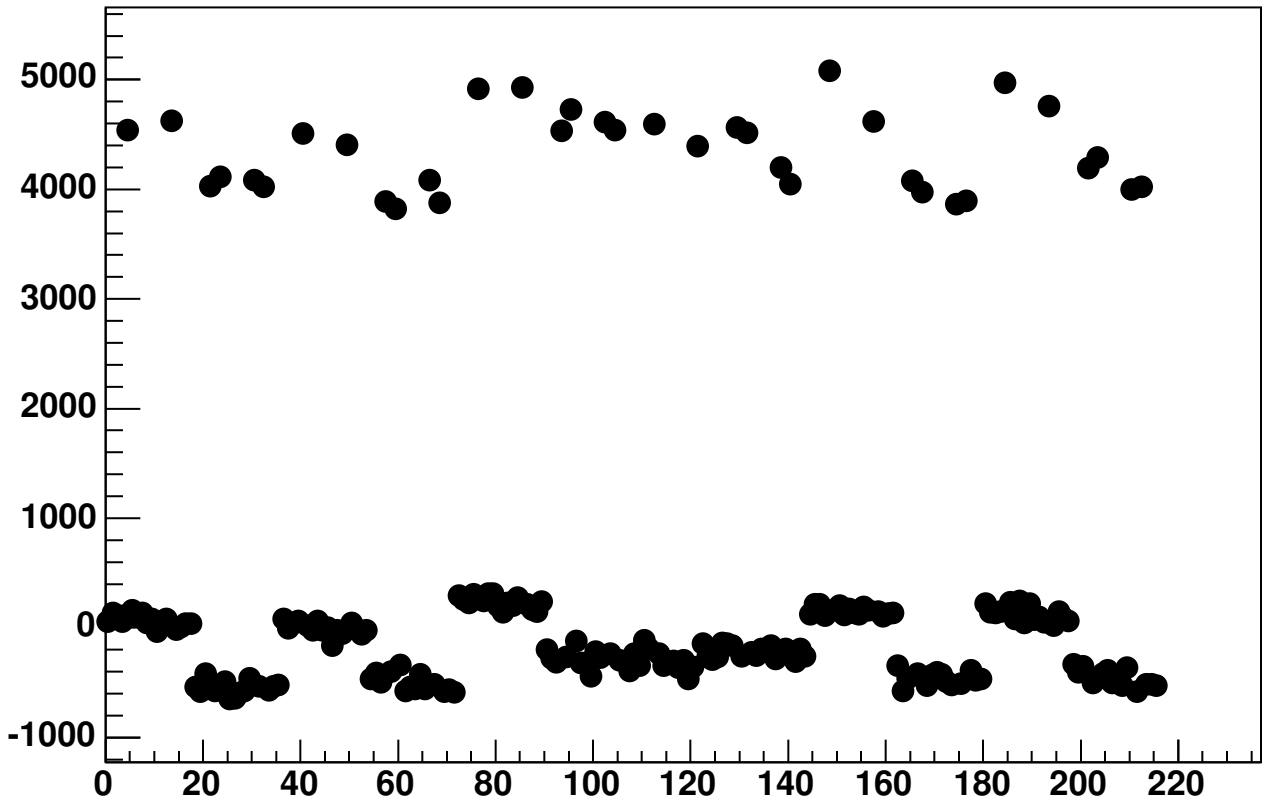
Enable 4, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



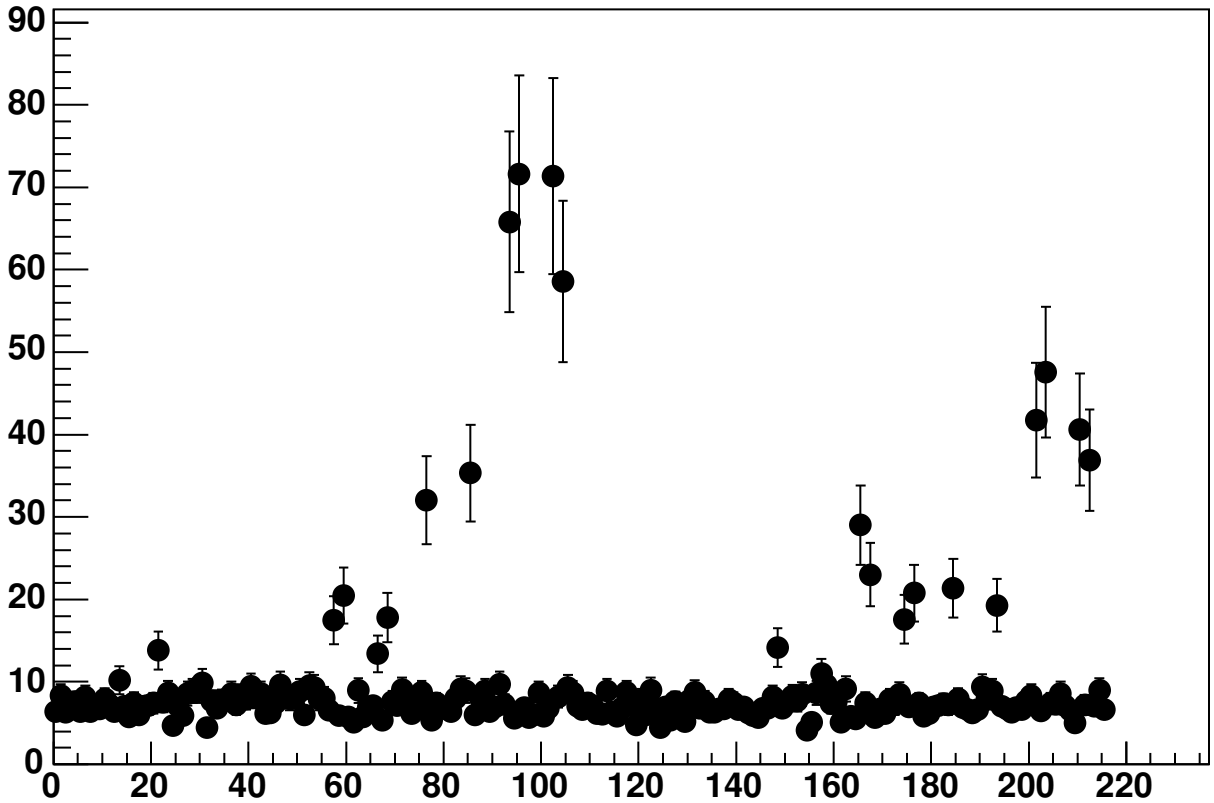
Enable 4, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



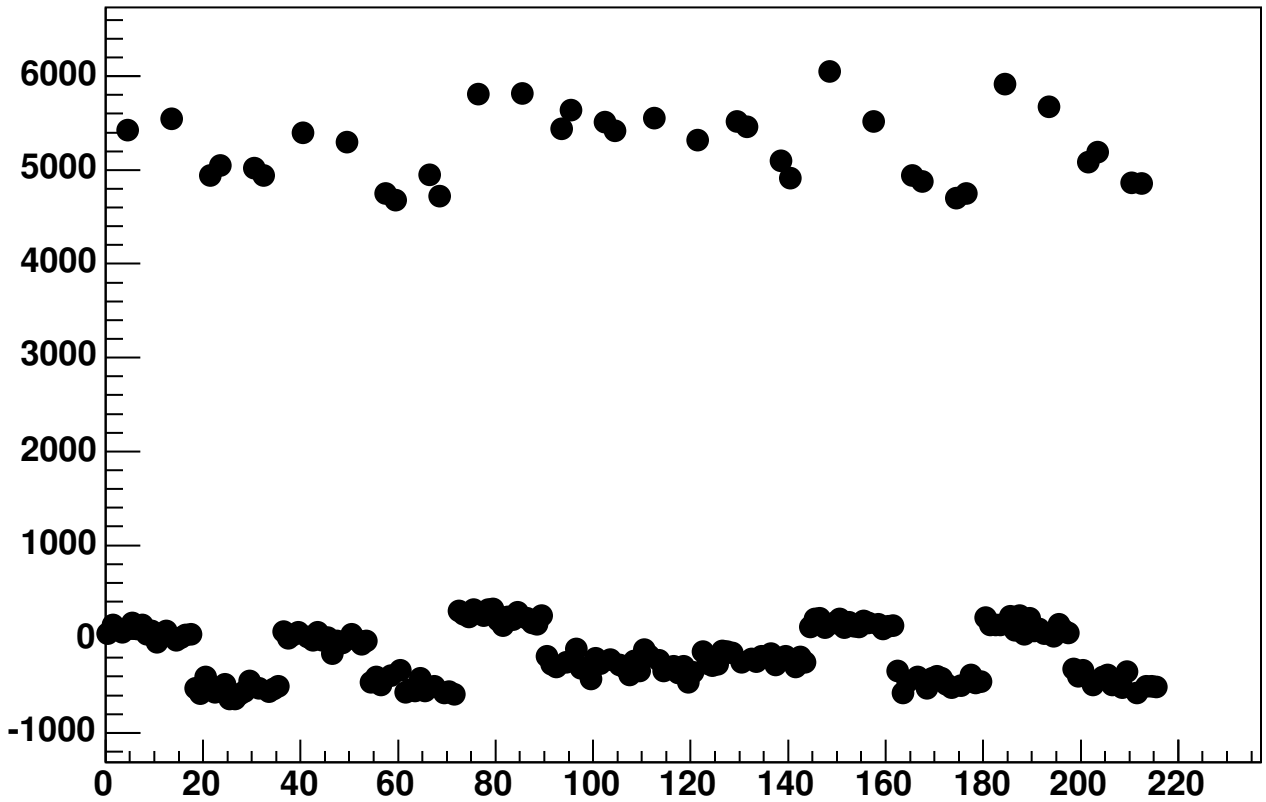
Enable 4, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



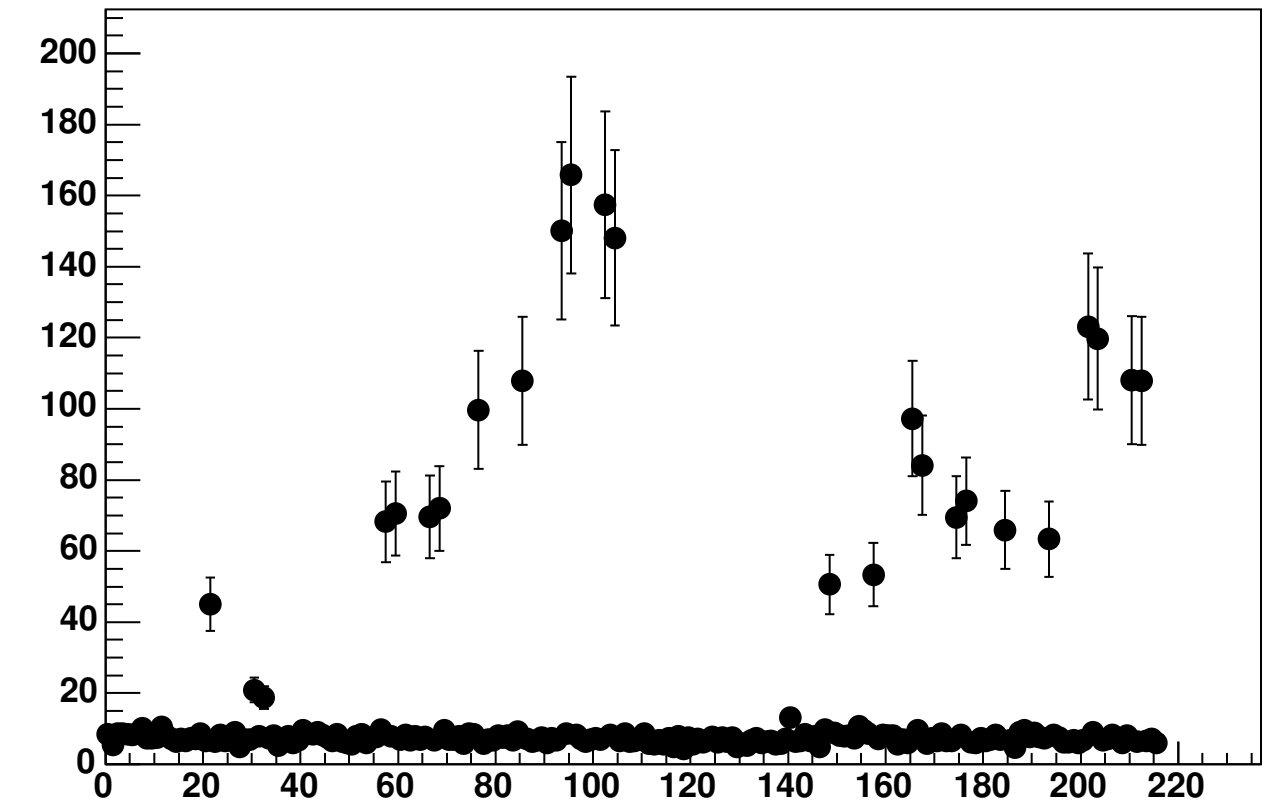
Enable 4, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



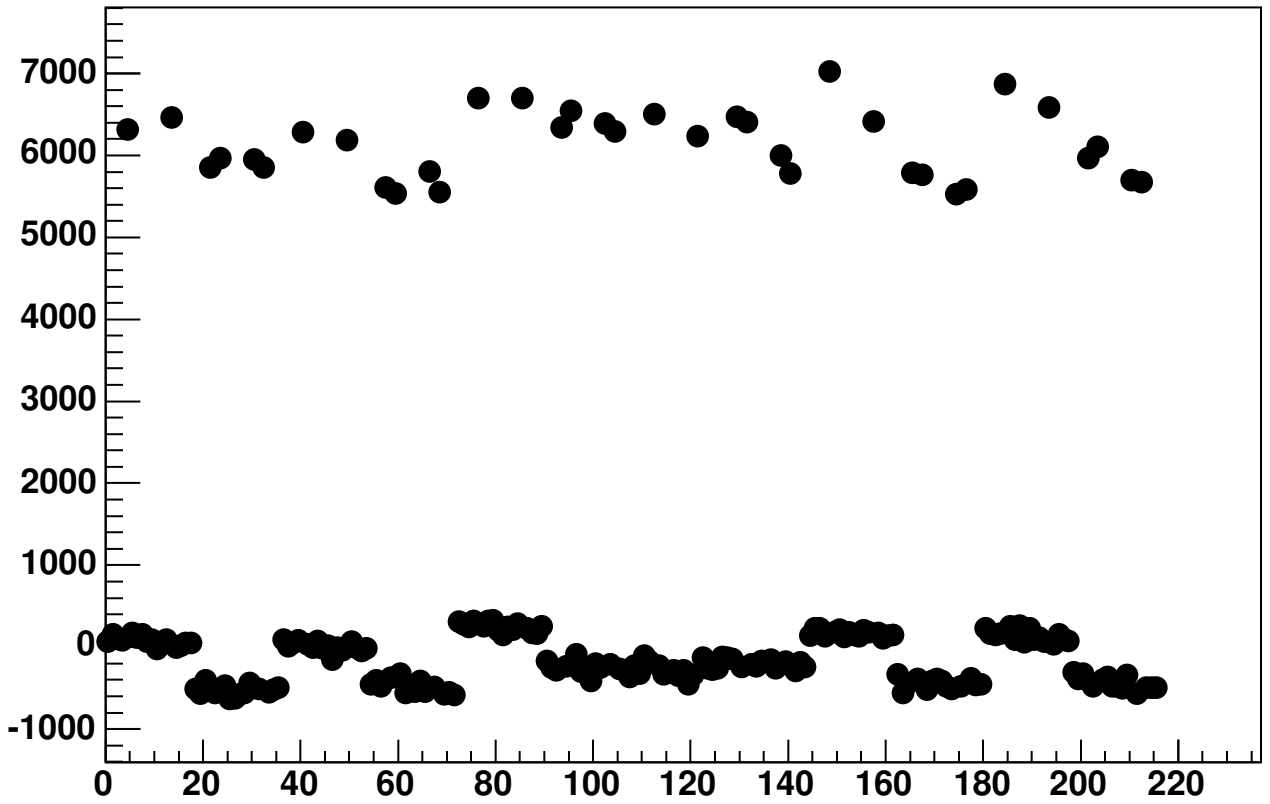
Enable 4, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



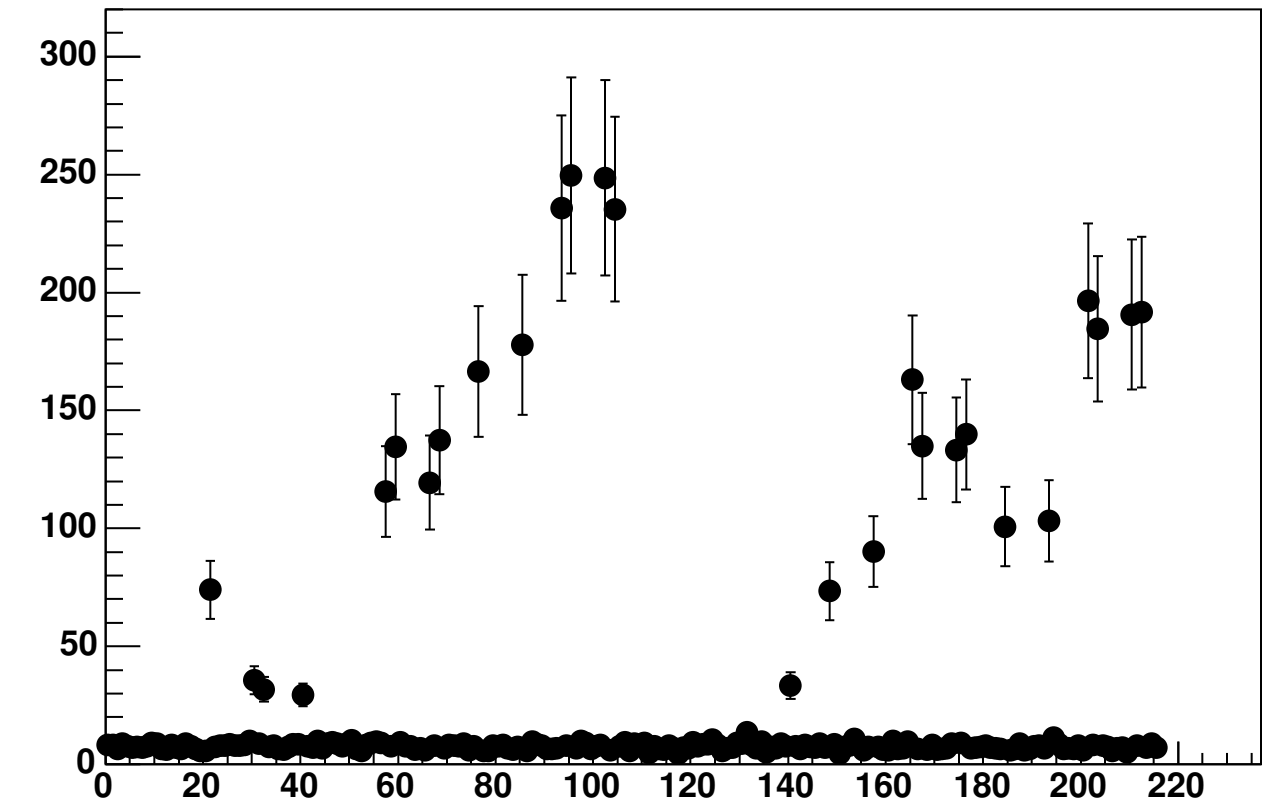
Enable 4, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



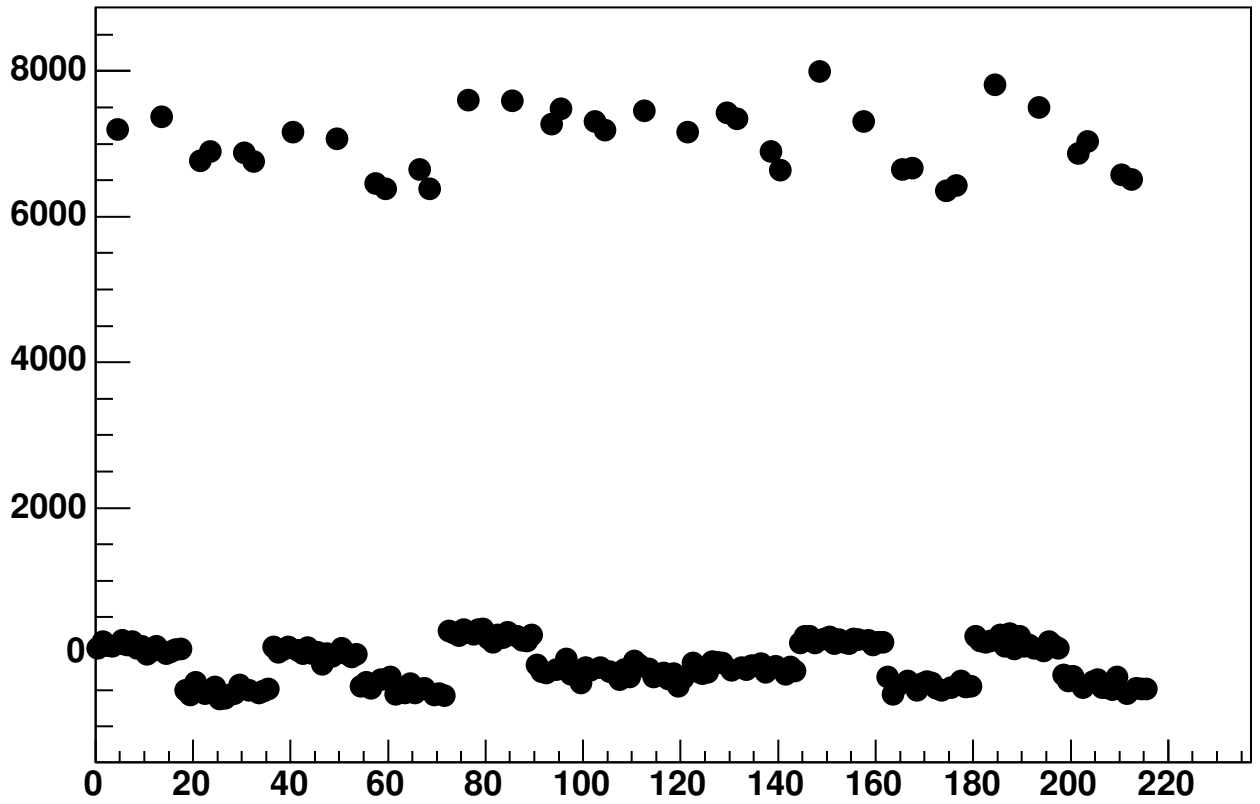
Enable 4, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



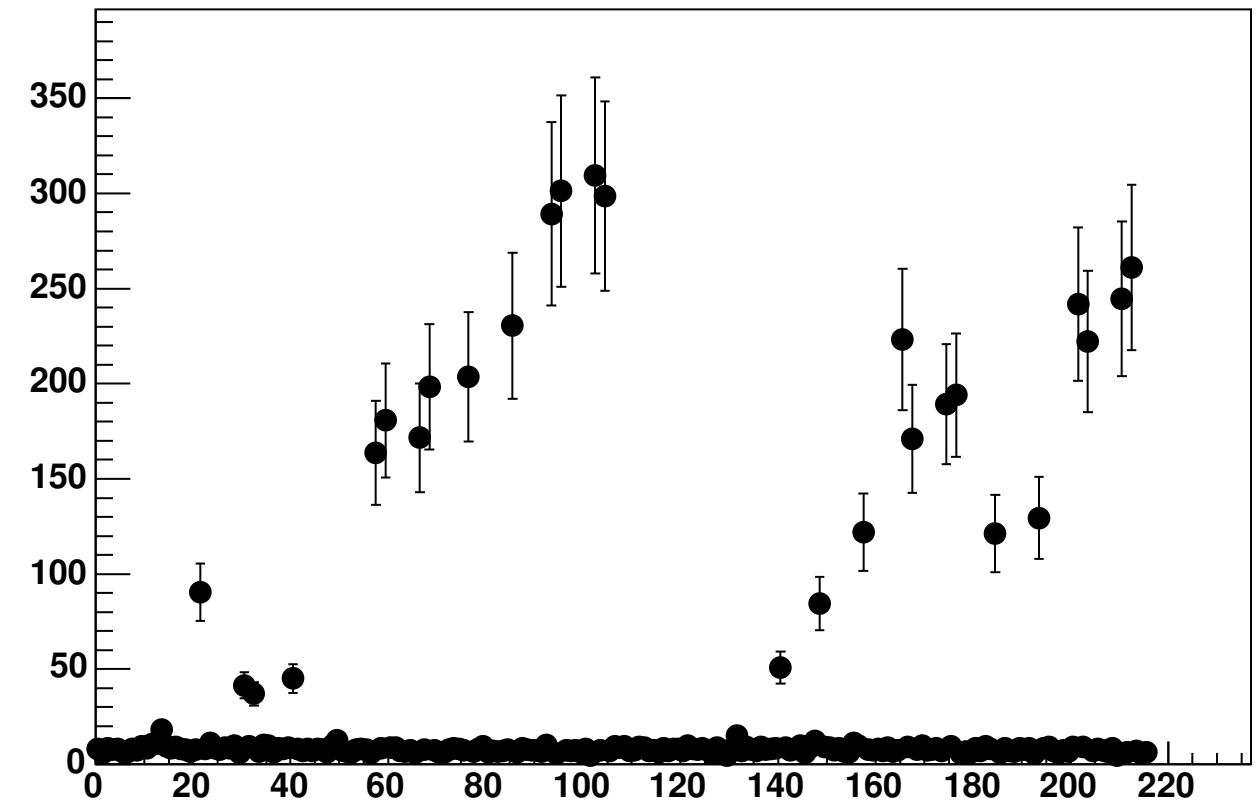
Enable 4, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



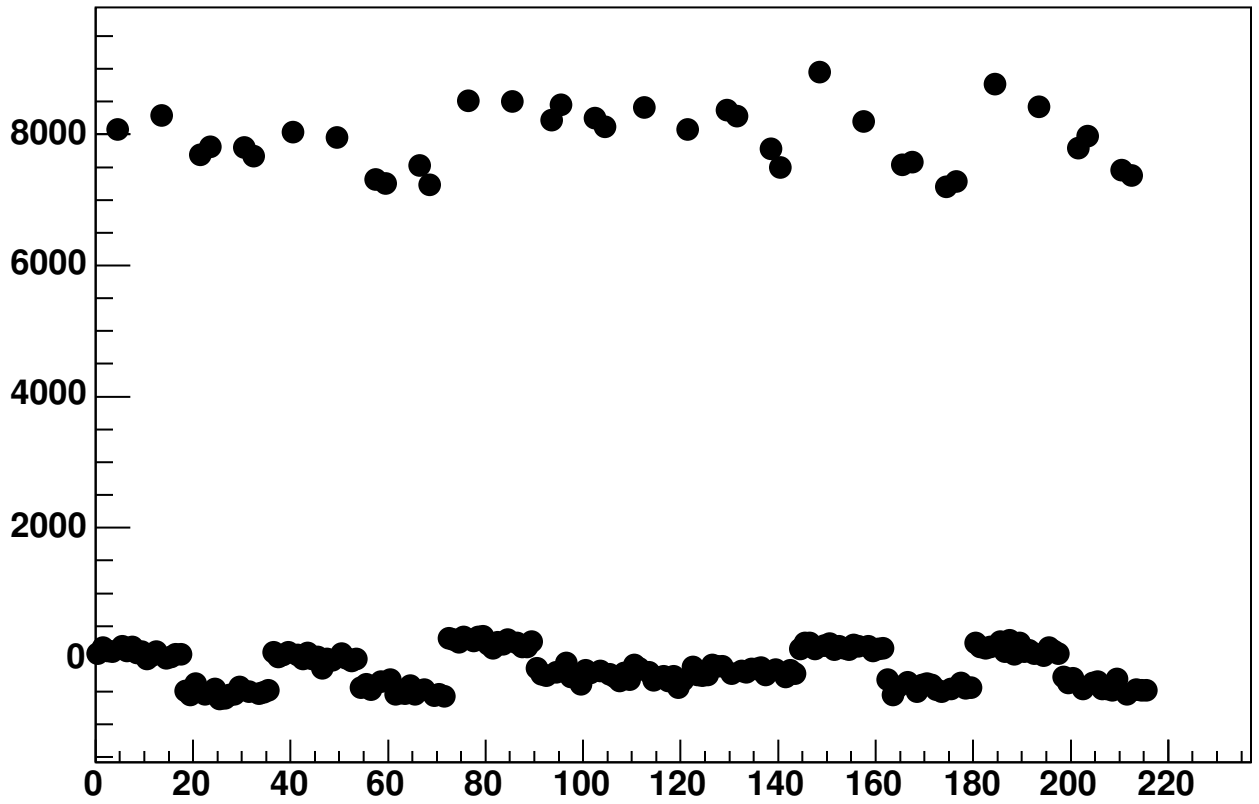
Enable 4, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



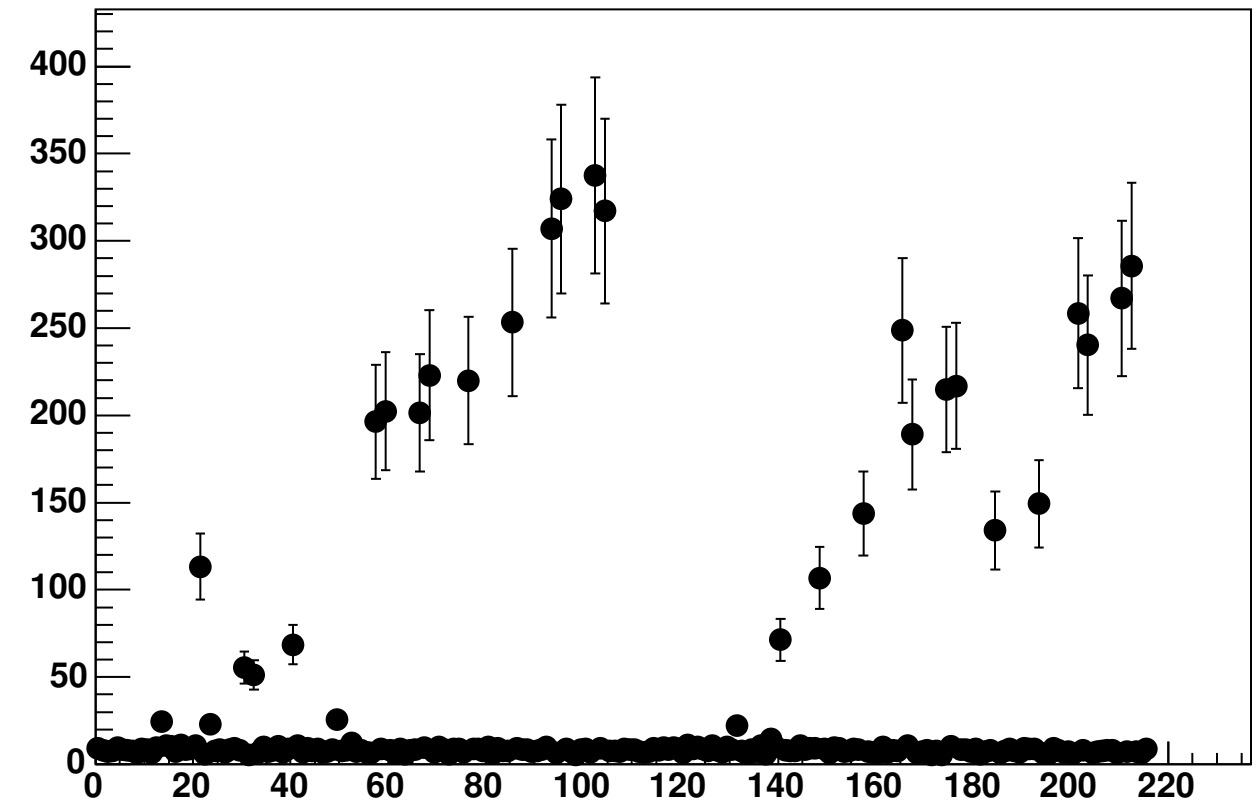
Enable 4, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



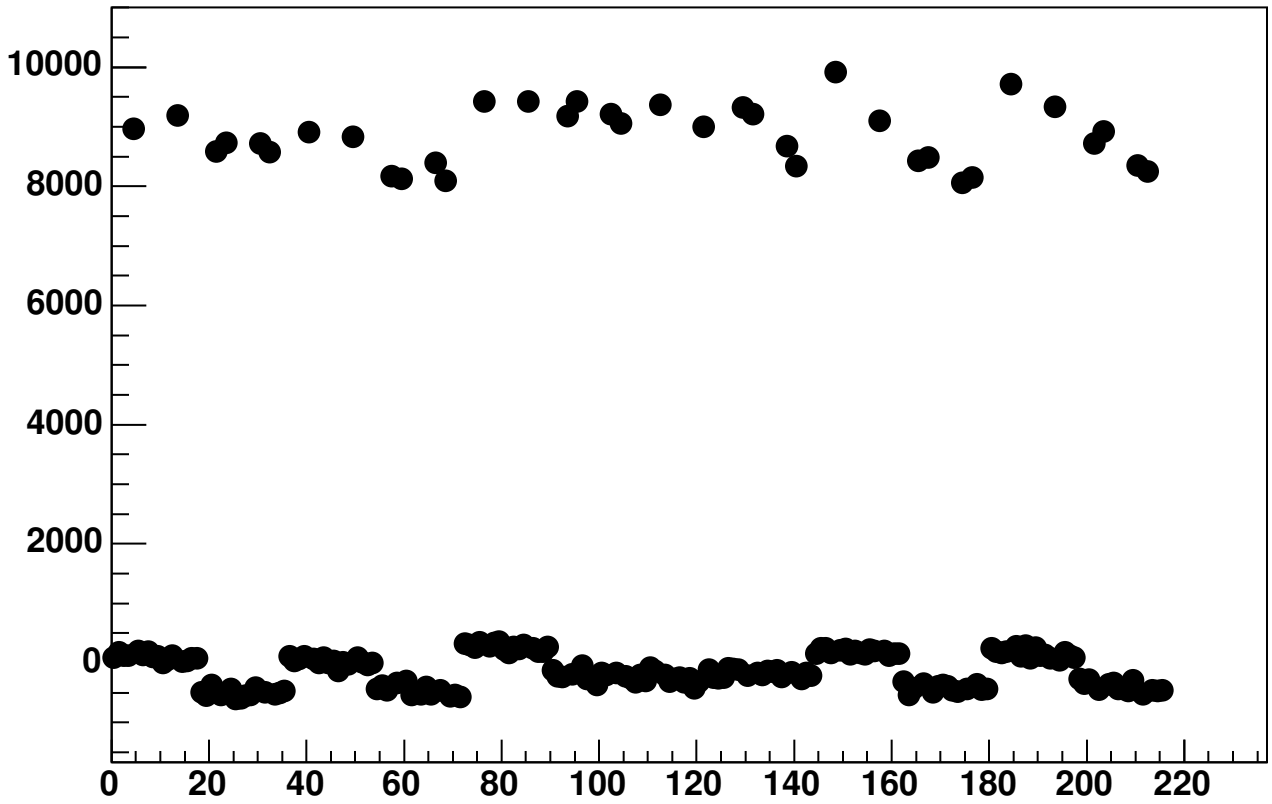
Enable 4, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



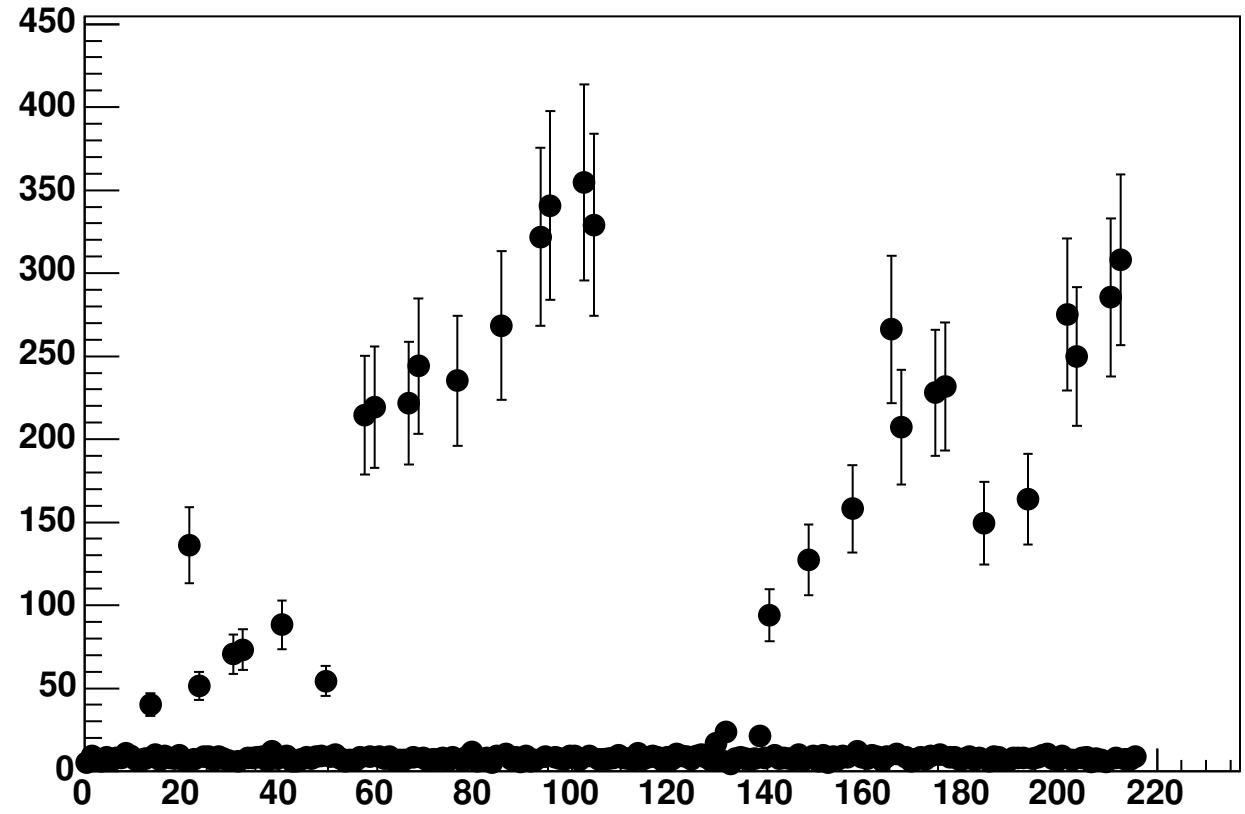
Enable 4, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



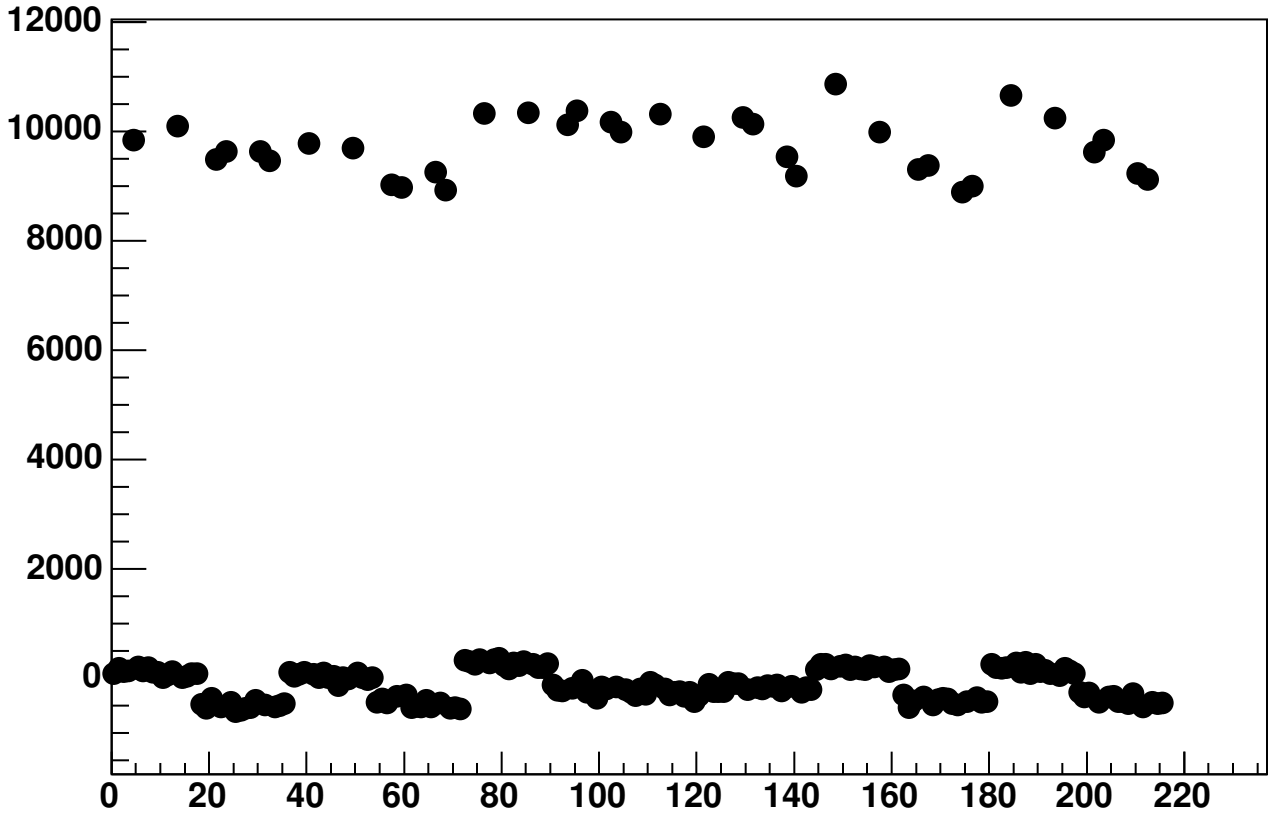
Enable 4, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



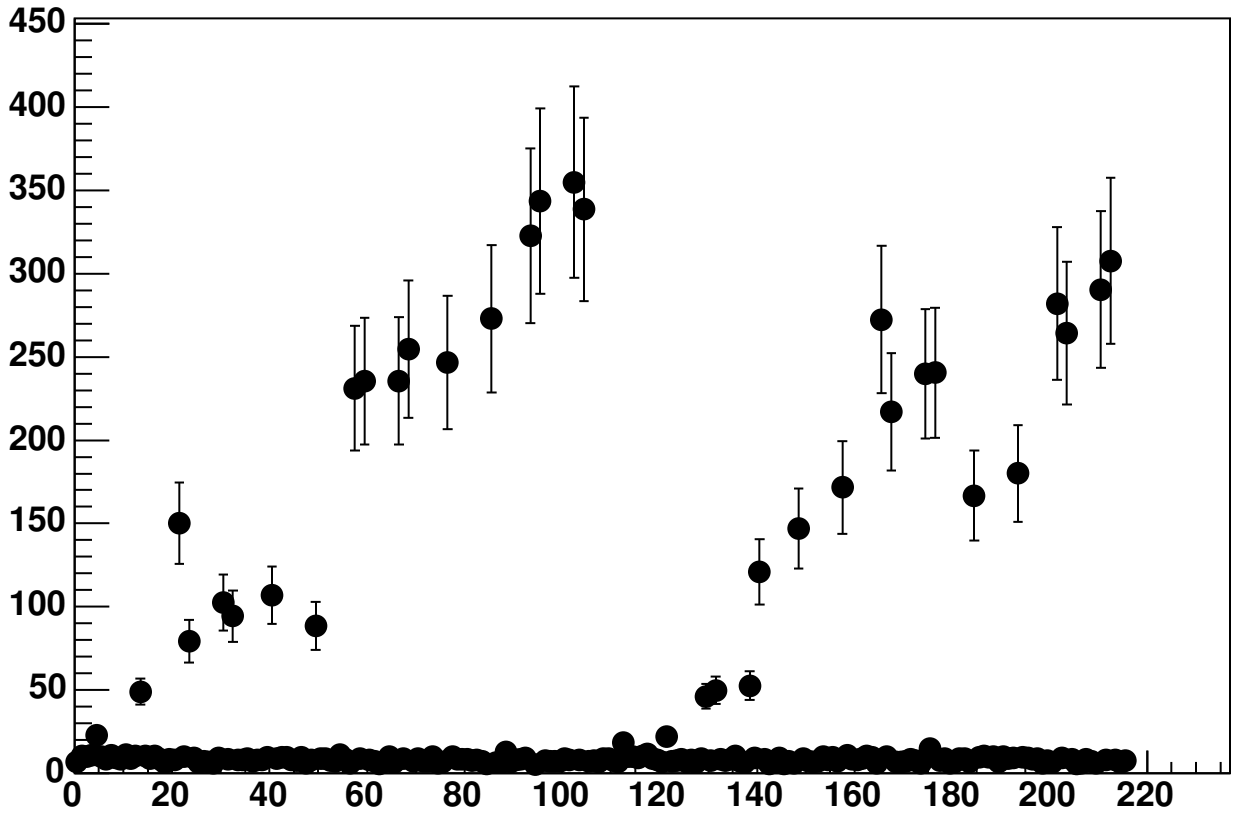
Enable 4, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



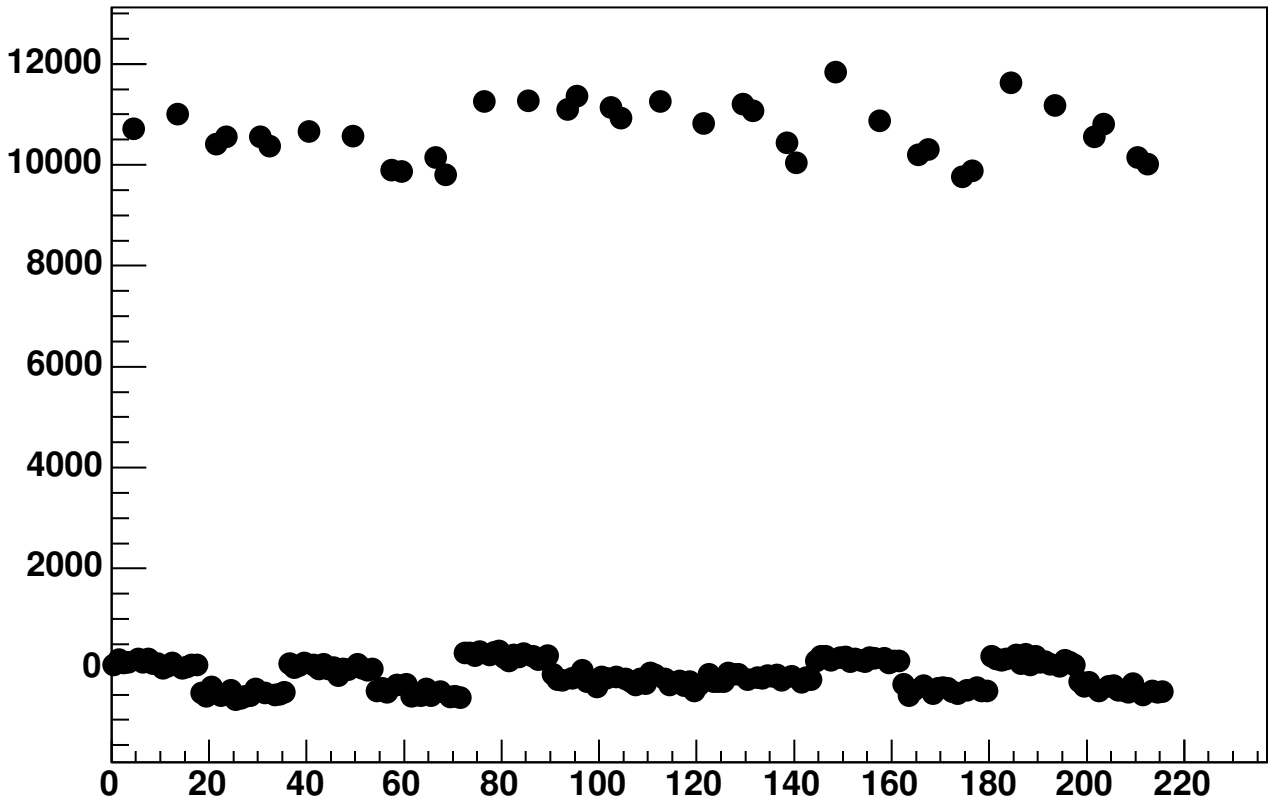
Enable 4, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



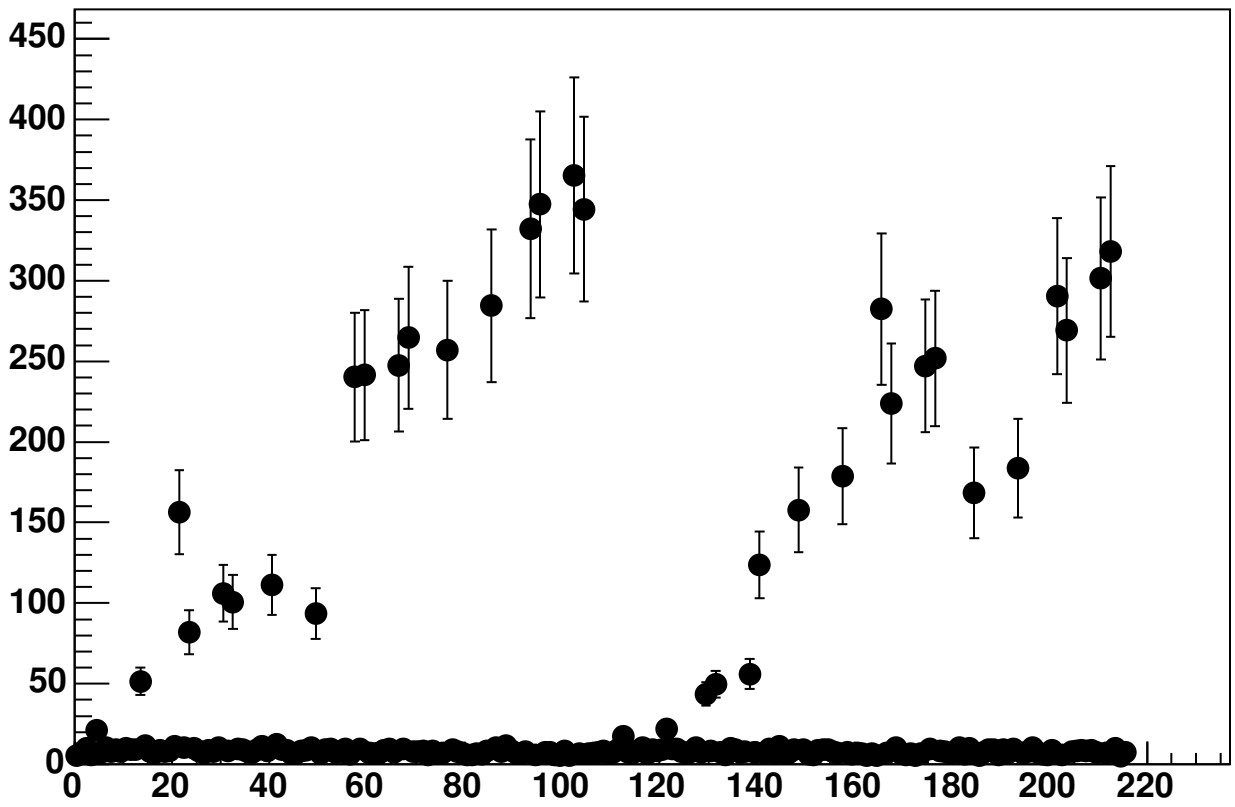
Enable 4, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



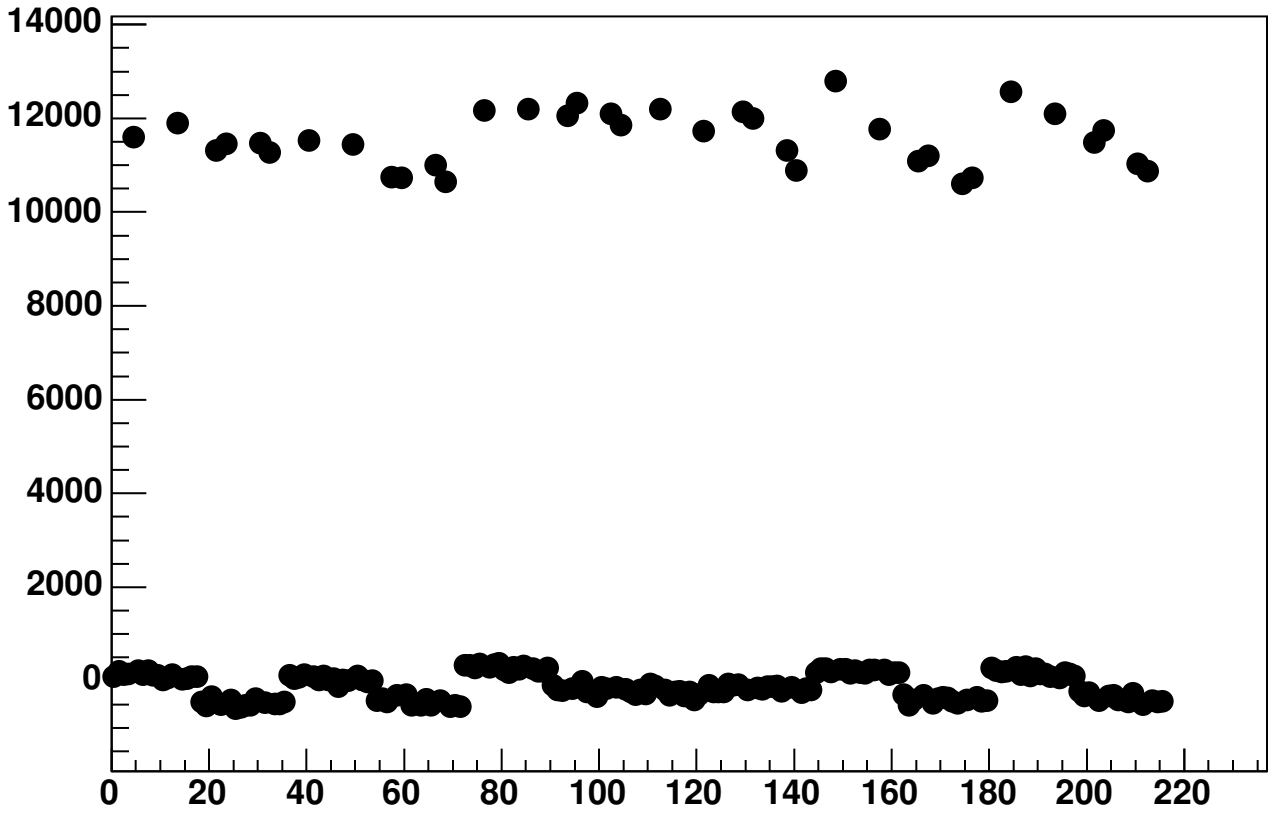
Enable 4, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



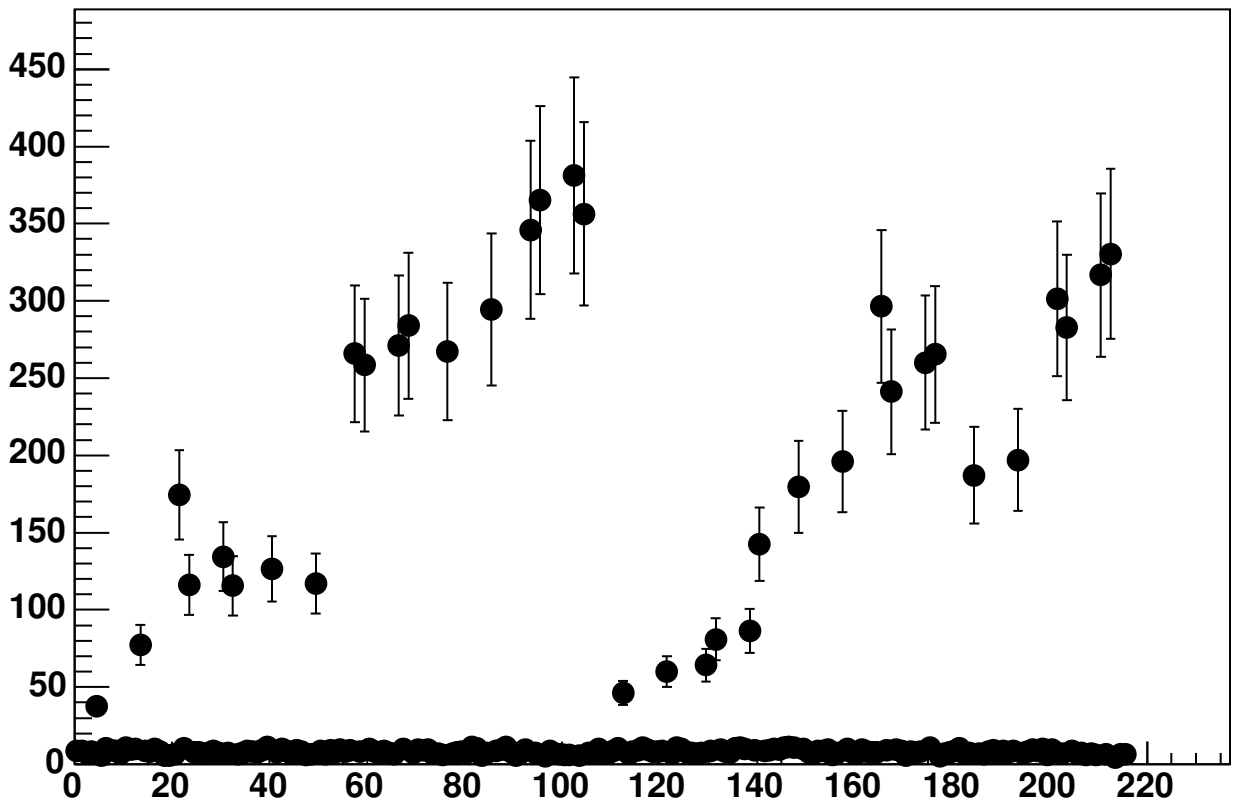
Enable 4, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



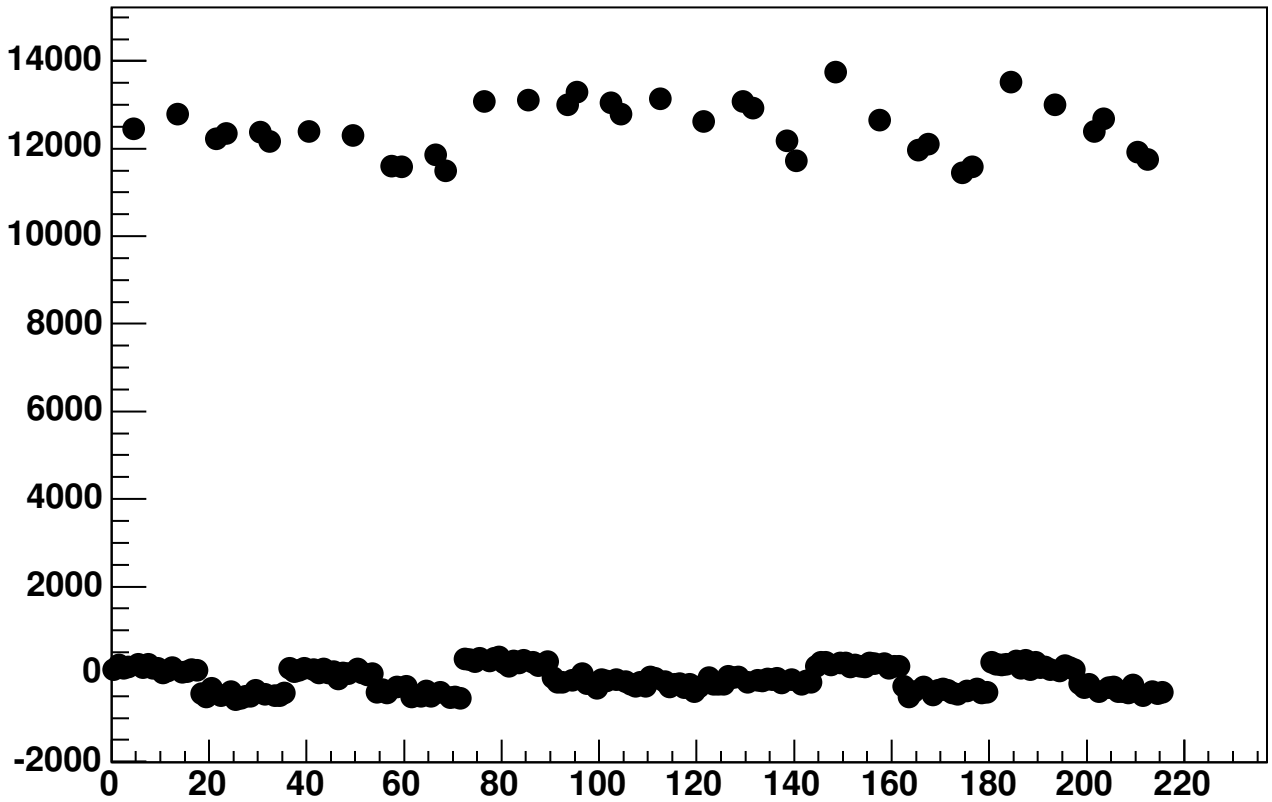
Enable 4, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



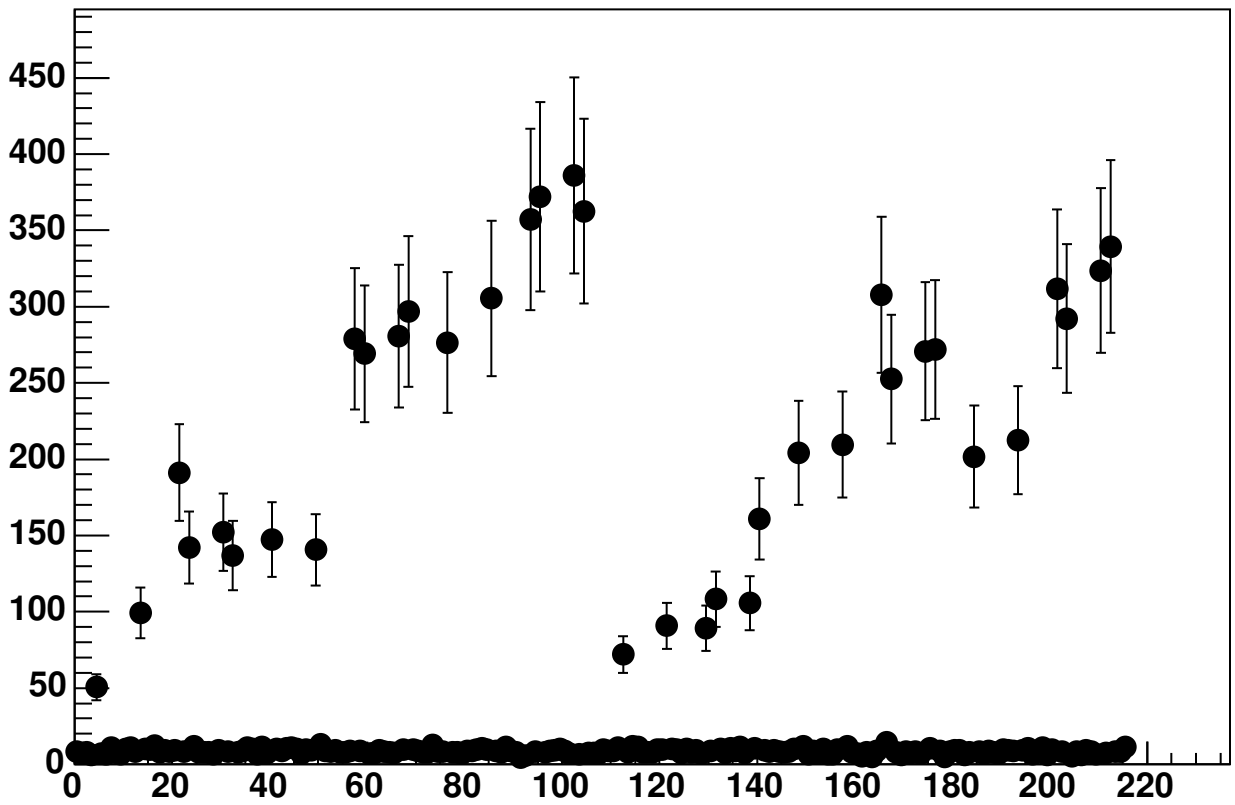
Enable 4, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



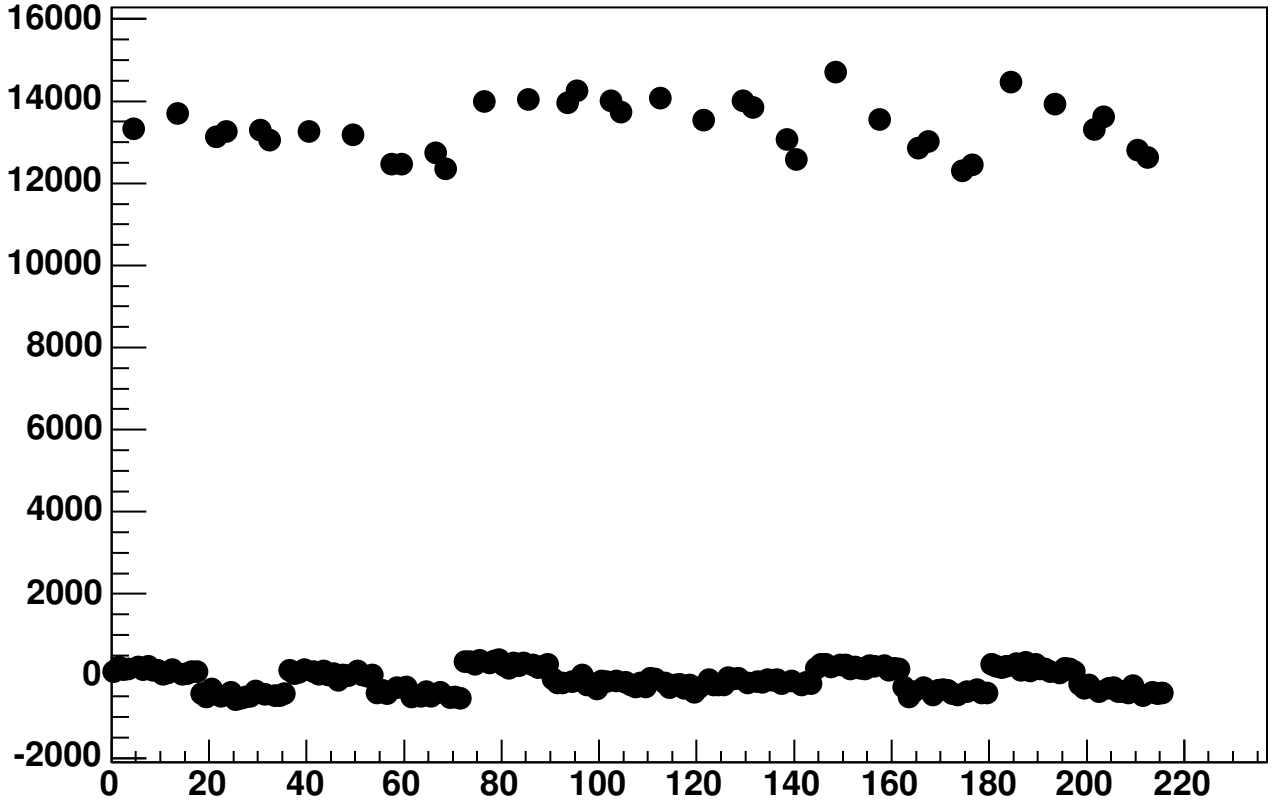
Enable 4, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



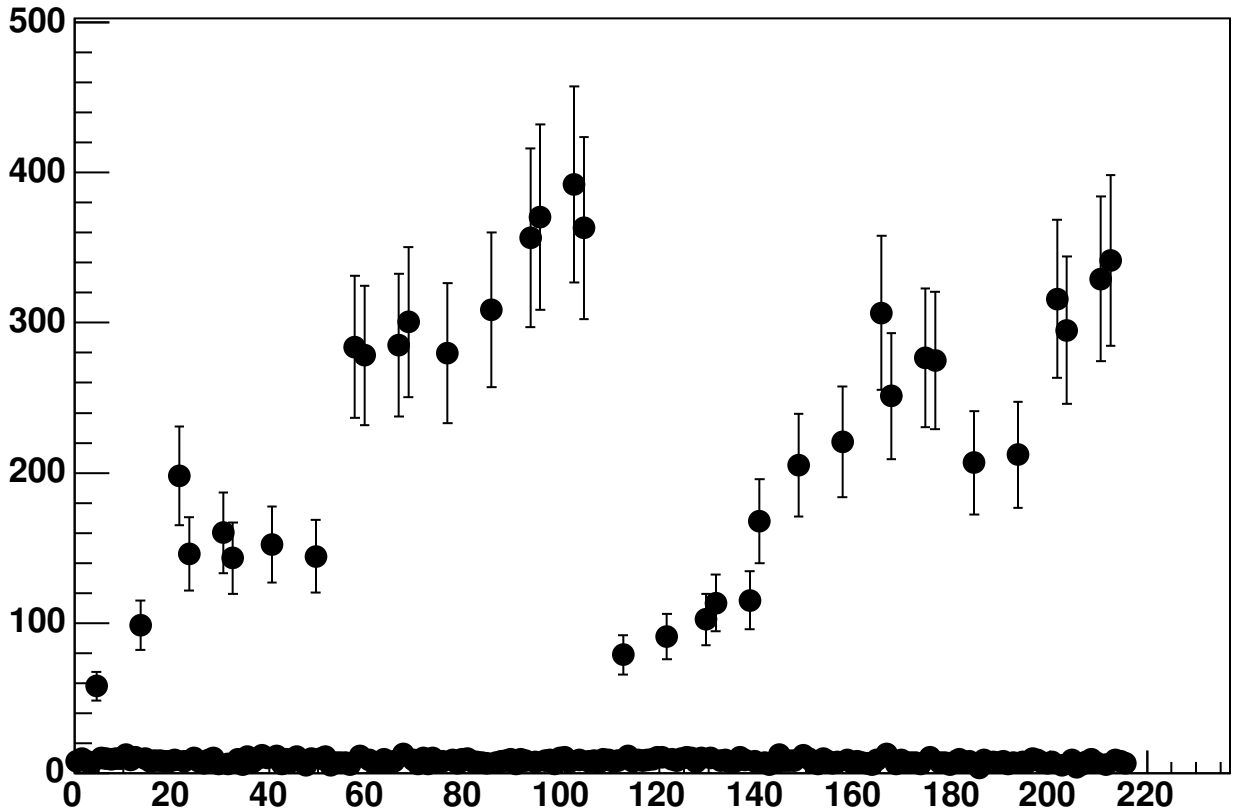
Enable 4, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



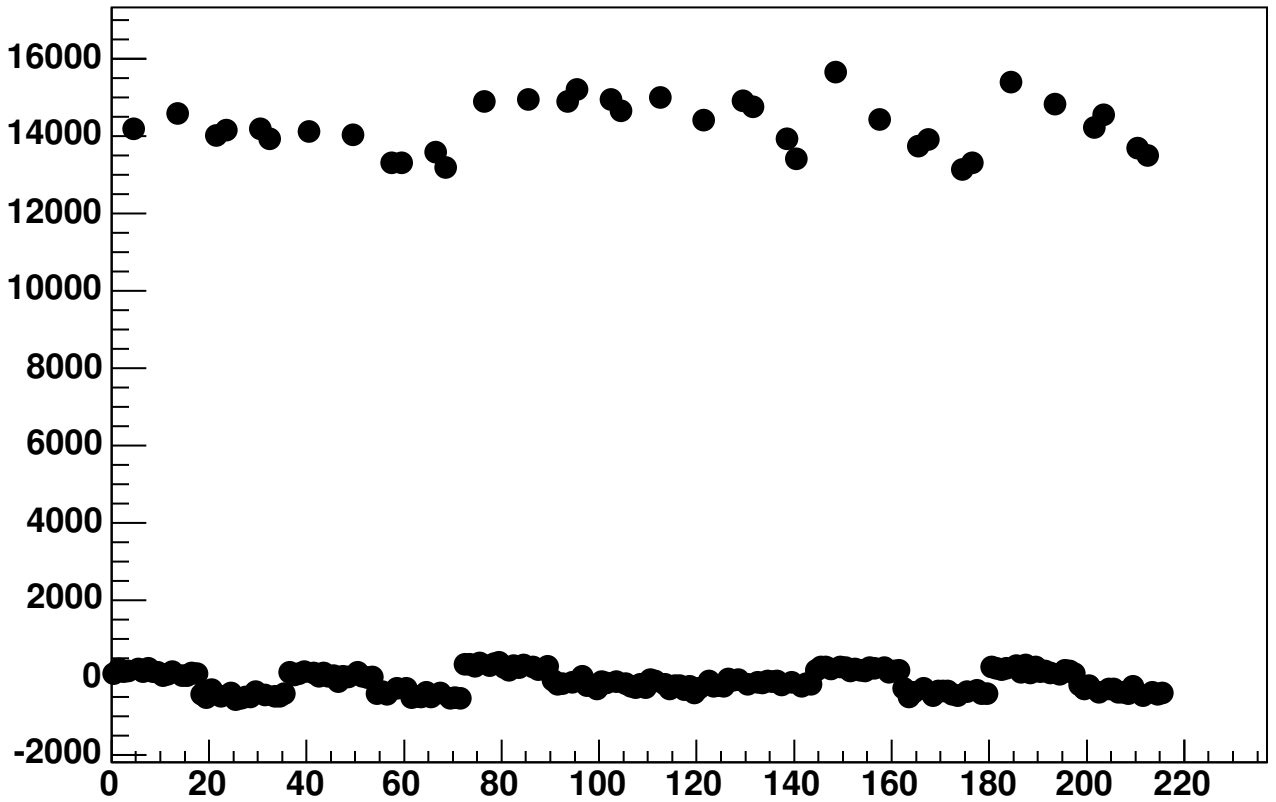
Enable 4, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



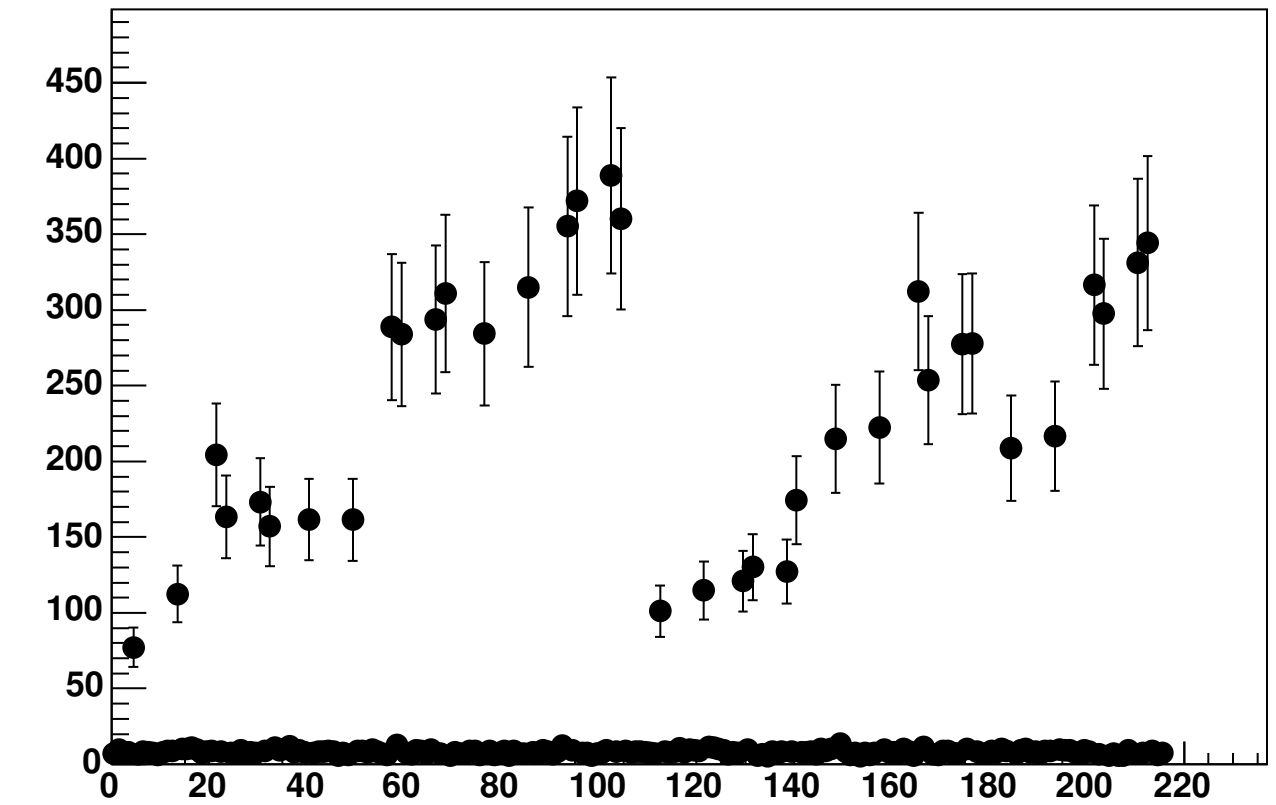
Enable 4, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



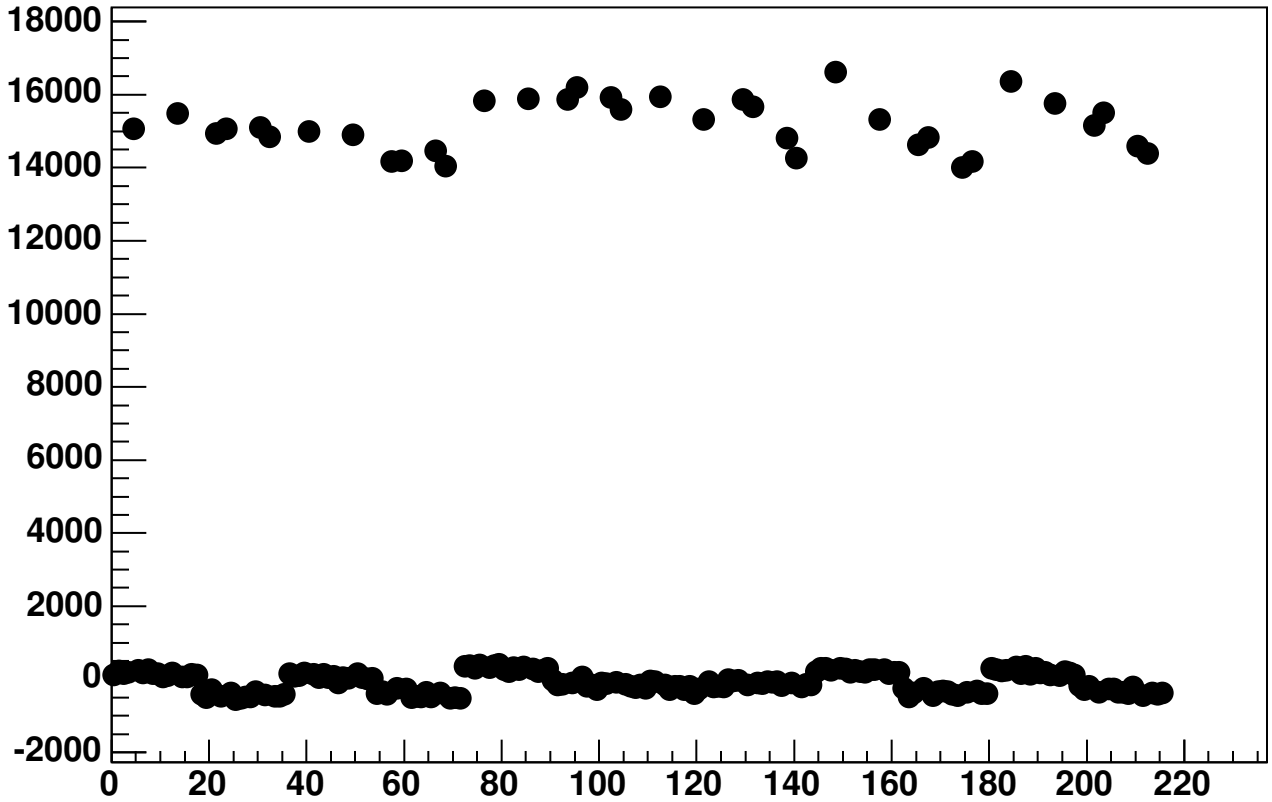
Enable 4, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



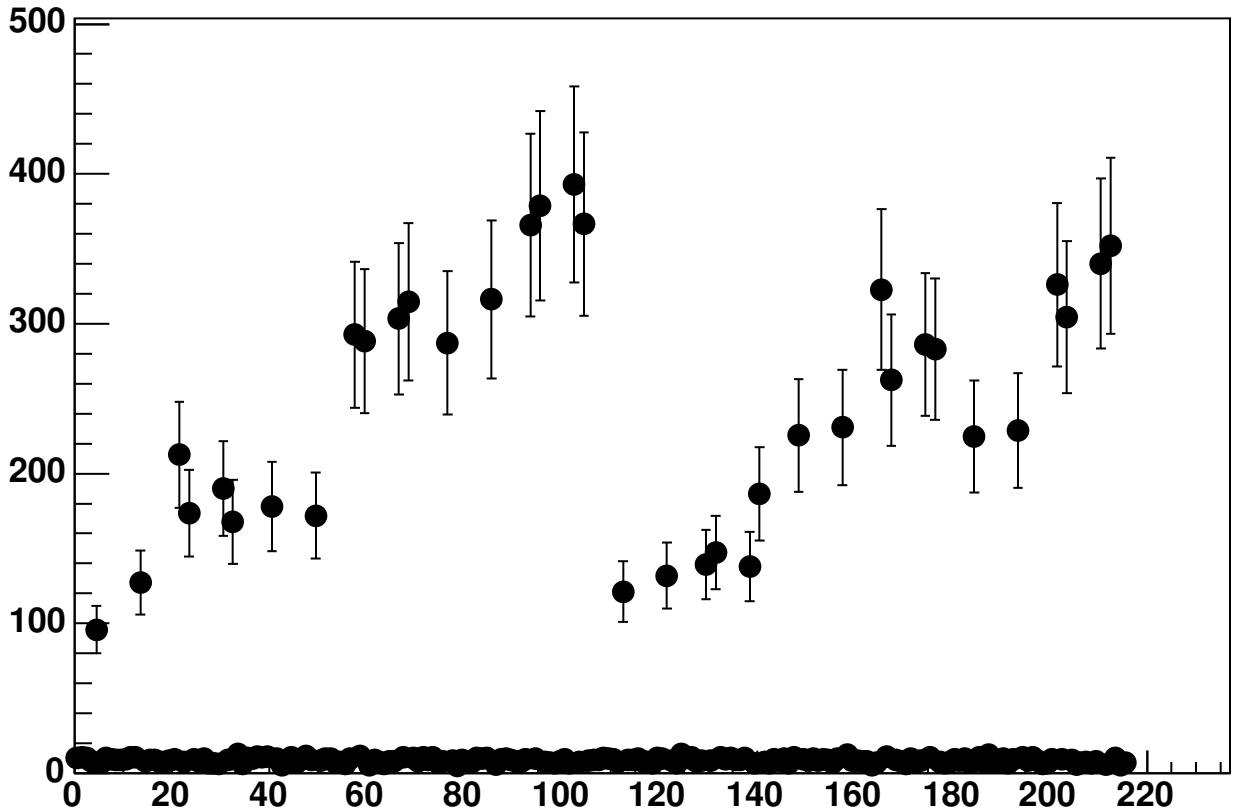
Enable 4, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



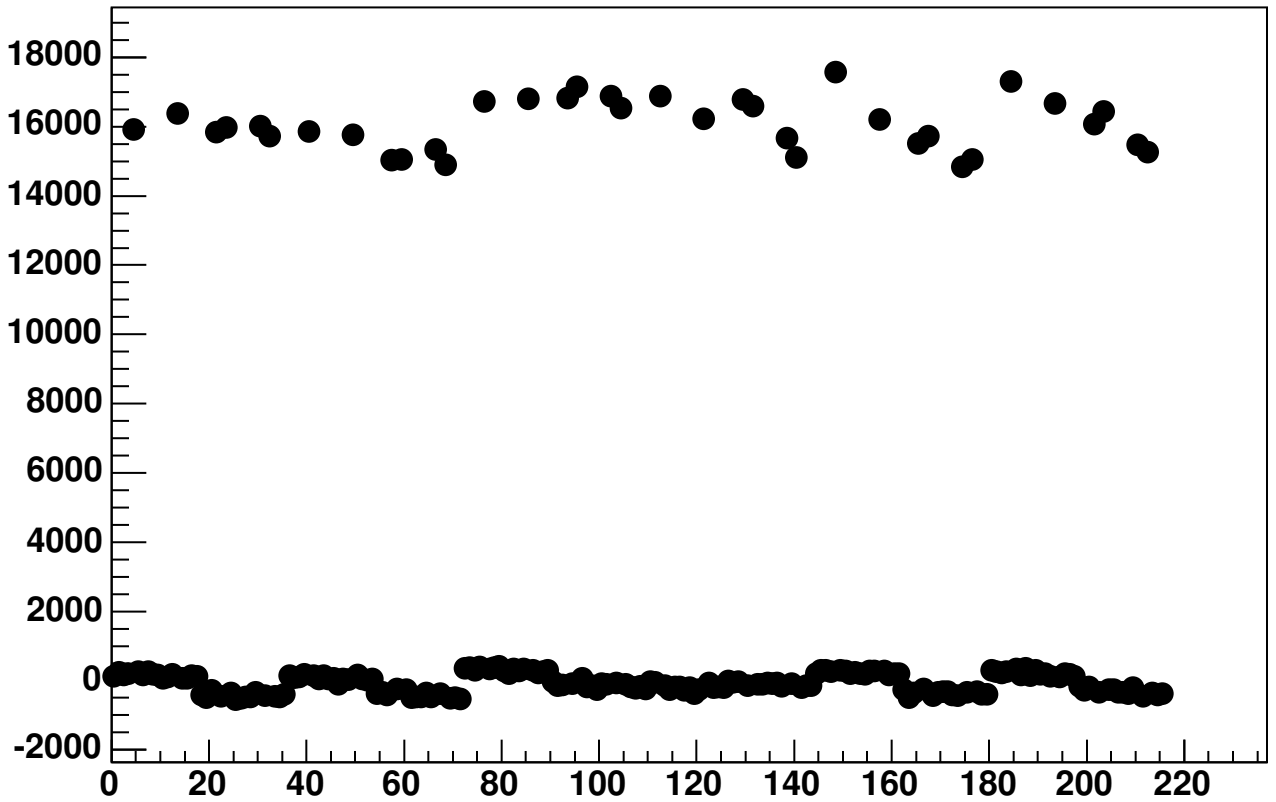
Enable 4, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



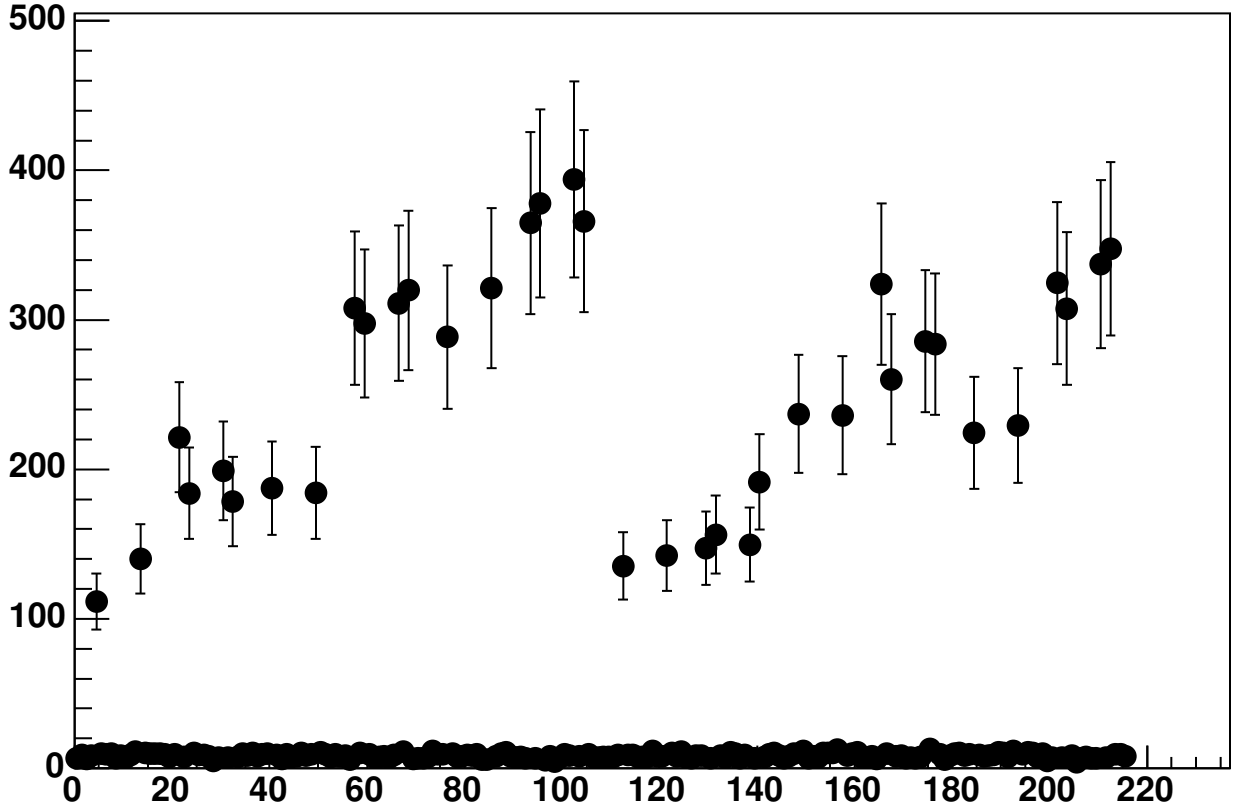
Enable 4, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



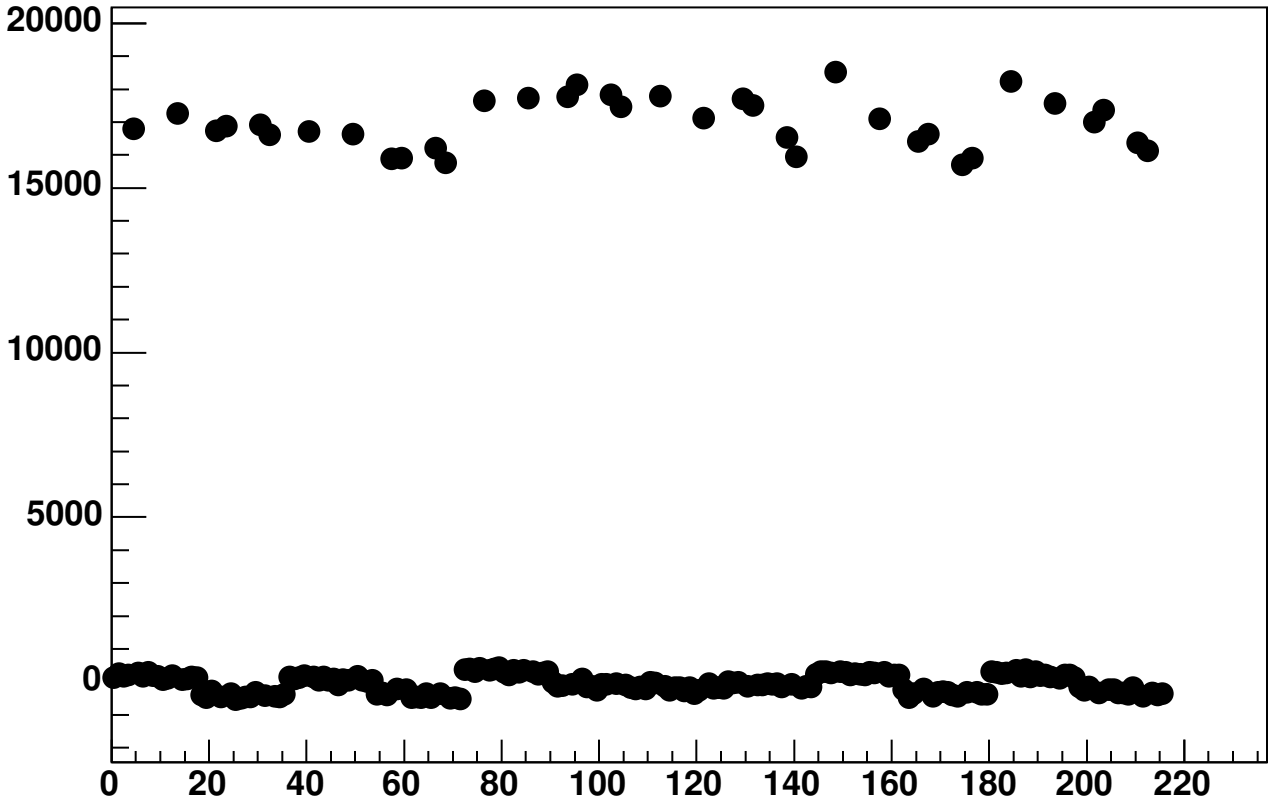
Enable 4, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



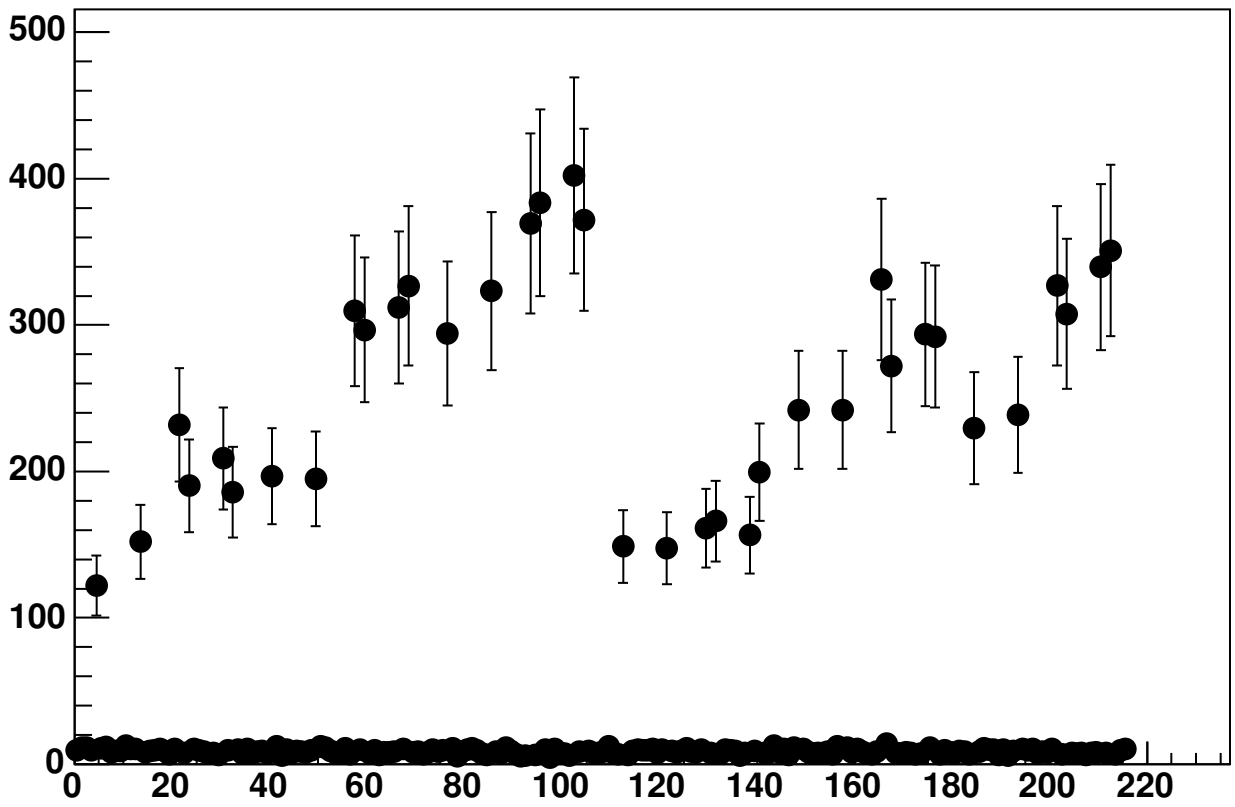
Enable 4, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



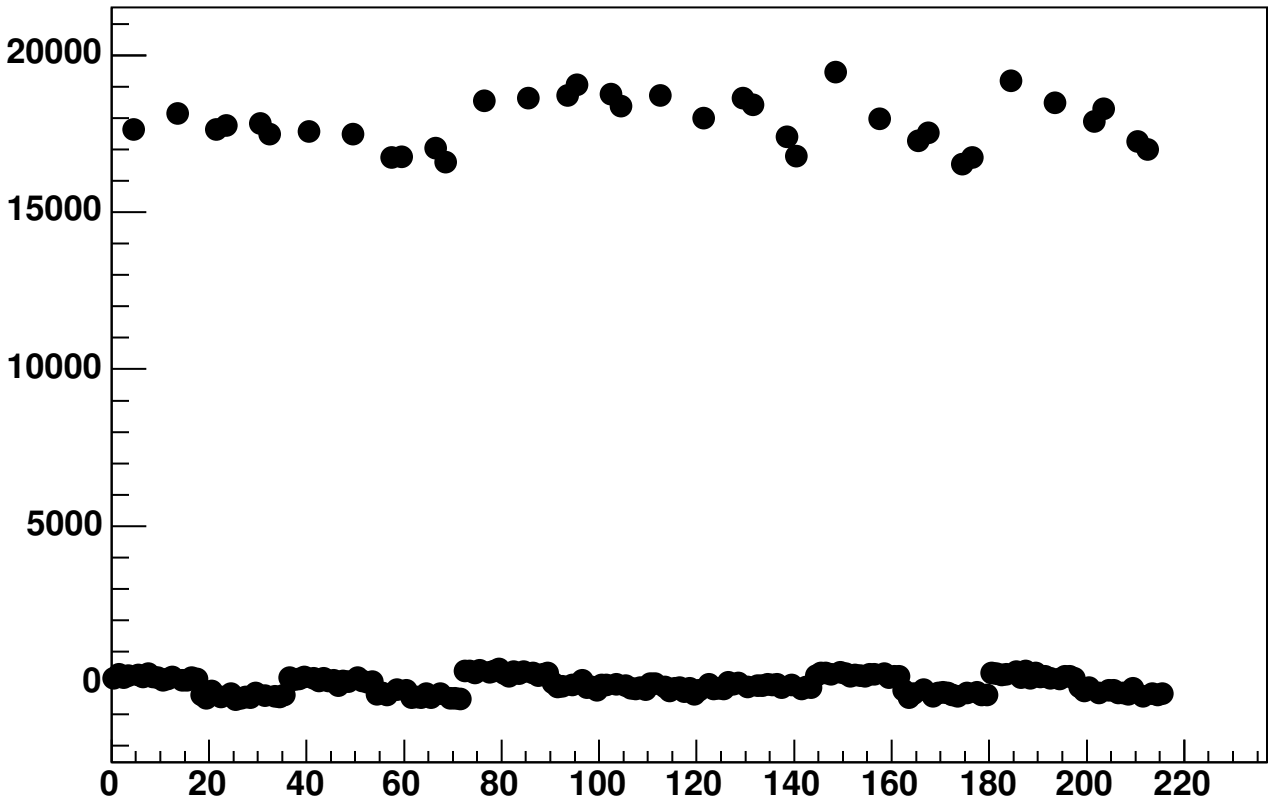
Enable 4, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



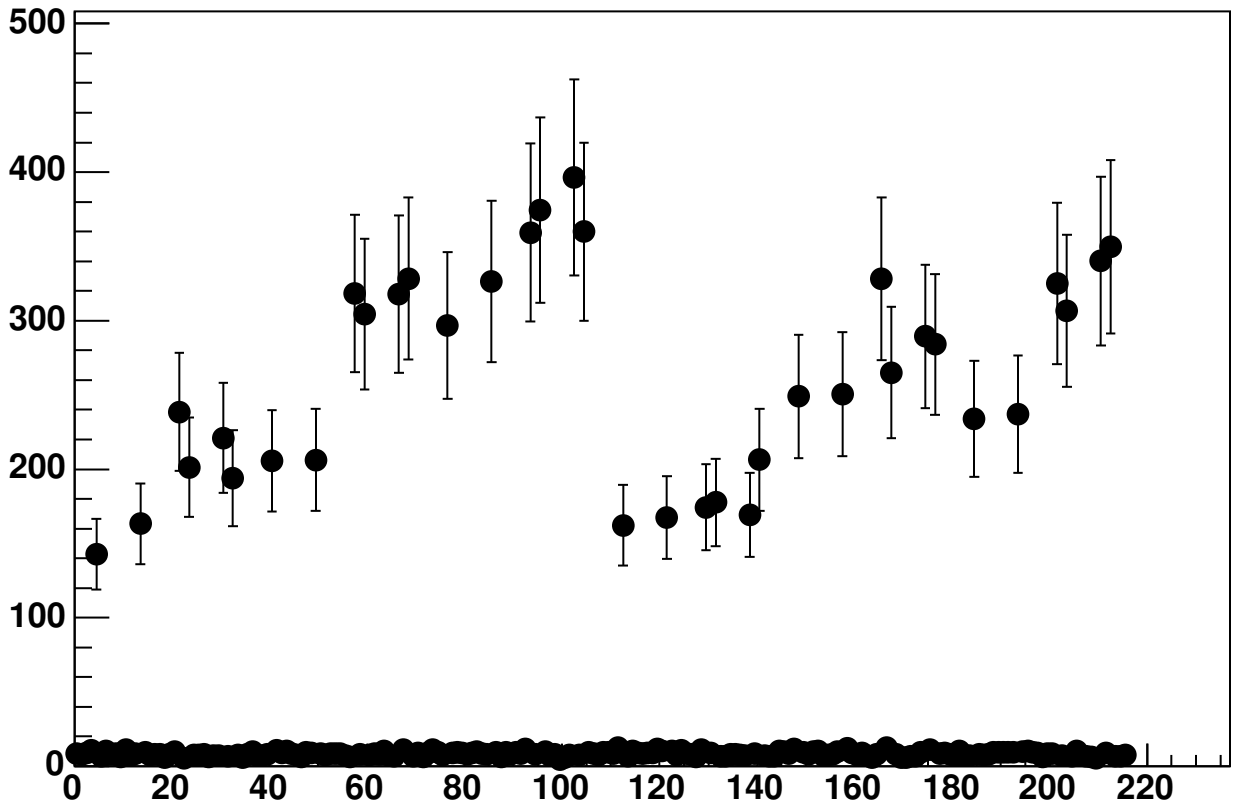
Enable 4, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



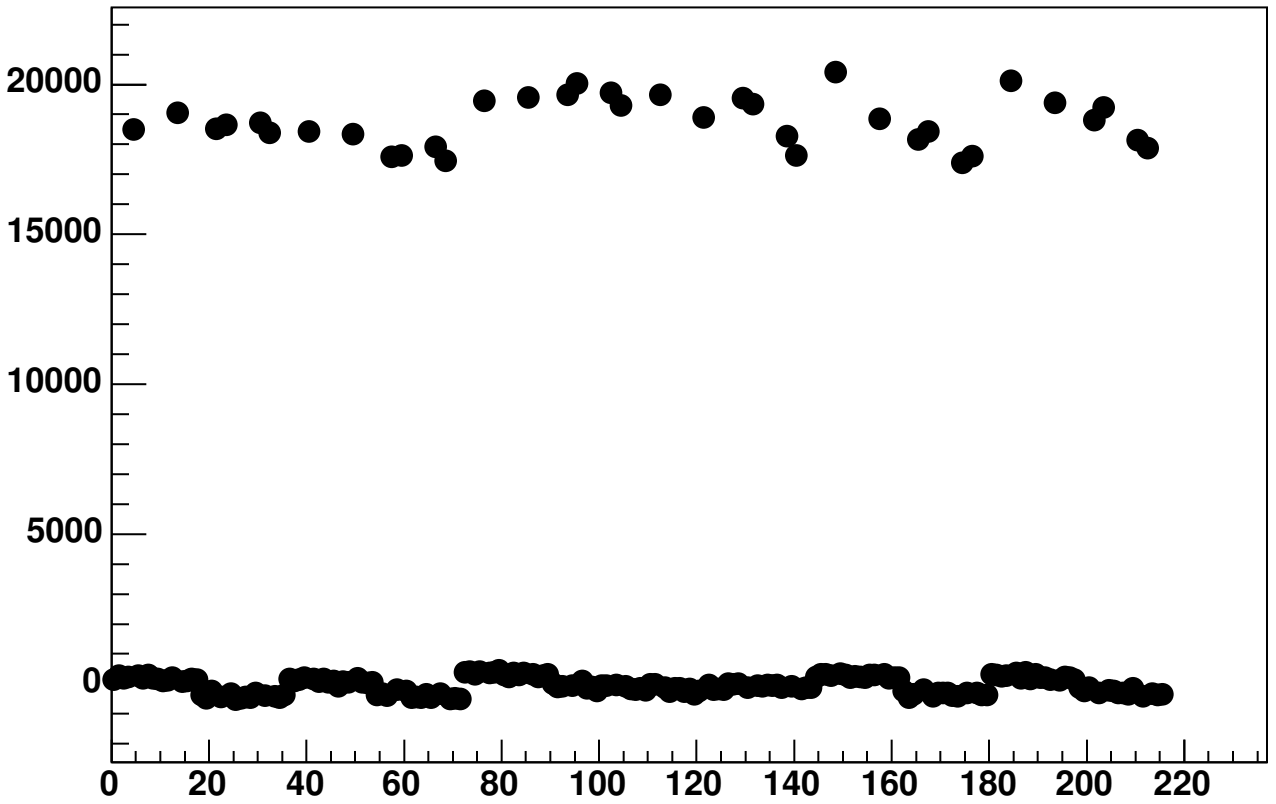
Enable 4, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



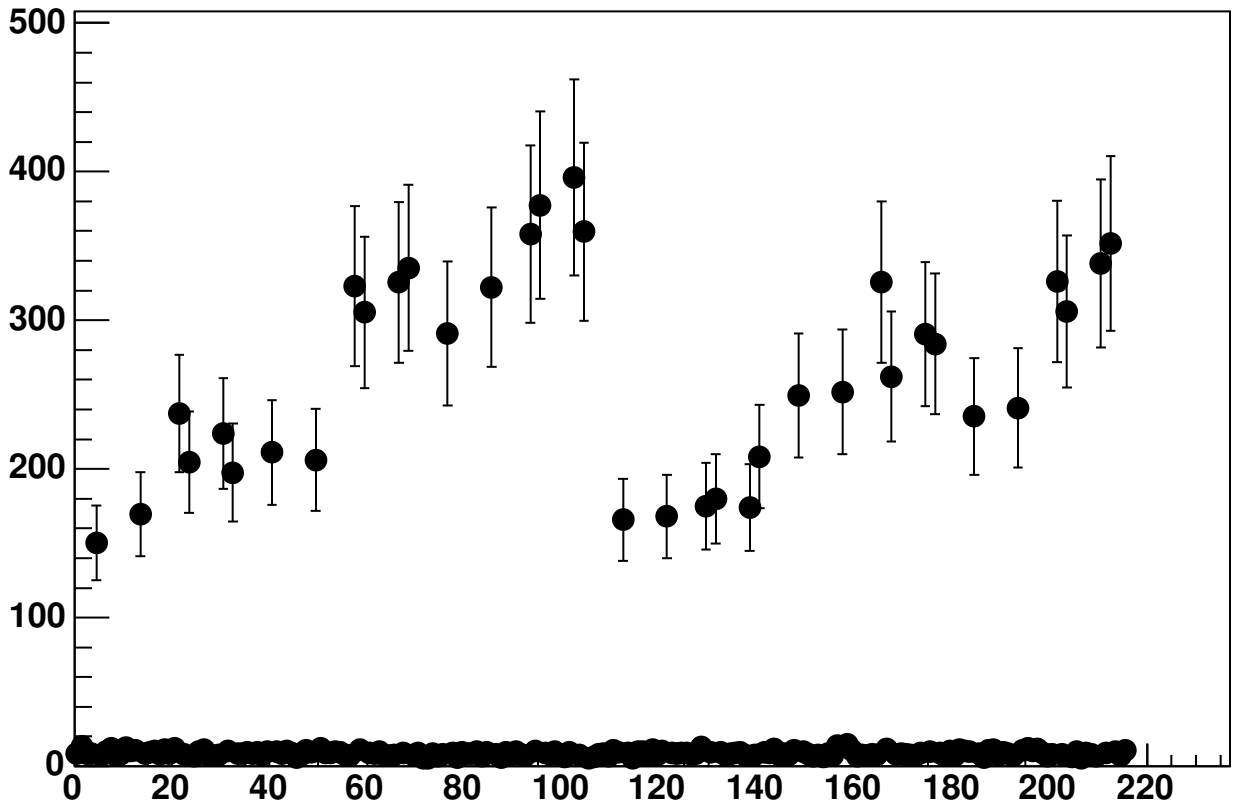
Enable 4, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



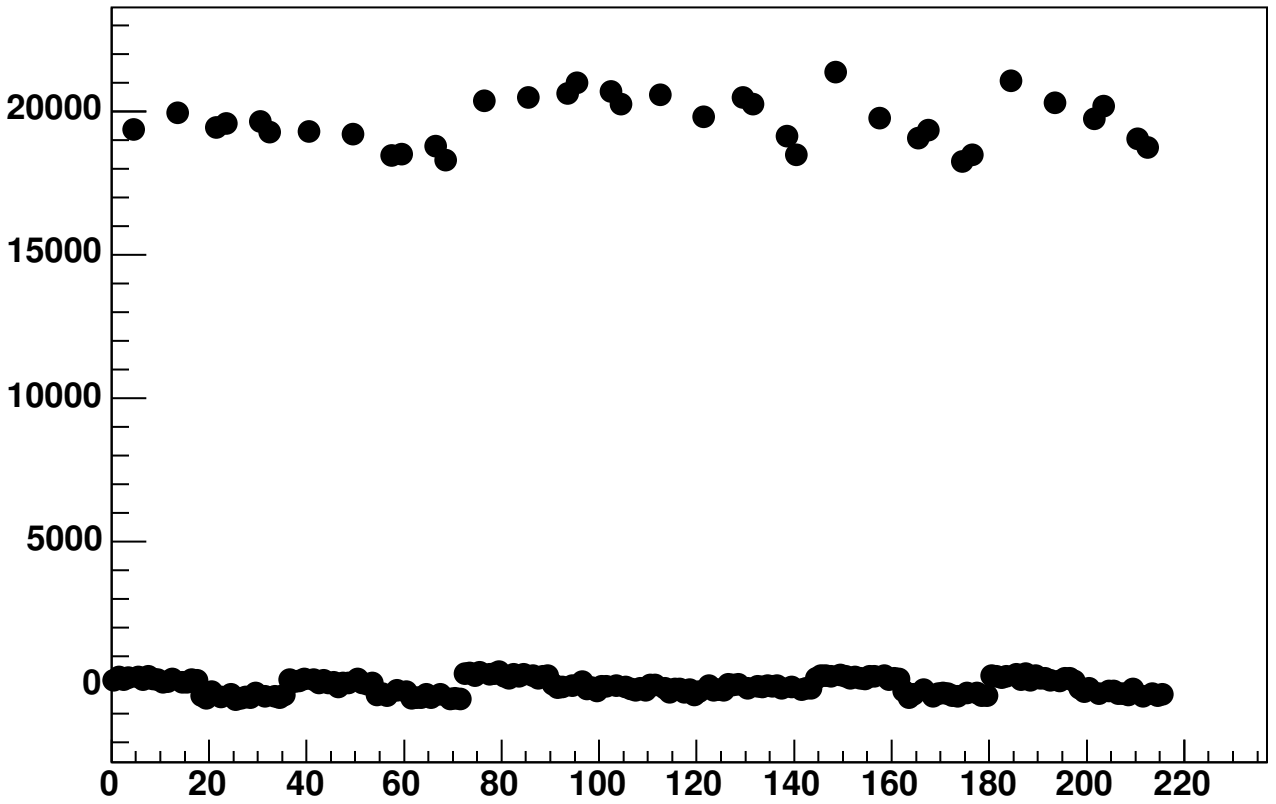
Enable 4, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



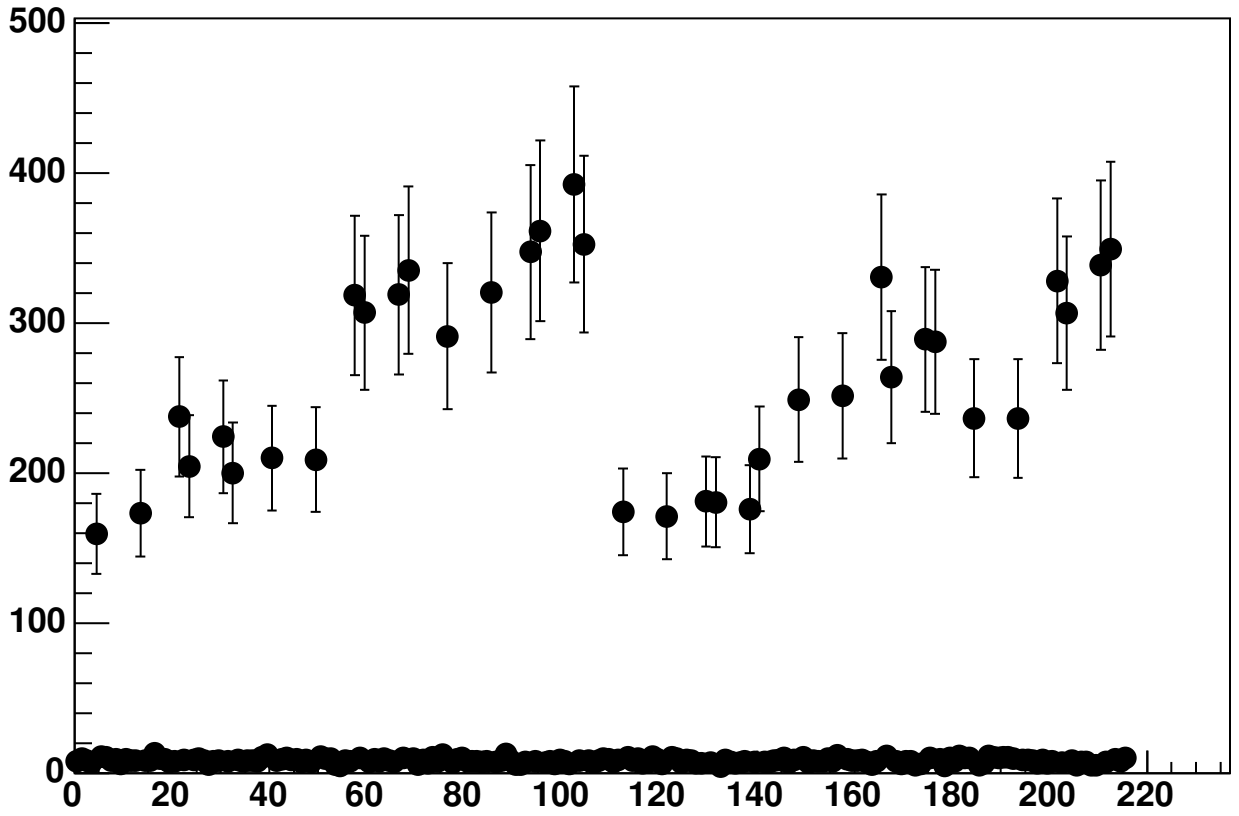
Enable 4, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



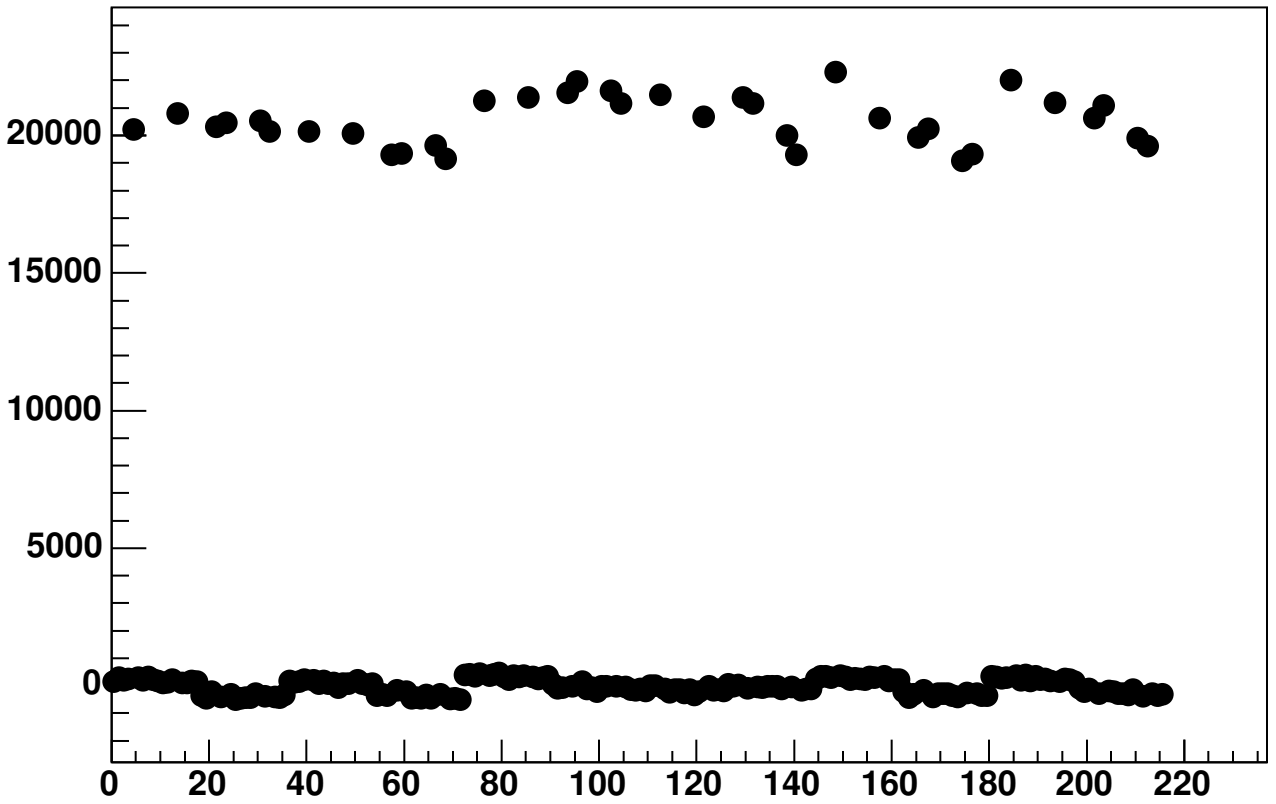
Enable 4, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



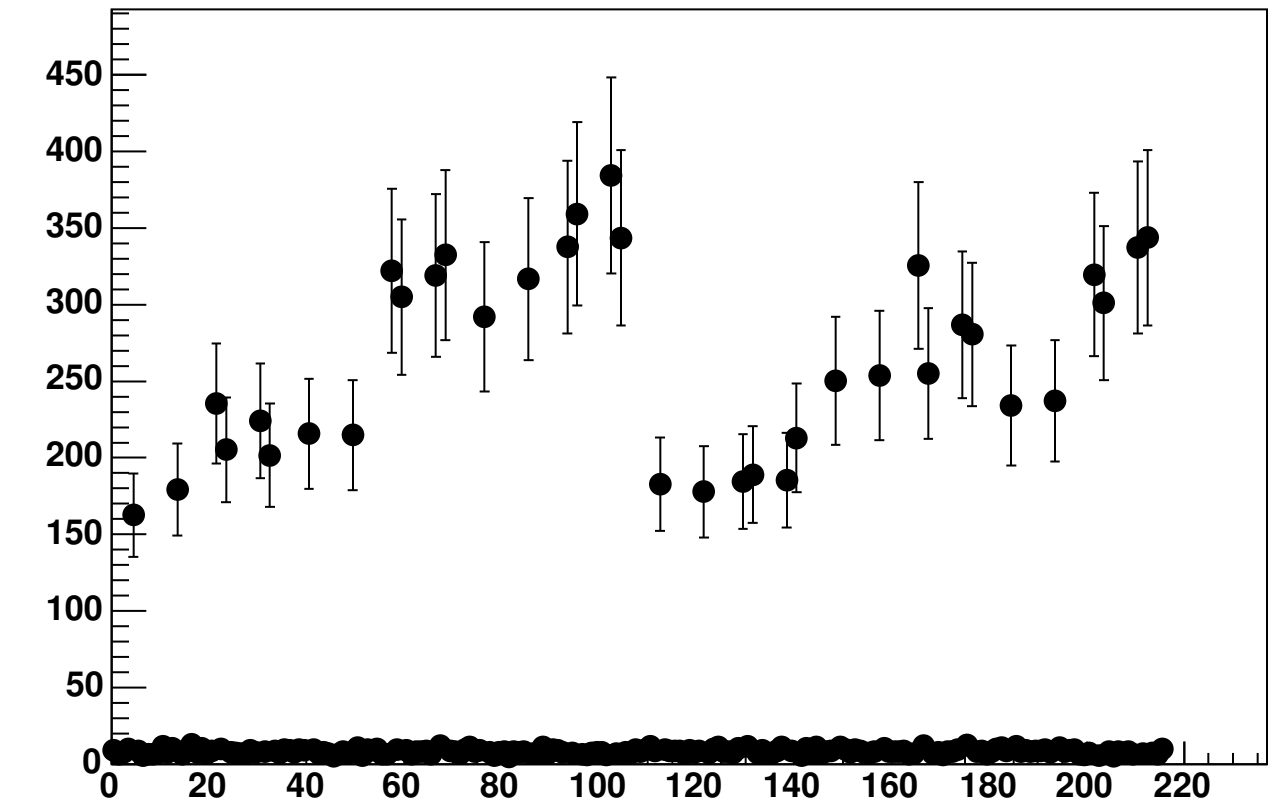
Enable 4, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



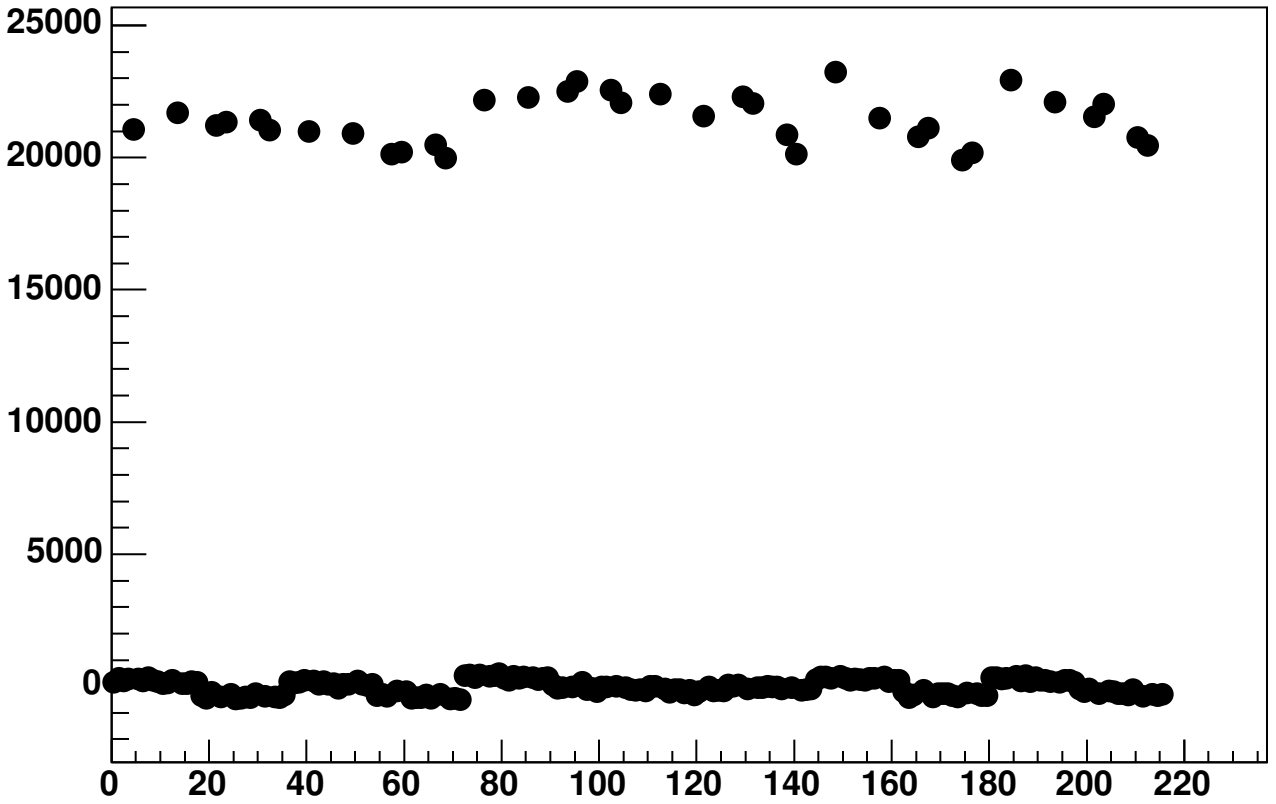
Enable 4, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



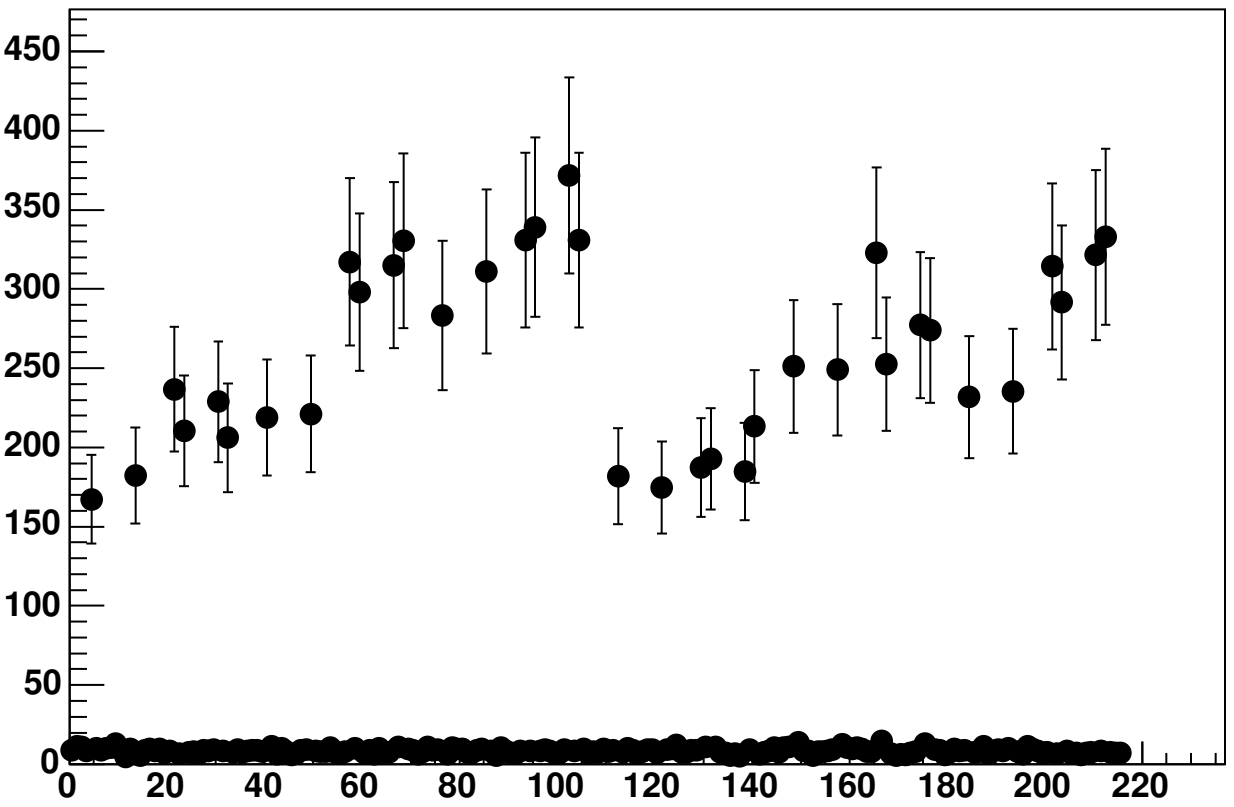
Enable 4, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



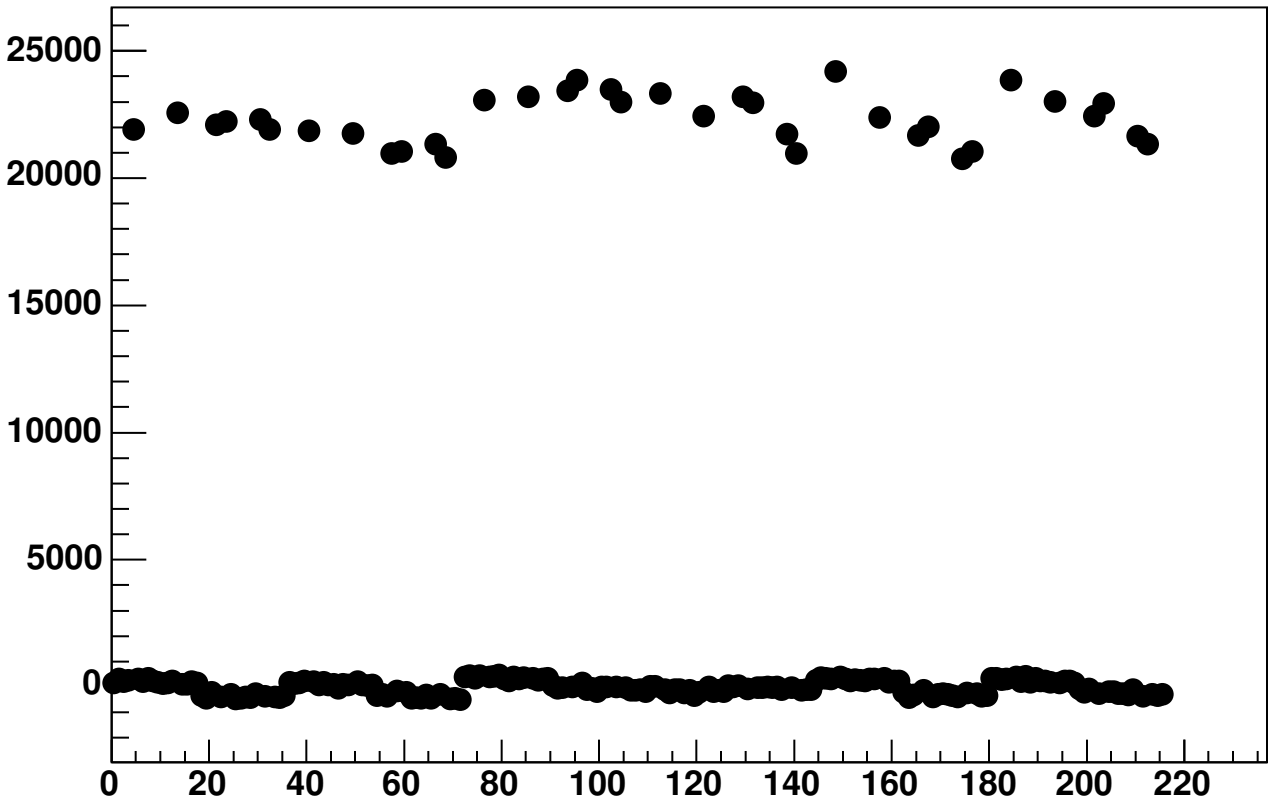
Enable 4, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



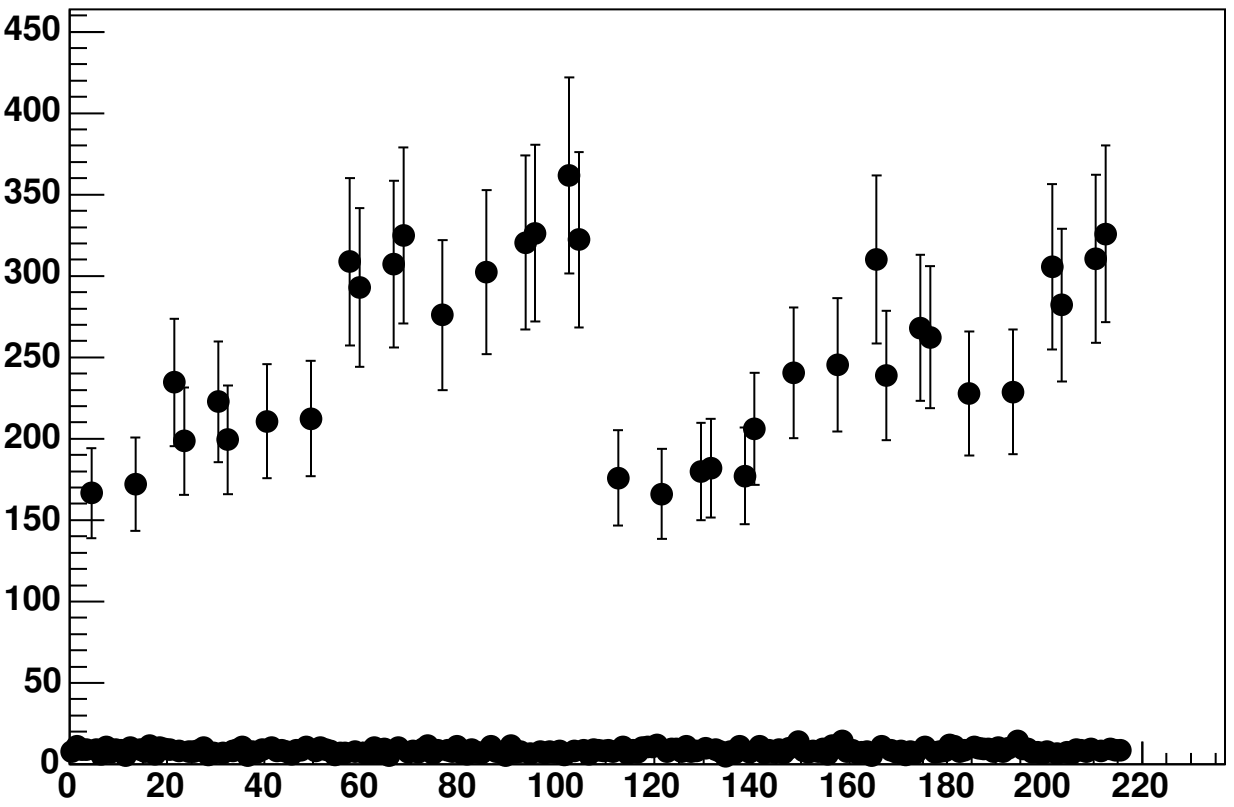
Enable 4, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



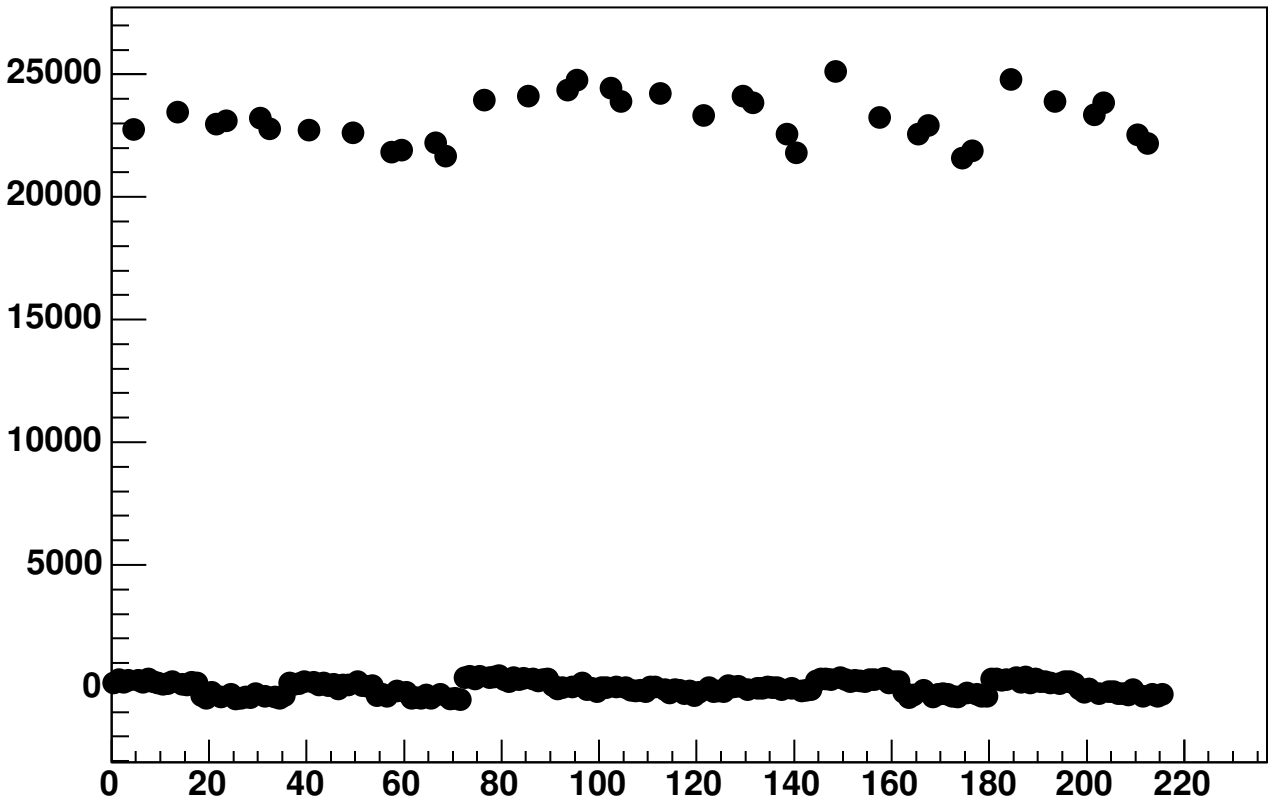
Enable 4, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



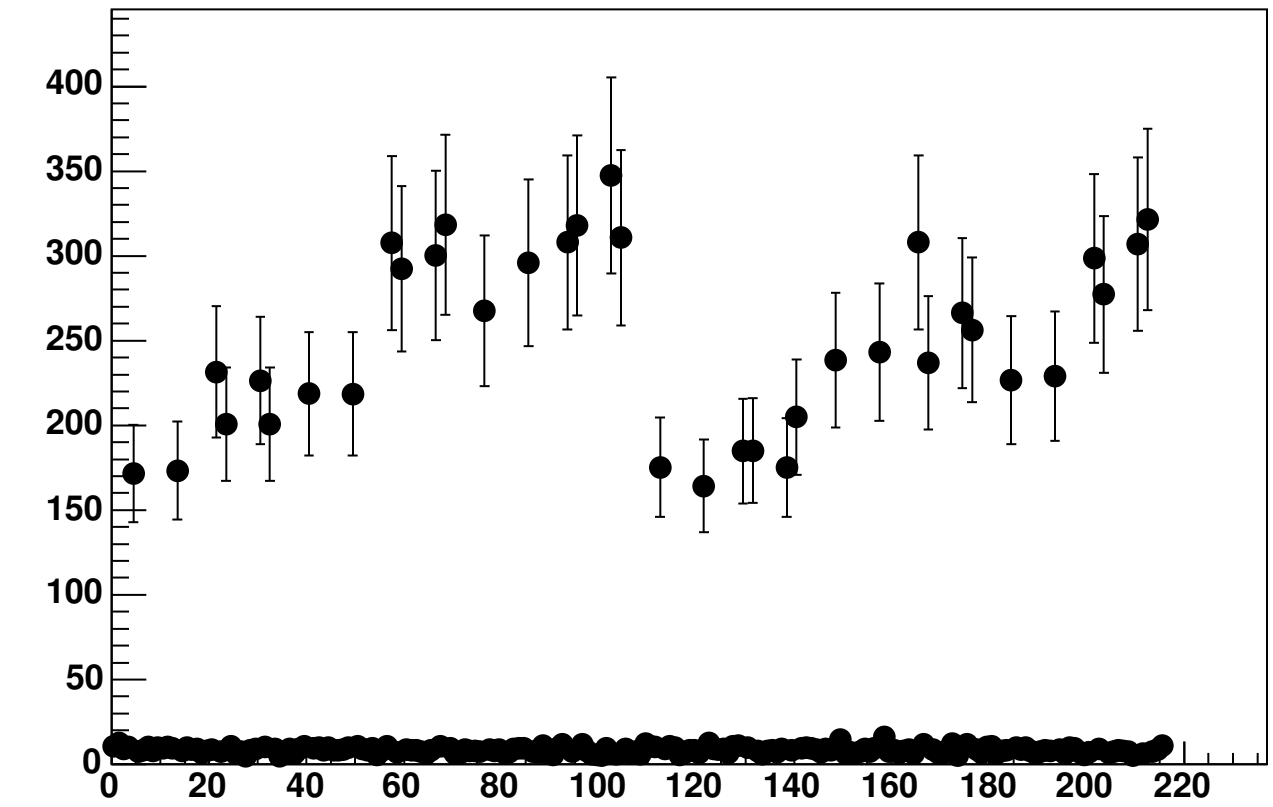
Enable 4, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



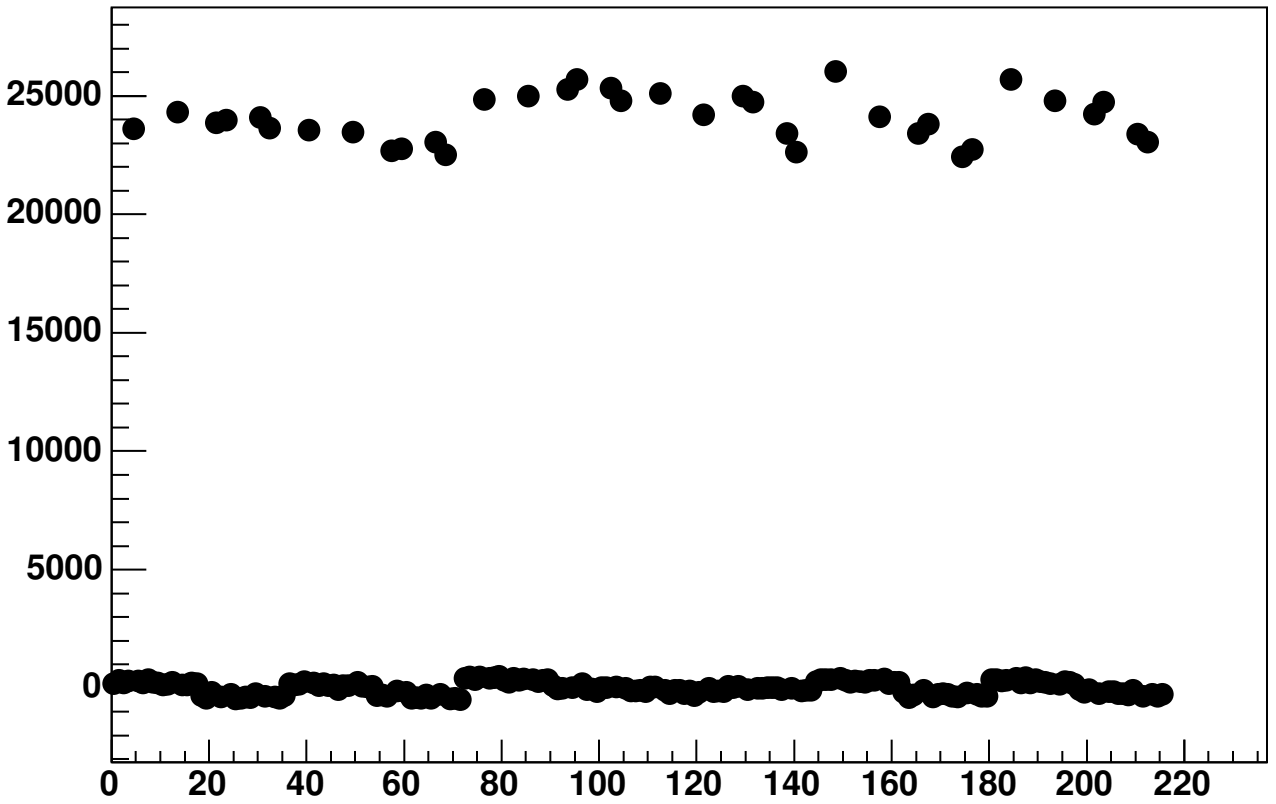
Enable 4, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



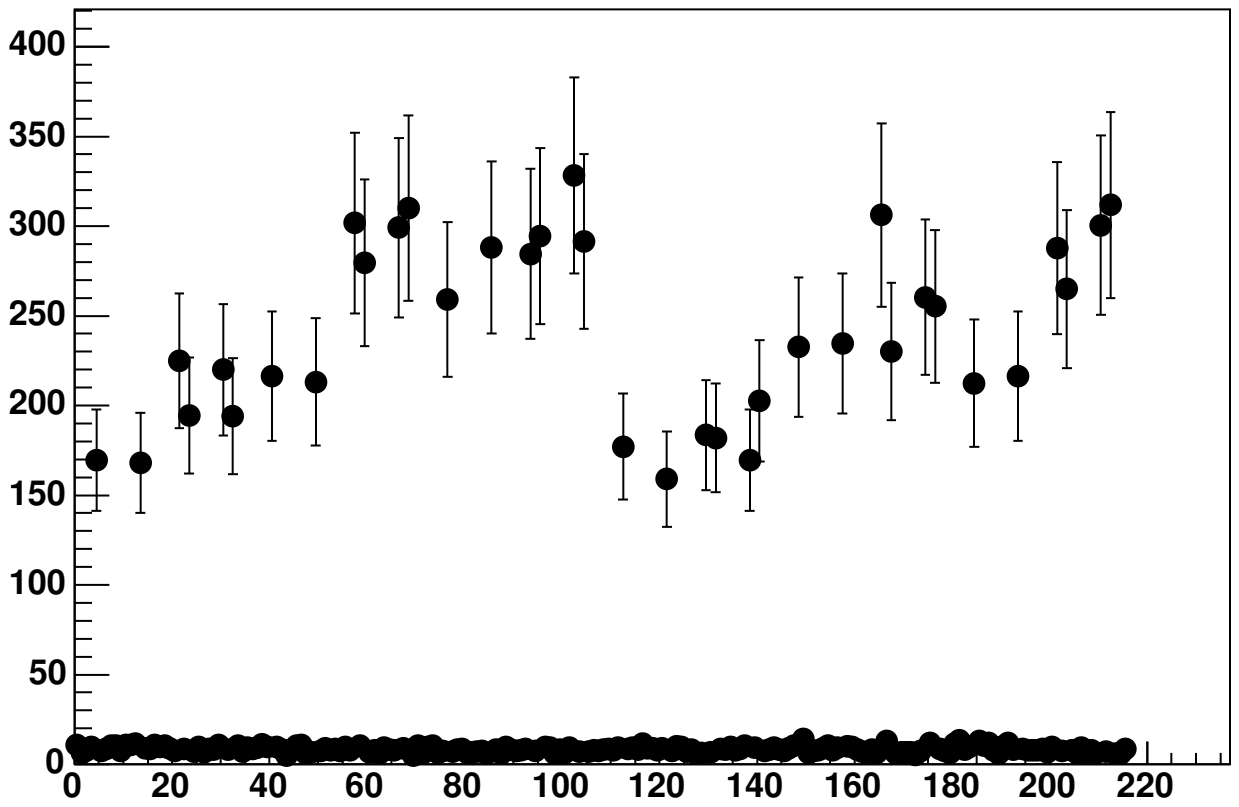
Enable 4, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



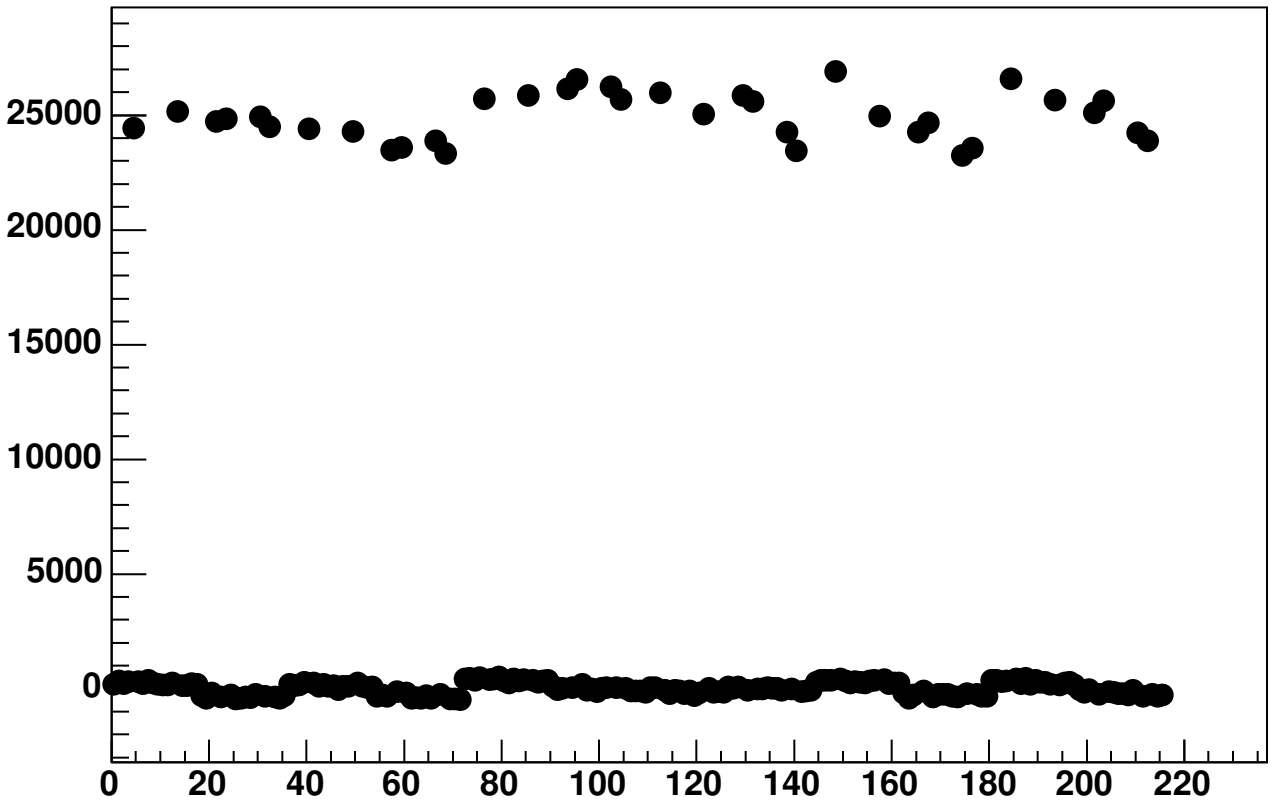
Enable 4, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



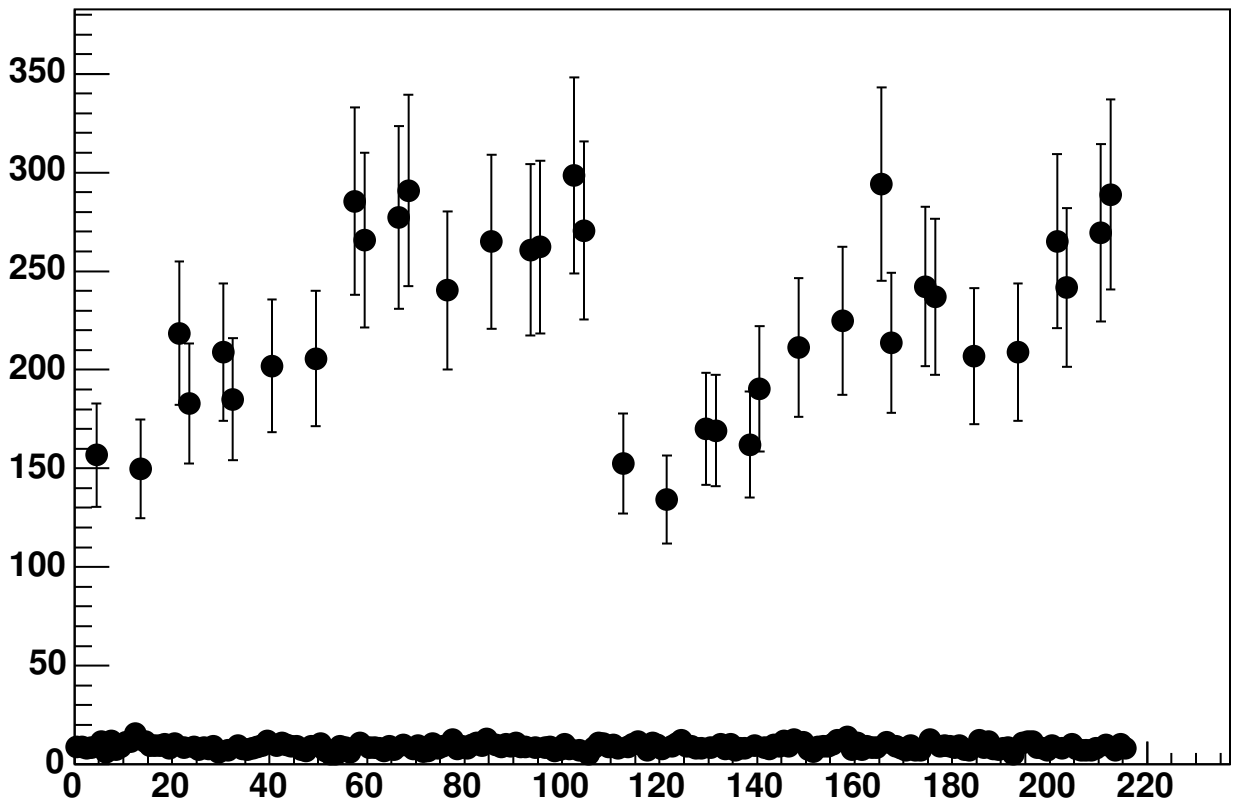
Enable 4, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



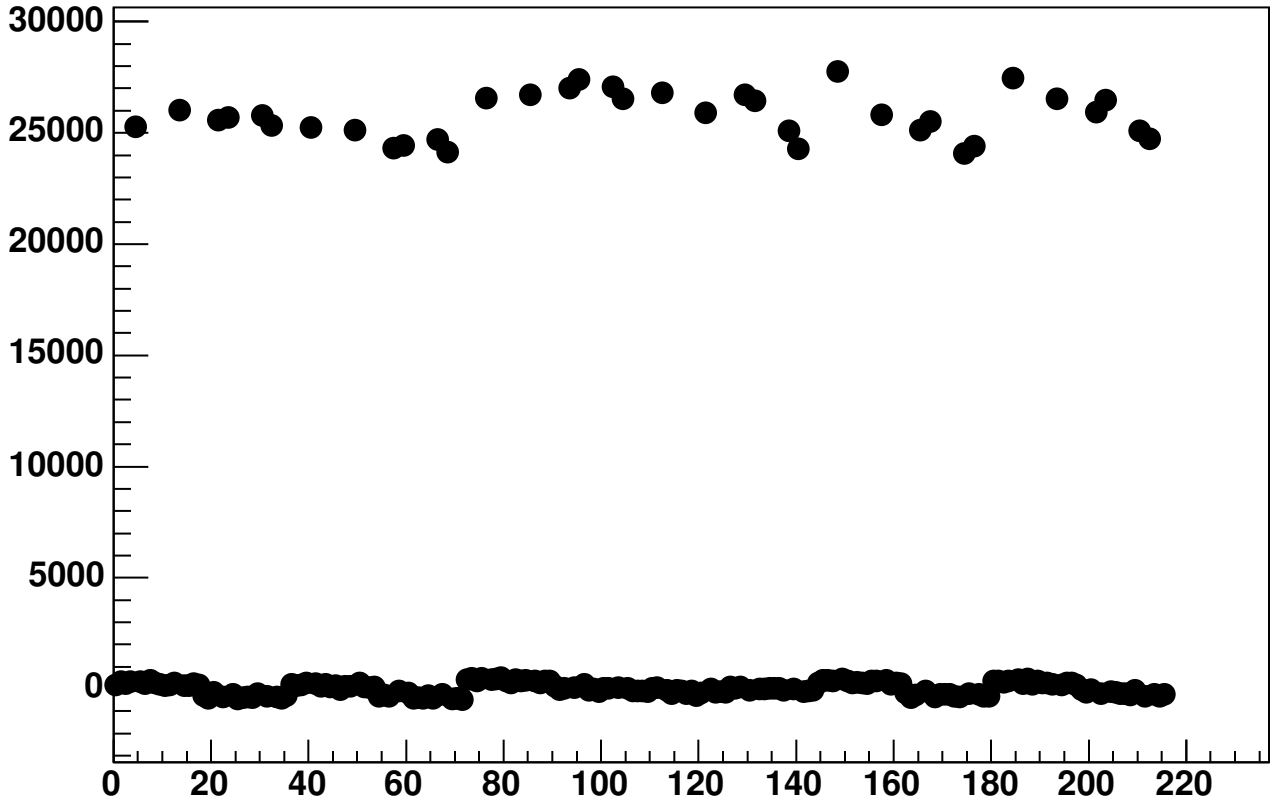
Enable 4, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



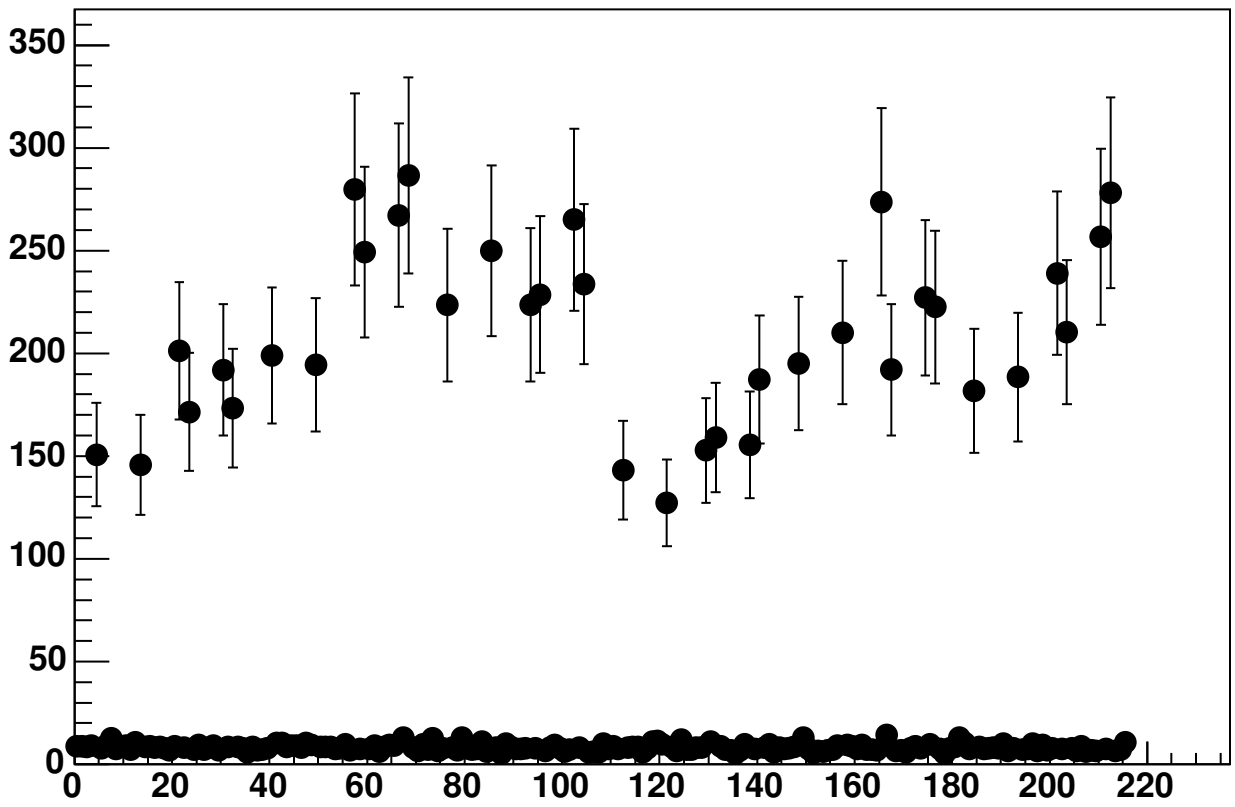
Enable 4, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



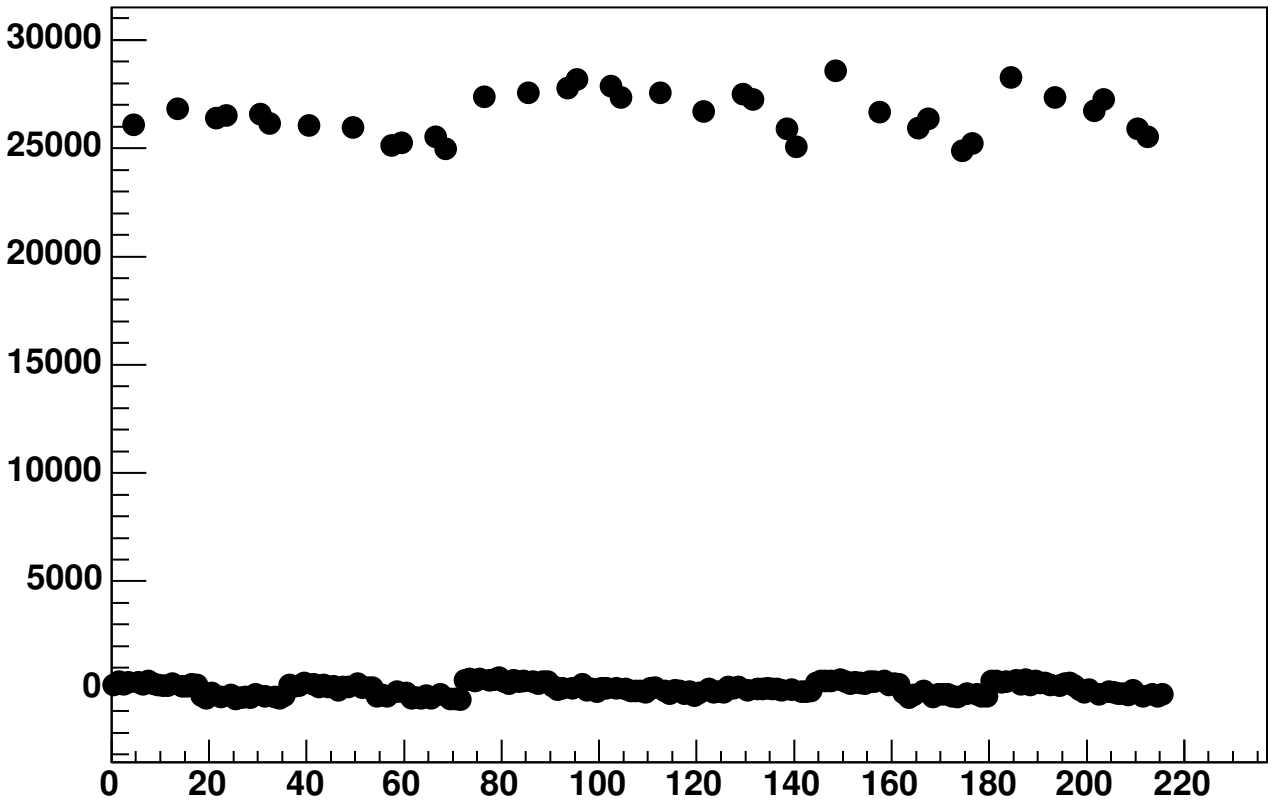
Enable 4, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



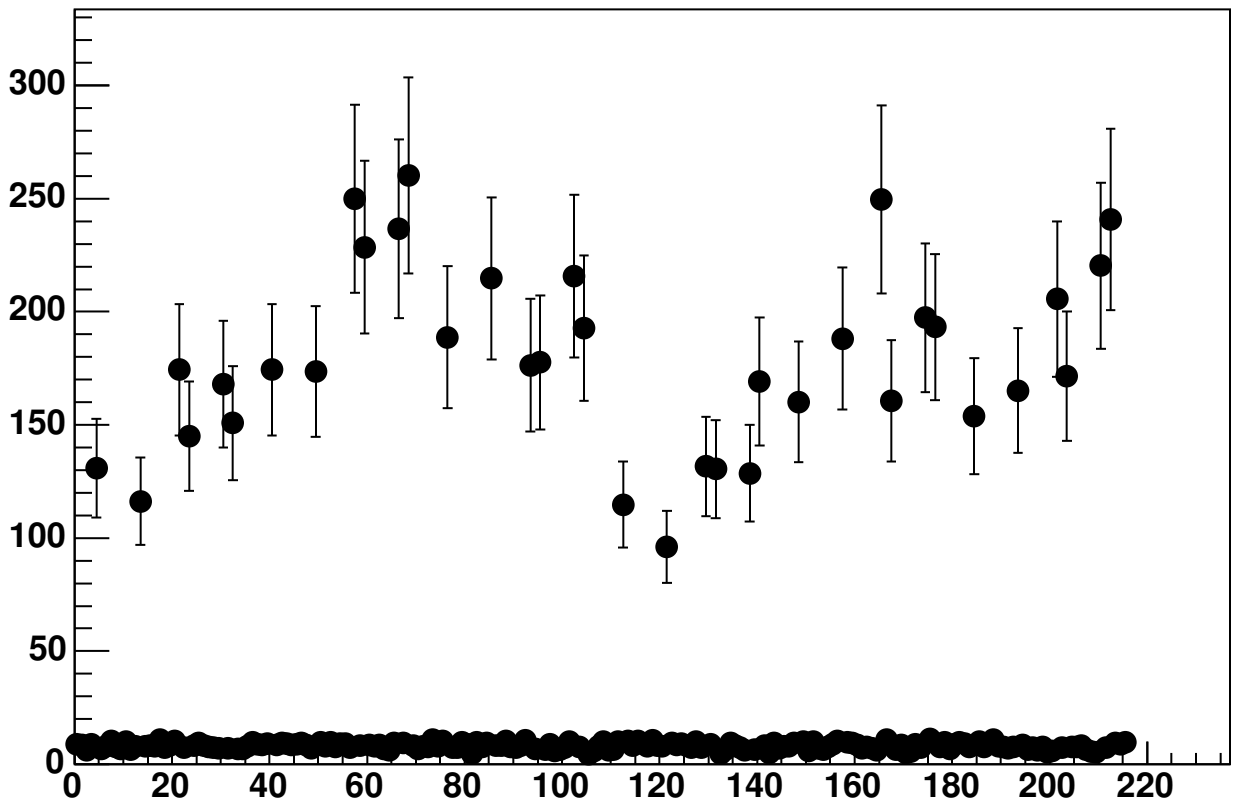
Enable 4, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



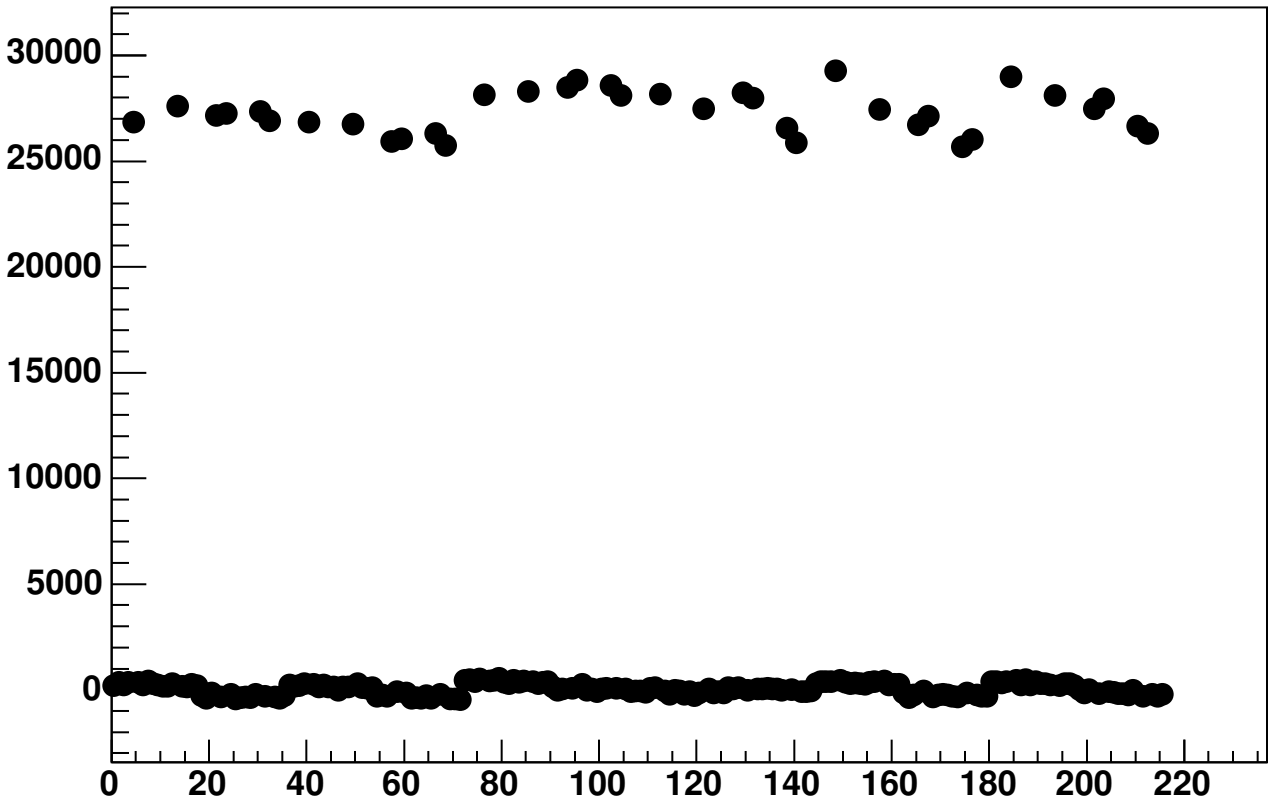
Enable 4, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



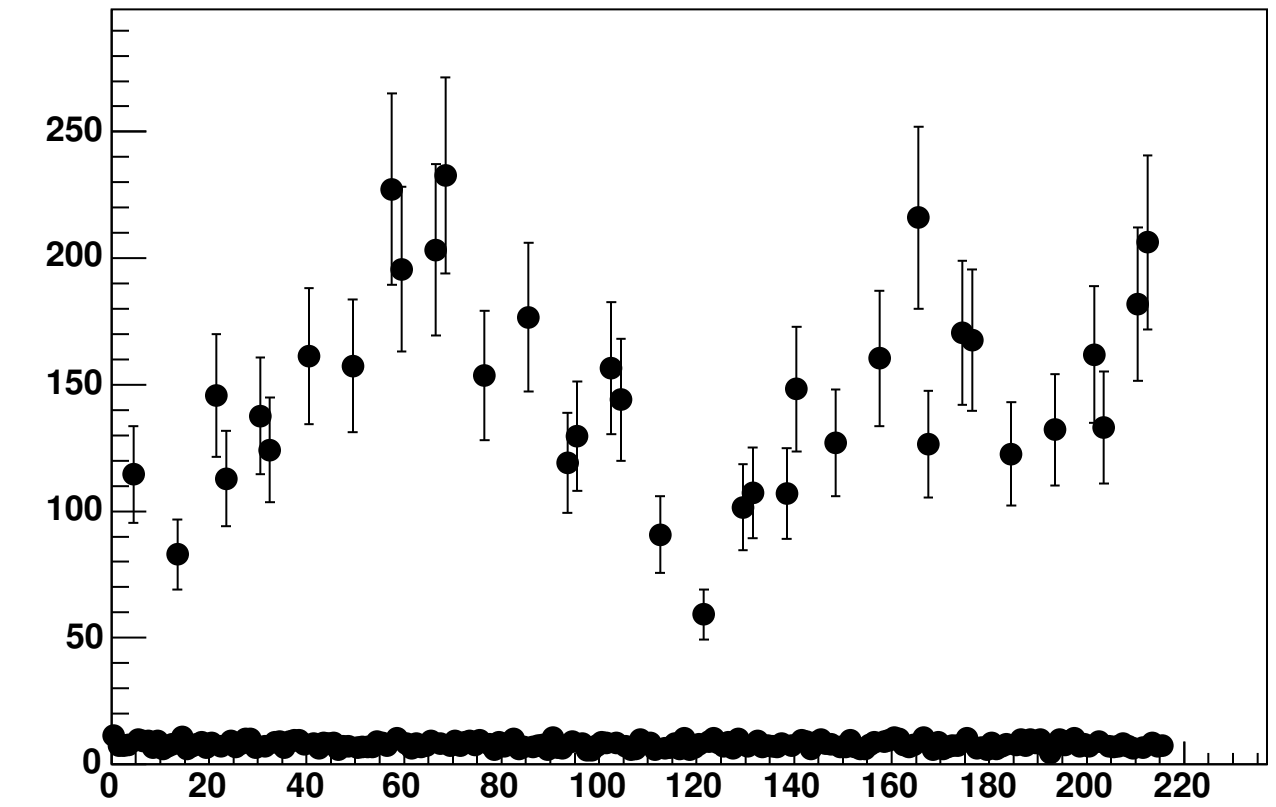
Enable 4, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



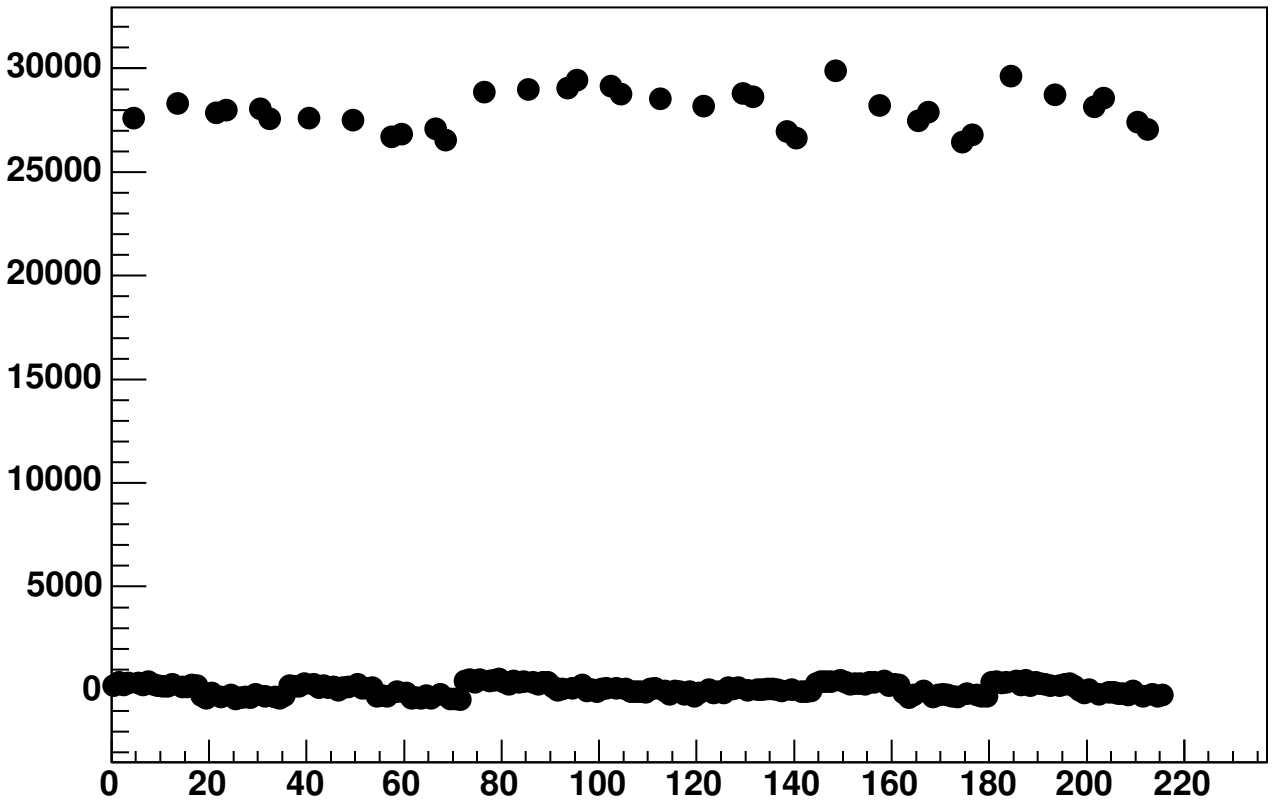
Enable 4, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



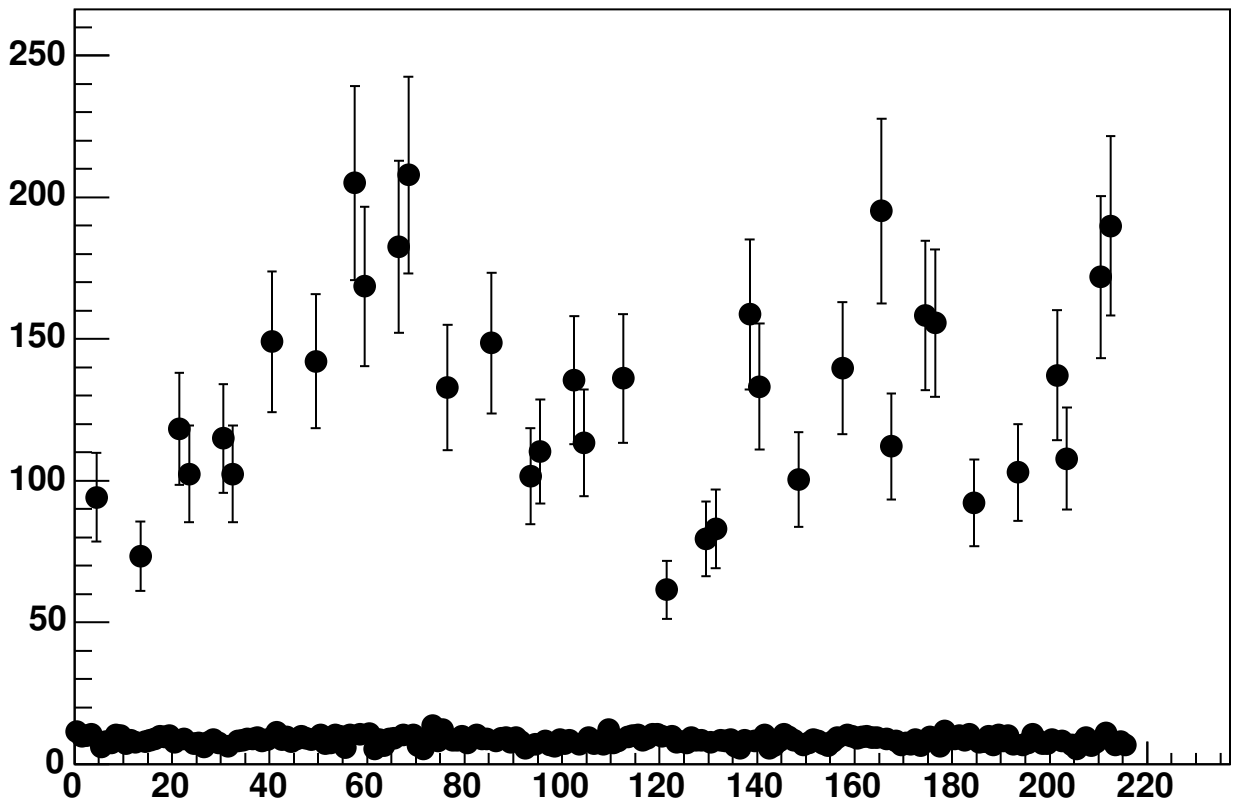
Enable 4, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



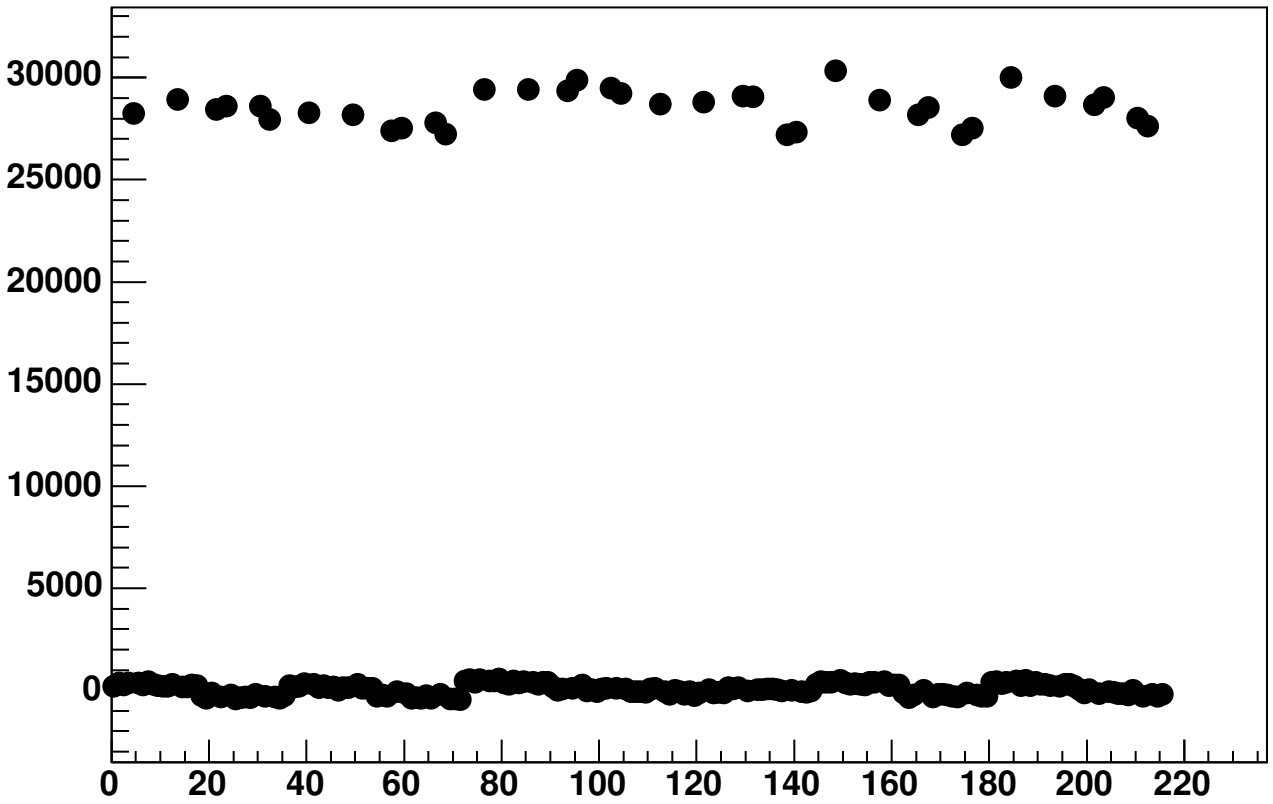
Enable 4, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



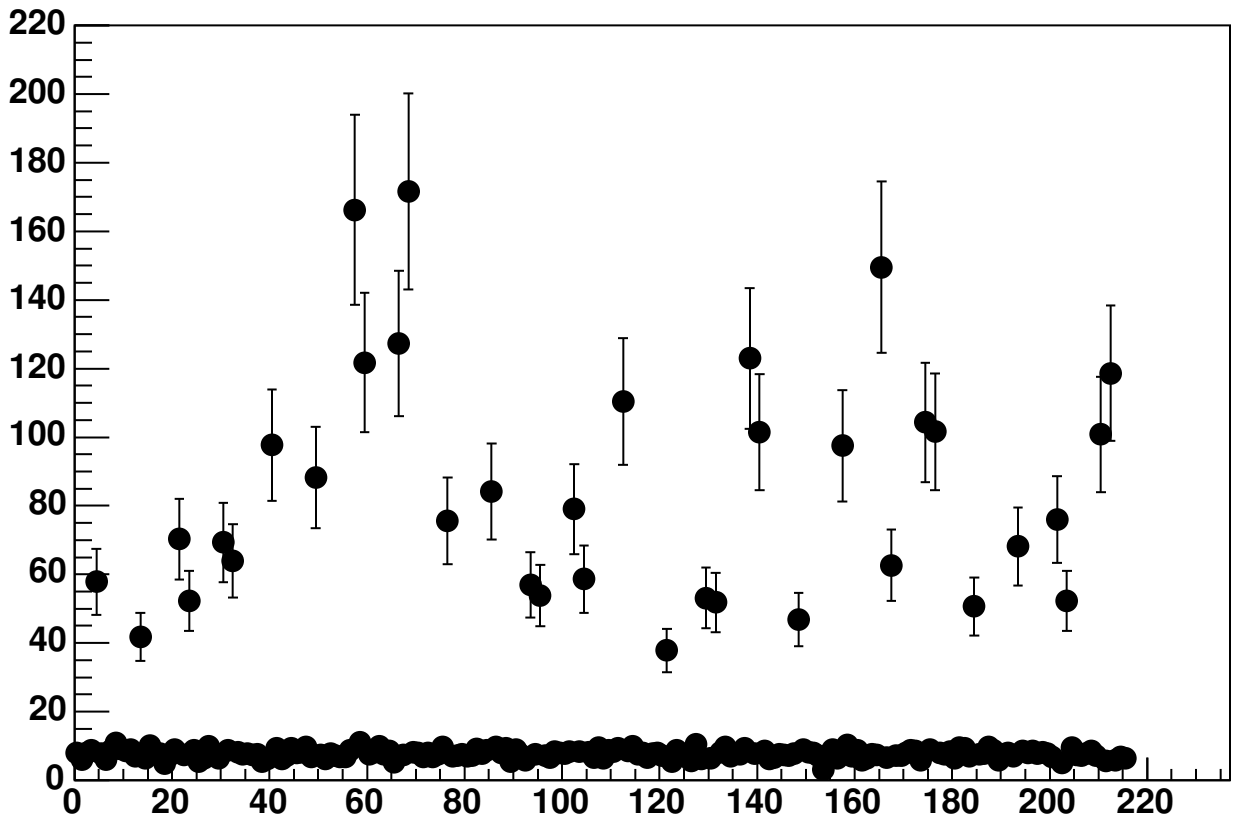
Enable 4, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan



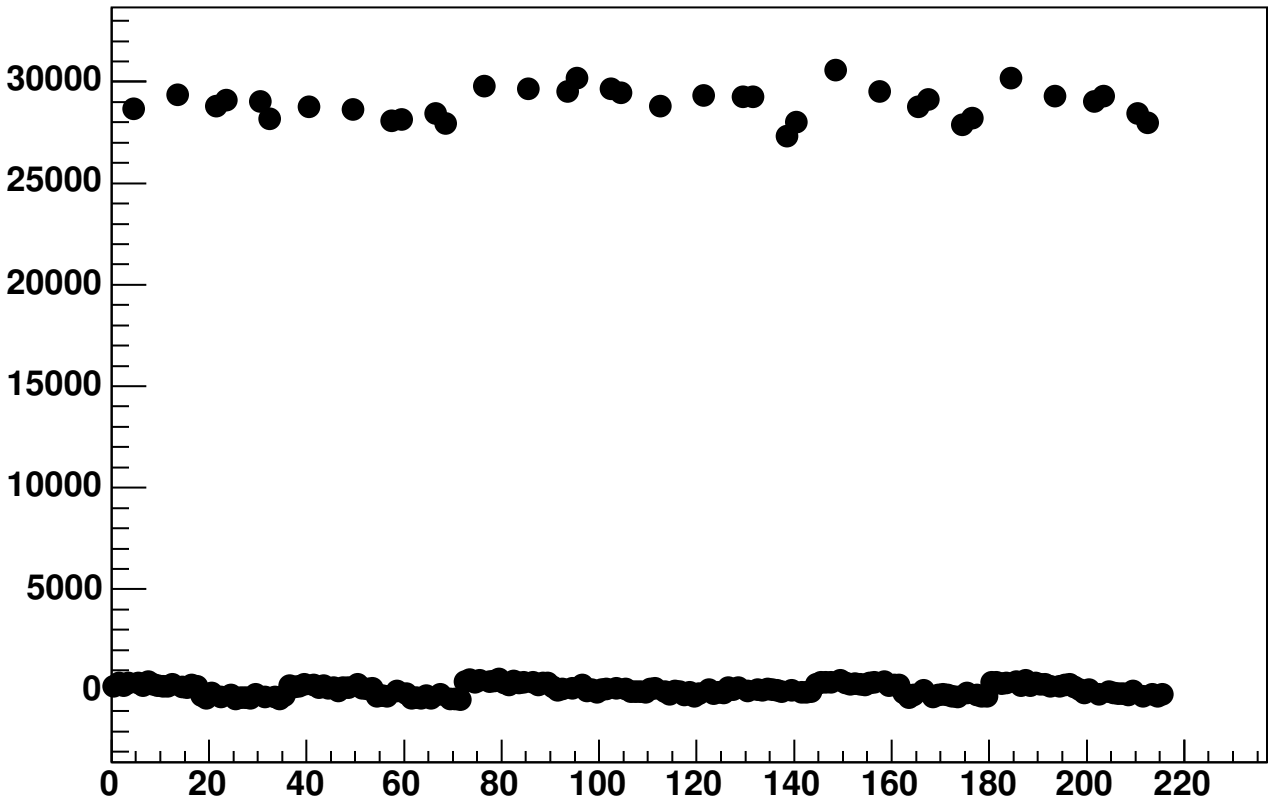
Enable 4, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan



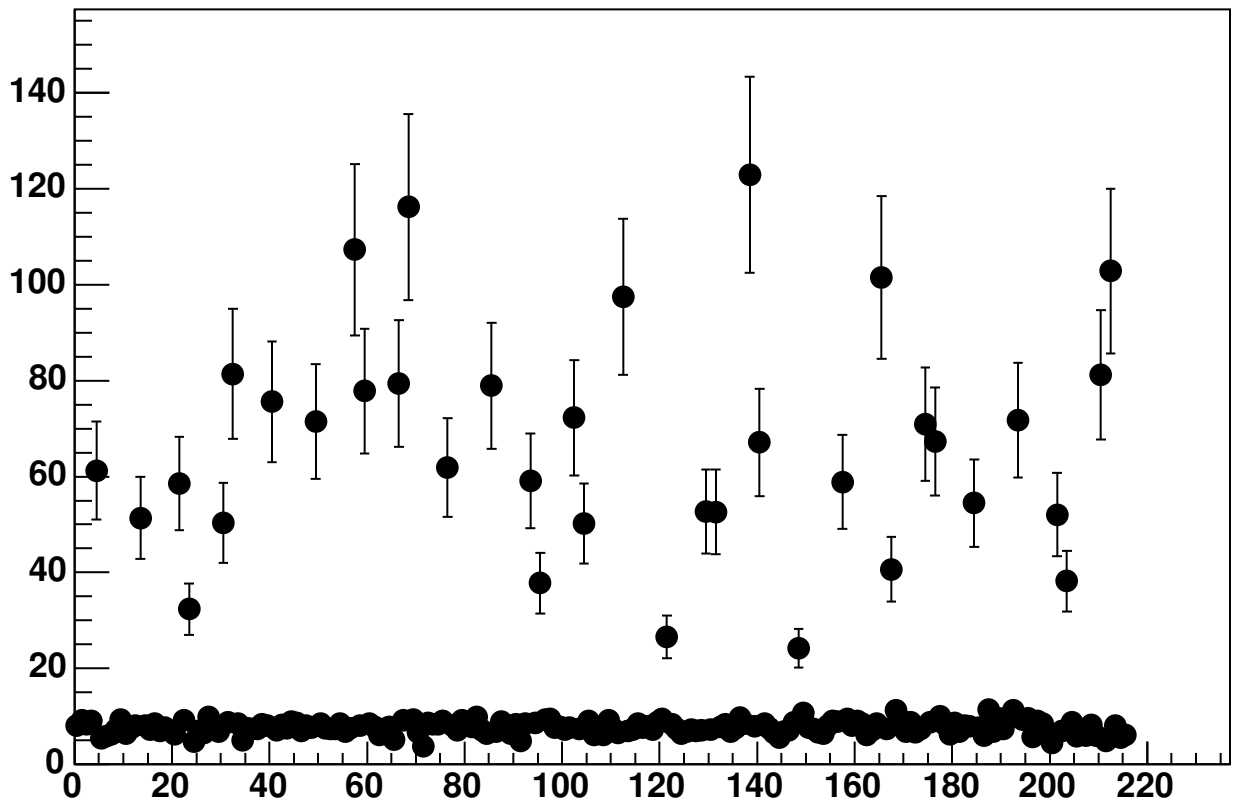
Enable 4, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan



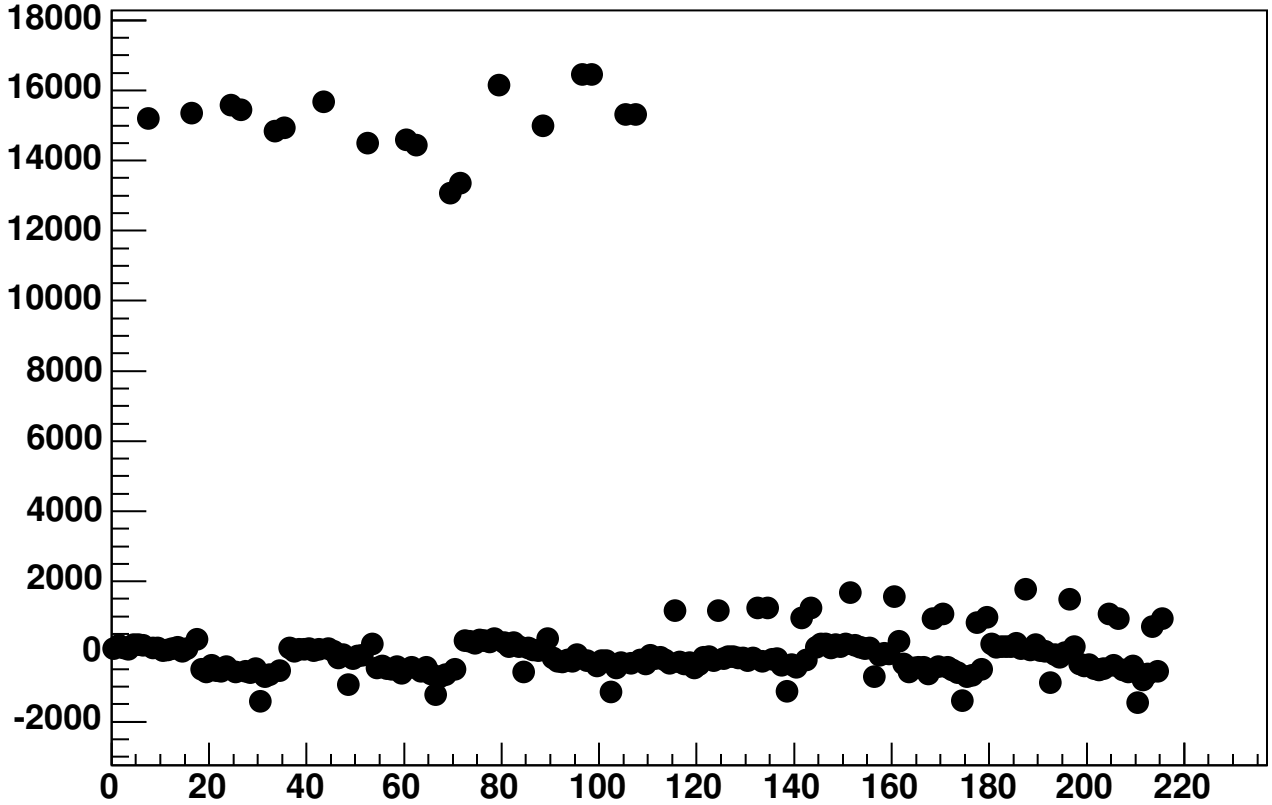
Enable 4, Hold=35, DAC=13600, ADC Mean vs 18*Chip+Chan



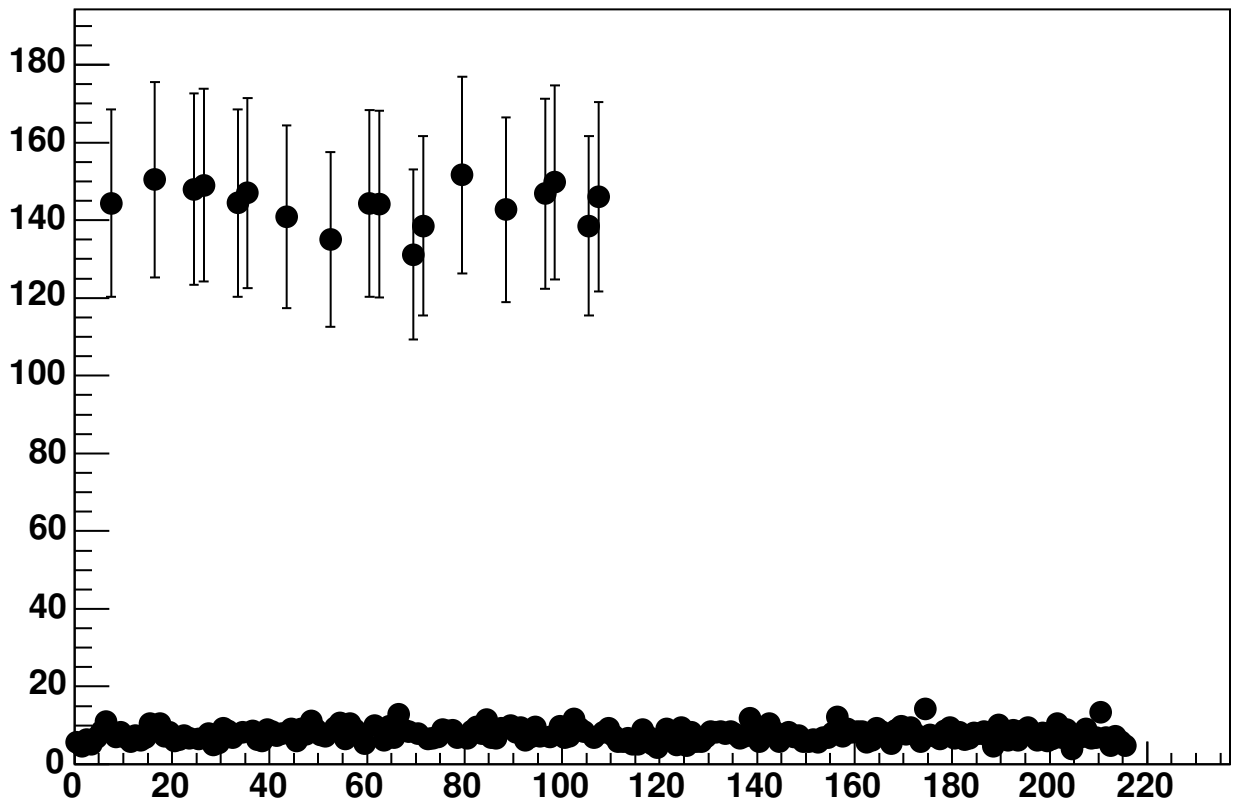
Enable 4, Hold=35, DAC=13600, ADC Noise vs 18*Chip+Chan



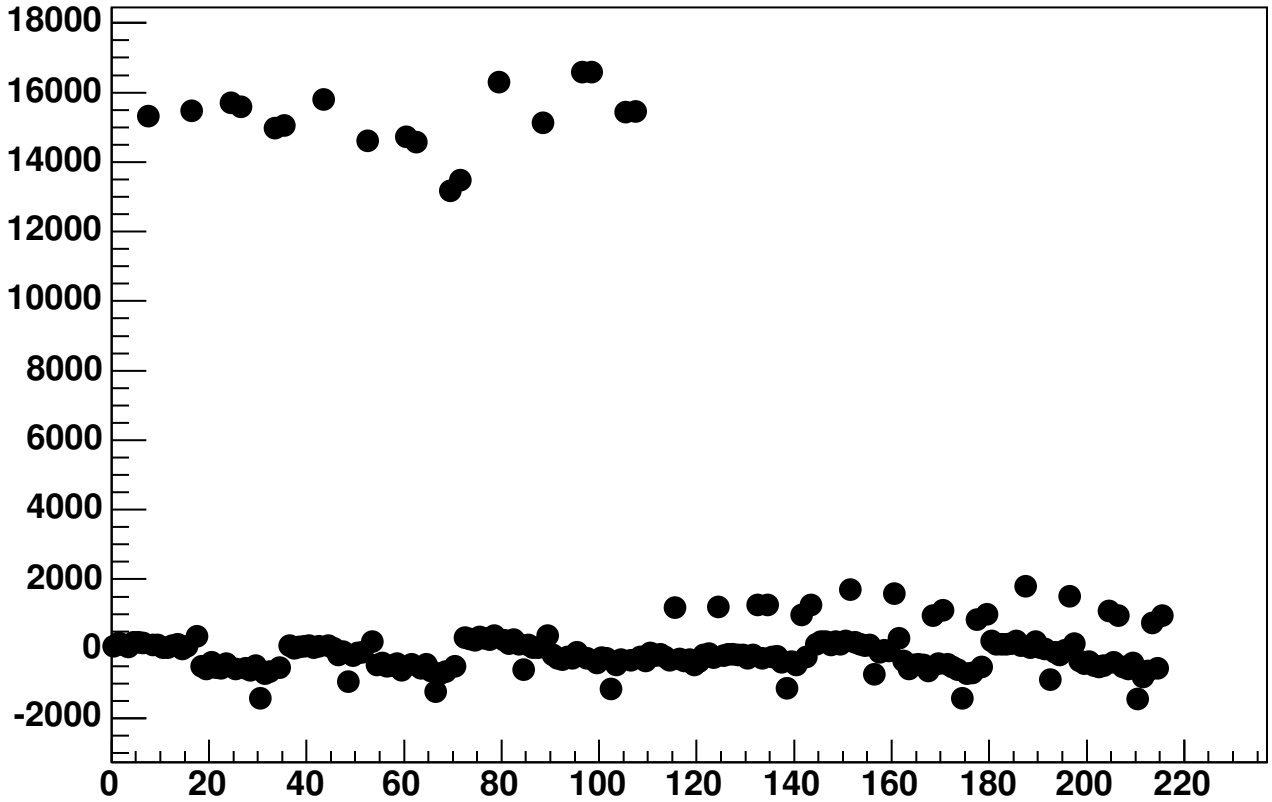
Enable 5, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



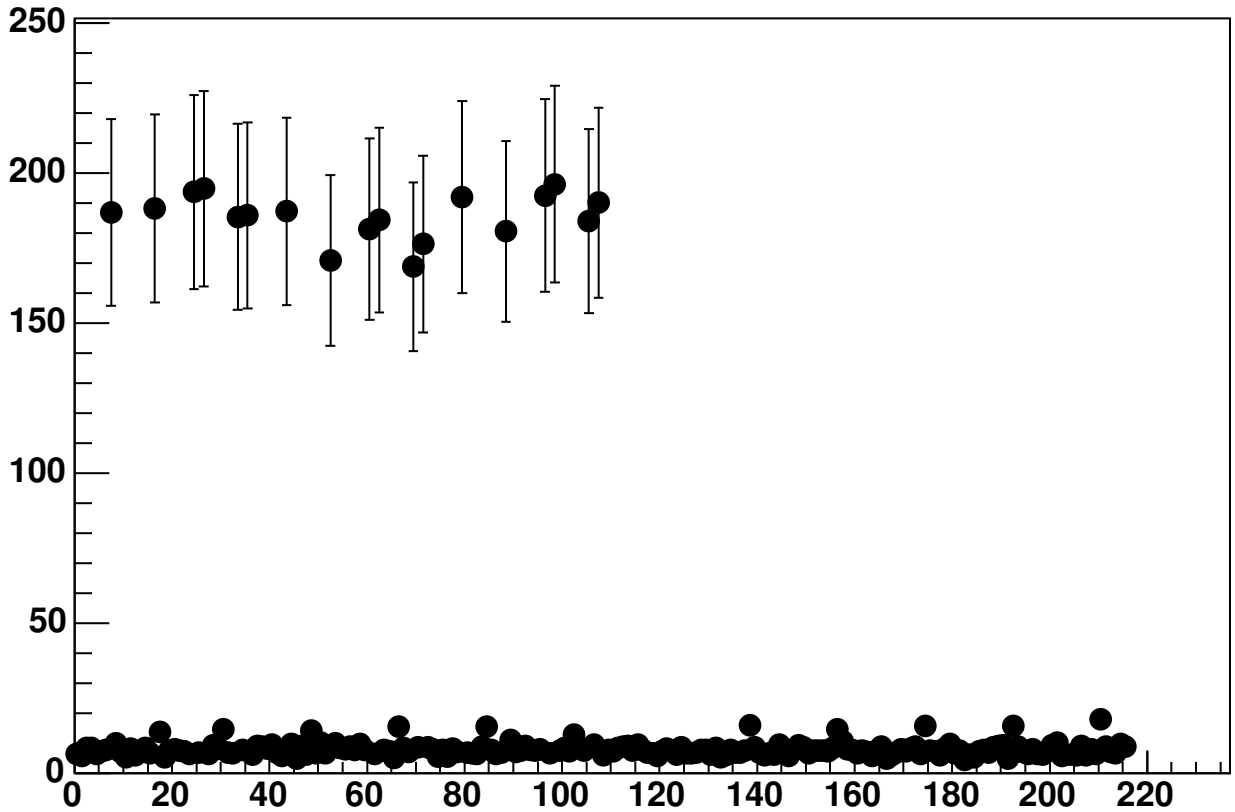
Enable 5, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



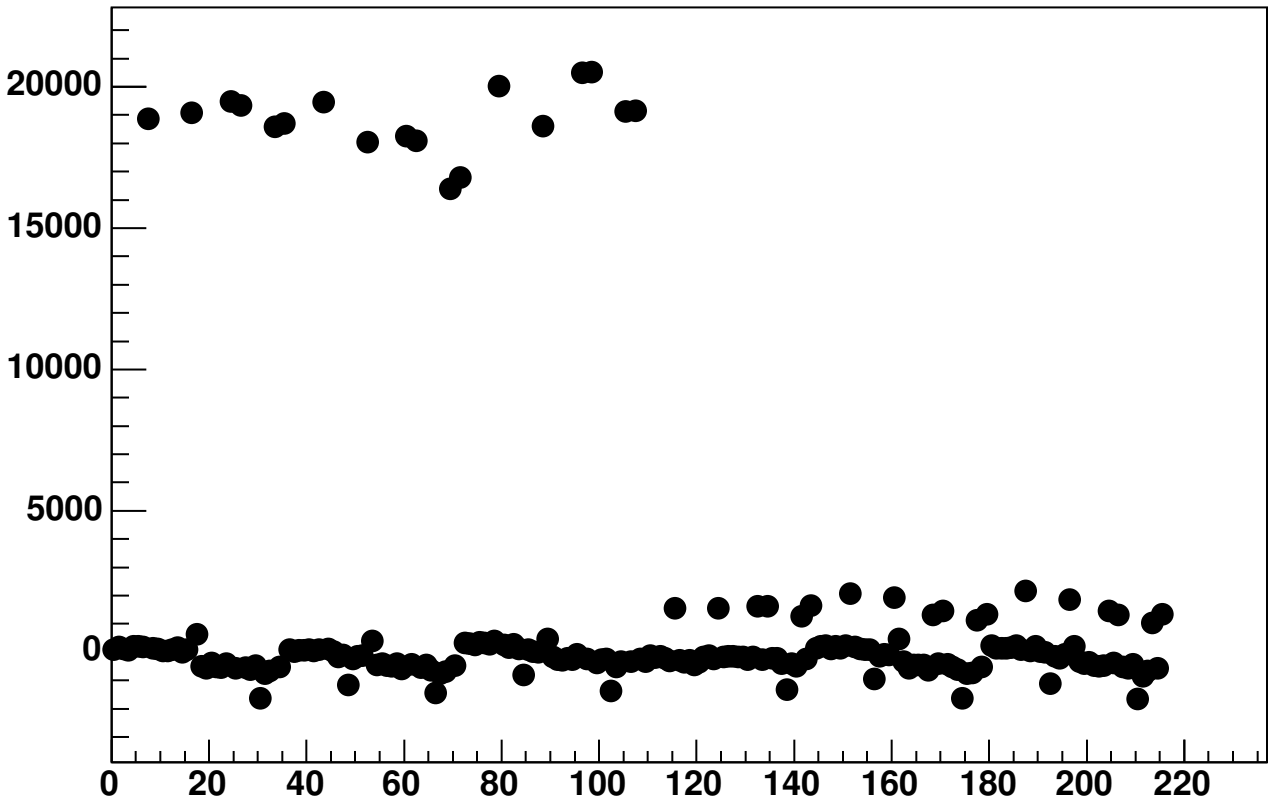
Enable 5, Hold=35, DAC=400, ADC Mean vs 18*Chip+Chan



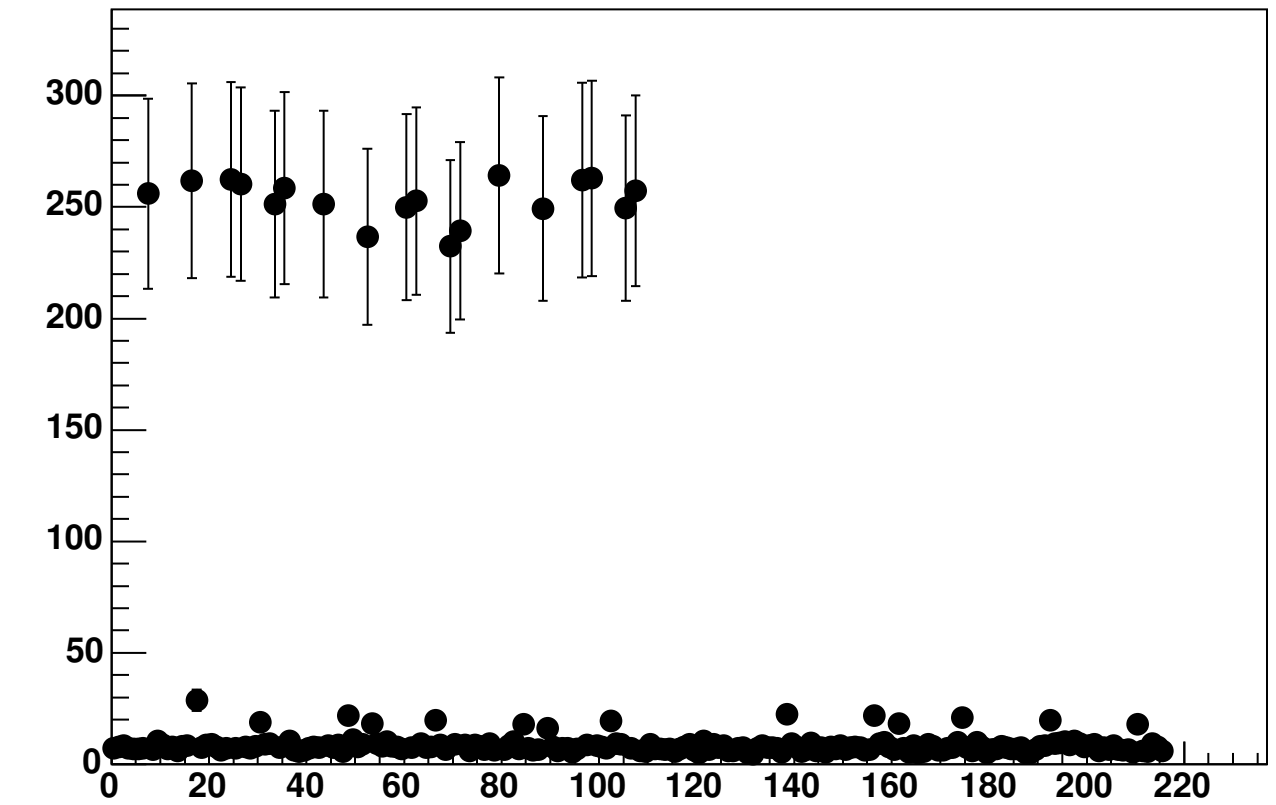
Enable 5, Hold=35, DAC=400, ADC Noise vs 18*Chip+Chan



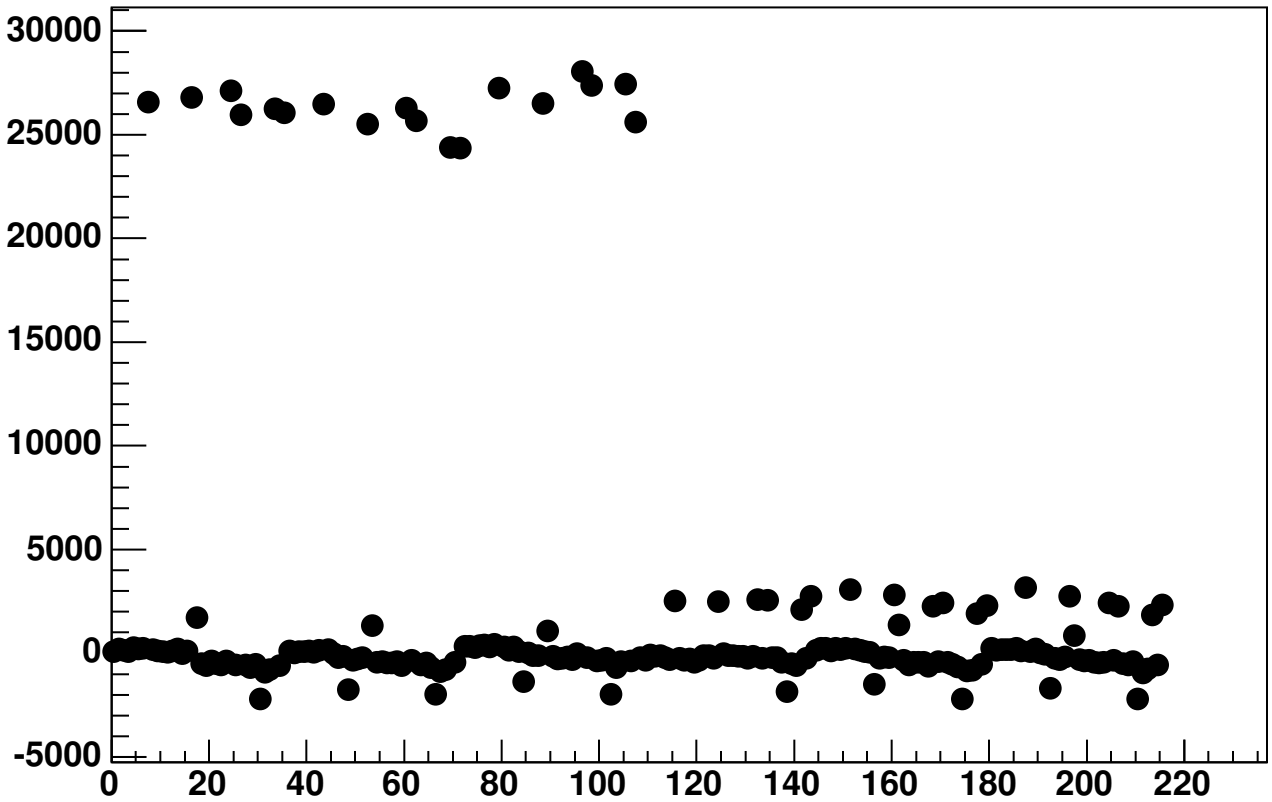
Enable 5, Hold=35, DAC=800, ADC Mean vs 18*Chip+Chan



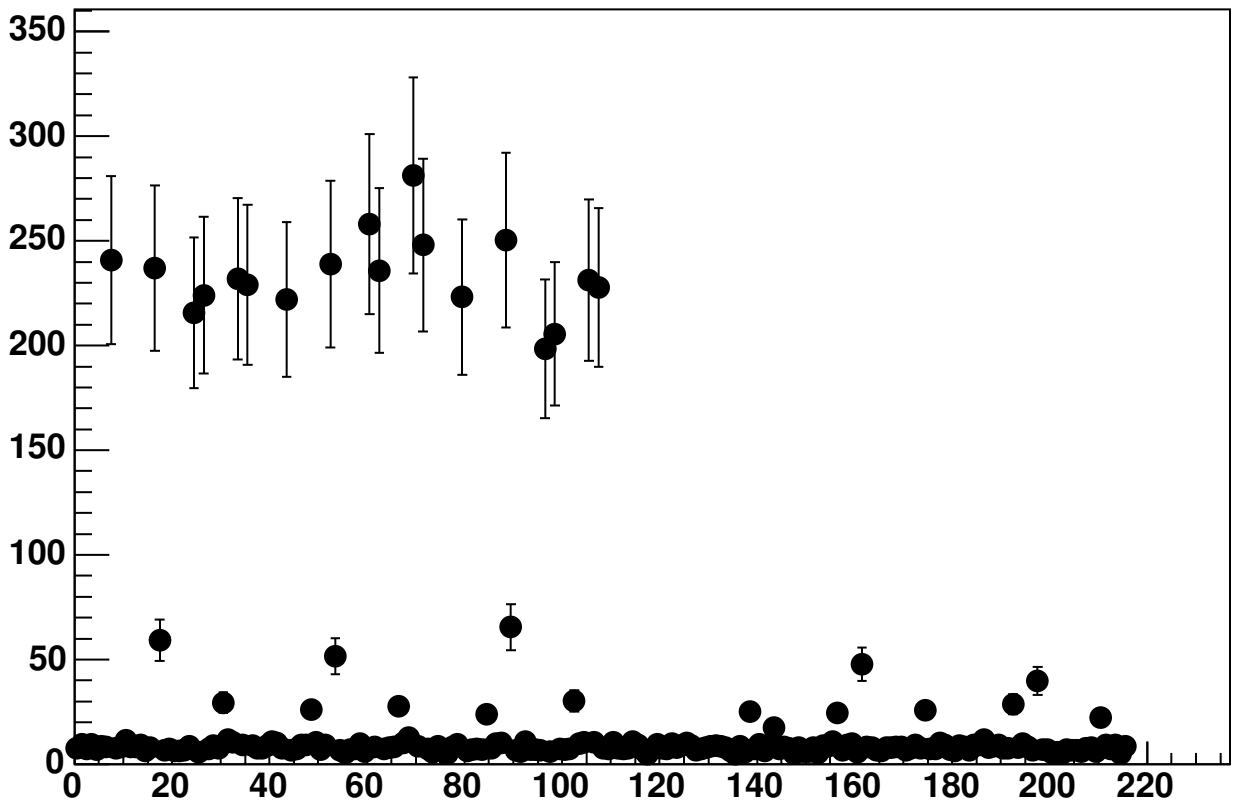
Enable 5, Hold=35, DAC=800, ADC Noise vs 18*Chip+Chan



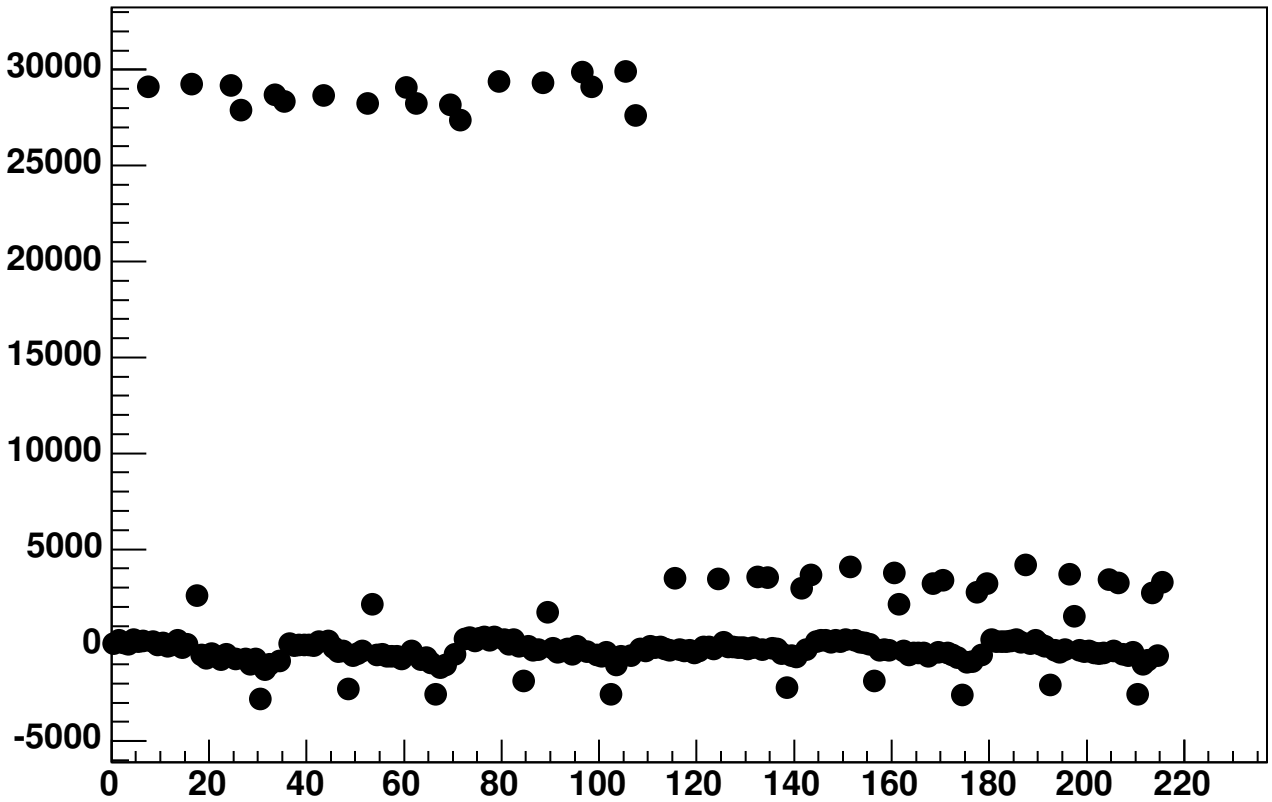
Enable 5, Hold=35, DAC=1200, ADC Mean vs 18*Chip+Chan



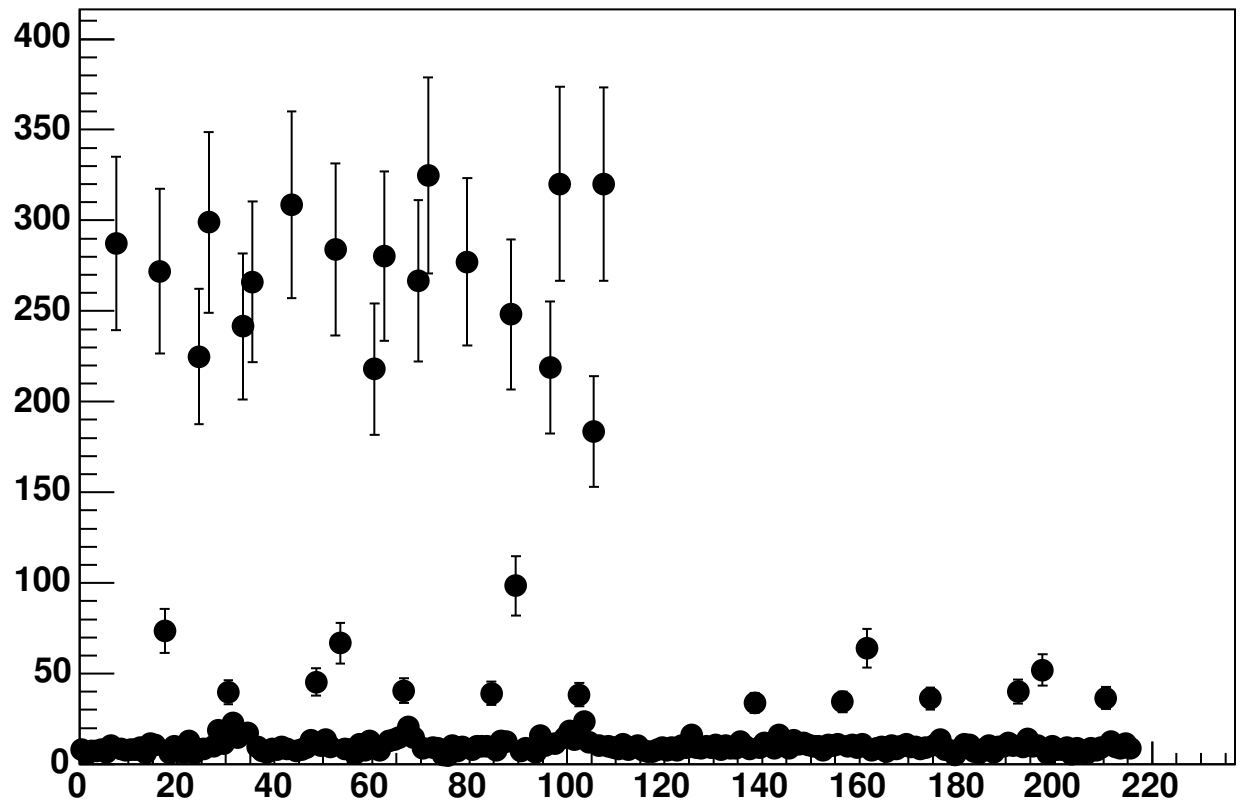
Enable 5, Hold=35, DAC=1200, ADC Noise vs 18*Chip+Chan



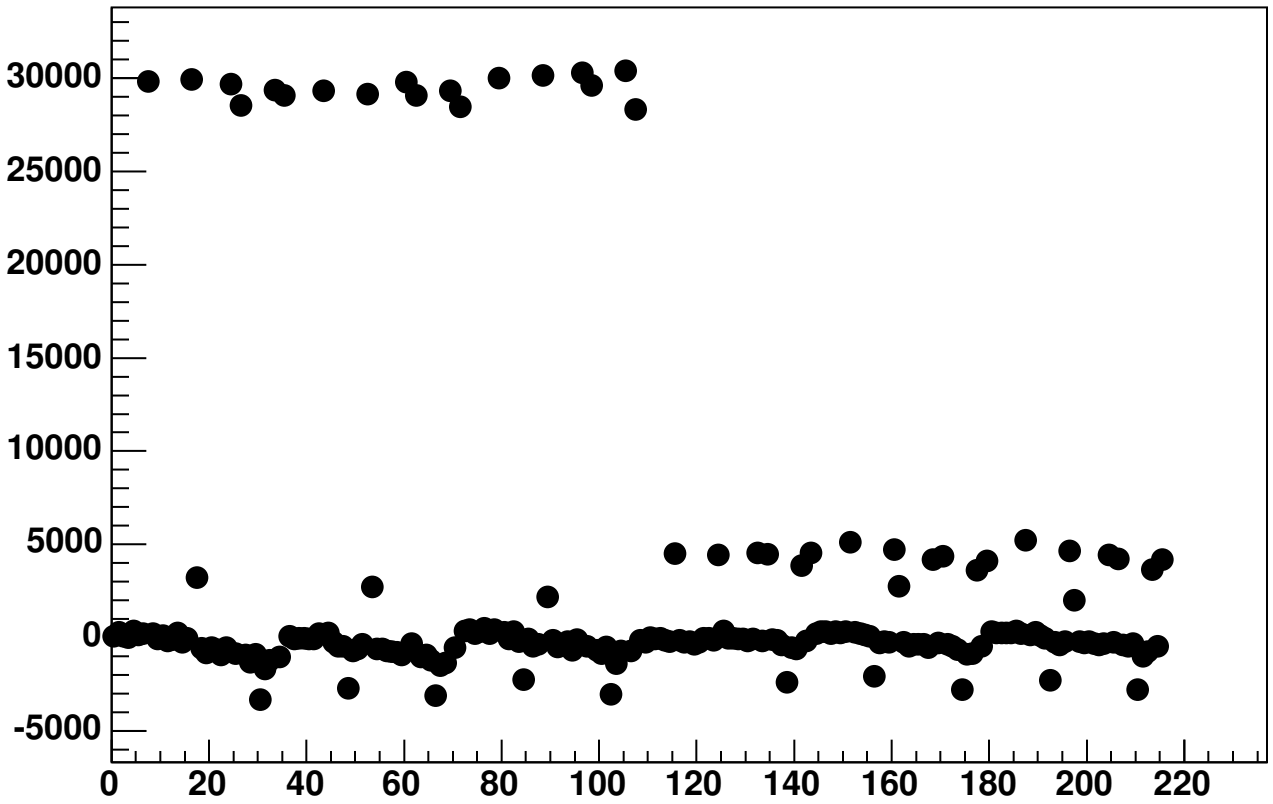
Enable 5, Hold=35, DAC=1600, ADC Mean vs 18*Chip+Chan



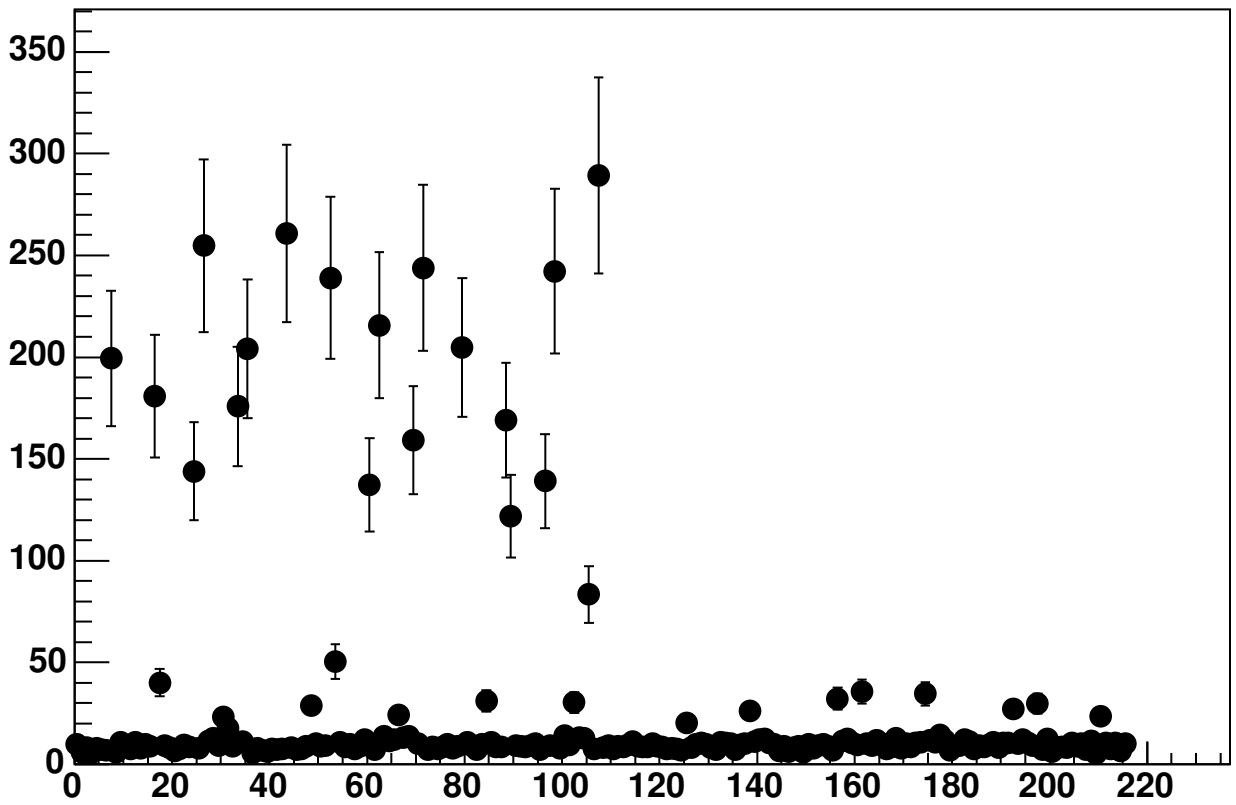
Enable 5, Hold=35, DAC=1600, ADC Noise vs 18*Chip+Chan



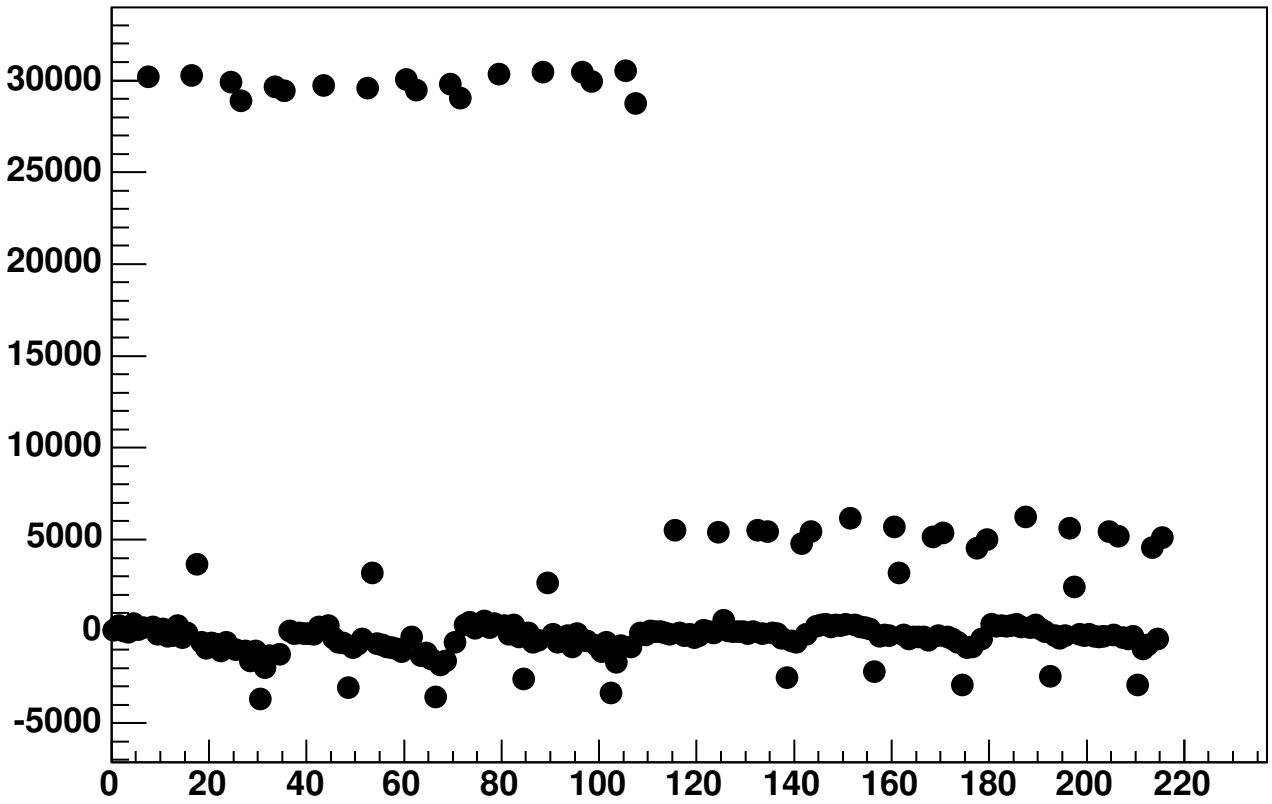
Enable 5, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



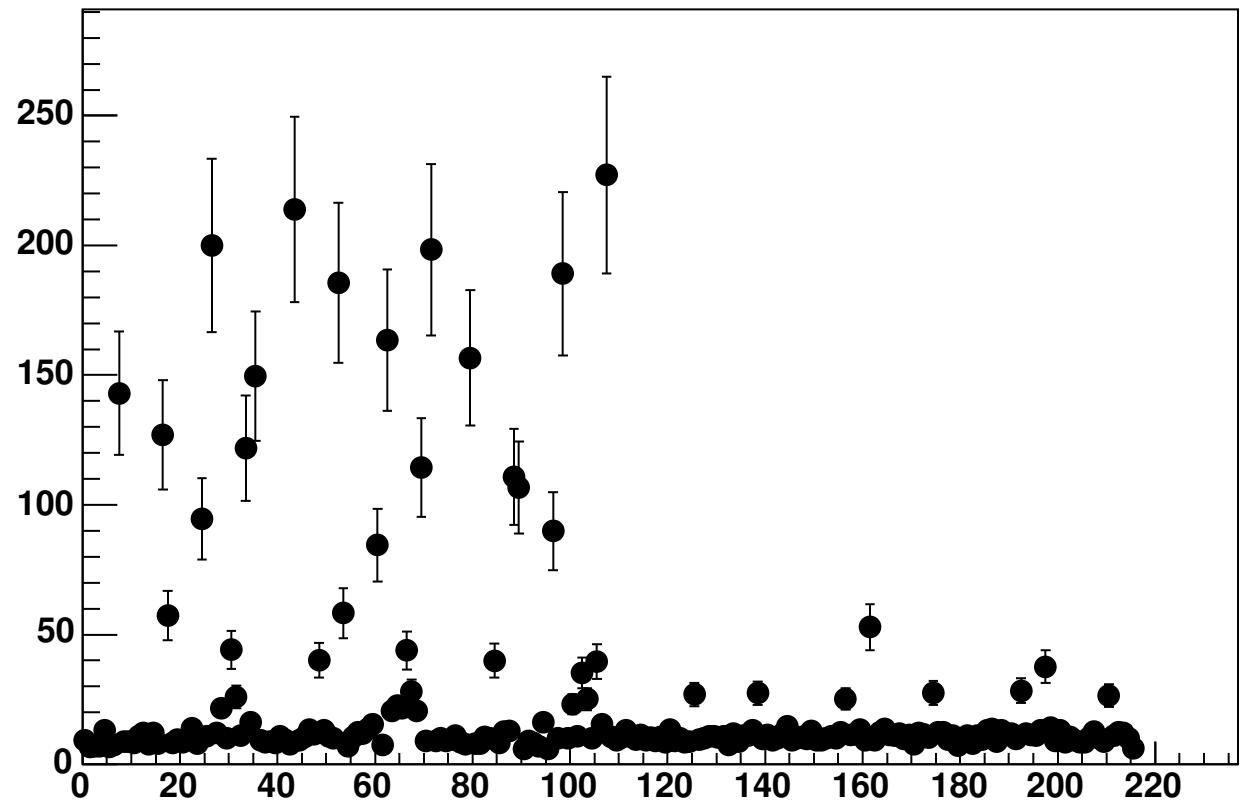
Enable 5, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



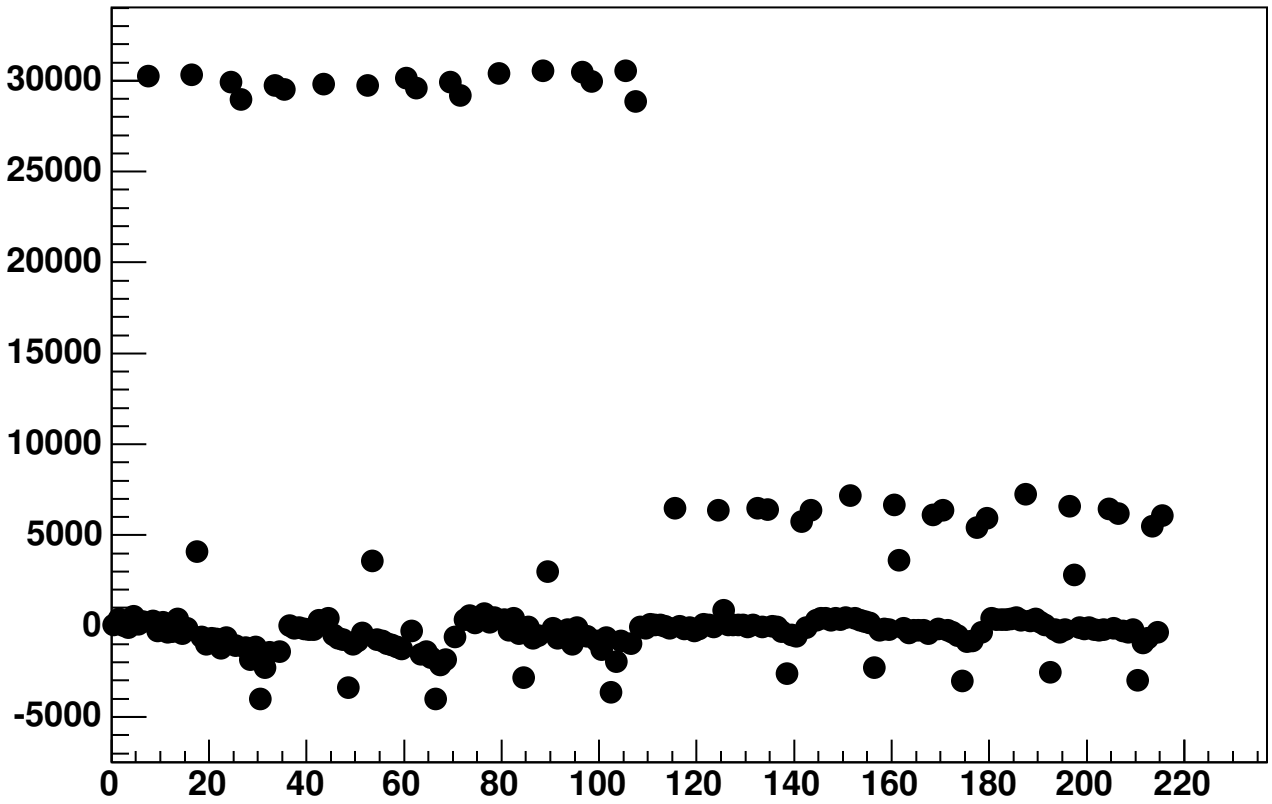
Enable 5, Hold=35, DAC=2400, ADC Mean vs 18*Chip+Chan



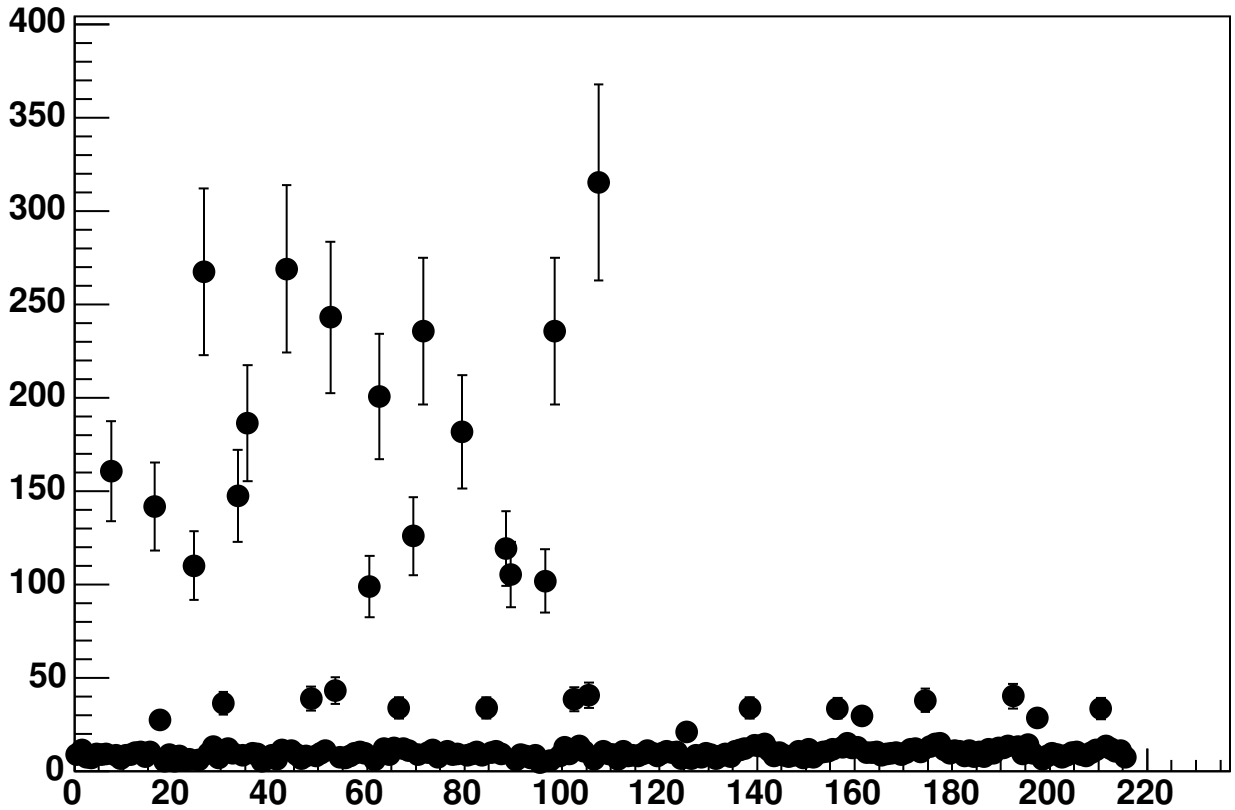
Enable 5, Hold=35, DAC=2400, ADC Noise vs 18*Chip+Chan



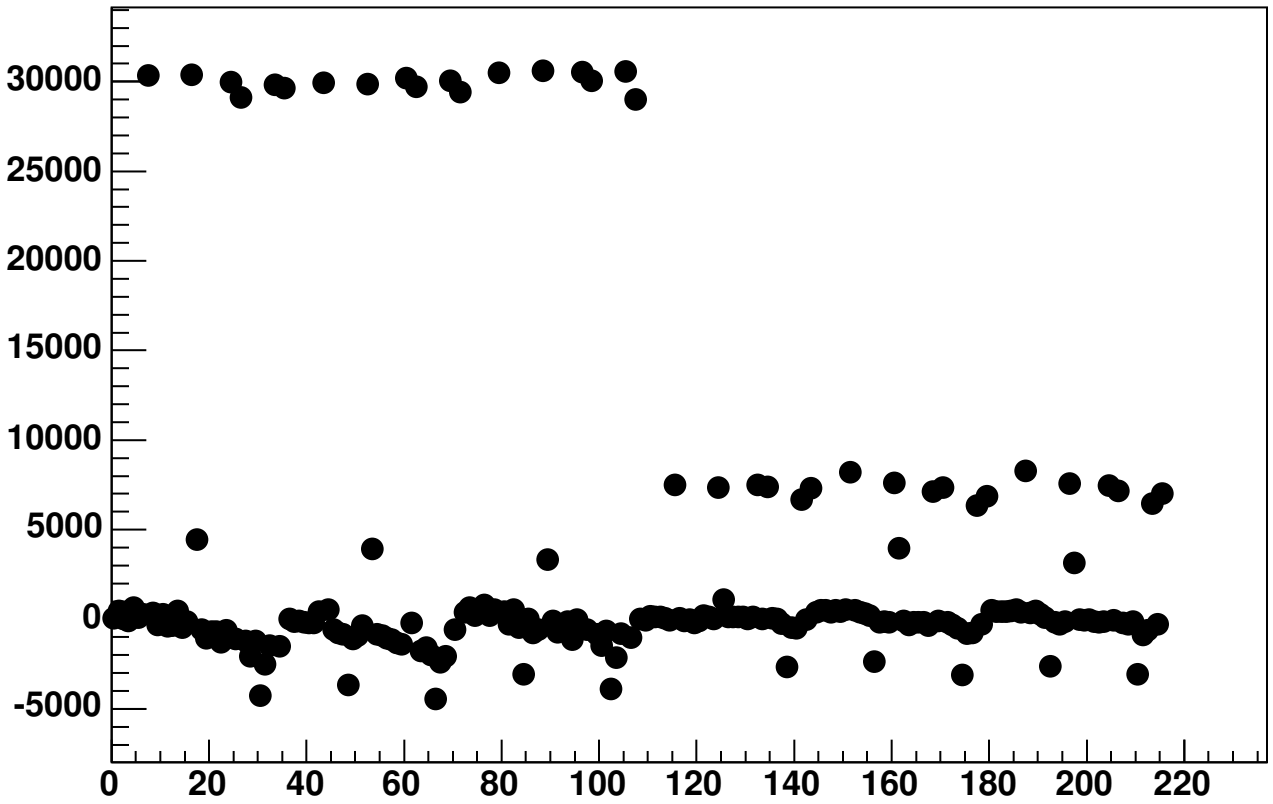
Enable 5, Hold=35, DAC=2800, ADC Mean vs 18*Chip+Chan



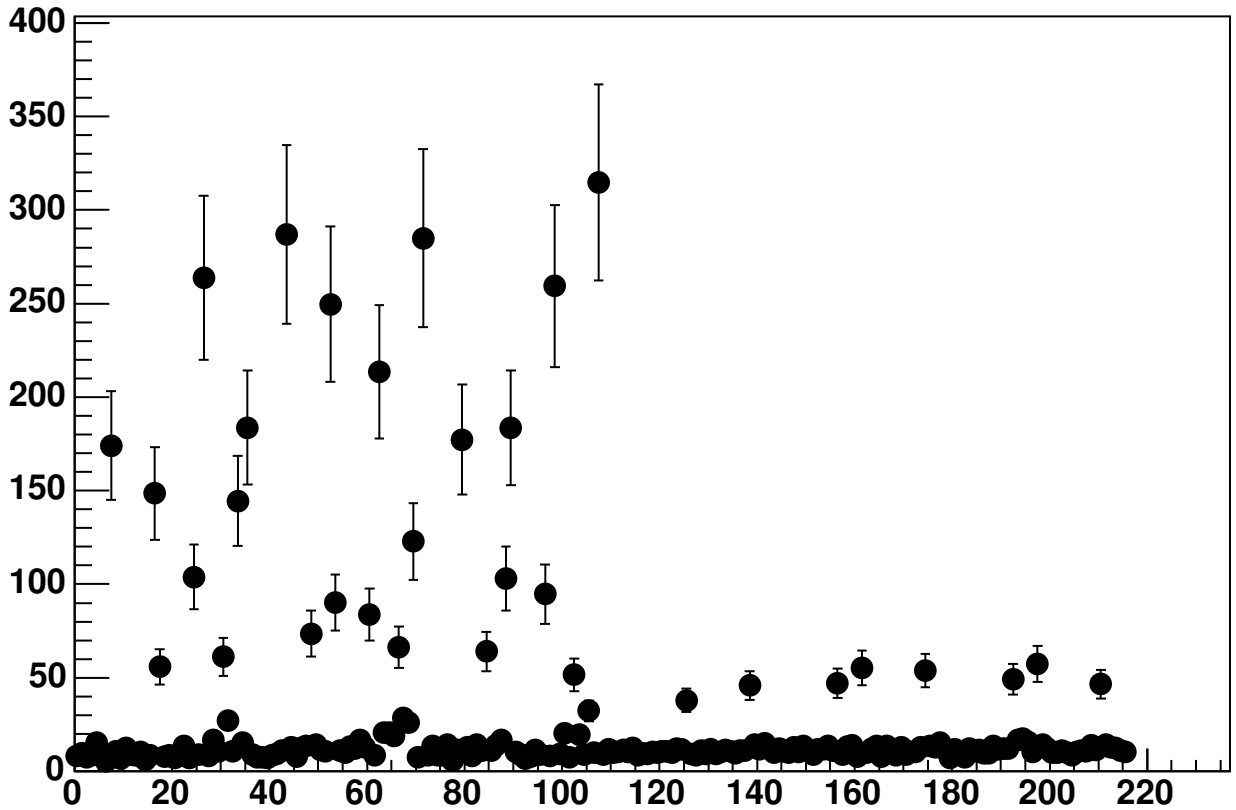
Enable 5, Hold=35, DAC=2800, ADC Noise vs 18*Chip+Chan



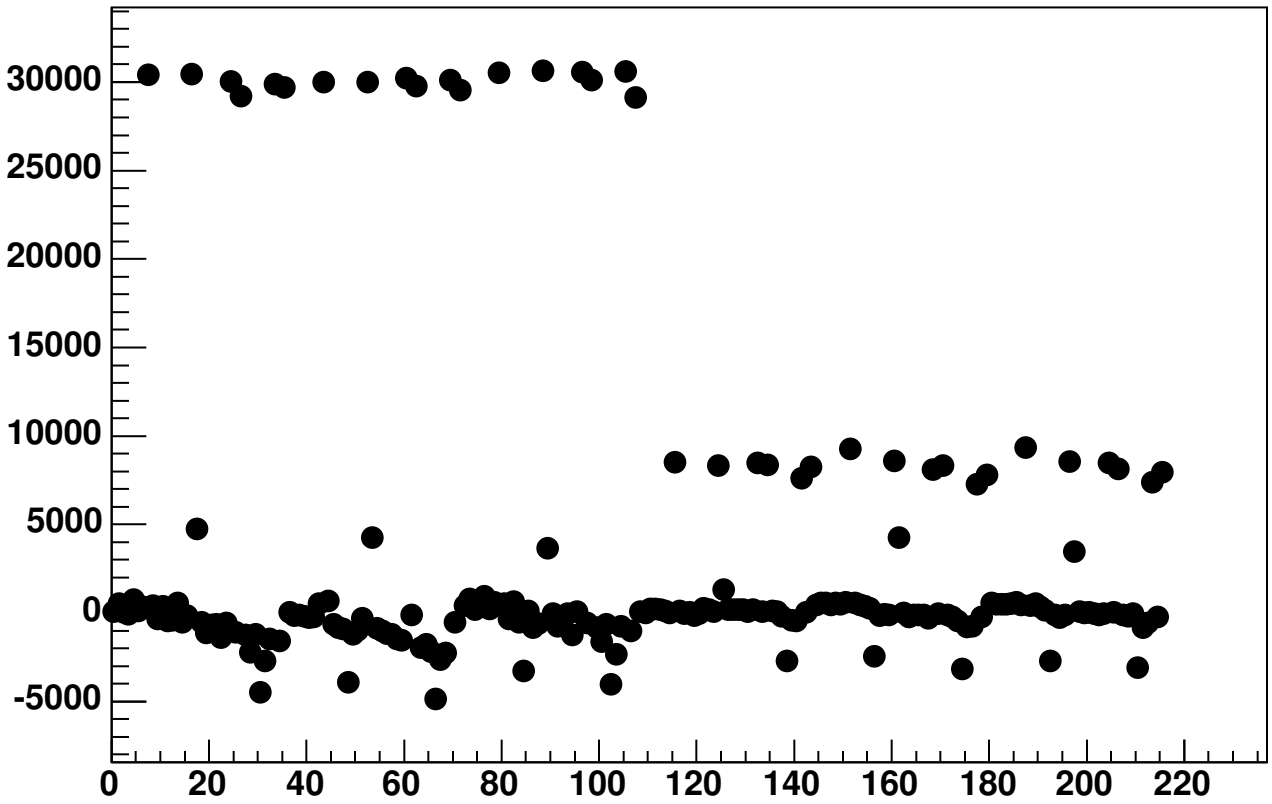
Enable 5, Hold=35, DAC=3200, ADC Mean vs 18*Chip+Chan



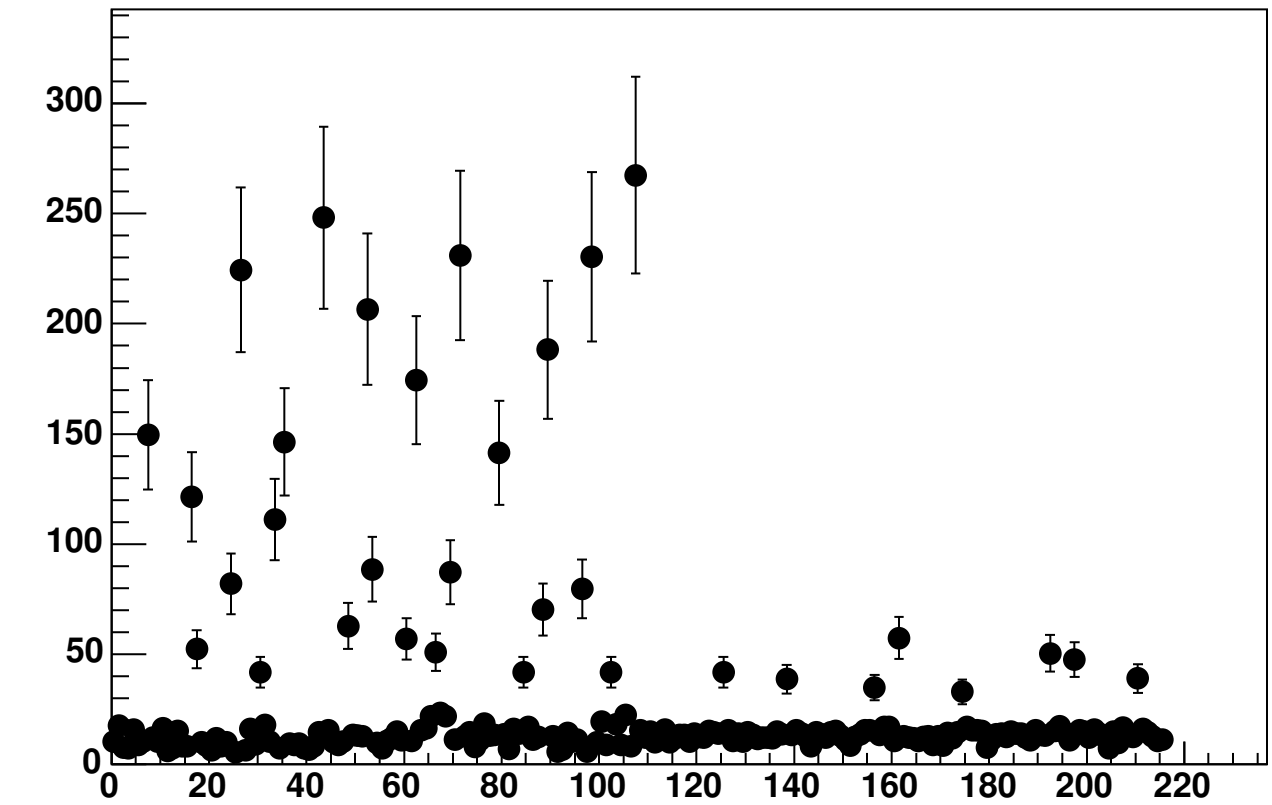
Enable 5, Hold=35, DAC=3200, ADC Noise vs 18*Chip+Chan



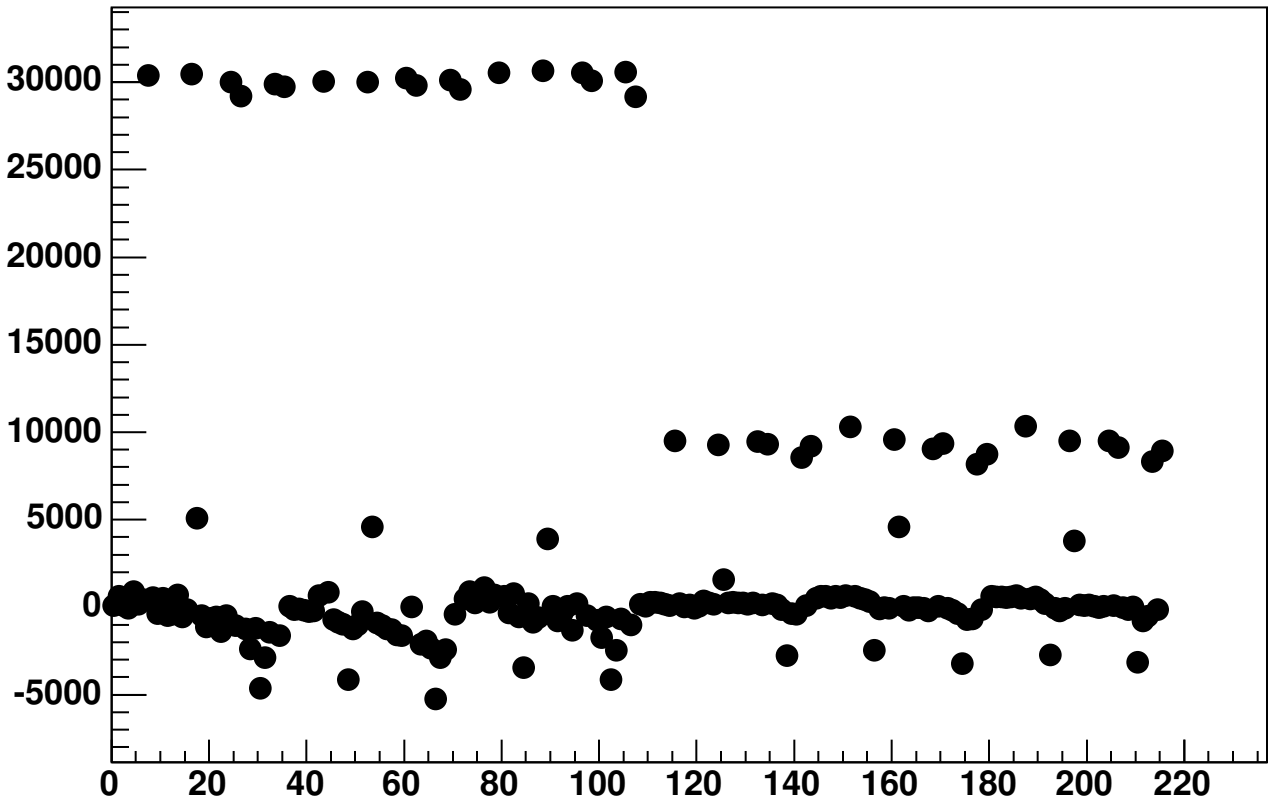
Enable 5, Hold=35, DAC=3600, ADC Mean vs 18*Chip+Chan



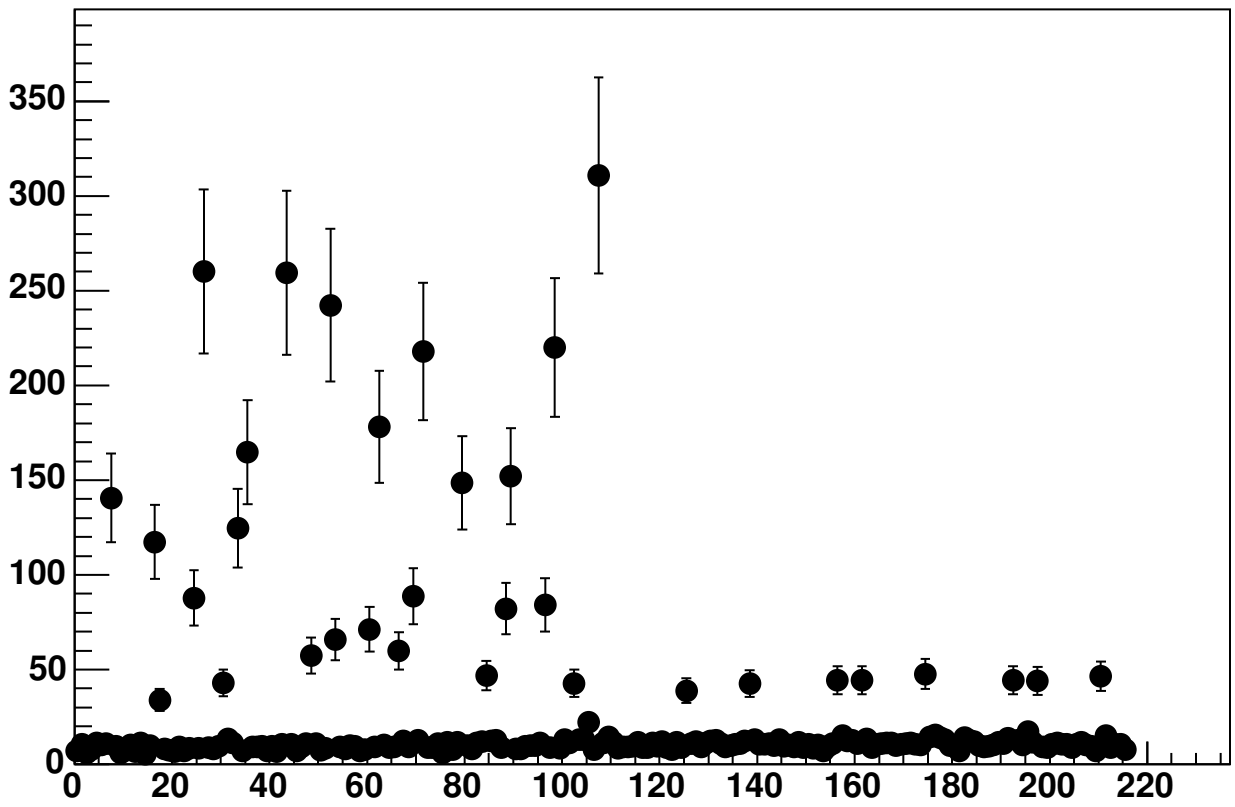
Enable 5, Hold=35, DAC=3600, ADC Noise vs 18*Chip+Chan



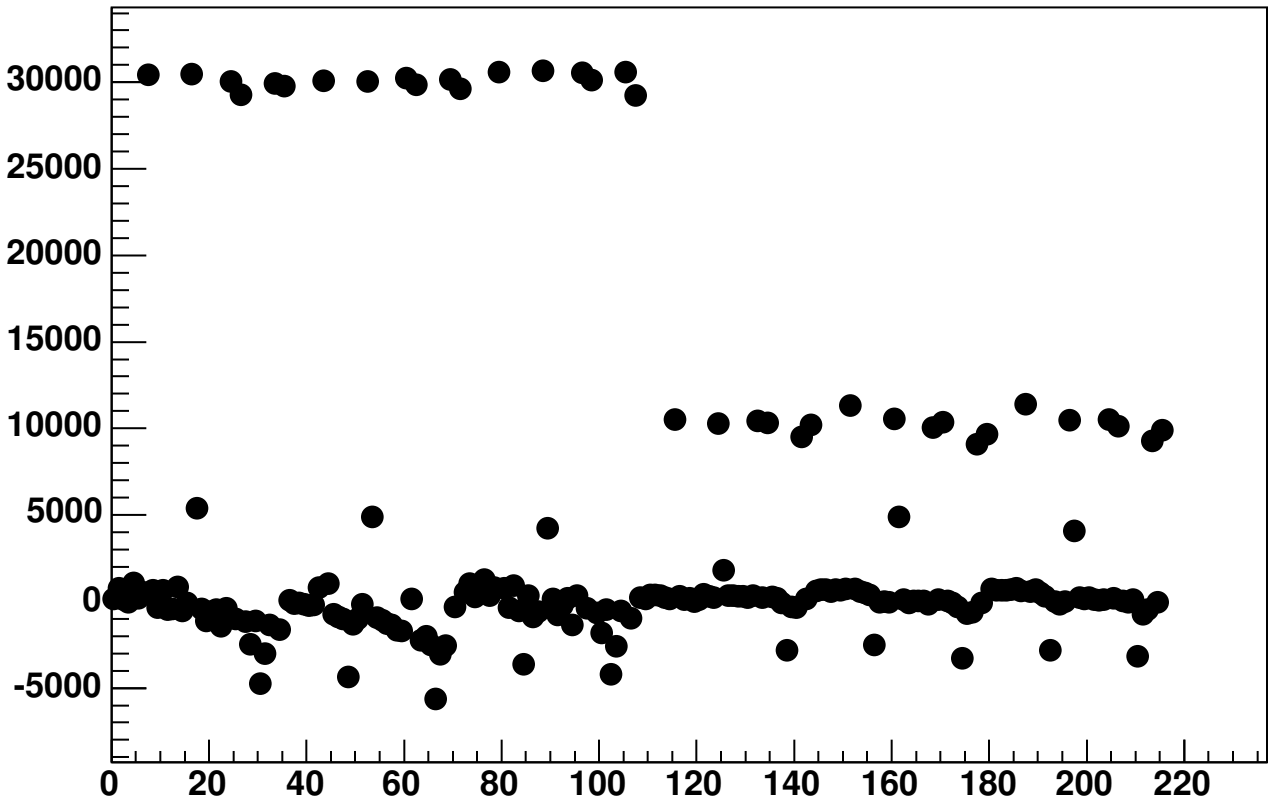
Enable 5, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



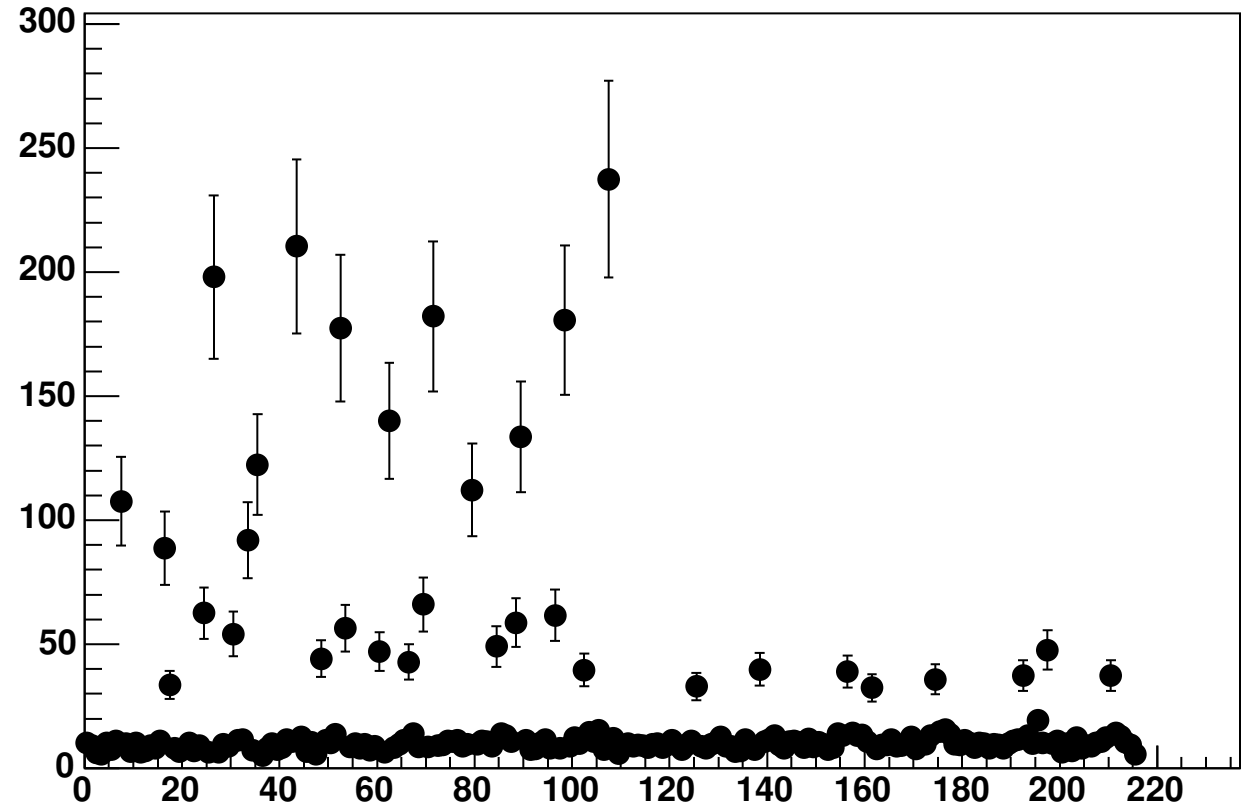
Enable 5, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



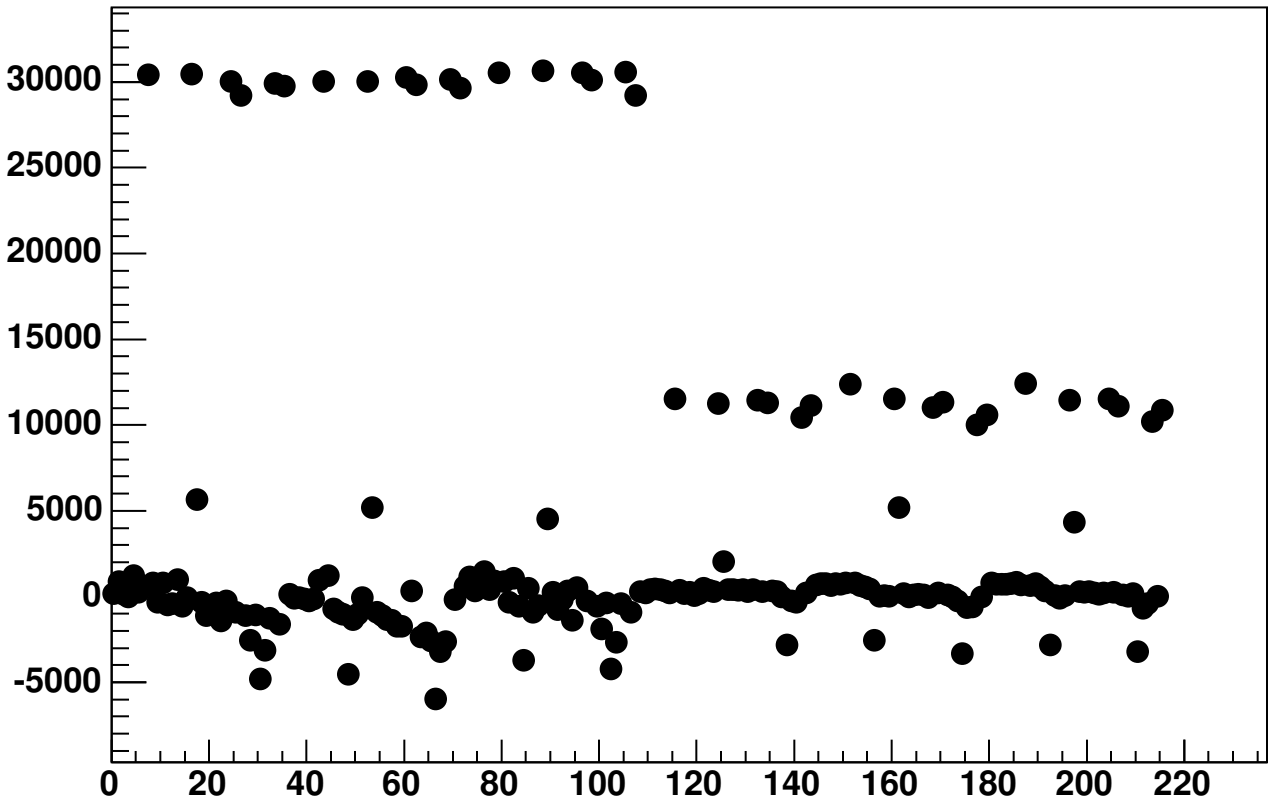
Enable 5, Hold=35, DAC=4400, ADC Mean vs 18*Chip+Chan



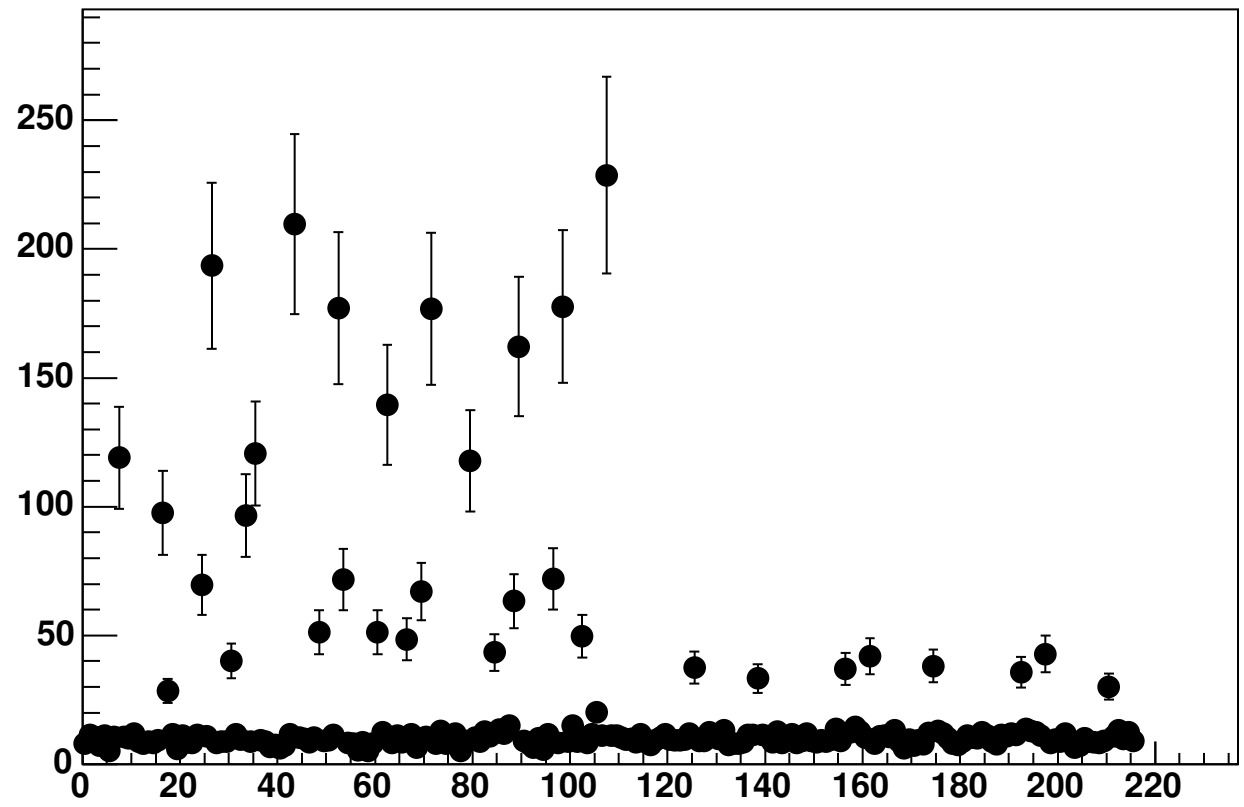
Enable 5, Hold=35, DAC=4400, ADC Noise vs 18*Chip+Chan



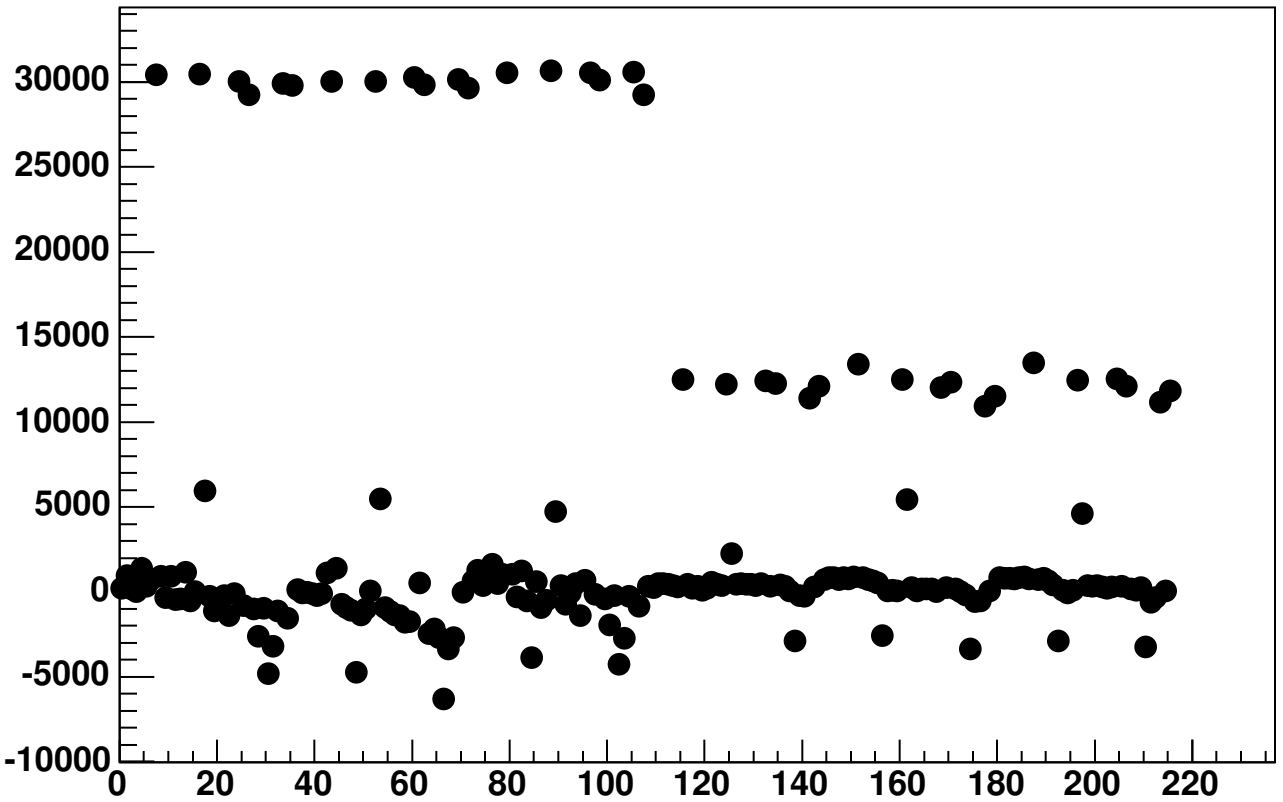
Enable 5, Hold=35, DAC=4800, ADC Mean vs 18*Chip+Chan



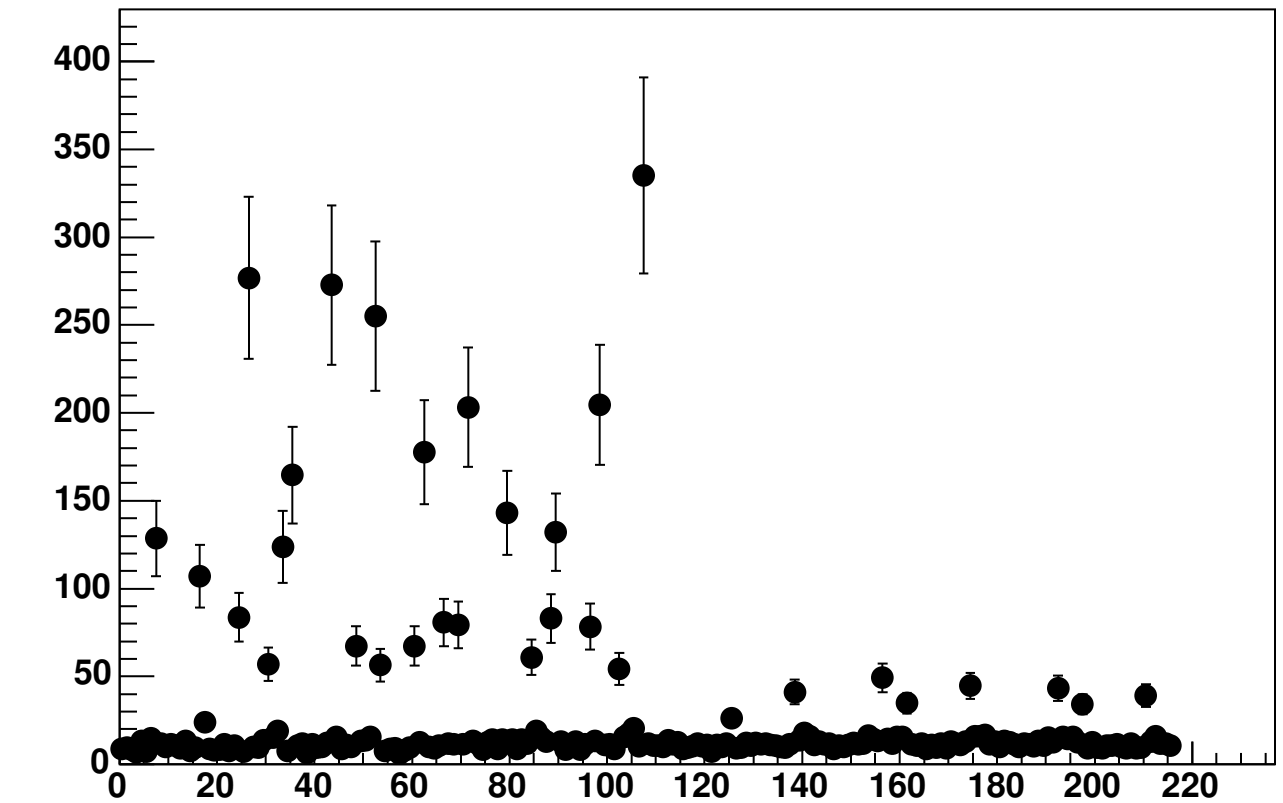
Enable 5, Hold=35, DAC=4800, ADC Noise vs 18*Chip+Chan



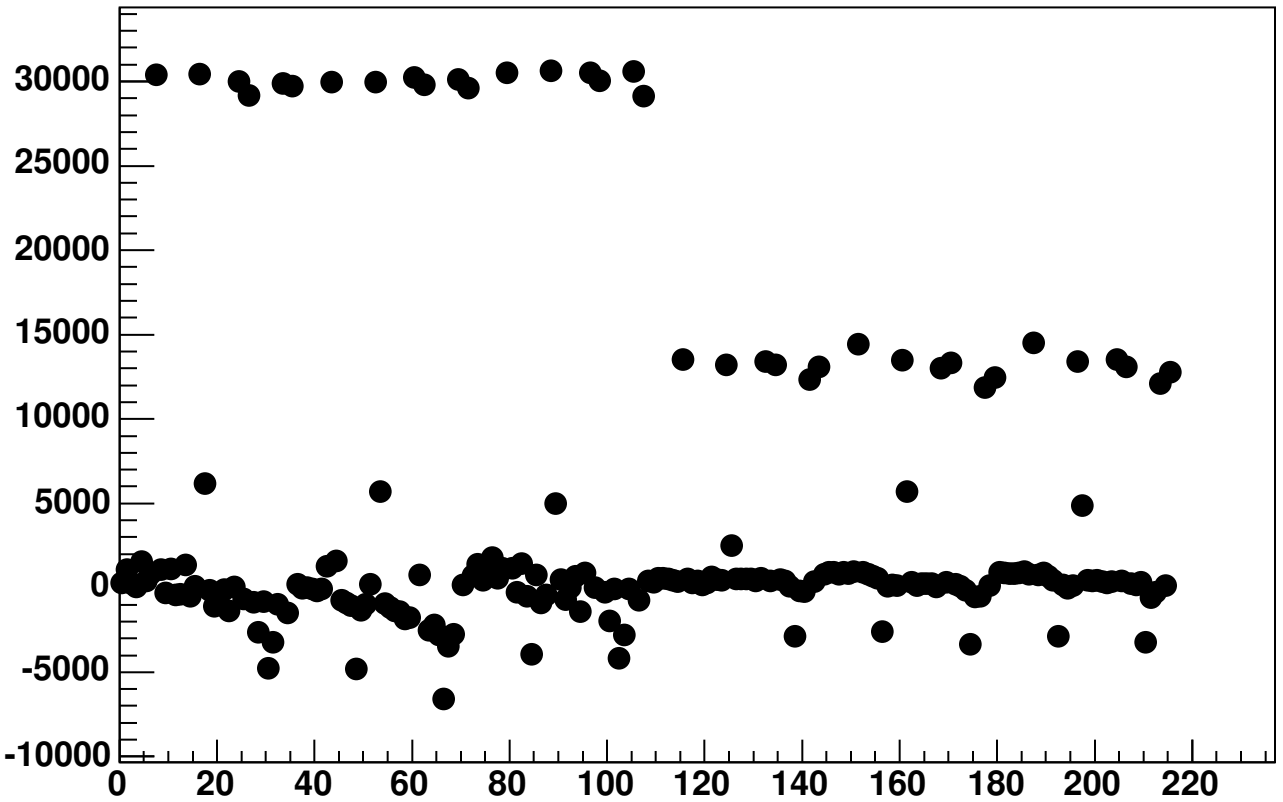
Enable 5, Hold=35, DAC=5200, ADC Mean vs 18*Chip+Chan



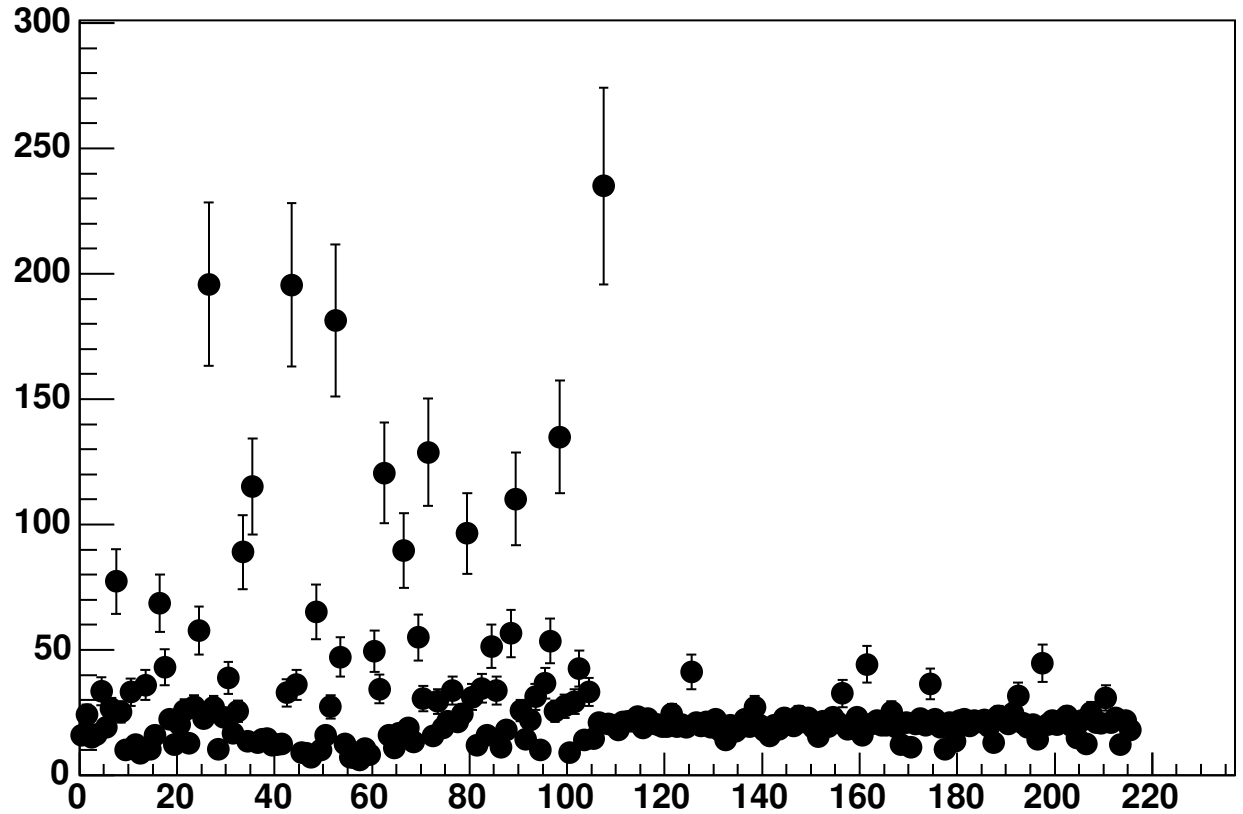
Enable 5, Hold=35, DAC=5200, ADC Noise vs 18*Chip+Chan



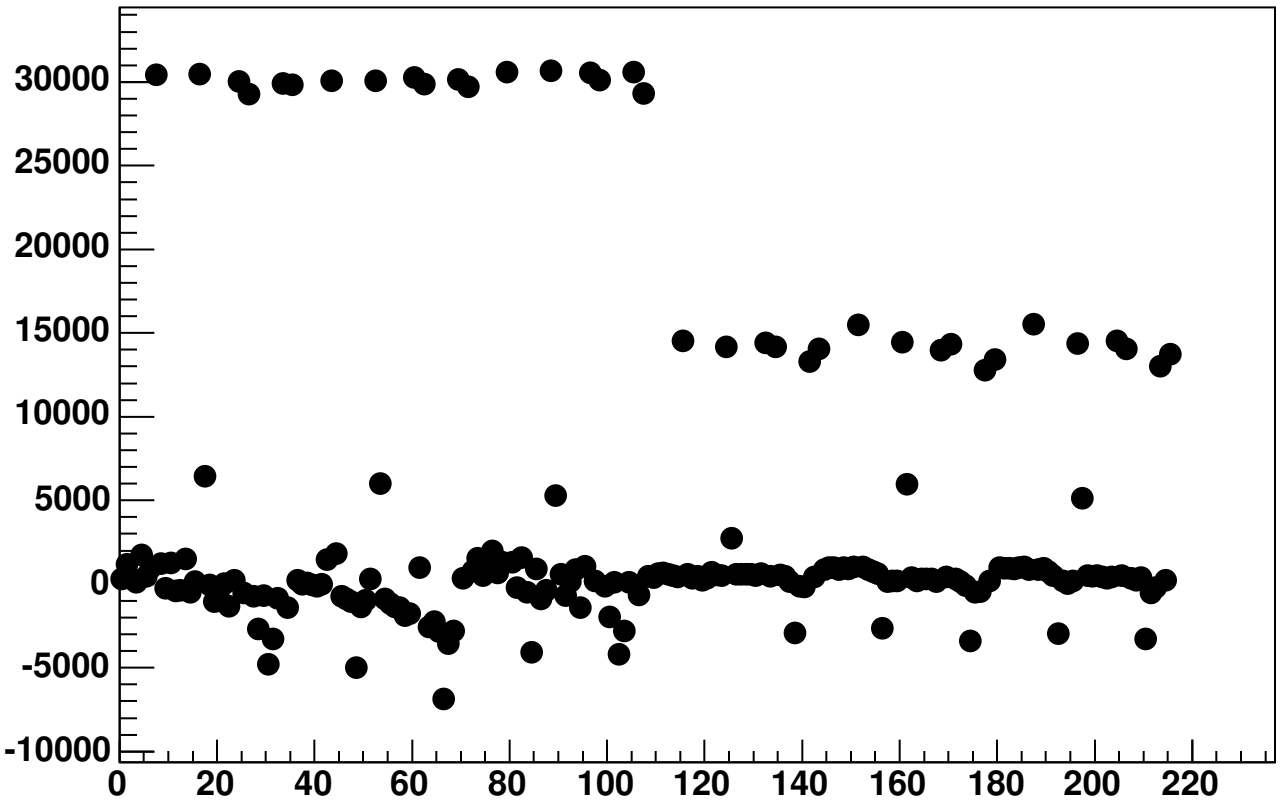
Enable 5, Hold=35, DAC=5600, ADC Mean vs 18*Chip+Chan



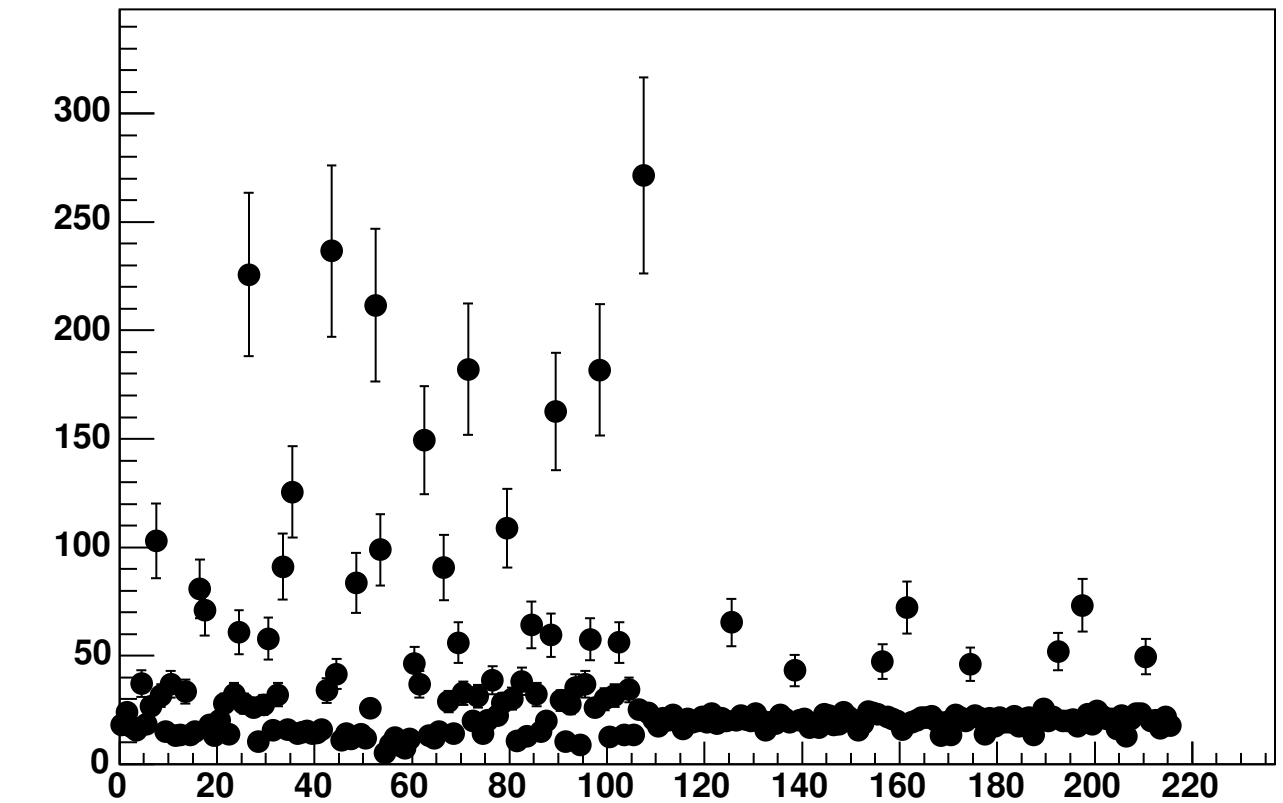
Enable 5, Hold=35, DAC=5600, ADC Noise vs 18*Chip+Chan



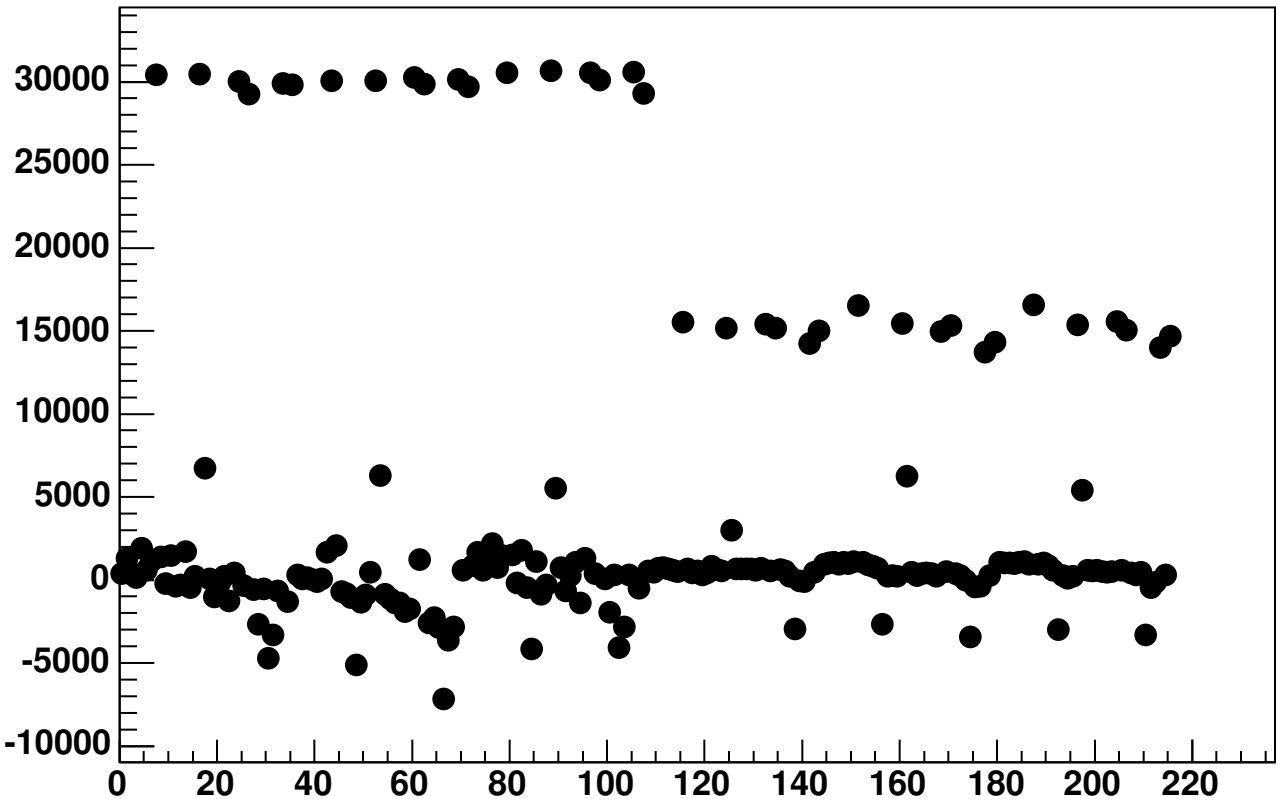
Enable 5, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



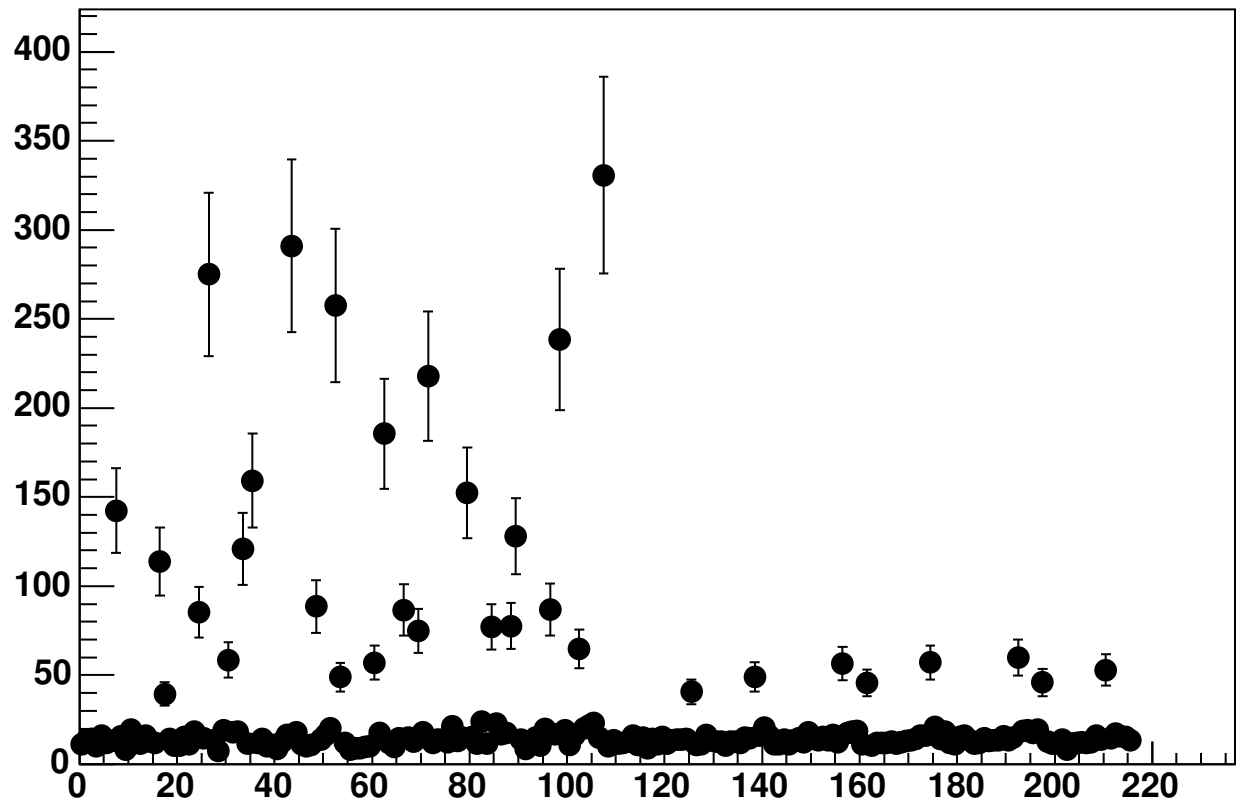
Enable 5, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



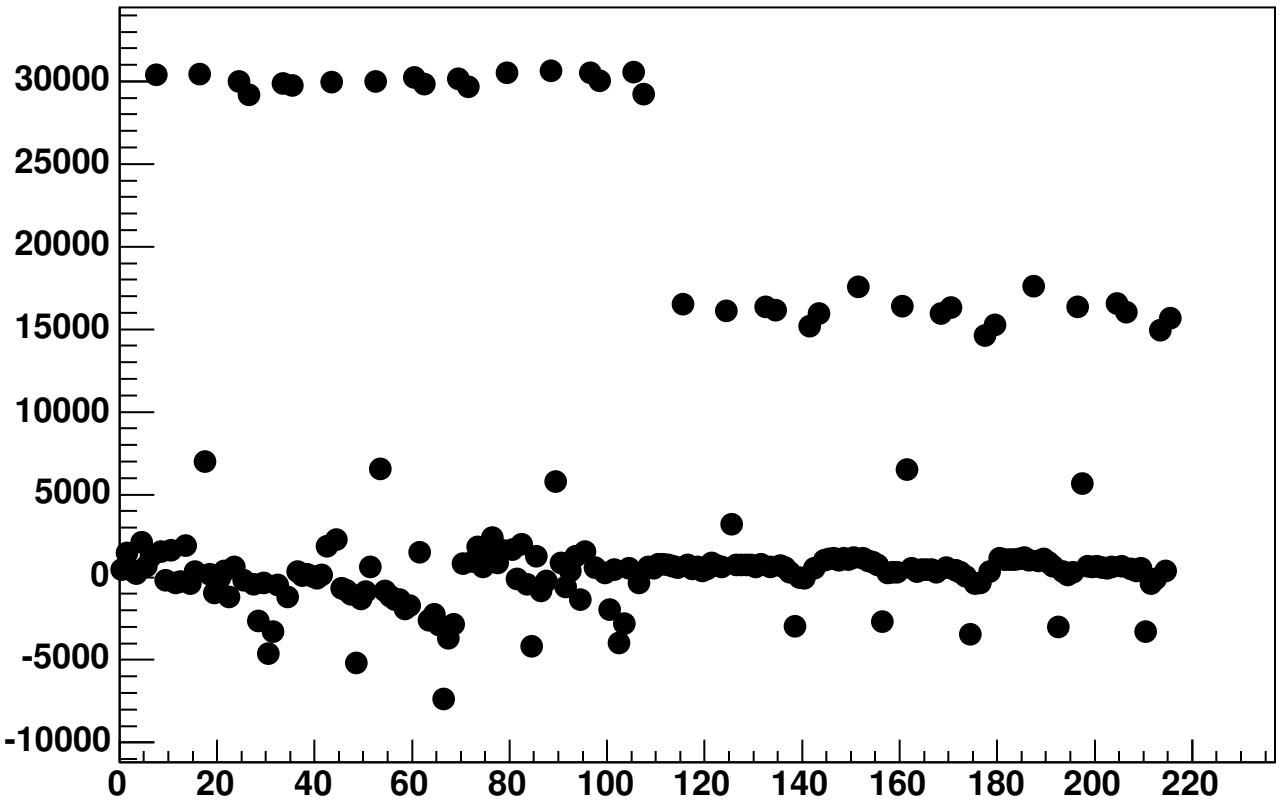
Enable 5, Hold=35, DAC=6400, ADC Mean vs 18*Chip+Chan



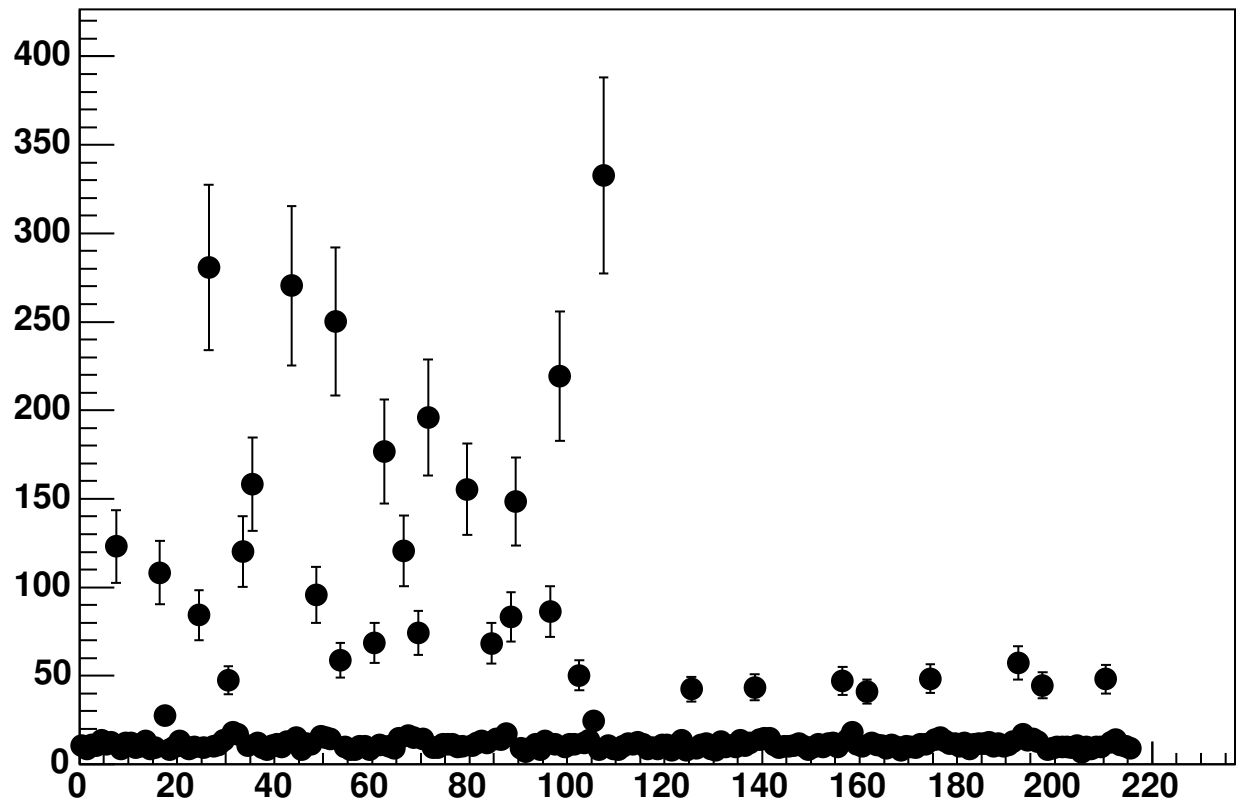
Enable 5, Hold=35, DAC=6400, ADC Noise vs 18*Chip+Chan



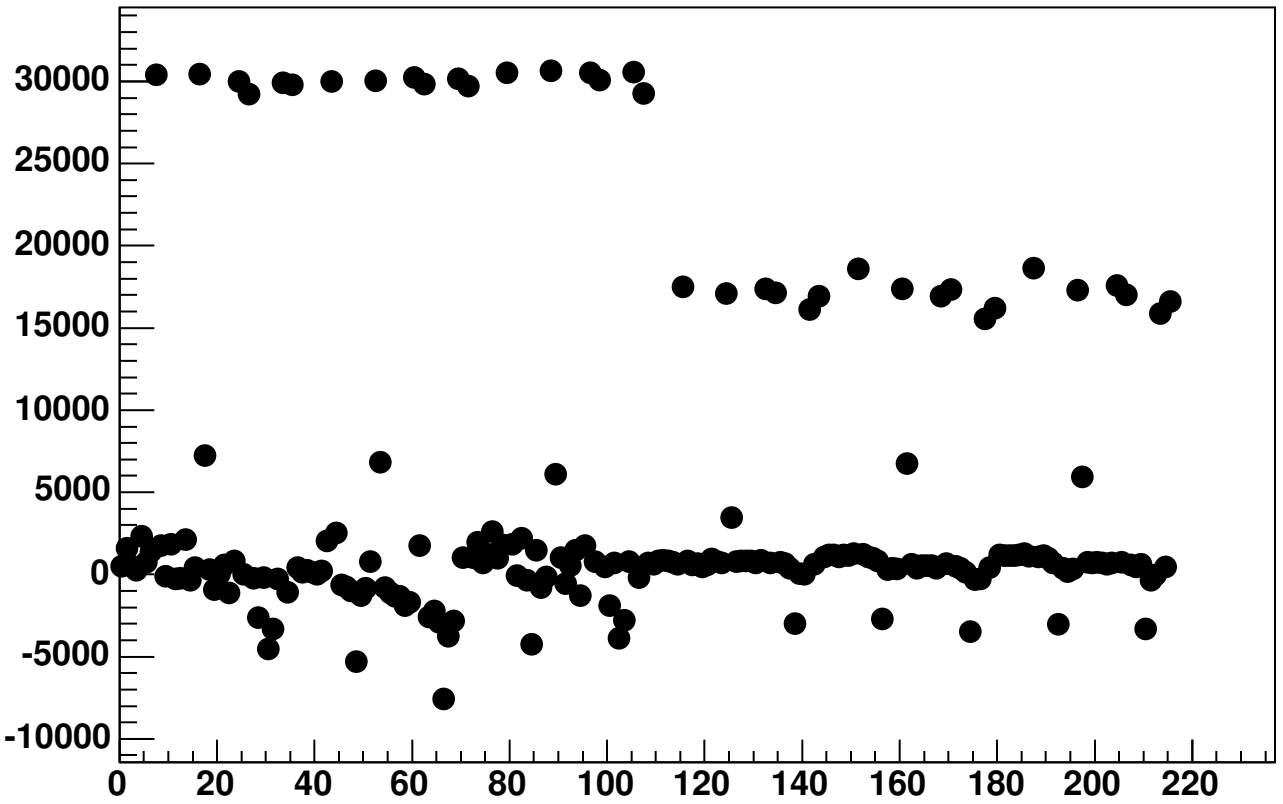
Enable 5, Hold=35, DAC=6800, ADC Mean vs 18*Chip+Chan



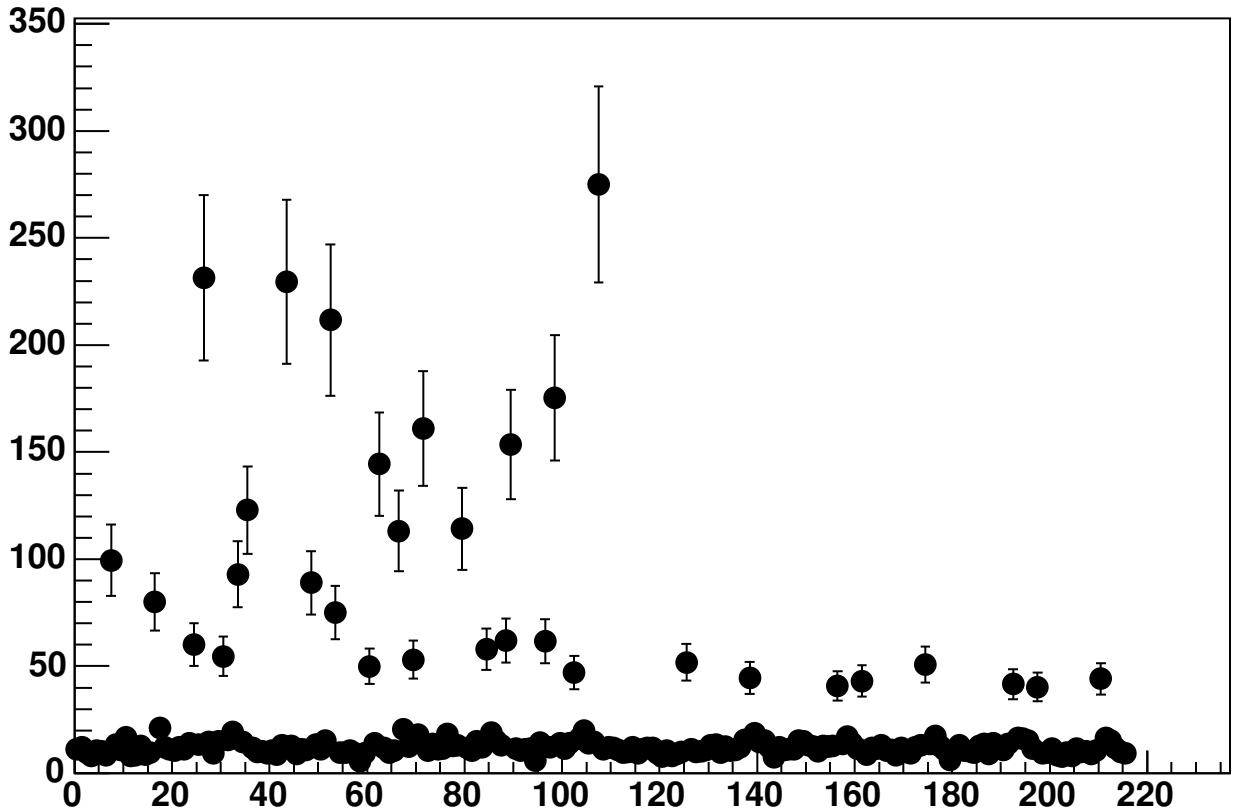
Enable 5, Hold=35, DAC=6800, ADC Noise vs 18*Chip+Chan



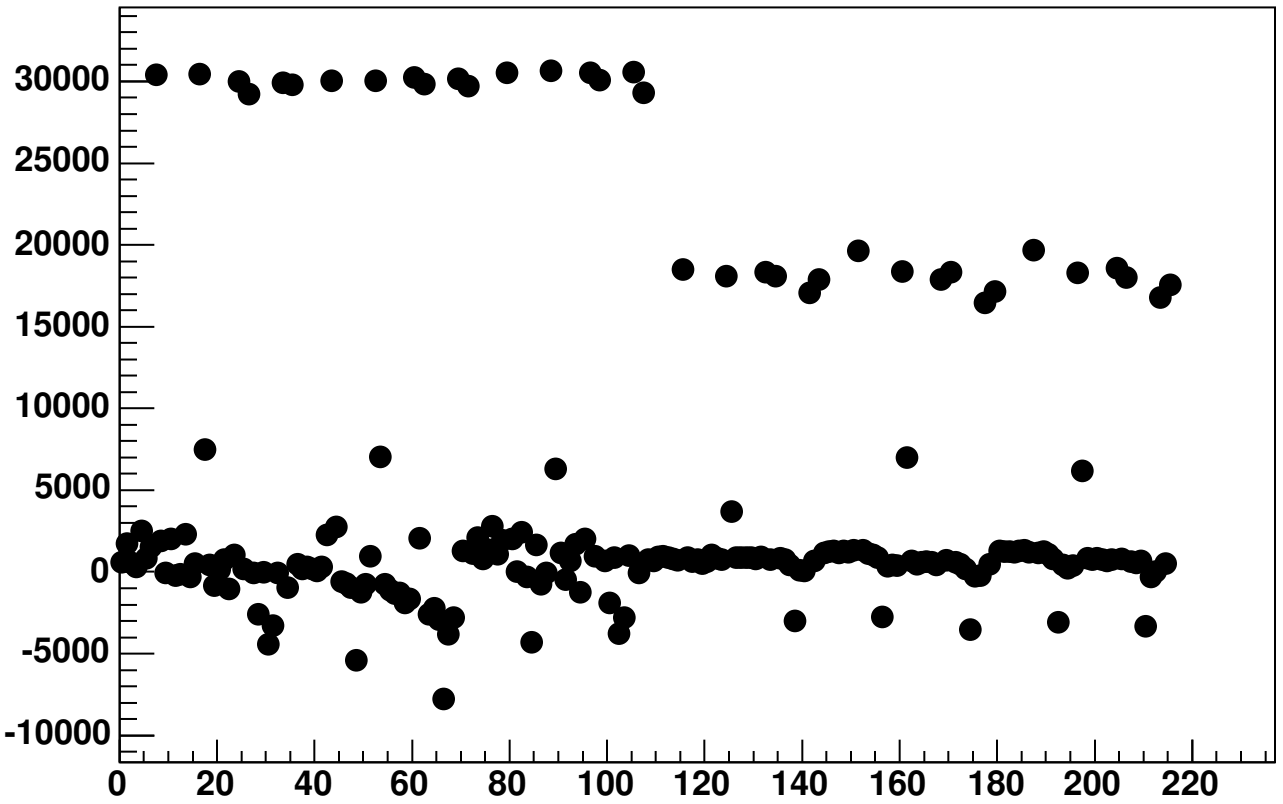
Enable 5, Hold=35, DAC=7200, ADC Mean vs 18*Chip+Chan



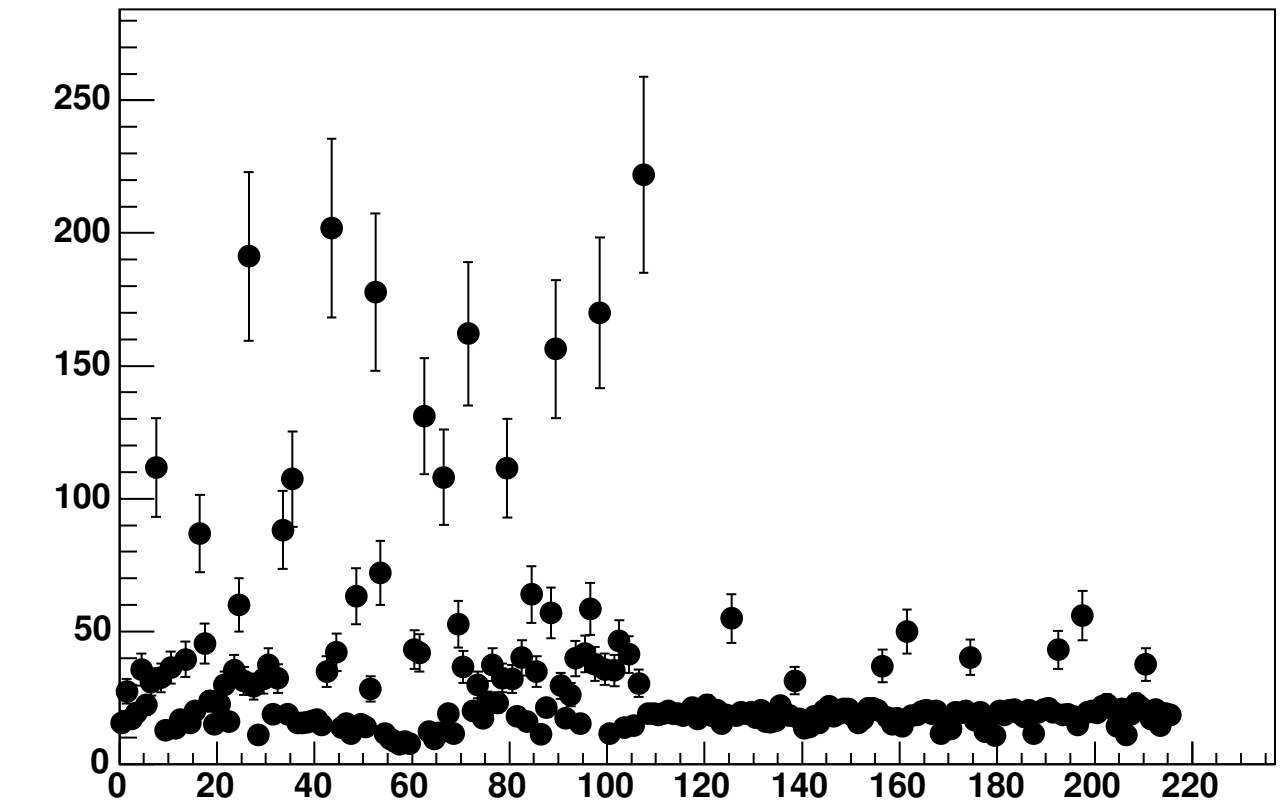
Enable 5, Hold=35, DAC=7200, ADC Noise vs 18*Chip+Chan



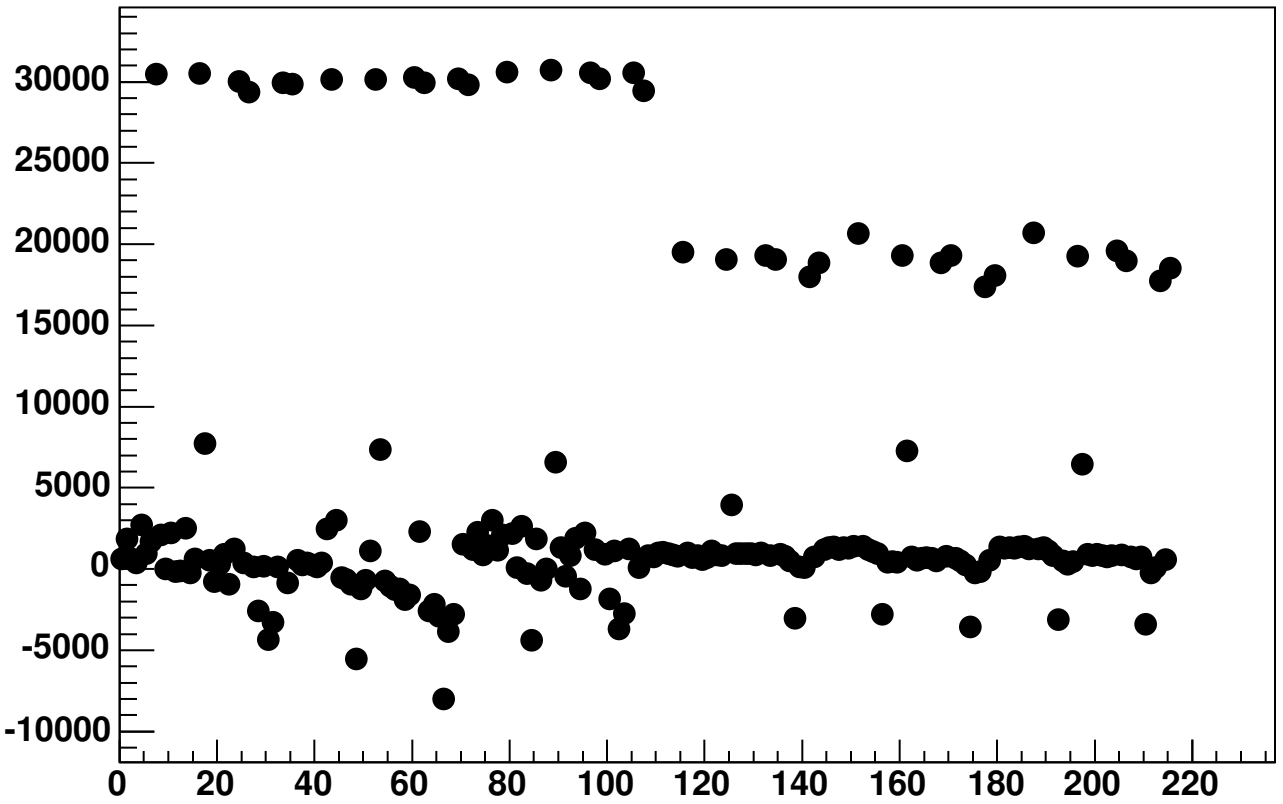
Enable 5, Hold=35, DAC=7600, ADC Mean vs 18*Chip+Chan



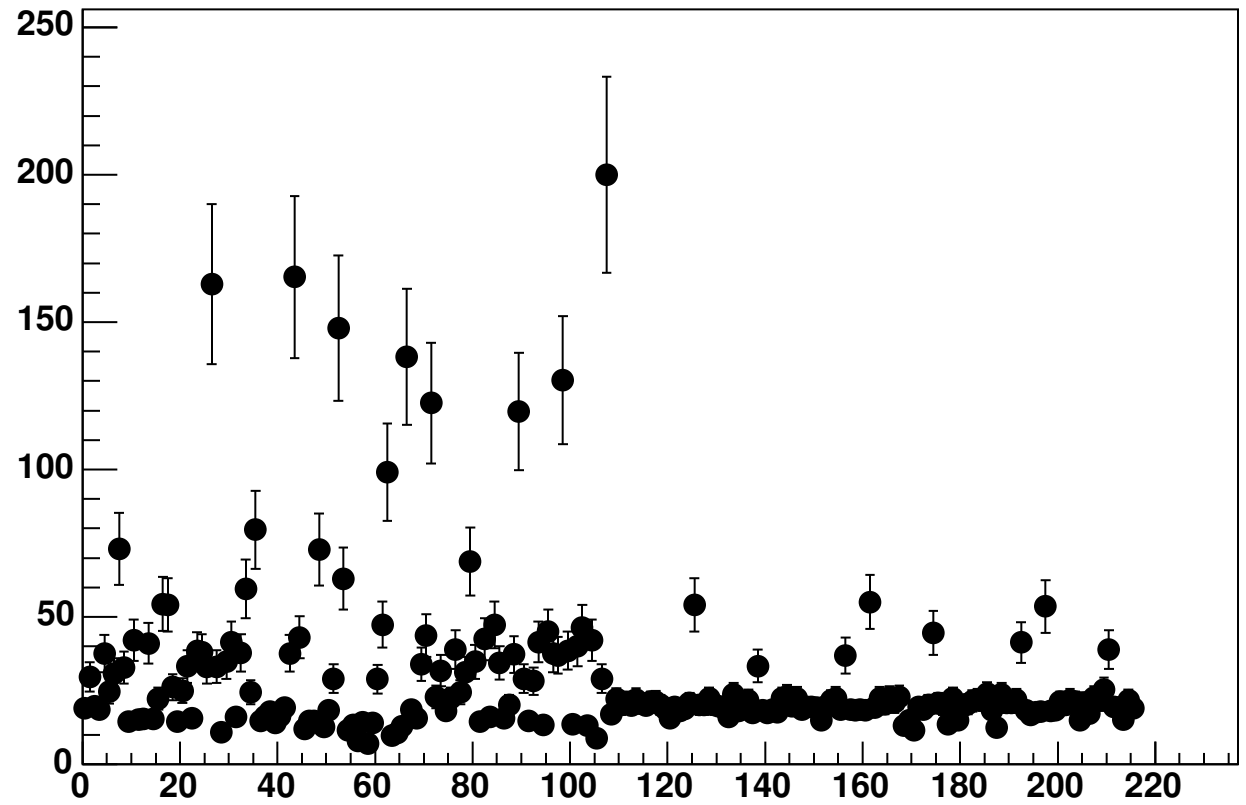
Enable 5, Hold=35, DAC=7600, ADC Noise vs 18*Chip+Chan



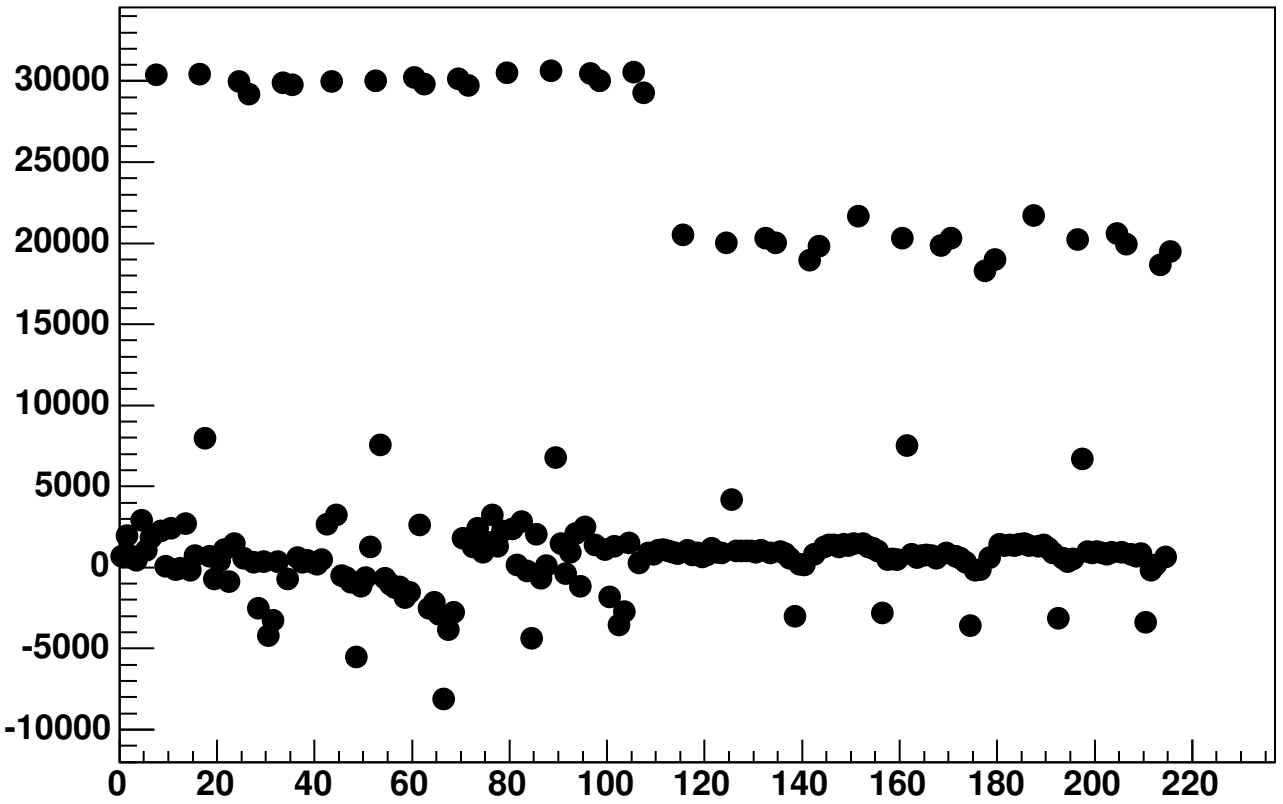
Enable 5, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



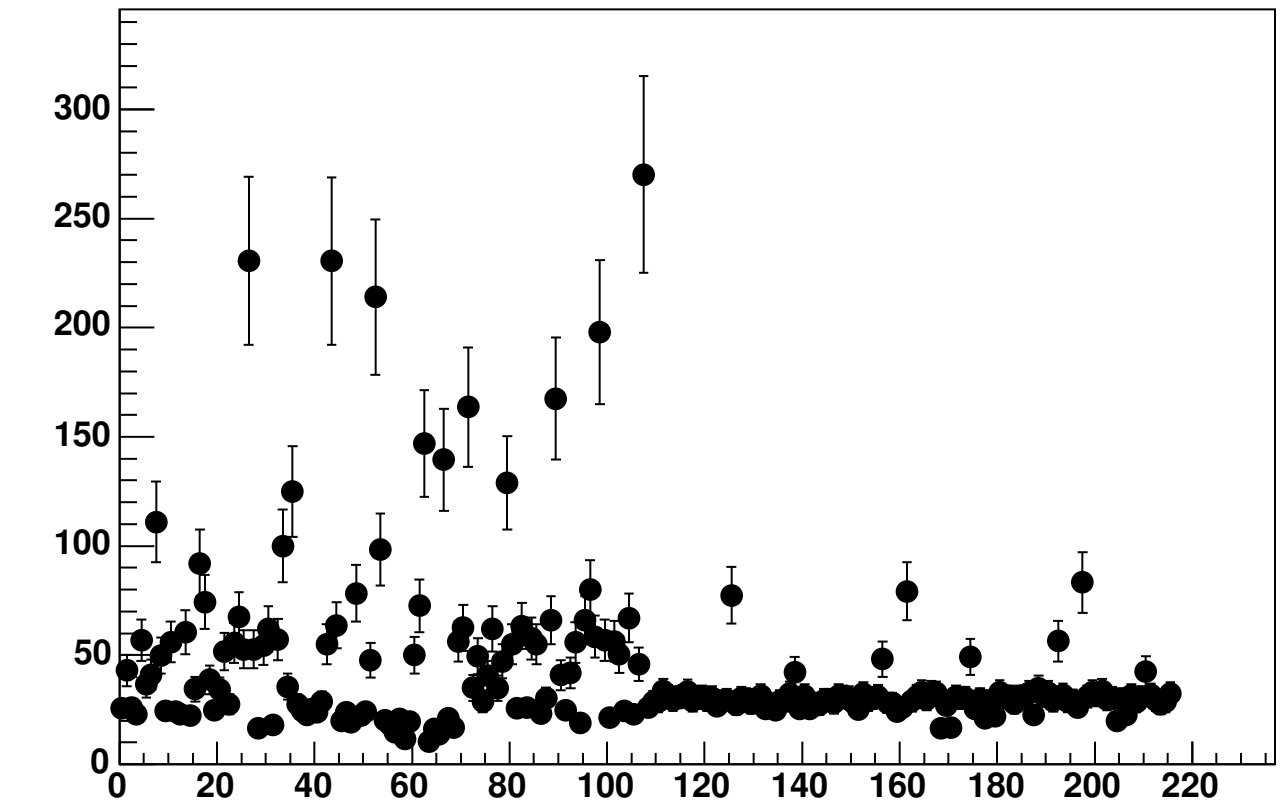
Enable 5, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



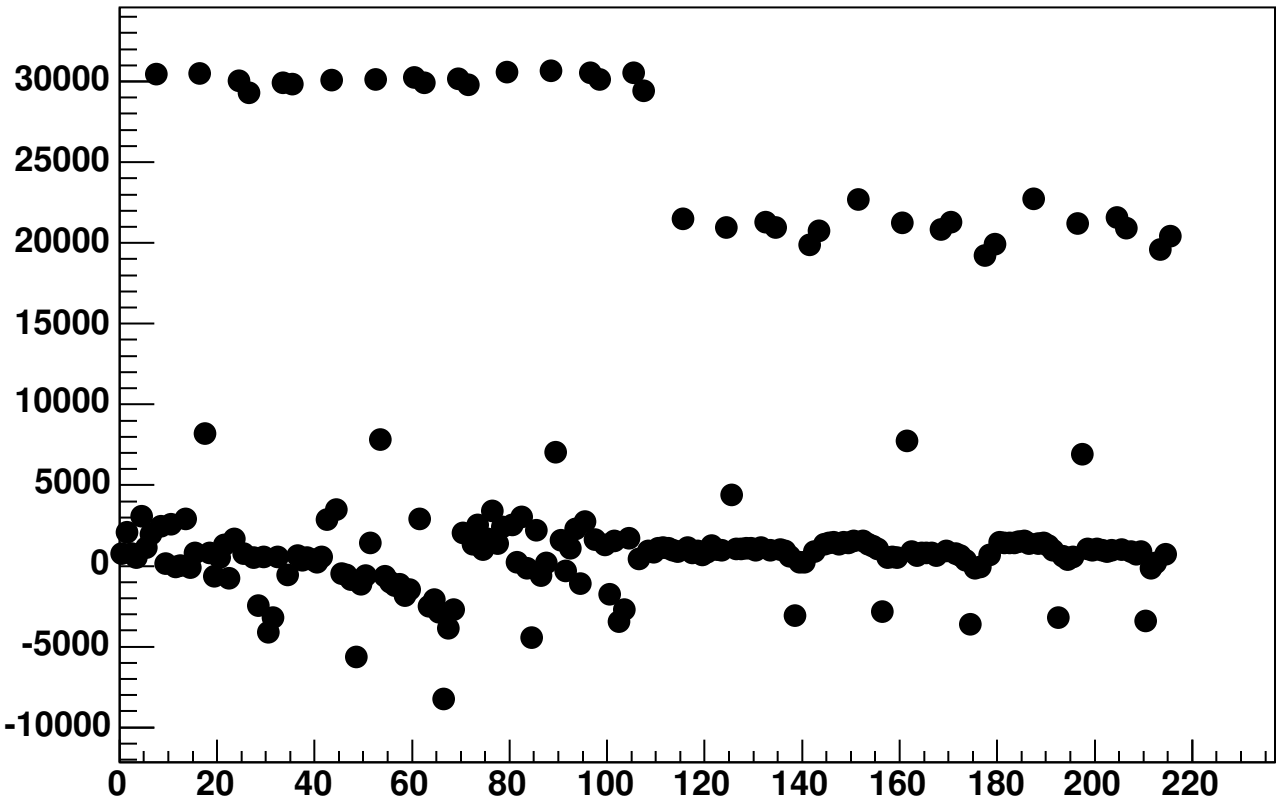
Enable 5, Hold=35, DAC=8400, ADC Mean vs 18*Chip+Chan



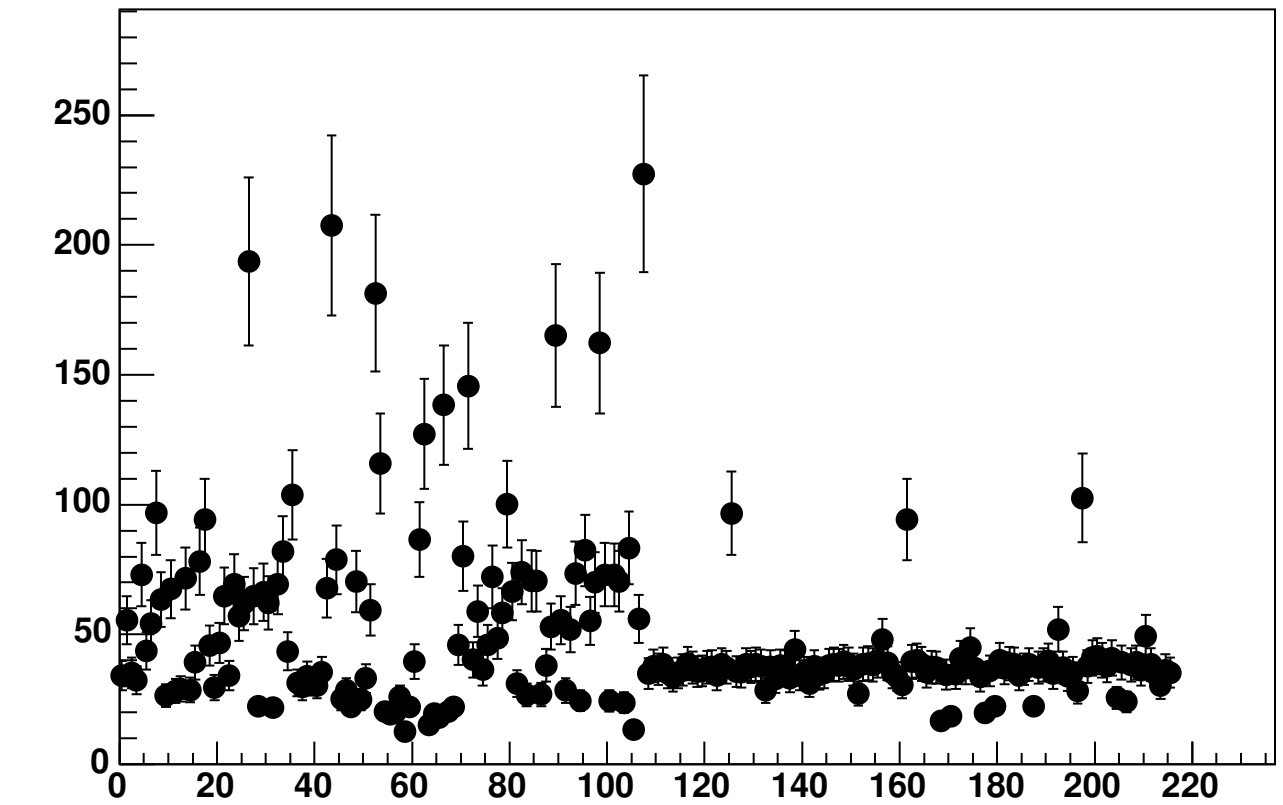
Enable 5, Hold=35, DAC=8400, ADC Noise vs 18*Chip+Chan



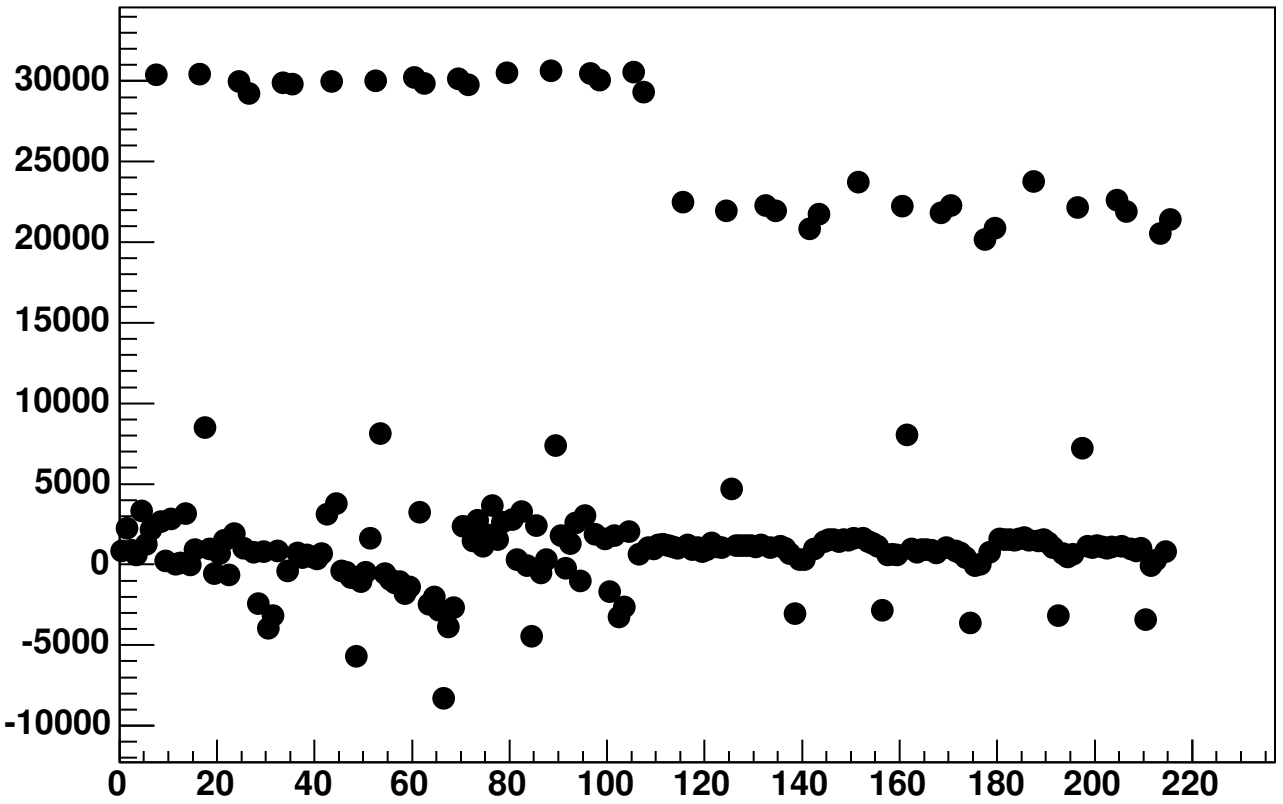
Enable 5, Hold=35, DAC=8800, ADC Mean vs 18*Chip+Chan



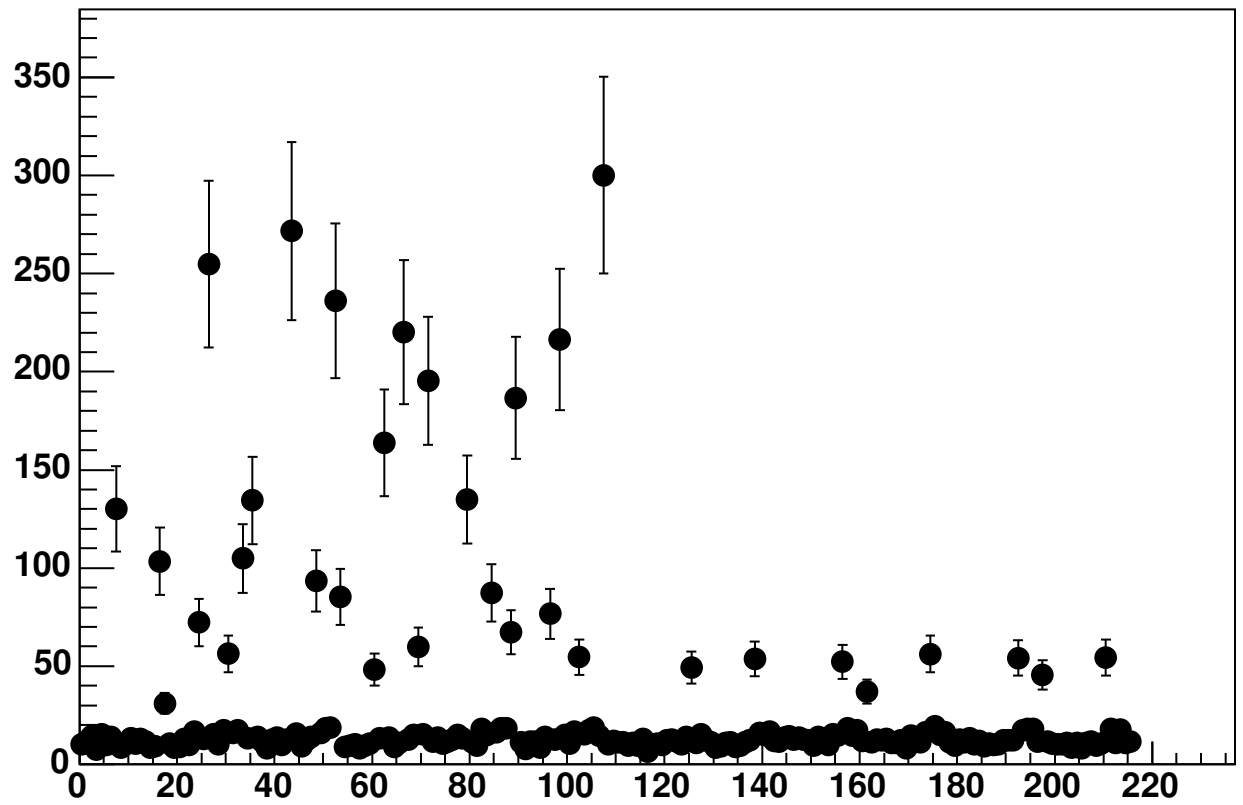
Enable 5, Hold=35, DAC=8800, ADC Noise vs 18*Chip+Chan



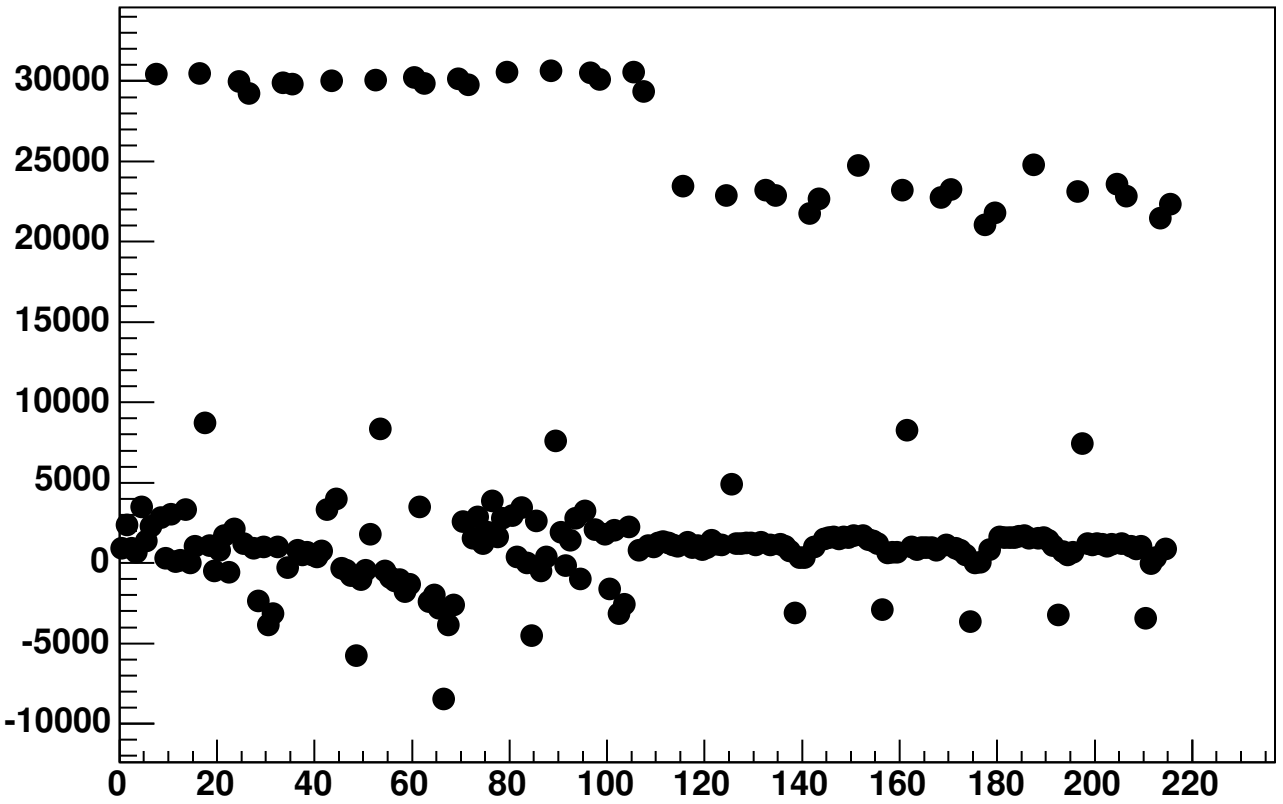
Enable 5, Hold=35, DAC=9200, ADC Mean vs 18*Chip+Chan



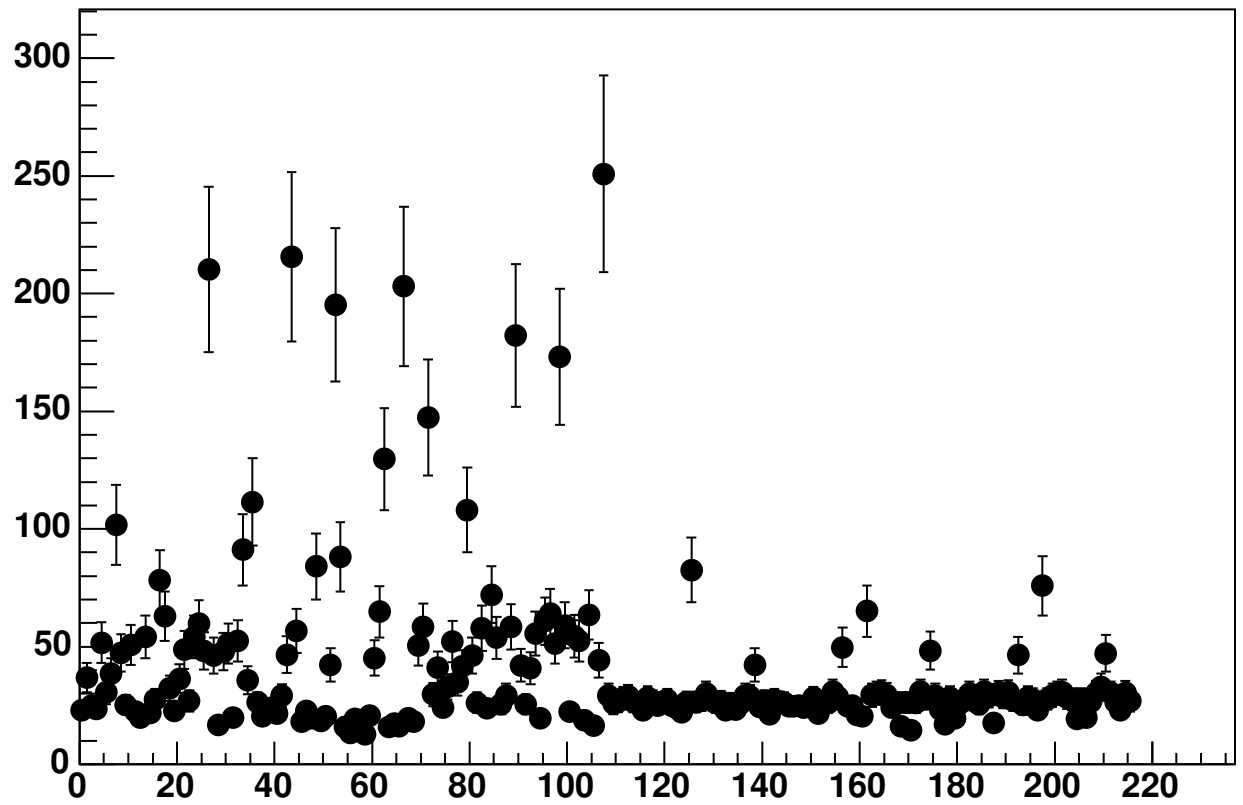
Enable 5, Hold=35, DAC=9200, ADC Noise vs 18*Chip+Chan



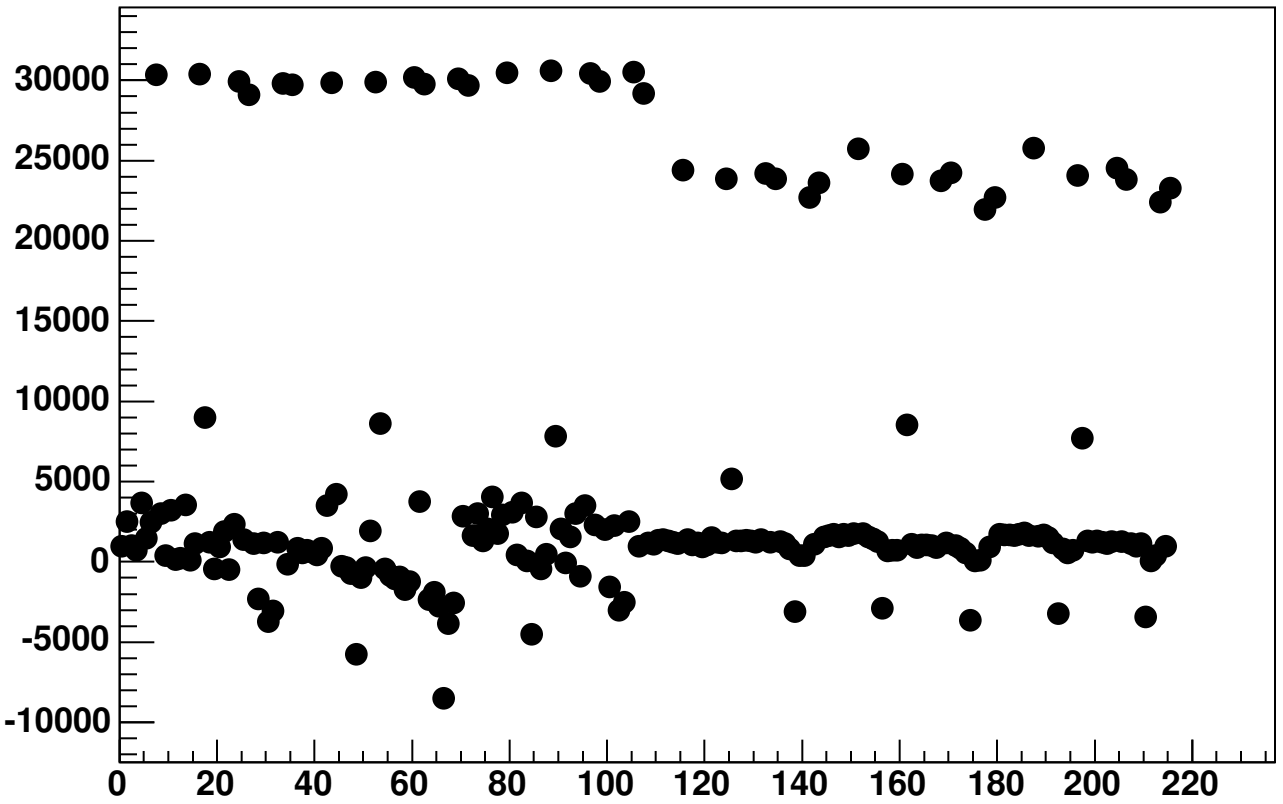
Enable 5, Hold=35, DAC=9600, ADC Mean vs 18*Chip+Chan



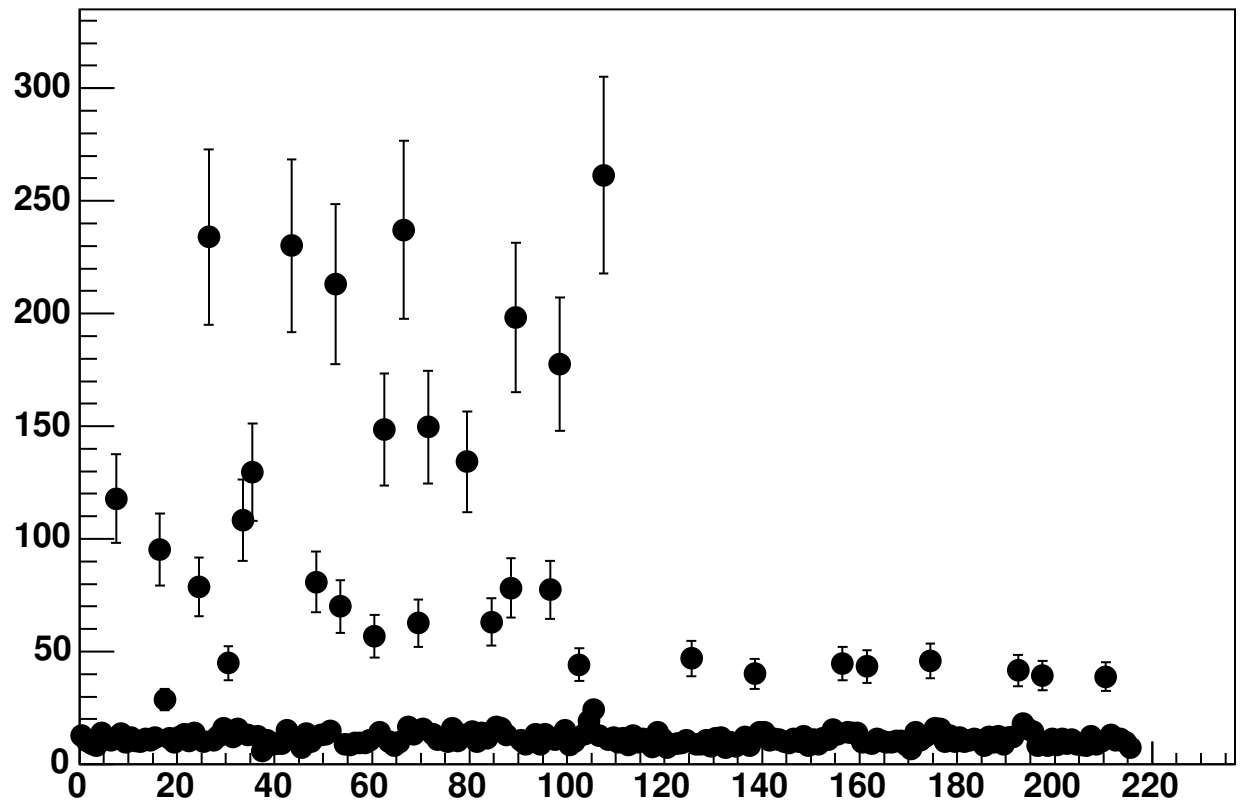
Enable 5, Hold=35, DAC=9600, ADC Noise vs 18*Chip+Chan



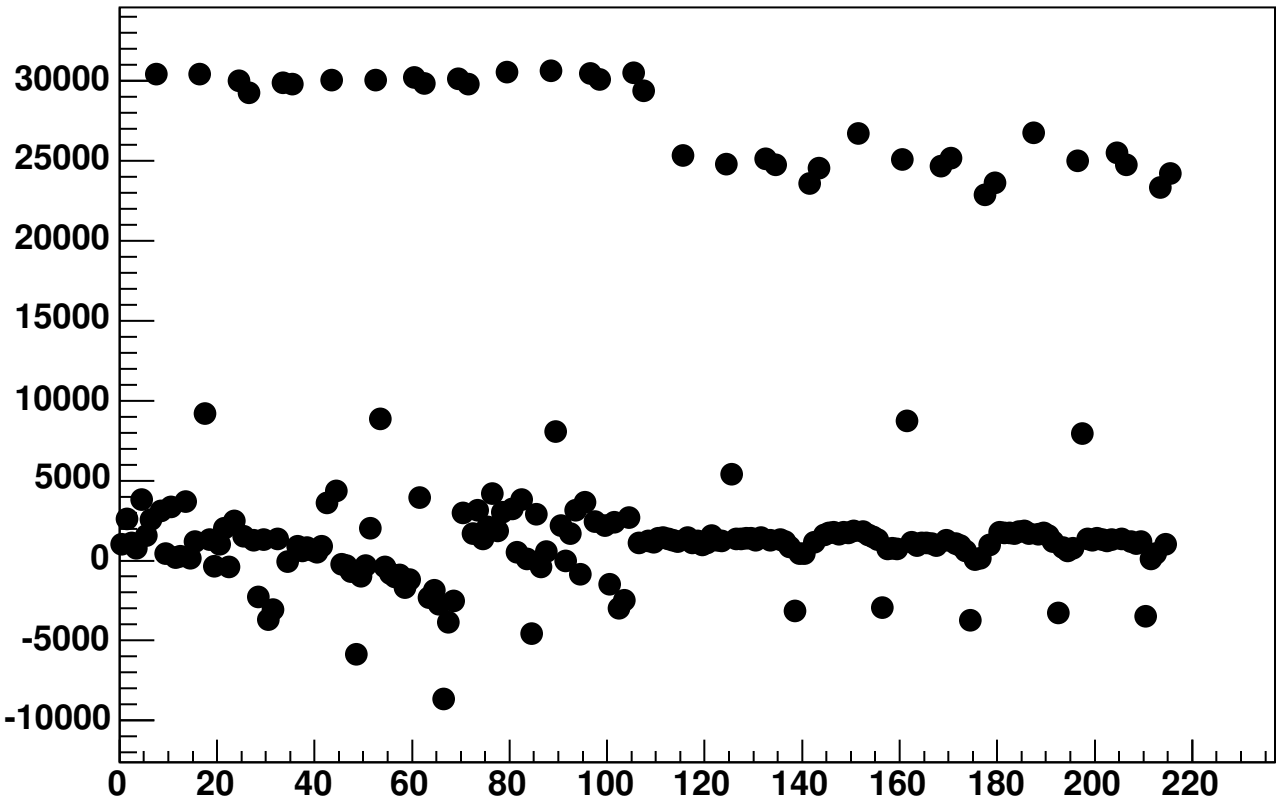
Enable 5, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



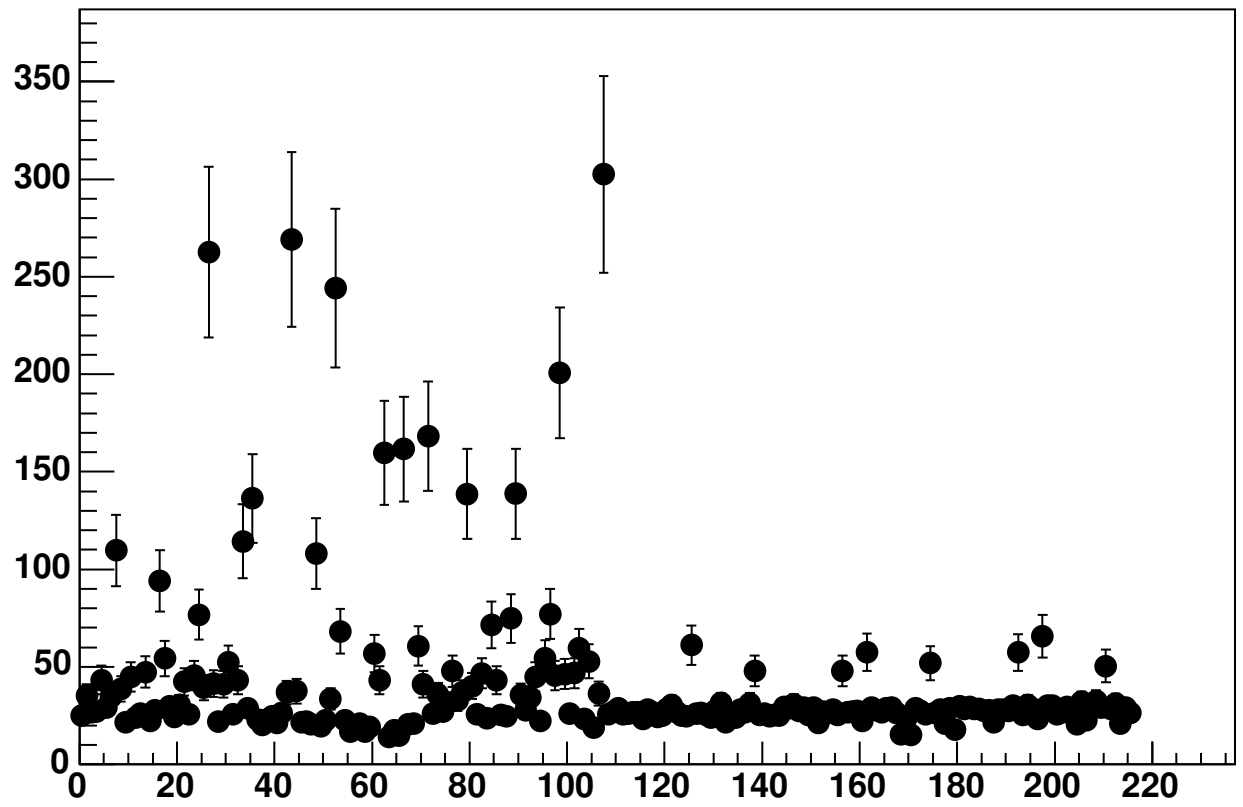
Enable 5, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



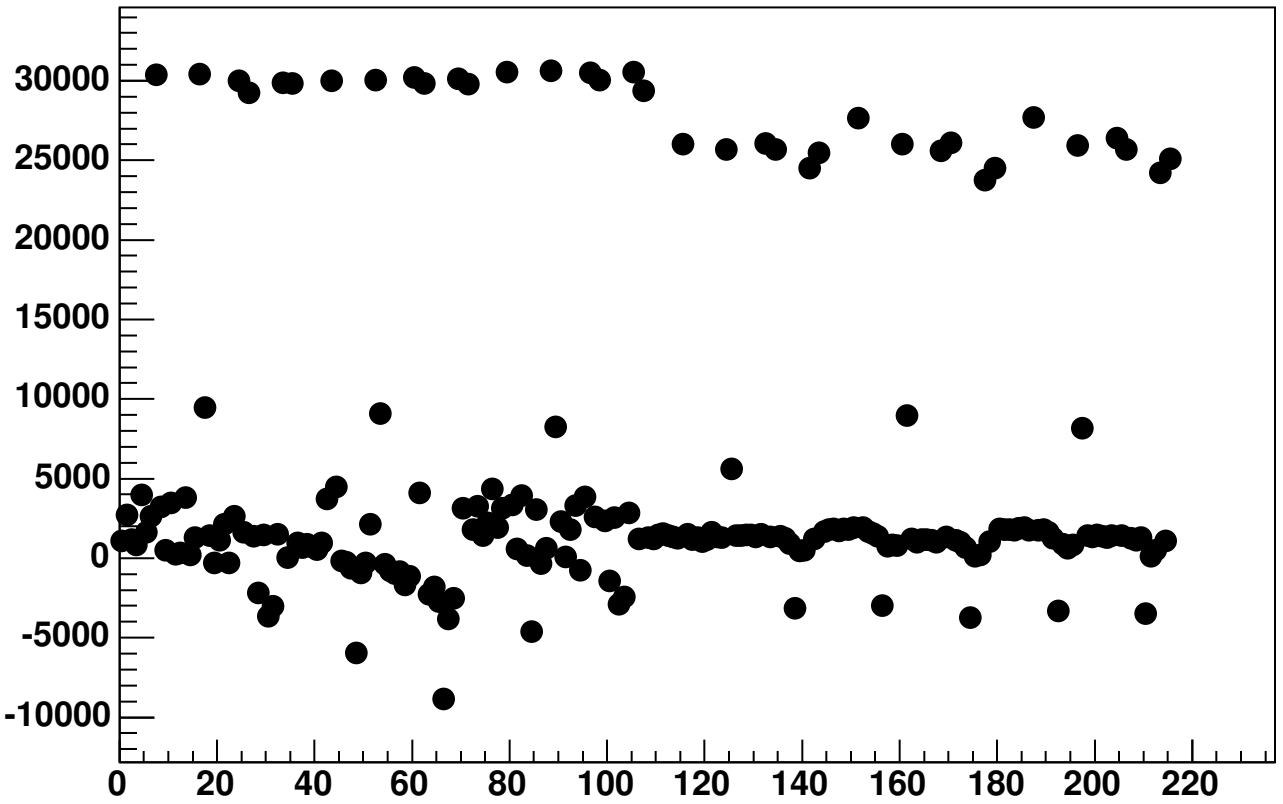
Enable 5, Hold=35, DAC=10400, ADC Mean vs 18*Chip+Chan



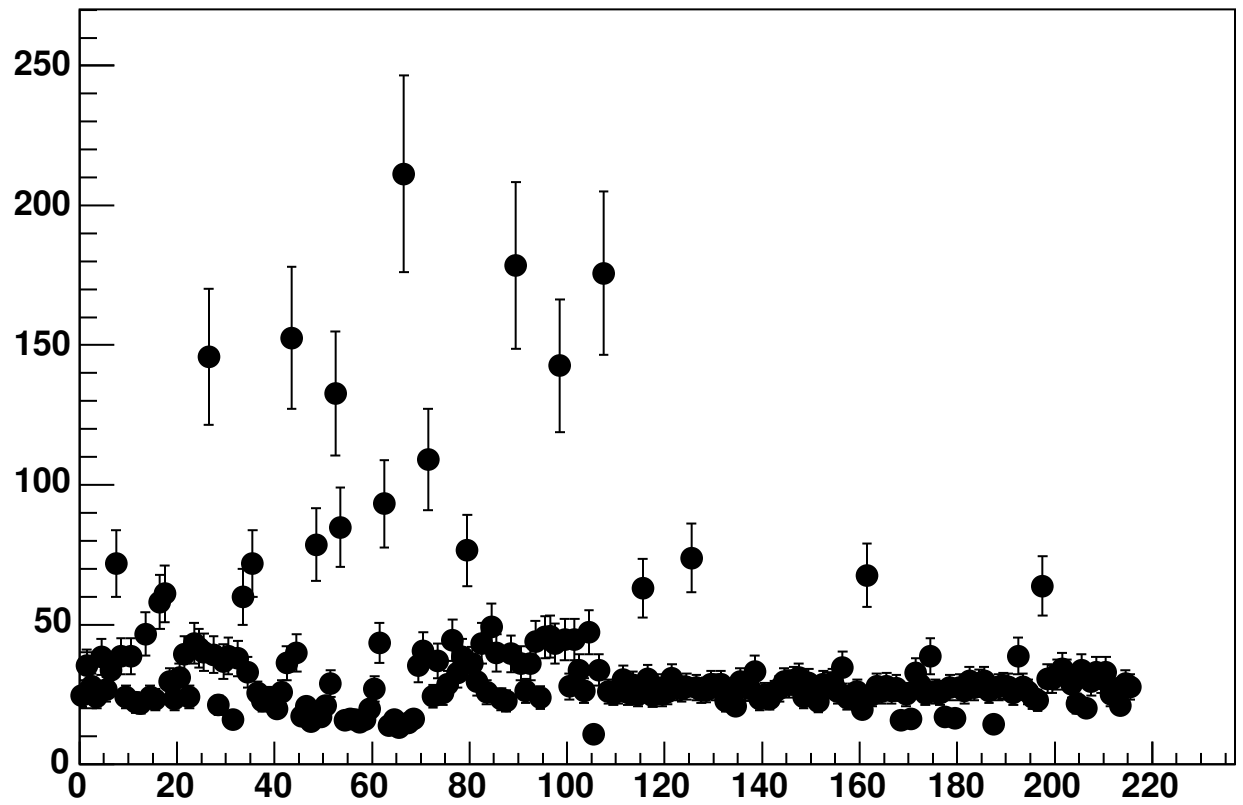
Enable 5, Hold=35, DAC=10400, ADC Noise vs 18*Chip+Chan



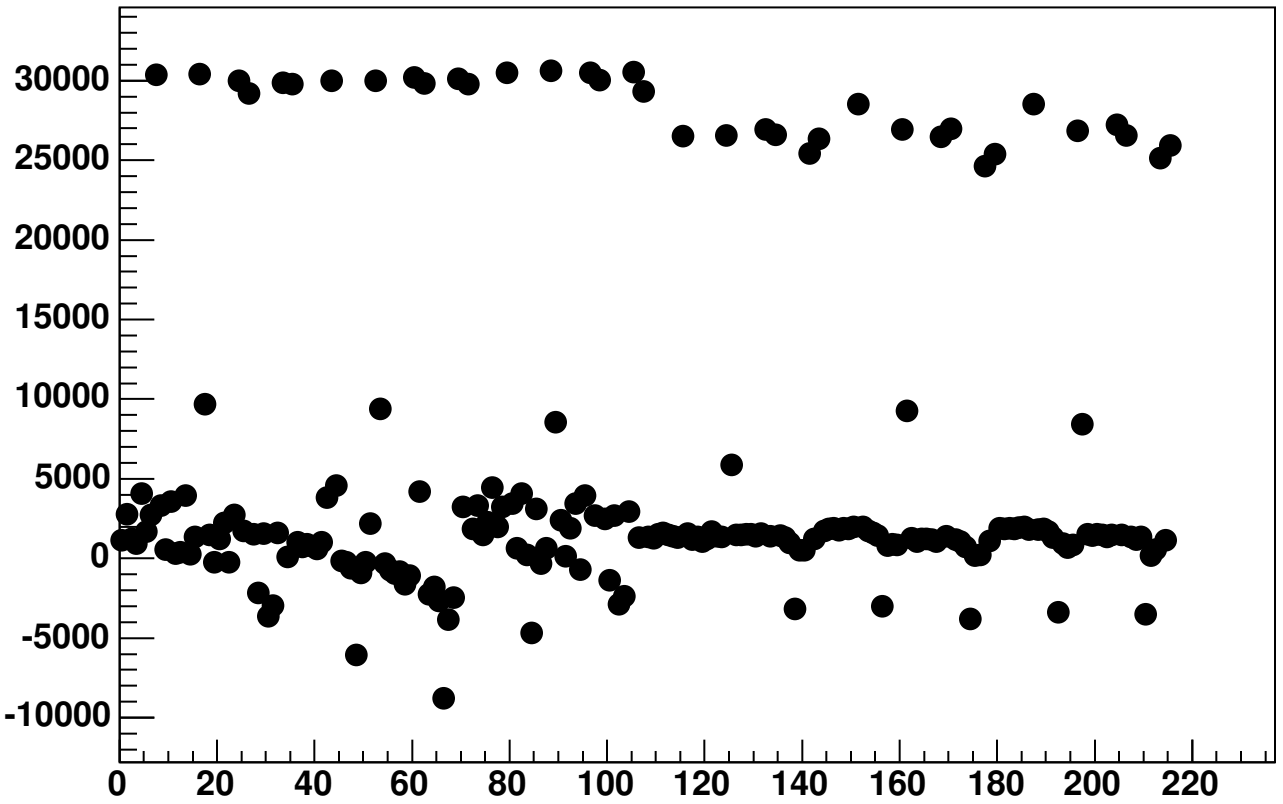
Enable 5, Hold=35, DAC=10800, ADC Mean vs 18*Chip+Chan



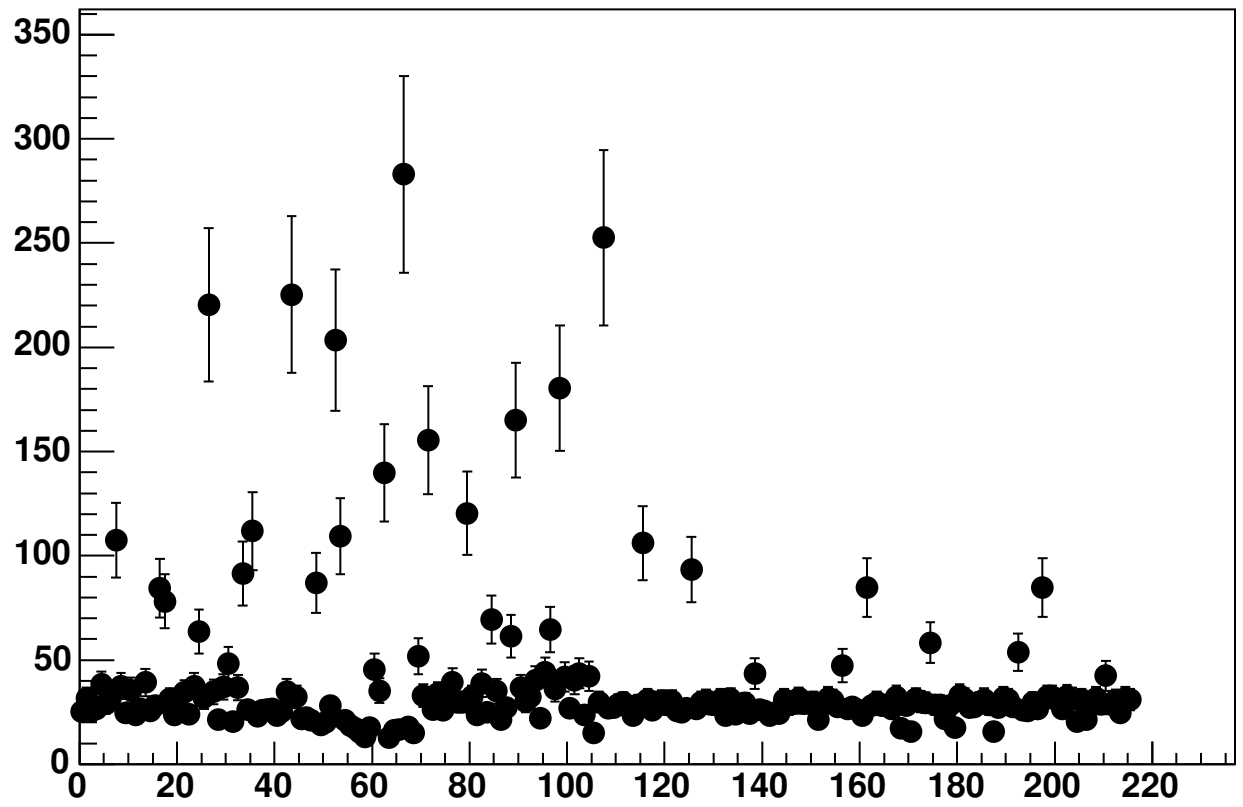
Enable 5, Hold=35, DAC=10800, ADC Noise vs 18*Chip+Chan



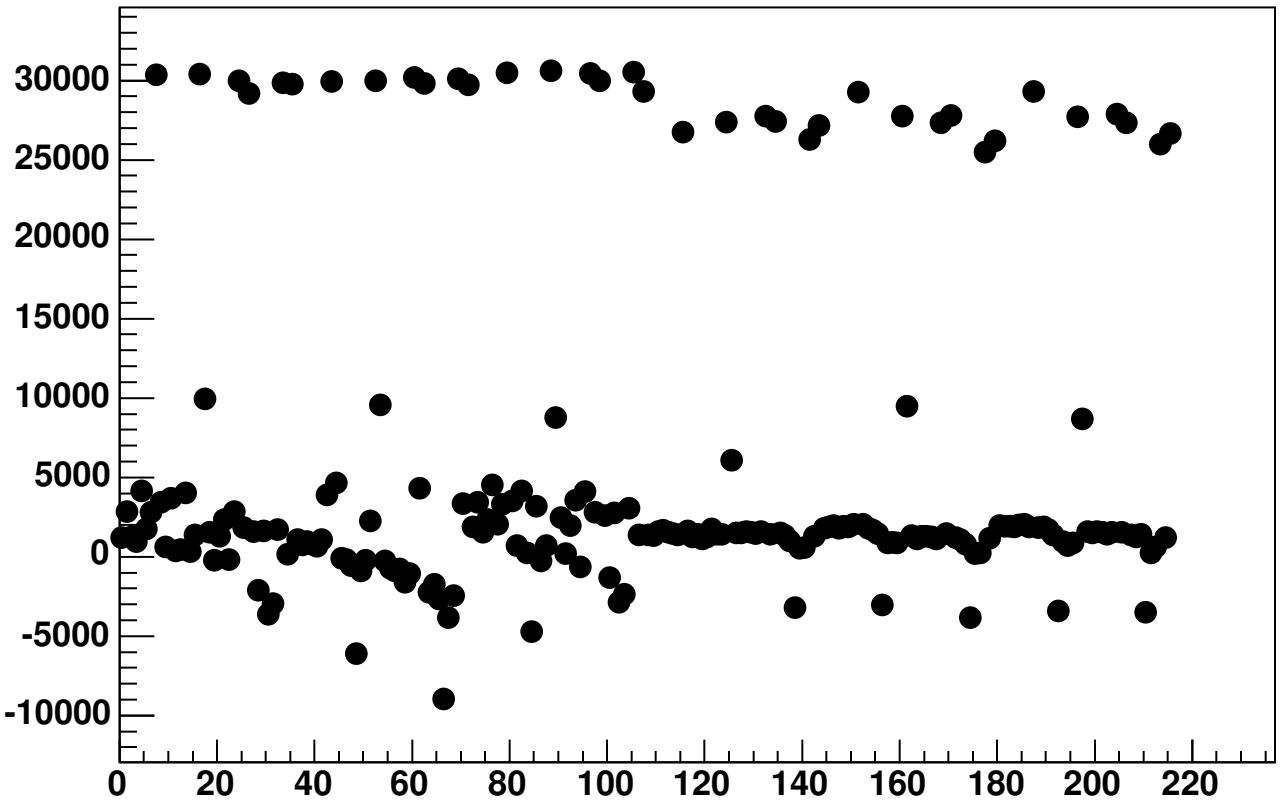
Enable 5, Hold=35, DAC=11200, ADC Mean vs 18*Chip+Chan



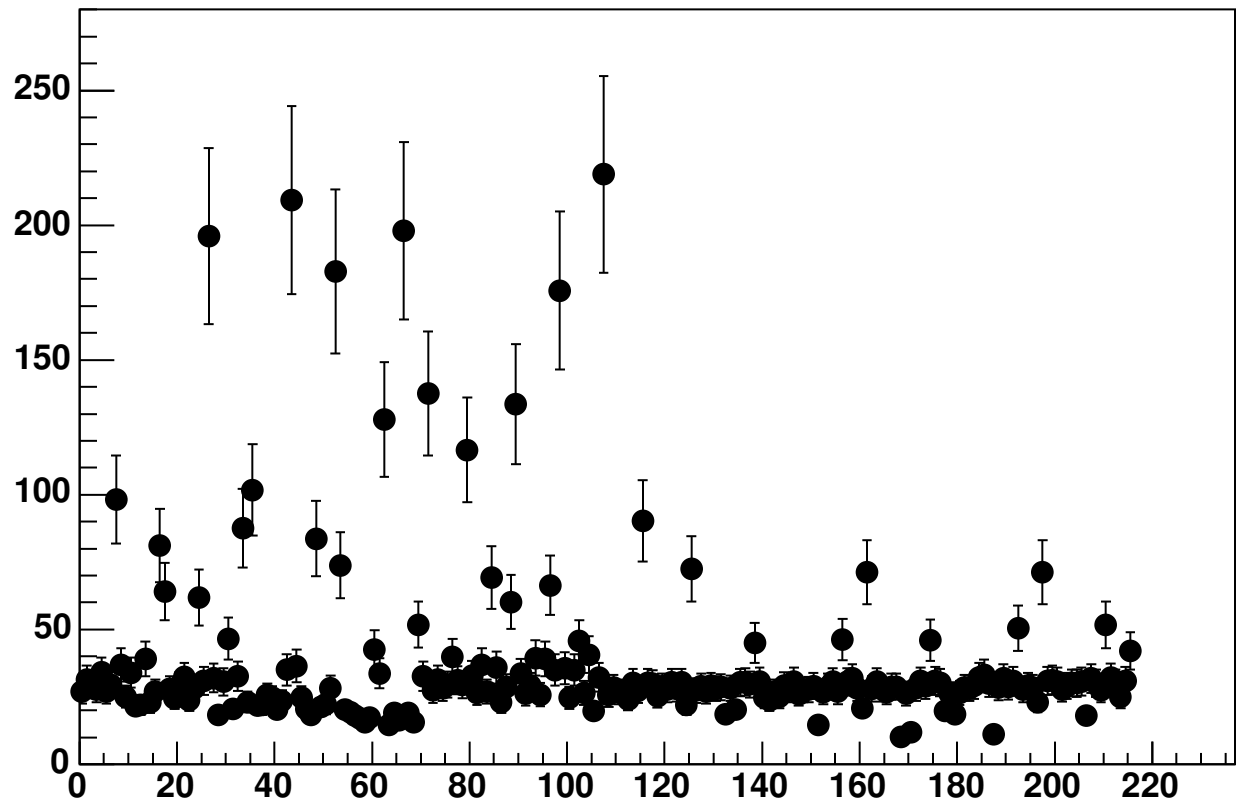
Enable 5, Hold=35, DAC=11200, ADC Noise vs 18*Chip+Chan



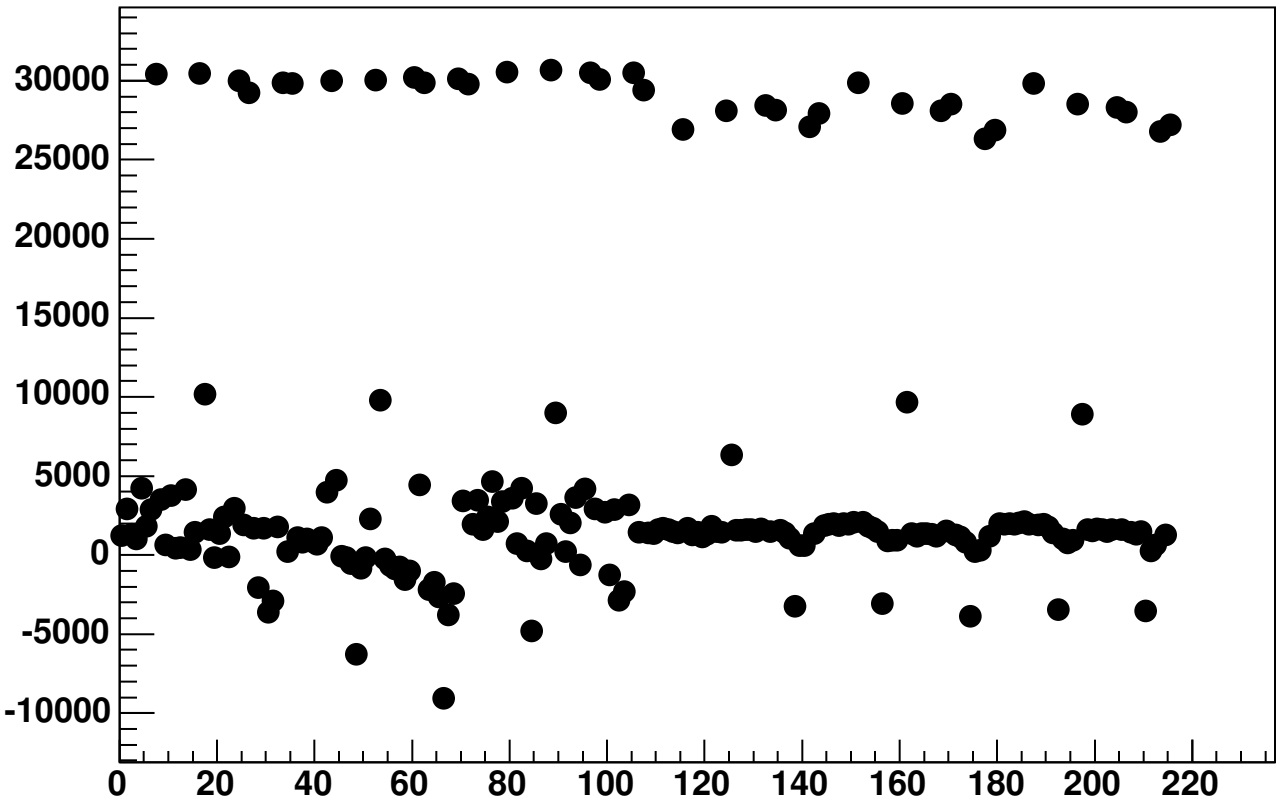
Enable 5, Hold=35, DAC=11600, ADC Mean vs 18*Chip+Chan



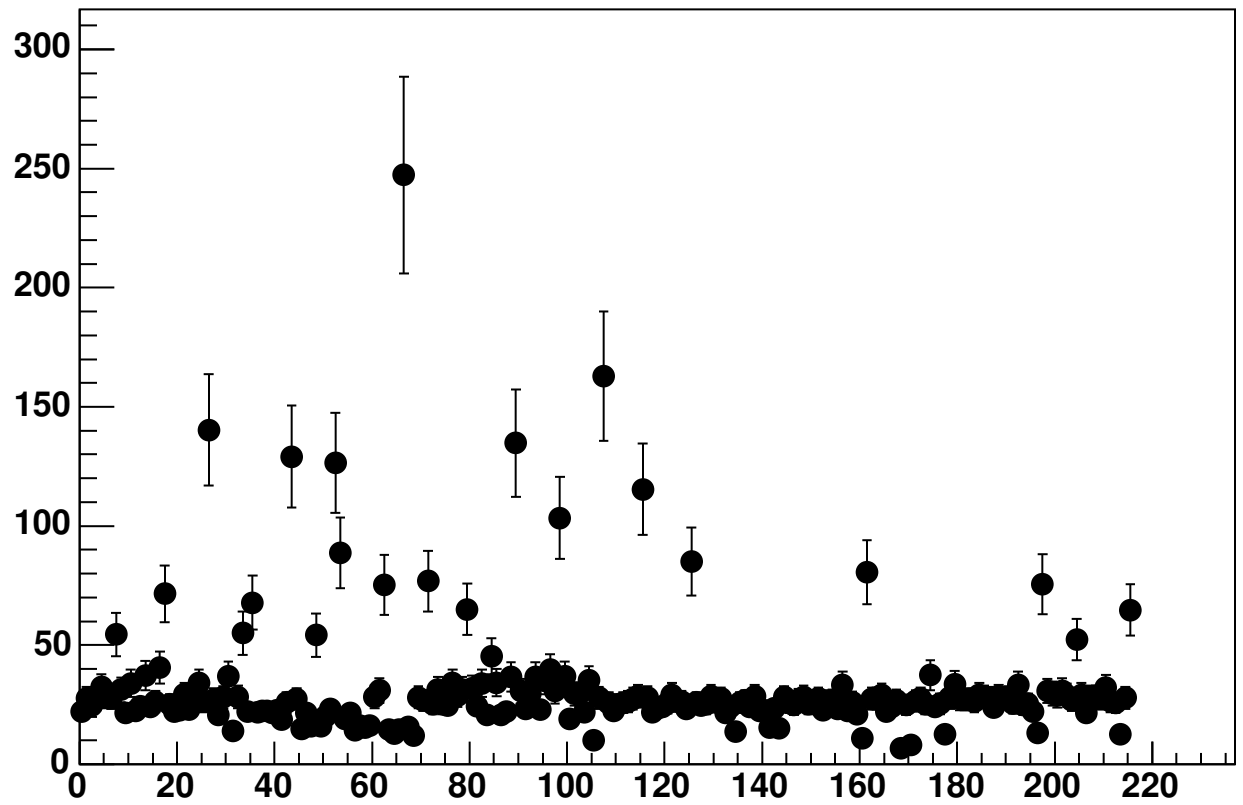
Enable 5, Hold=35, DAC=11600, ADC Noise vs 18*Chip+Chan



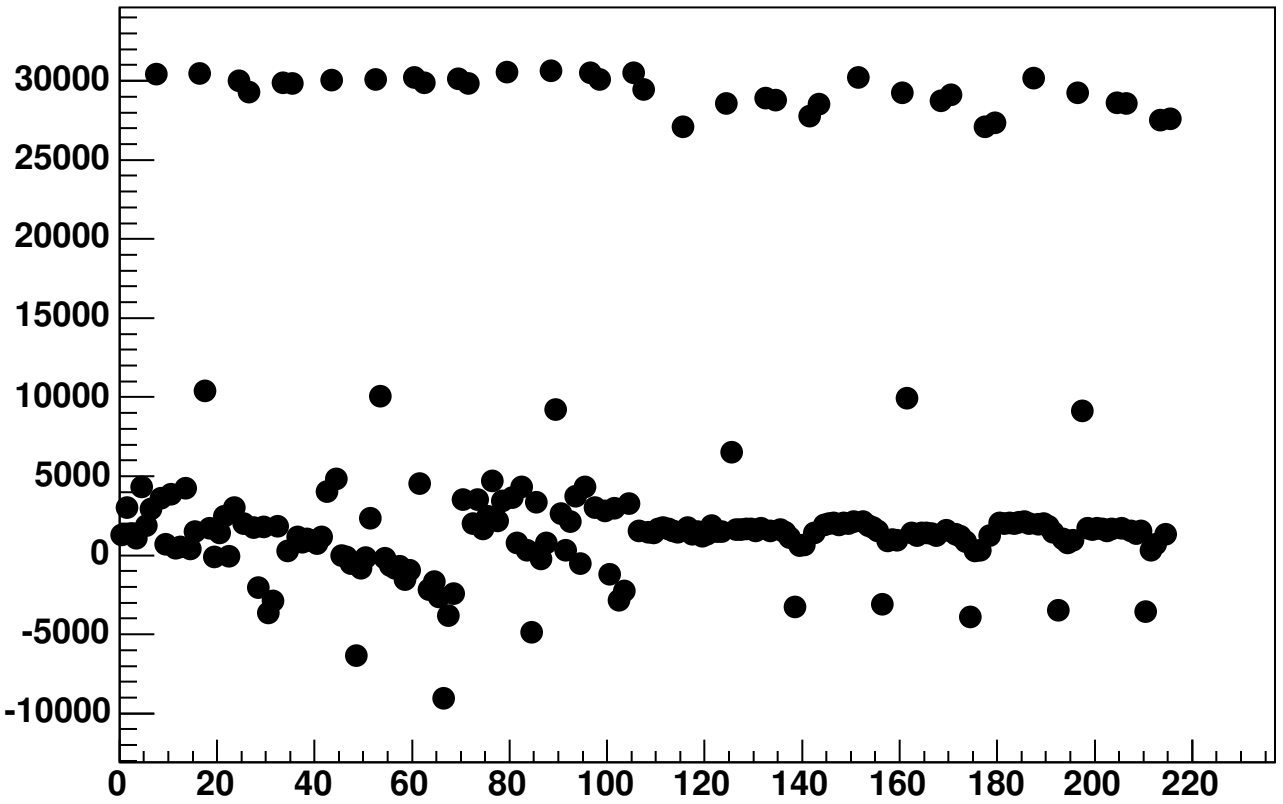
Enable 5, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



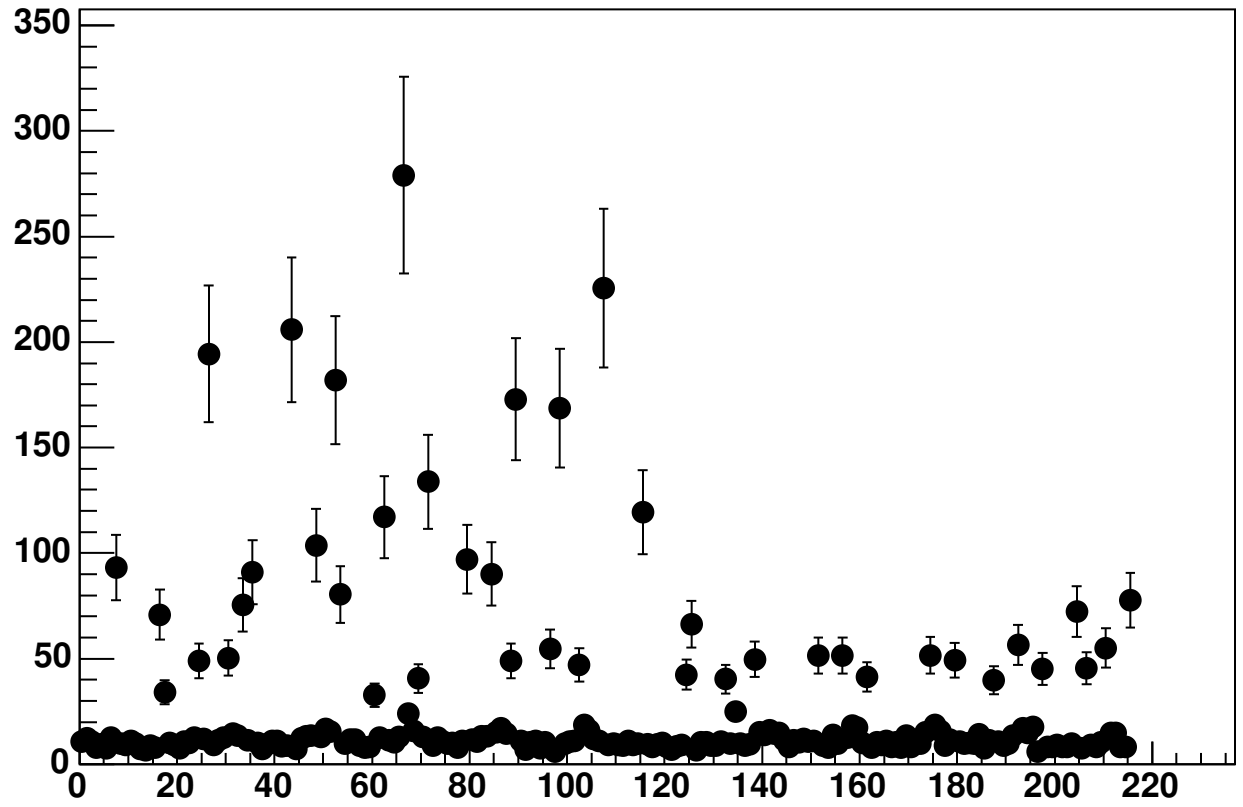
Enable 5, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



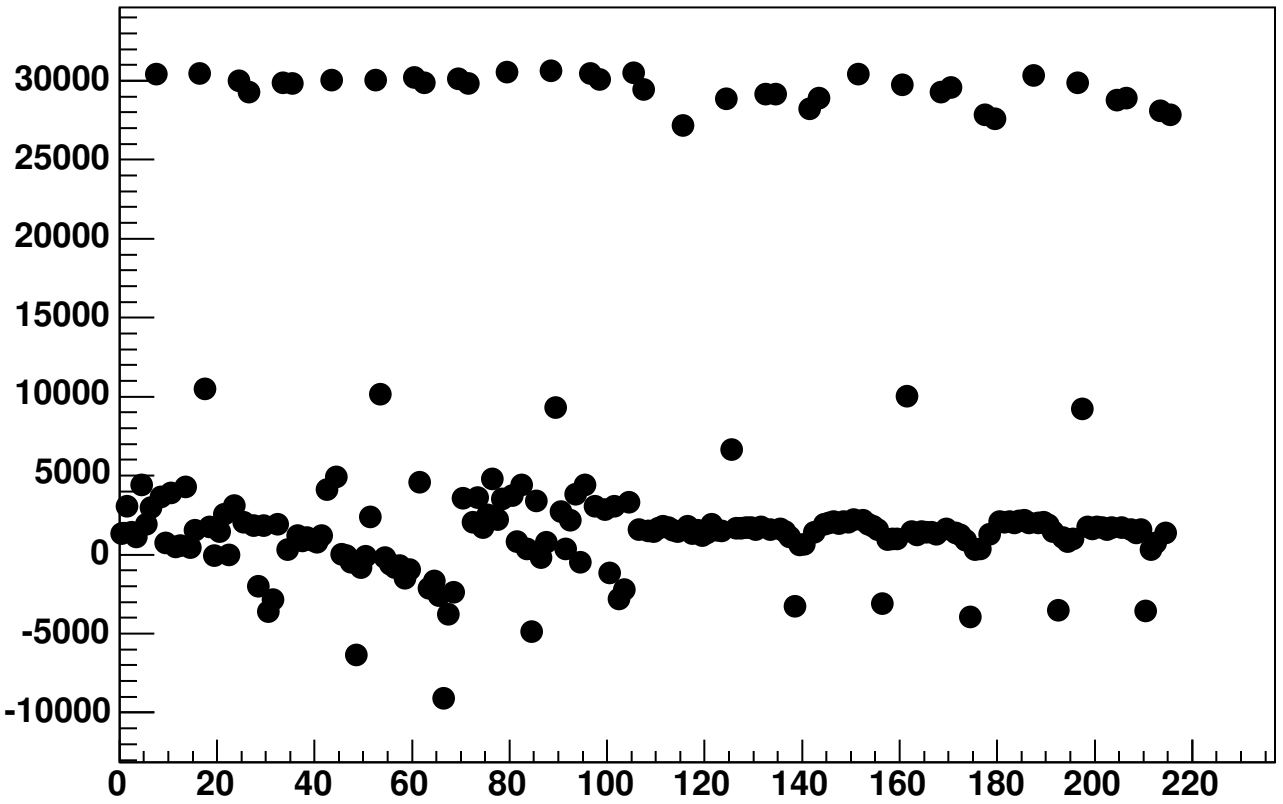
Enable 5, Hold=35, DAC=12400, ADC Mean vs 18*Chip+Chan



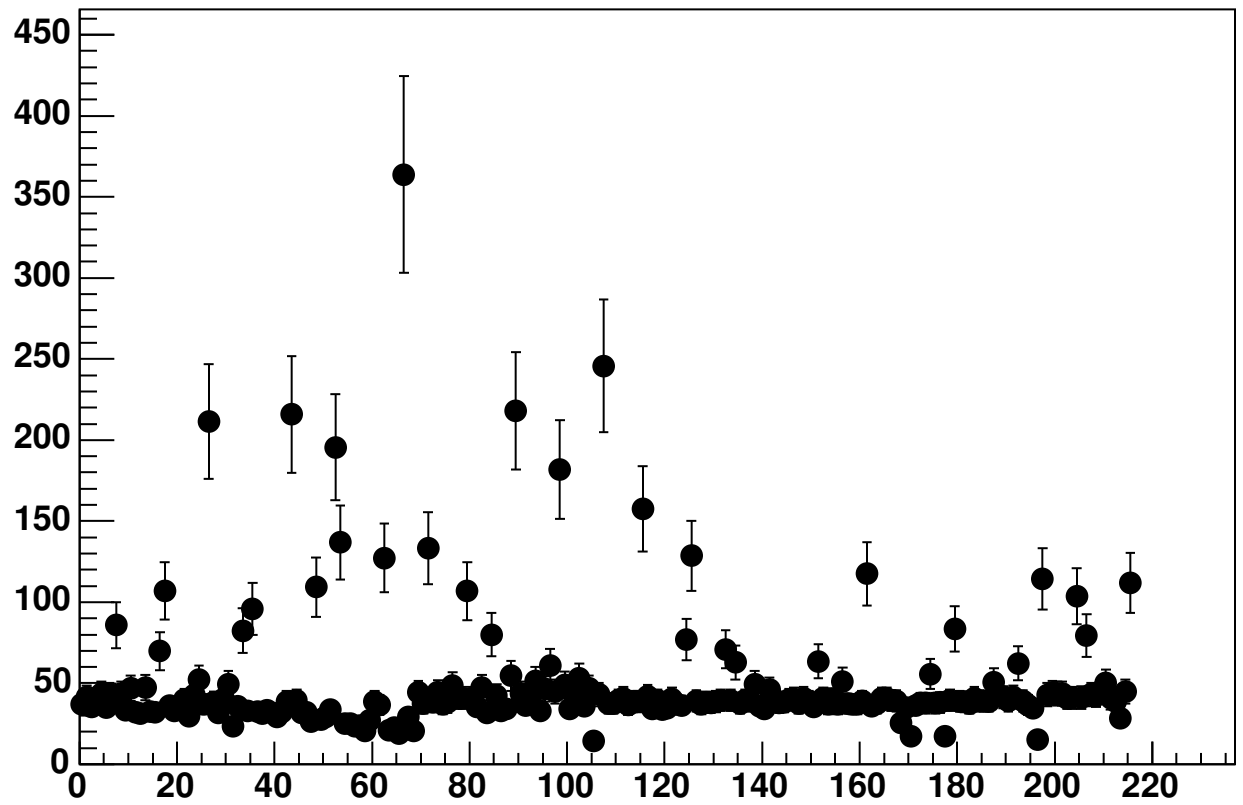
Enable 5, Hold=35, DAC=12400, ADC Noise vs 18*Chip+Chan



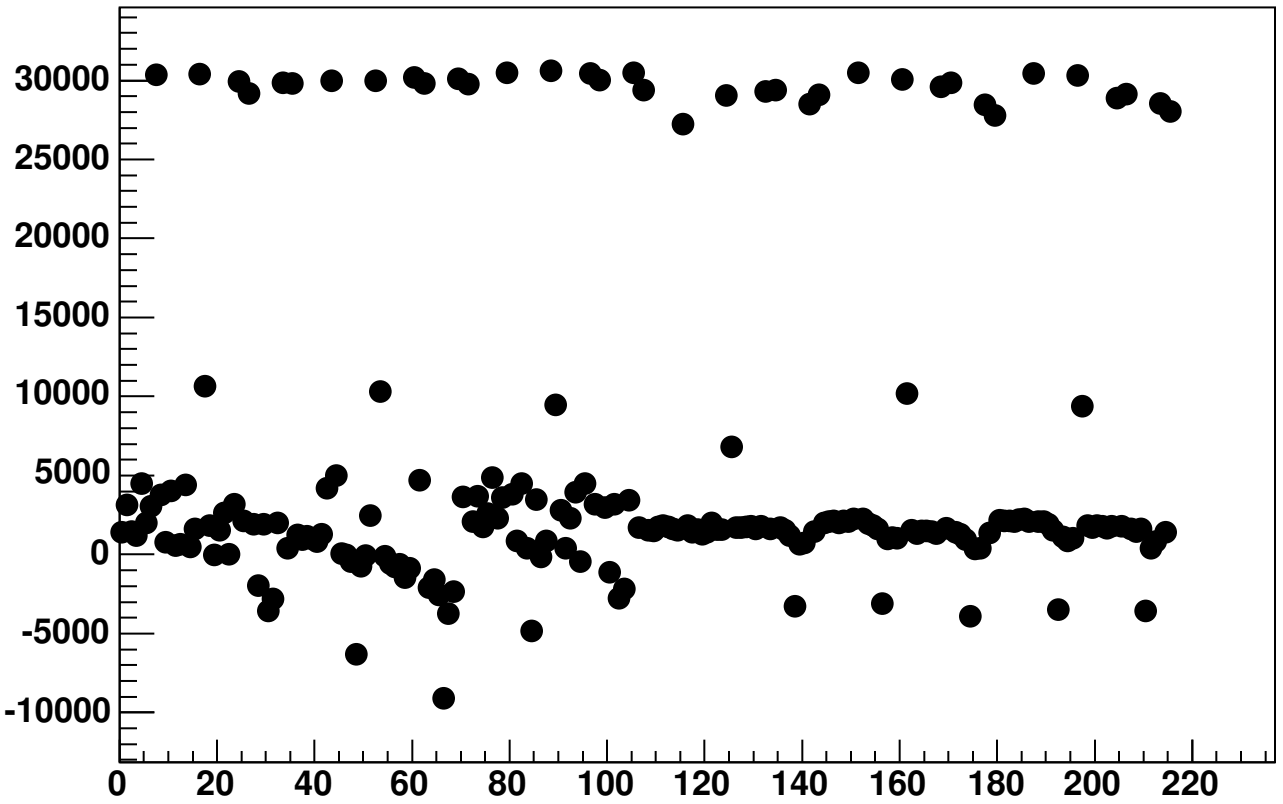
Enable 5, Hold=35, DAC=12800, ADC Mean vs 18*Chip+Chan



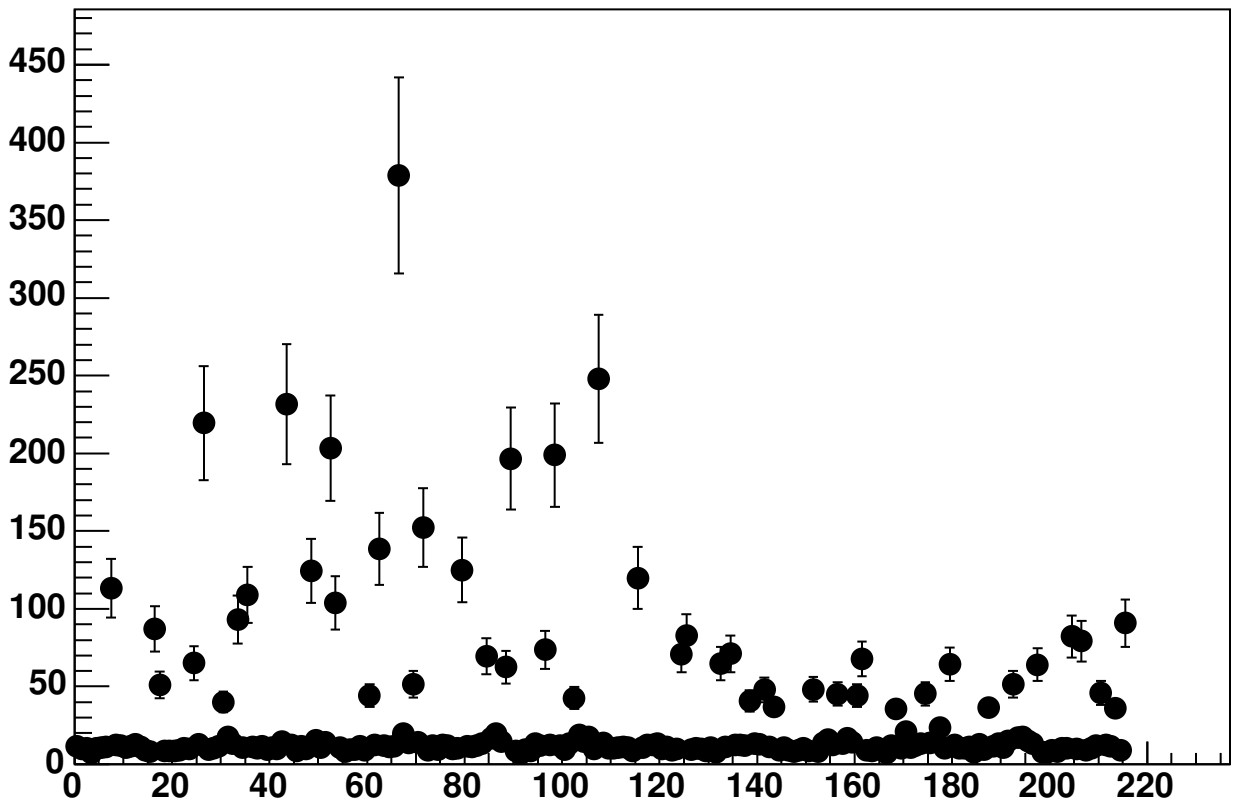
Enable 5, Hold=35, DAC=12800, ADC Noise vs 18*Chip+Chan

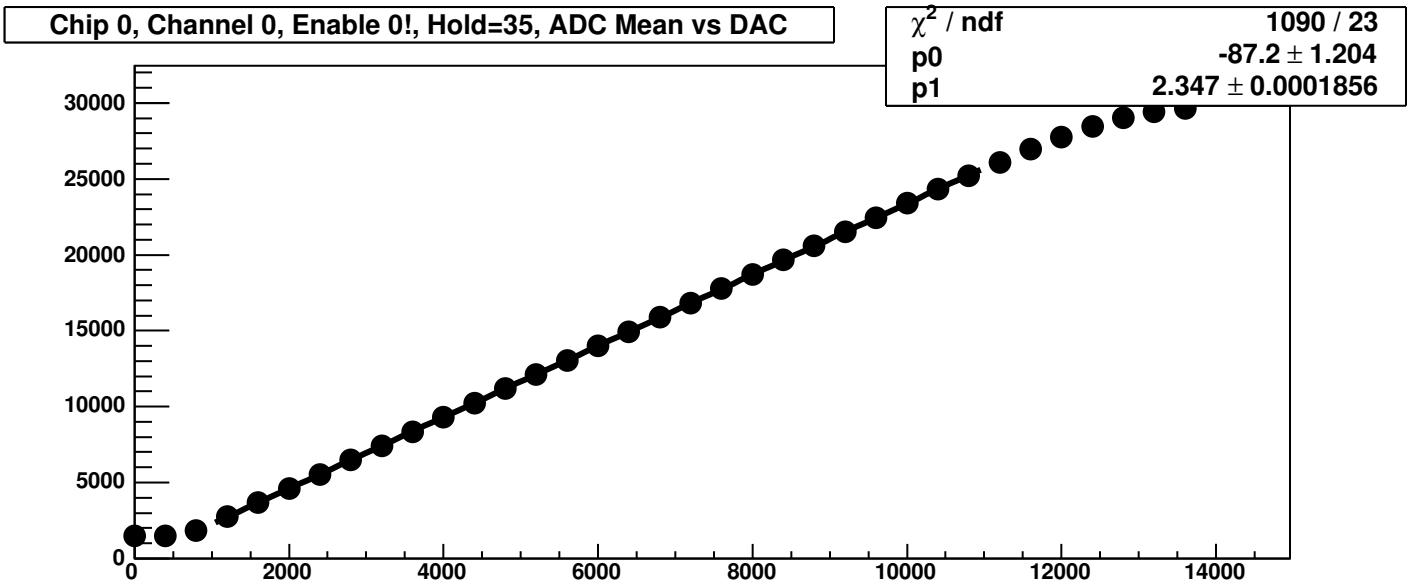


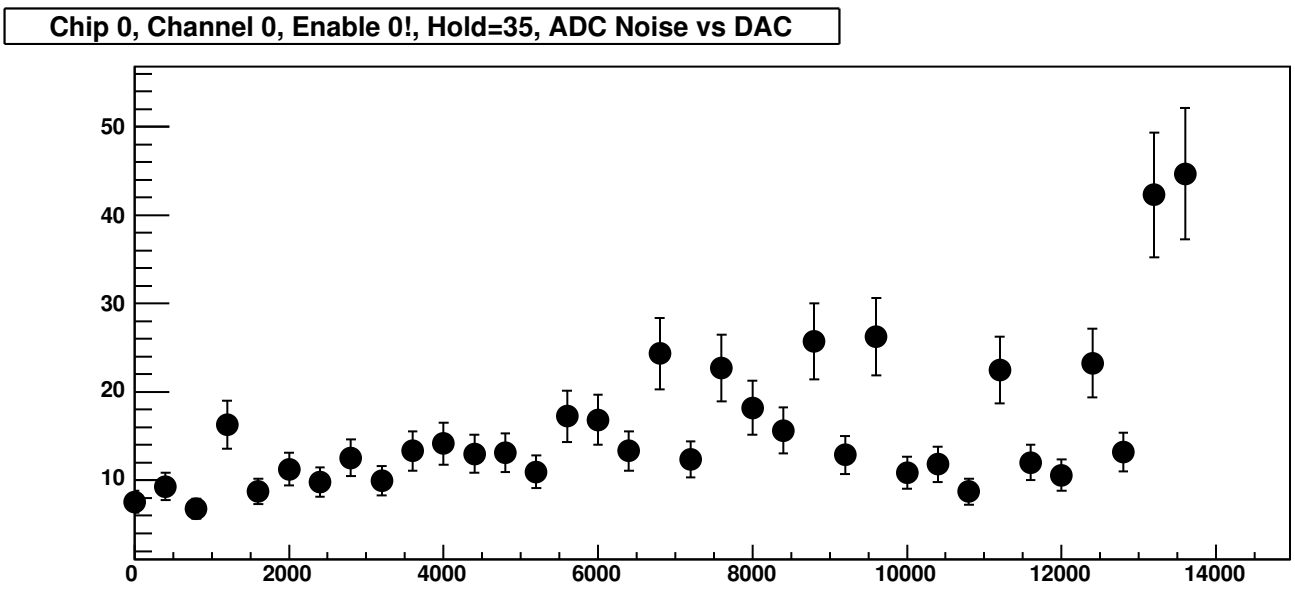
Enable 5, Hold=35, DAC=13200, ADC Mean vs 18*Chip+Chan

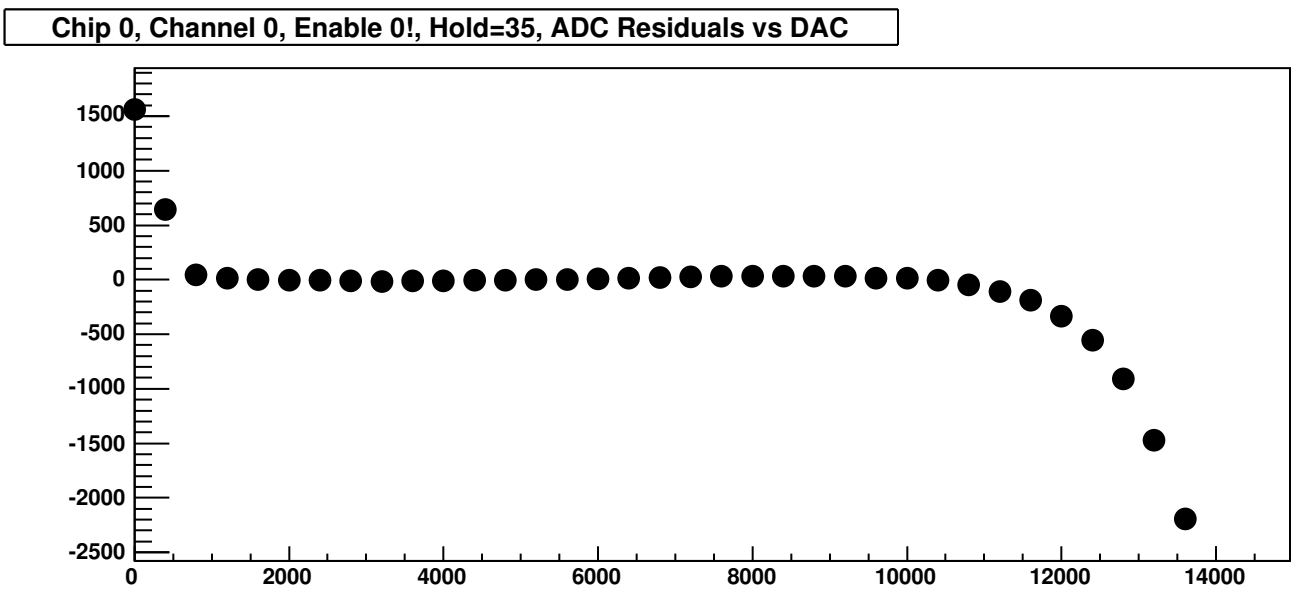


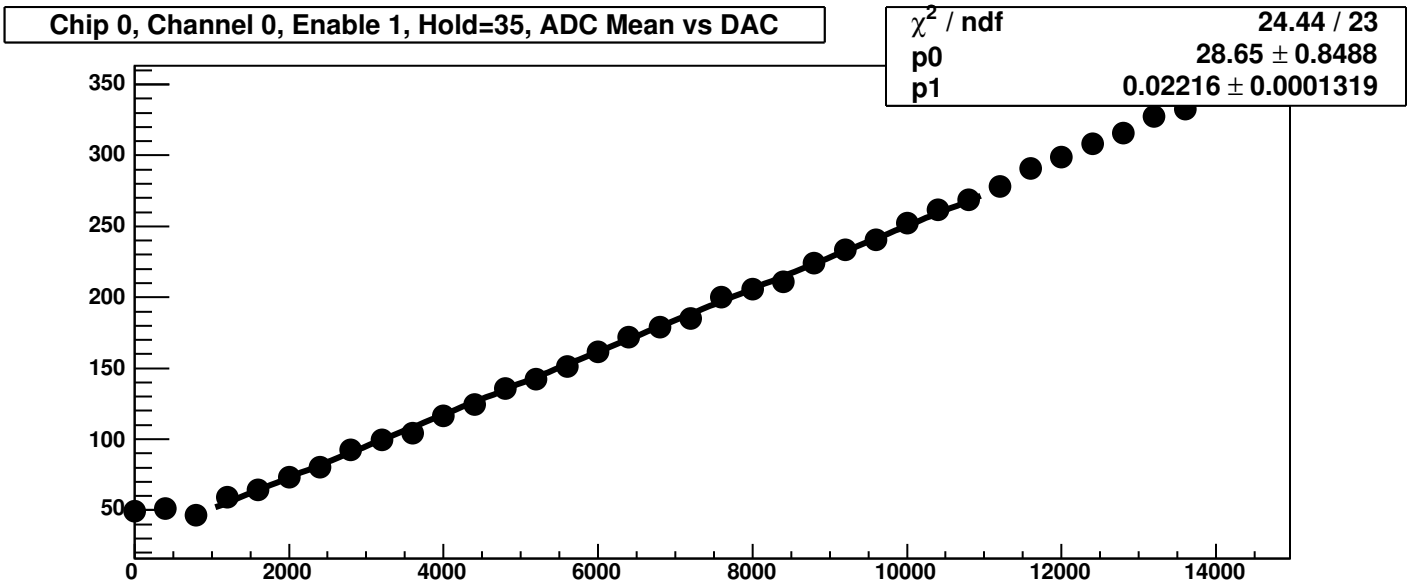
Enable 5, Hold=35, DAC=13200, ADC Noise vs 18*Chip+Chan

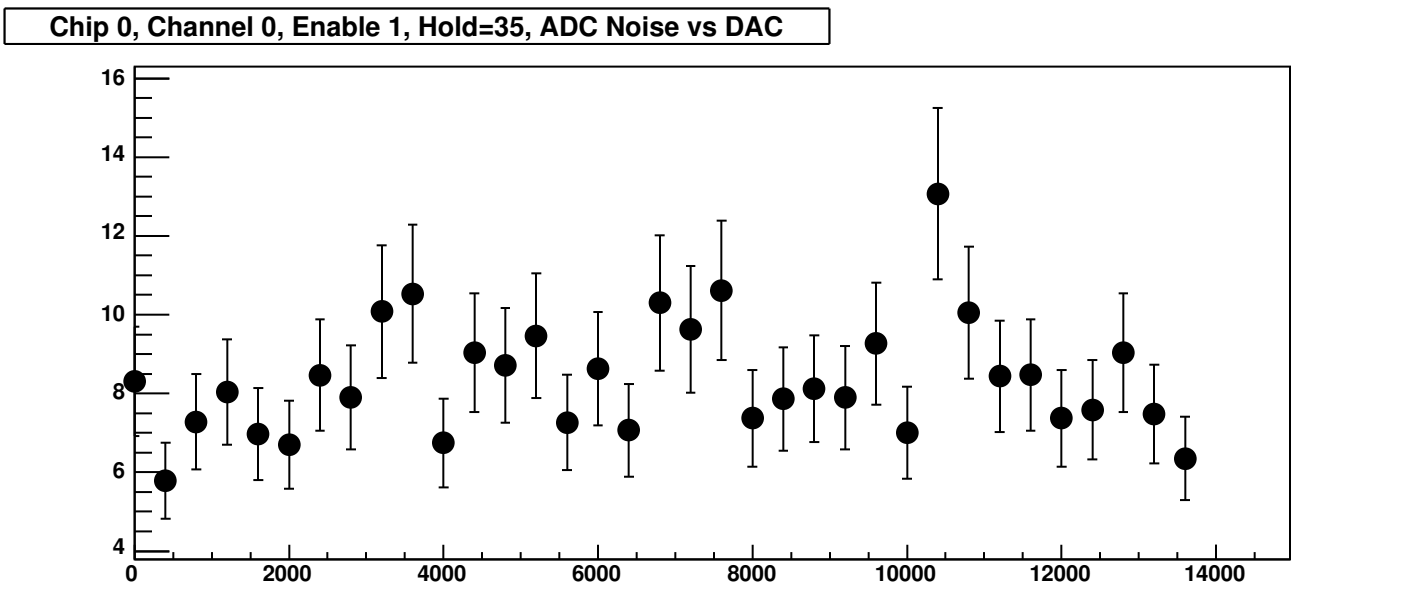


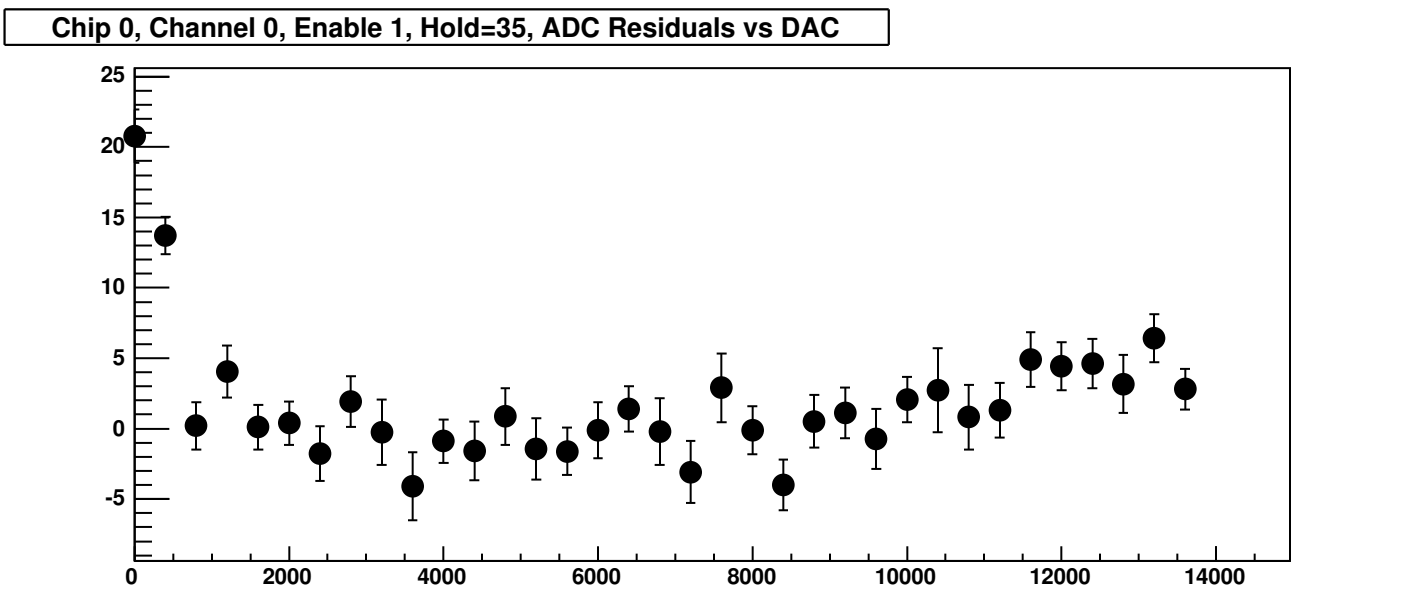

Chip 0, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1090 / 23
p0 -87.2 ± 1.204
p1 2.347 ± 0.0001856
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

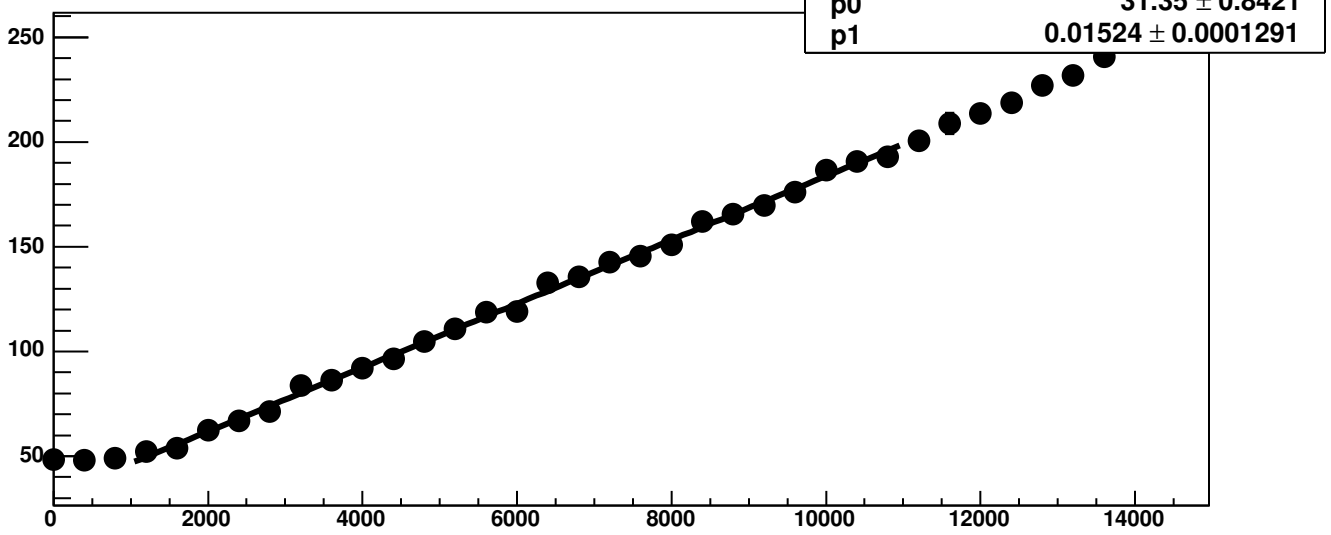

Chip 0, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 24.44 / 23
p0 28.65 ± 0.8488
p1 0.02216 ± 0.0001319
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

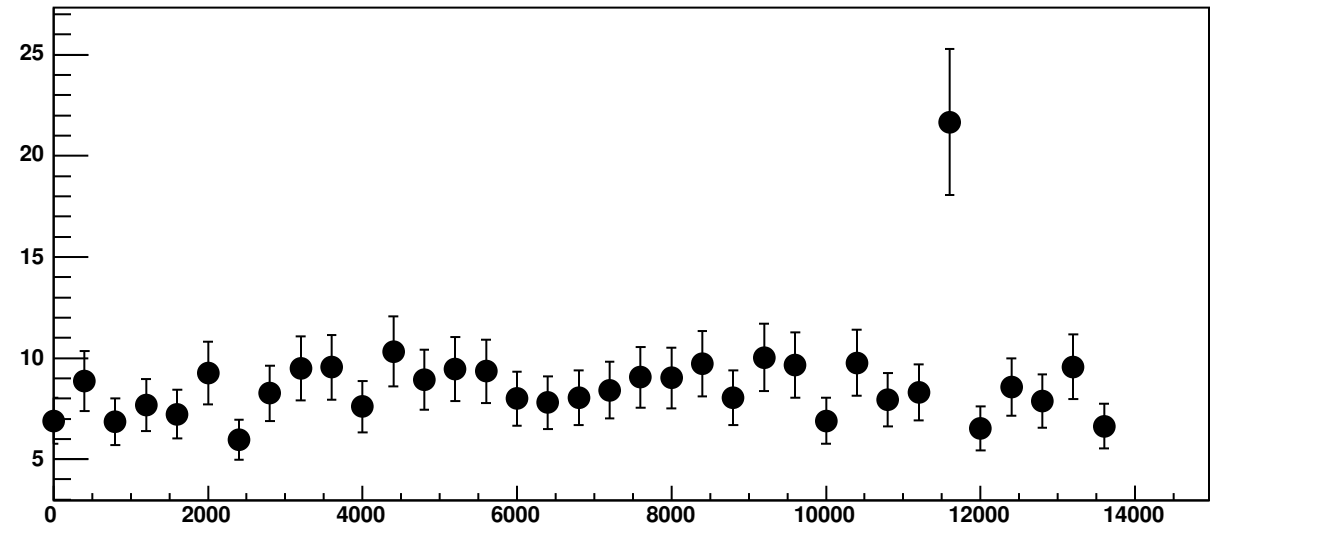

Chip 0, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

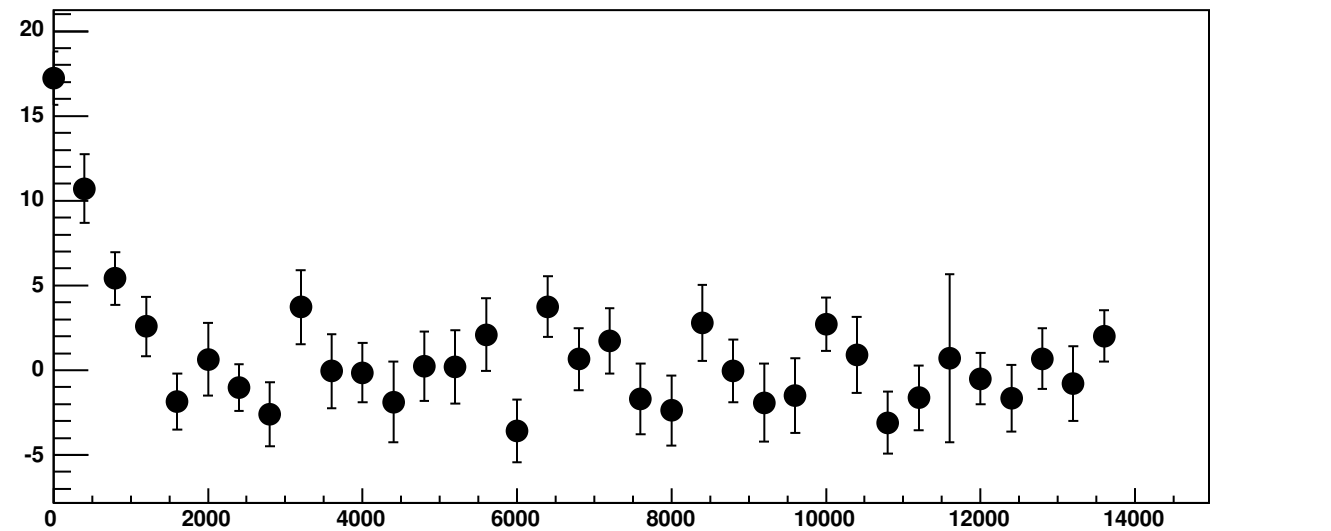
Chip 0, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

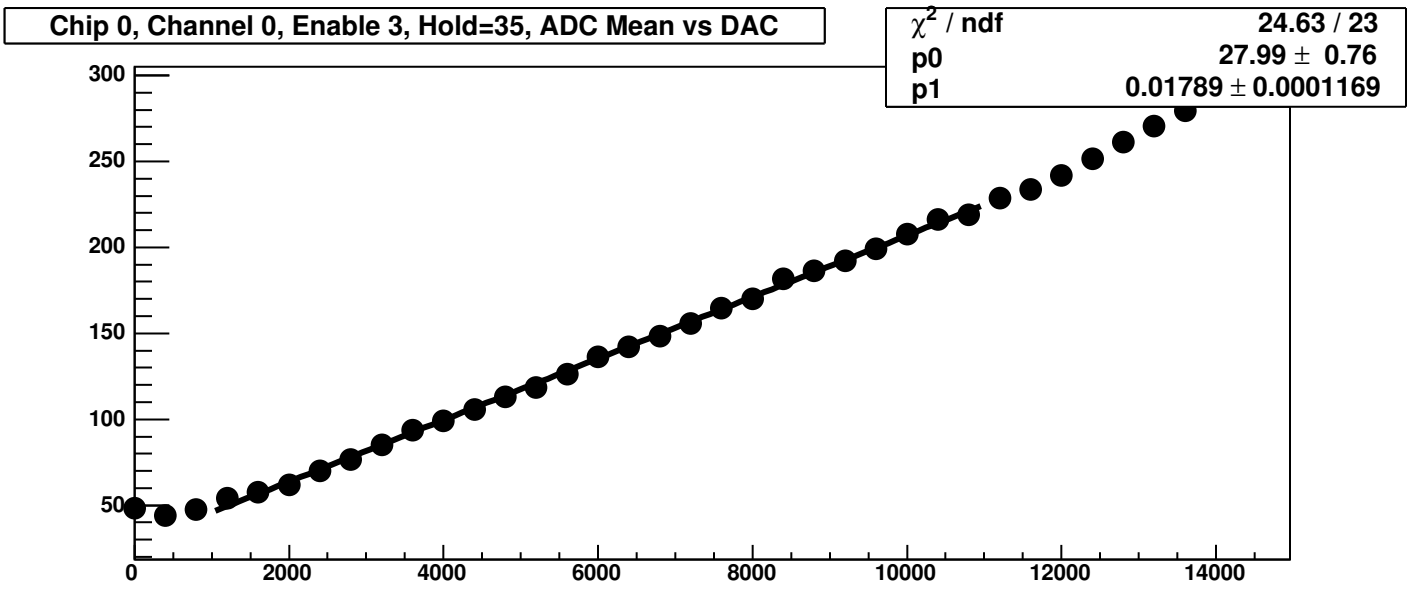


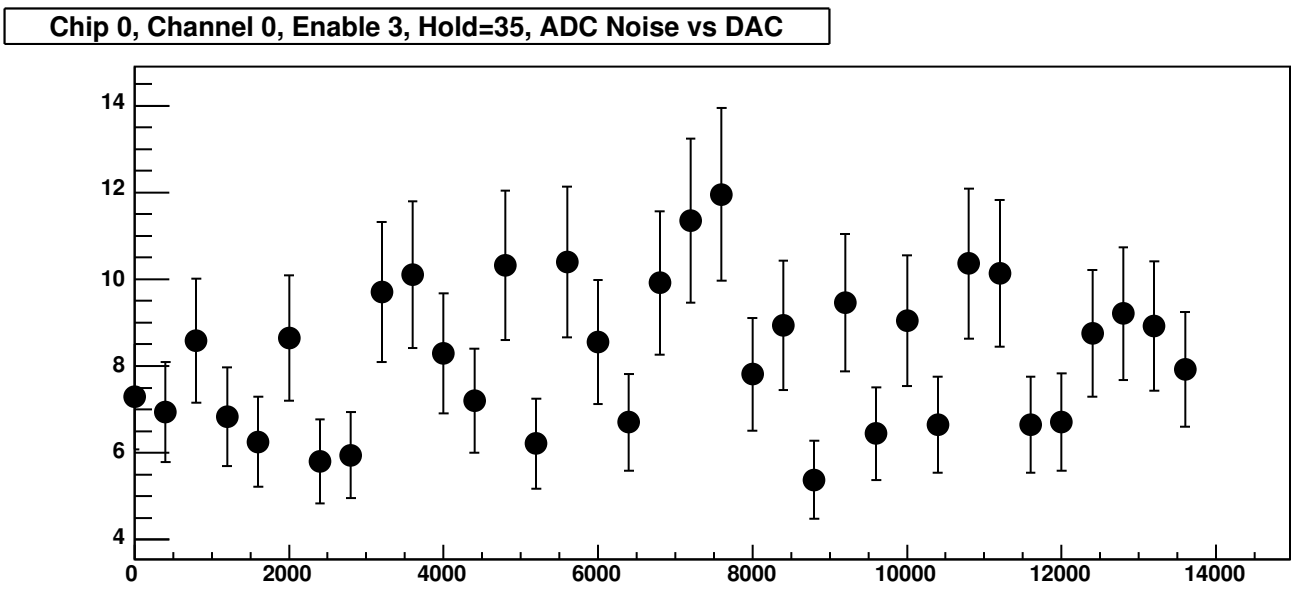
Chip 0, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC

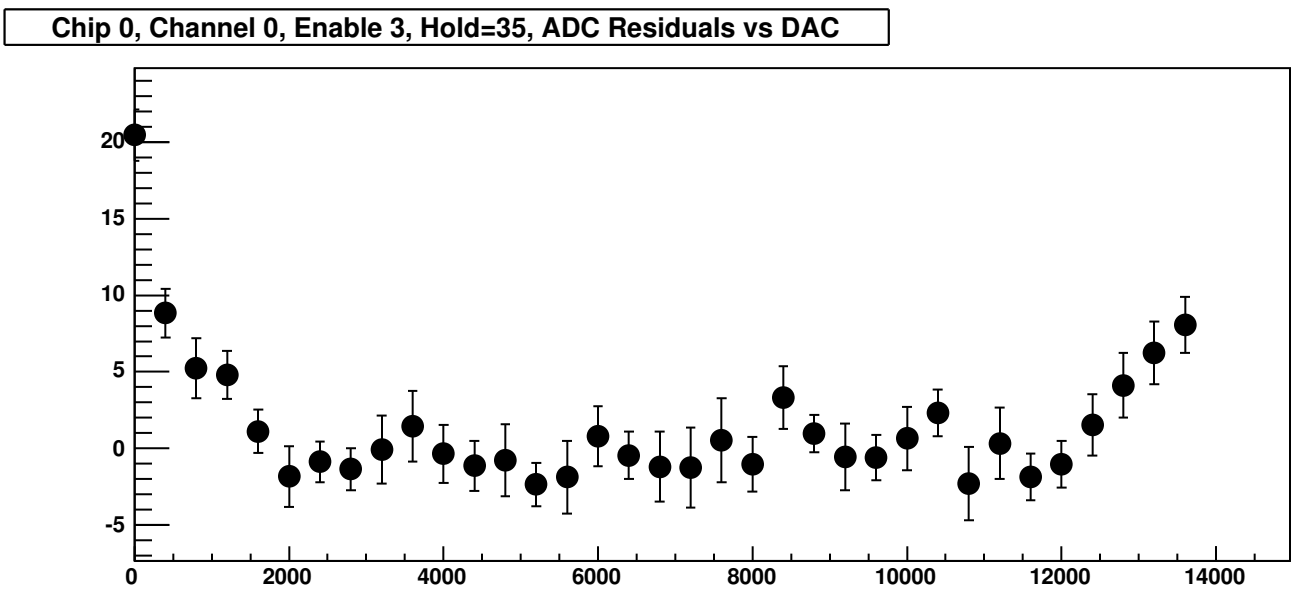


Chip 0, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

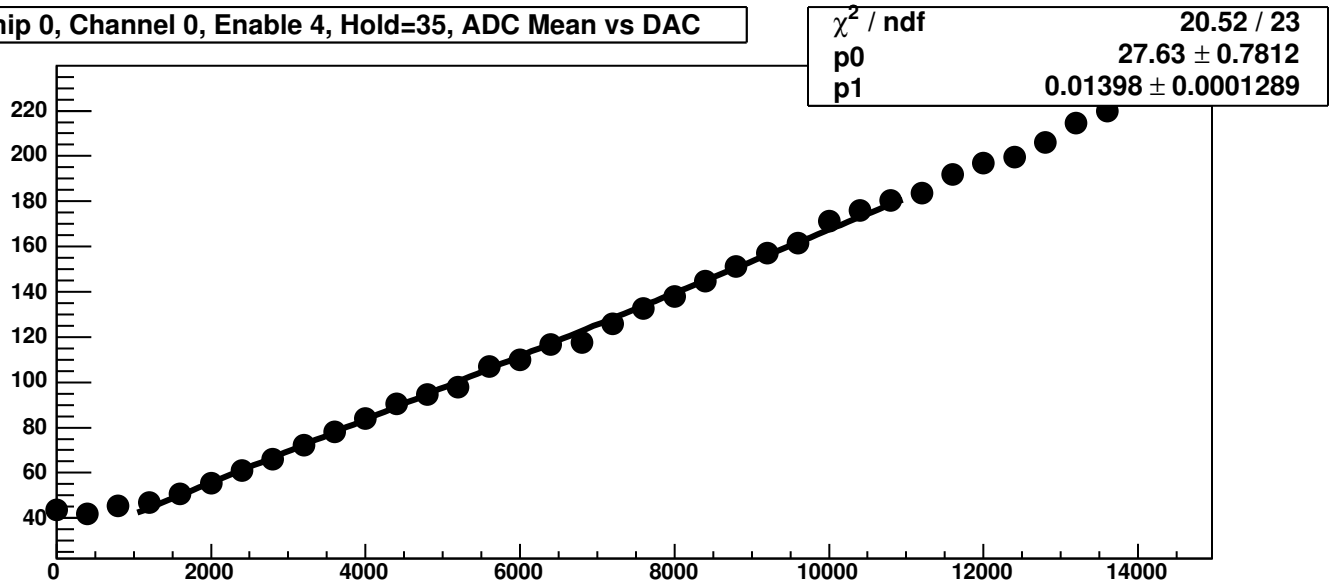



Chip 0, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.63 / 23
p0 27.99 ± 0.76
p1 0.01789 ± 0.0001169
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

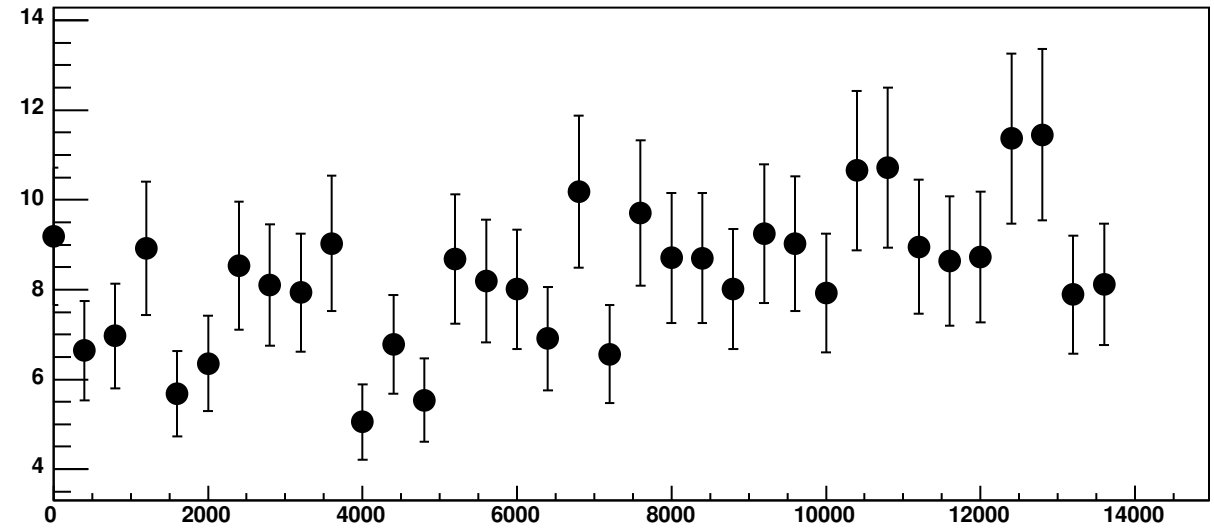

Chip 0, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

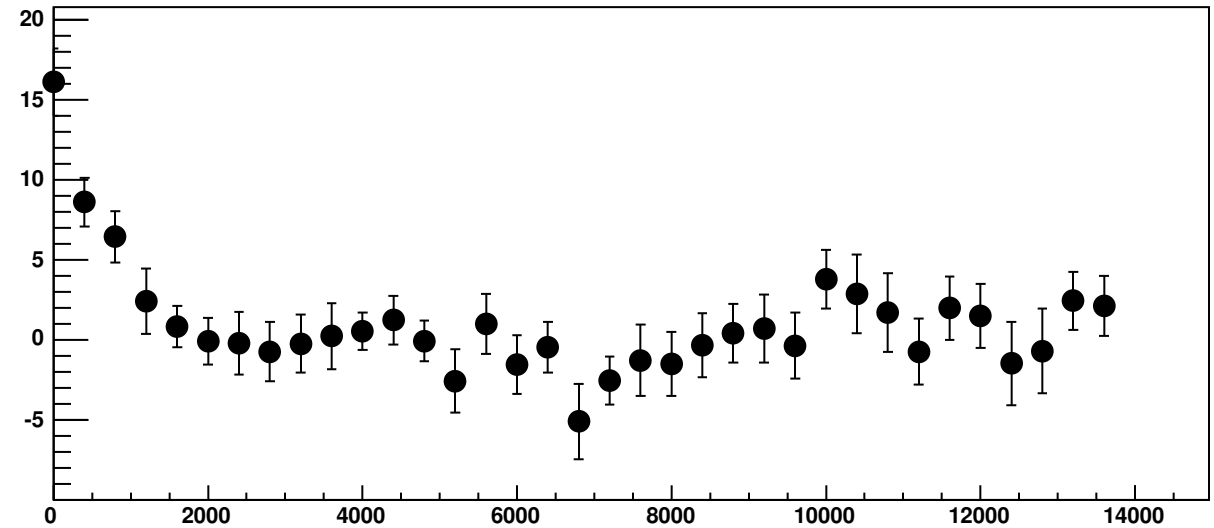
Chip 0, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

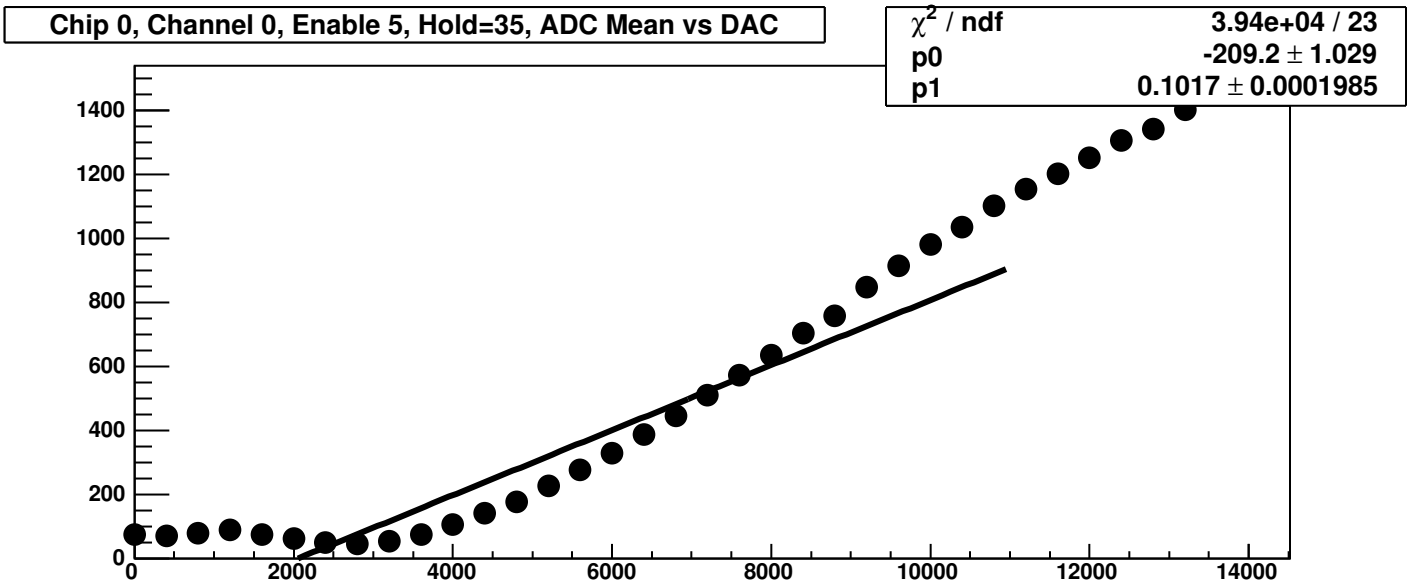


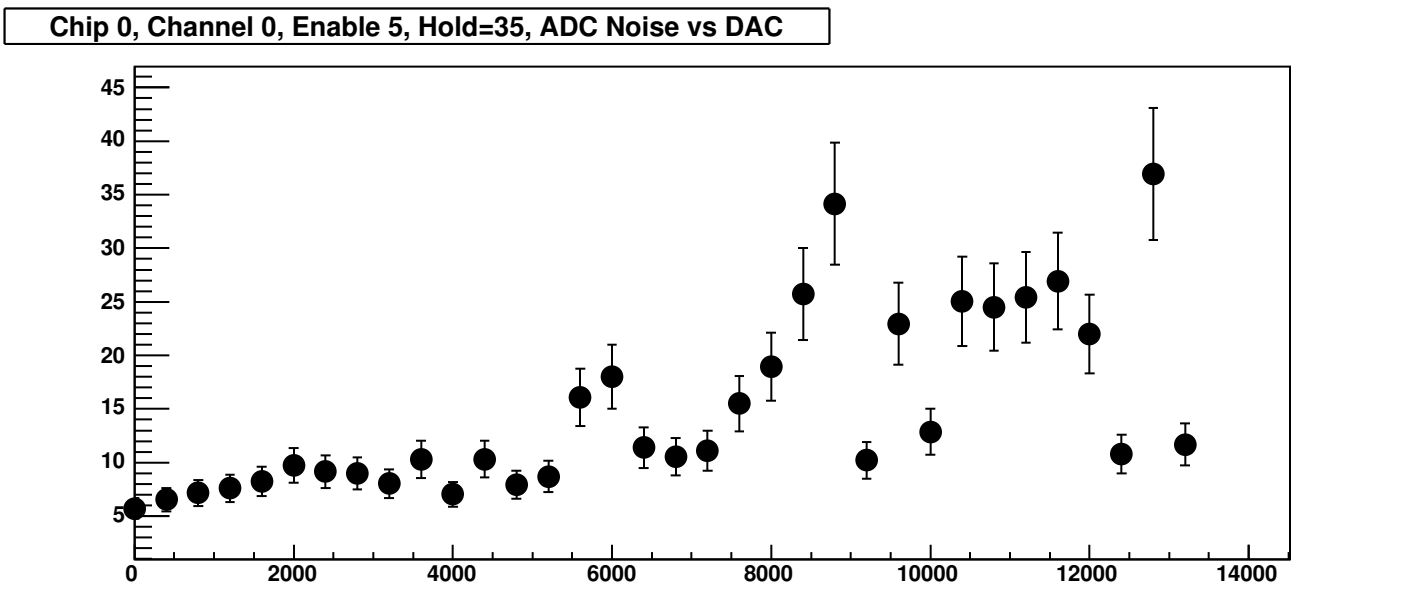
Chip 0, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

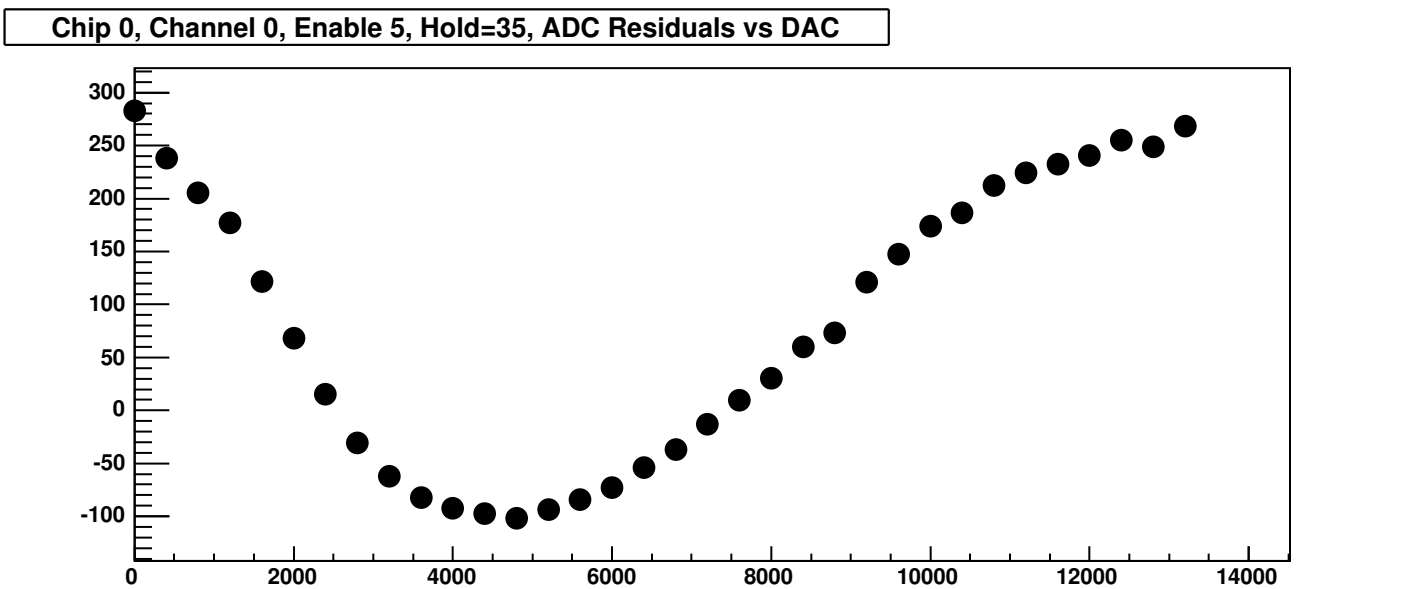


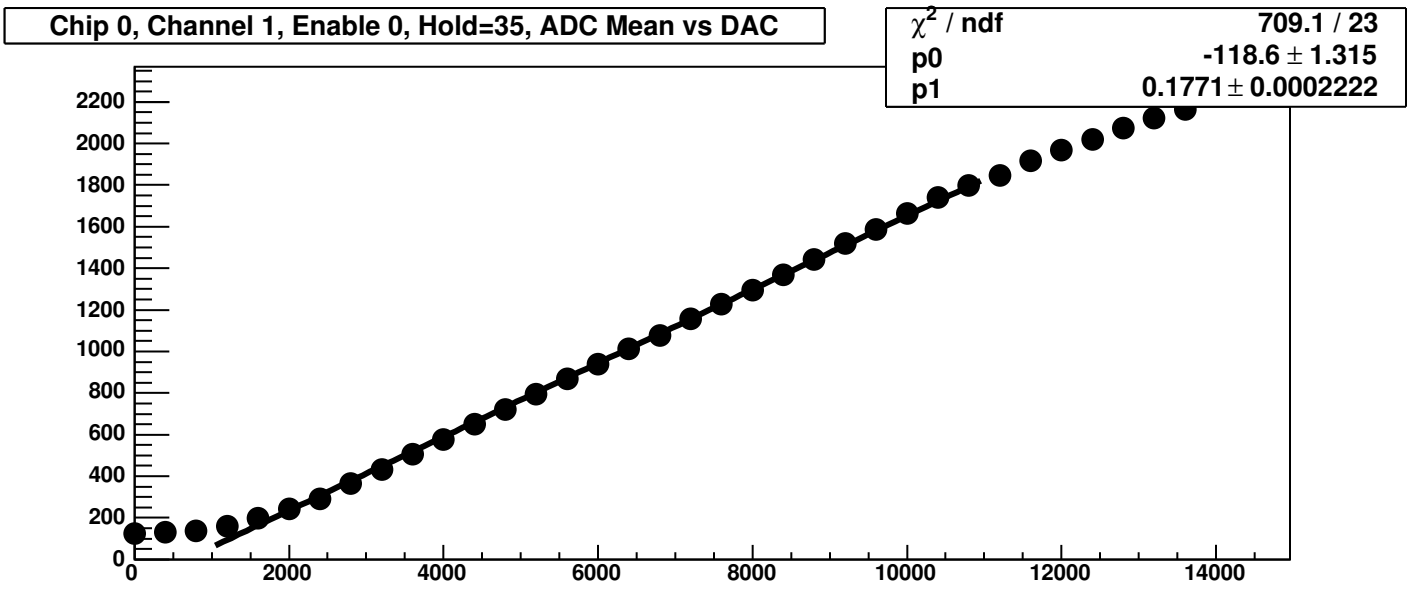
Chip 0, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

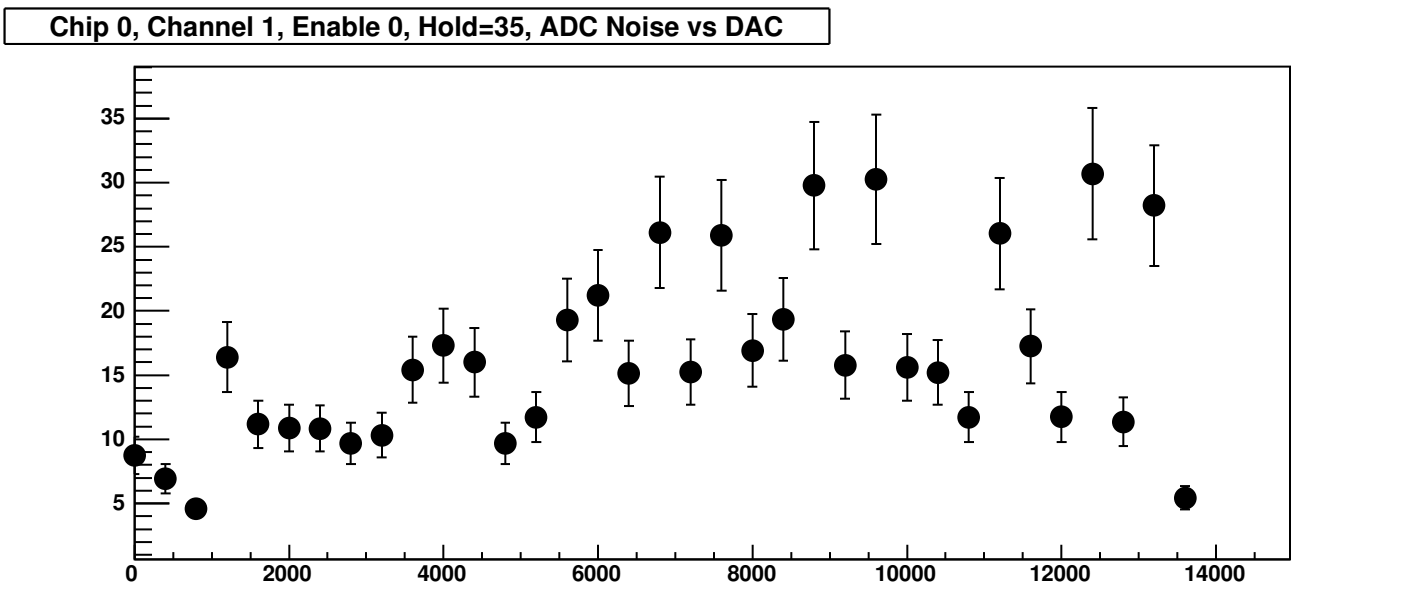


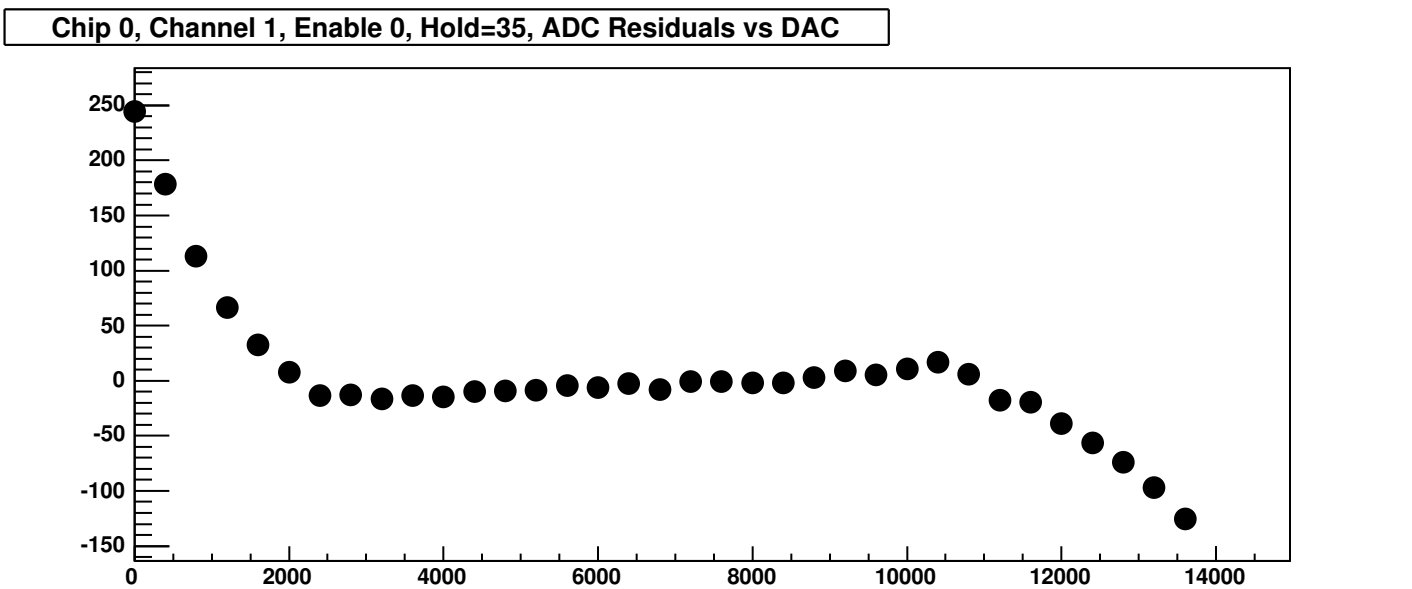

Chip 0, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.94e+04 / 23
p0 -209.2 ± 1.029
p1 0.1017 ± 0.0001985
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

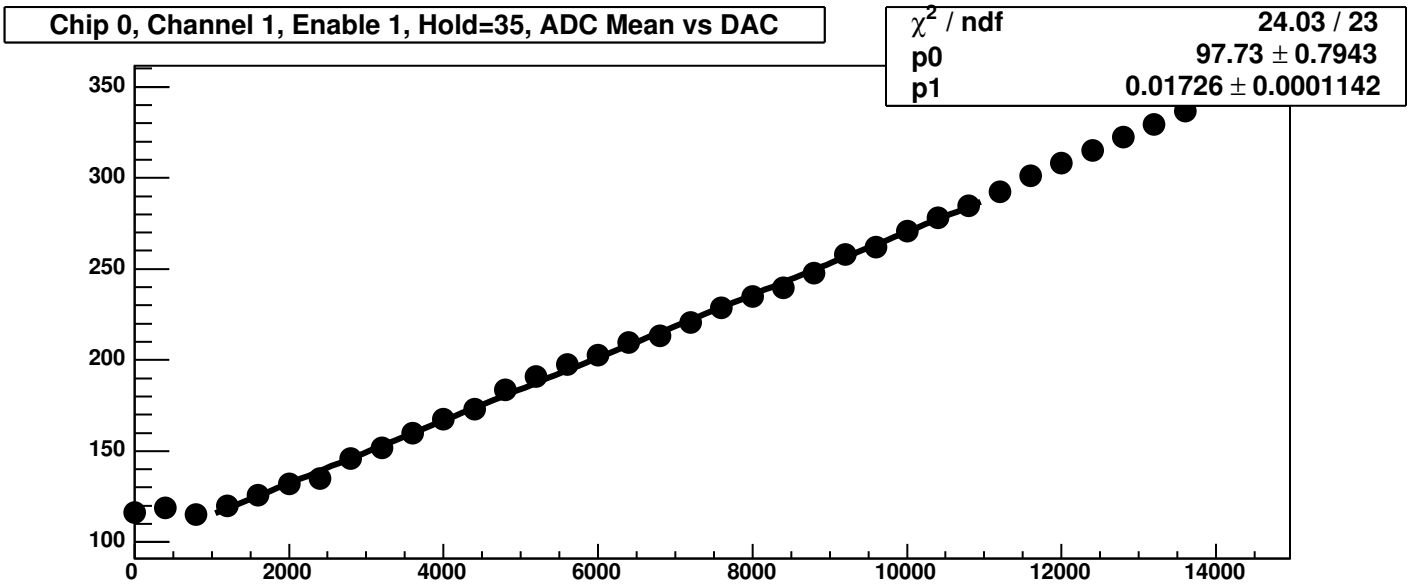

Chip 0, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

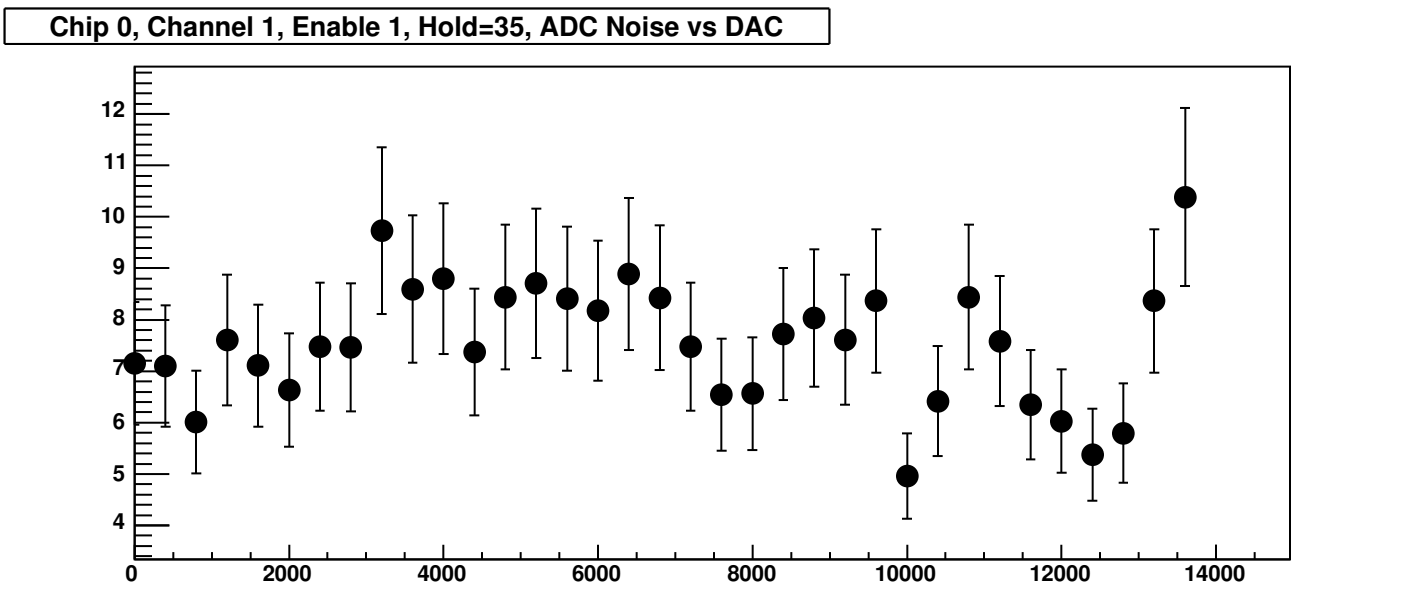

Chip 0, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

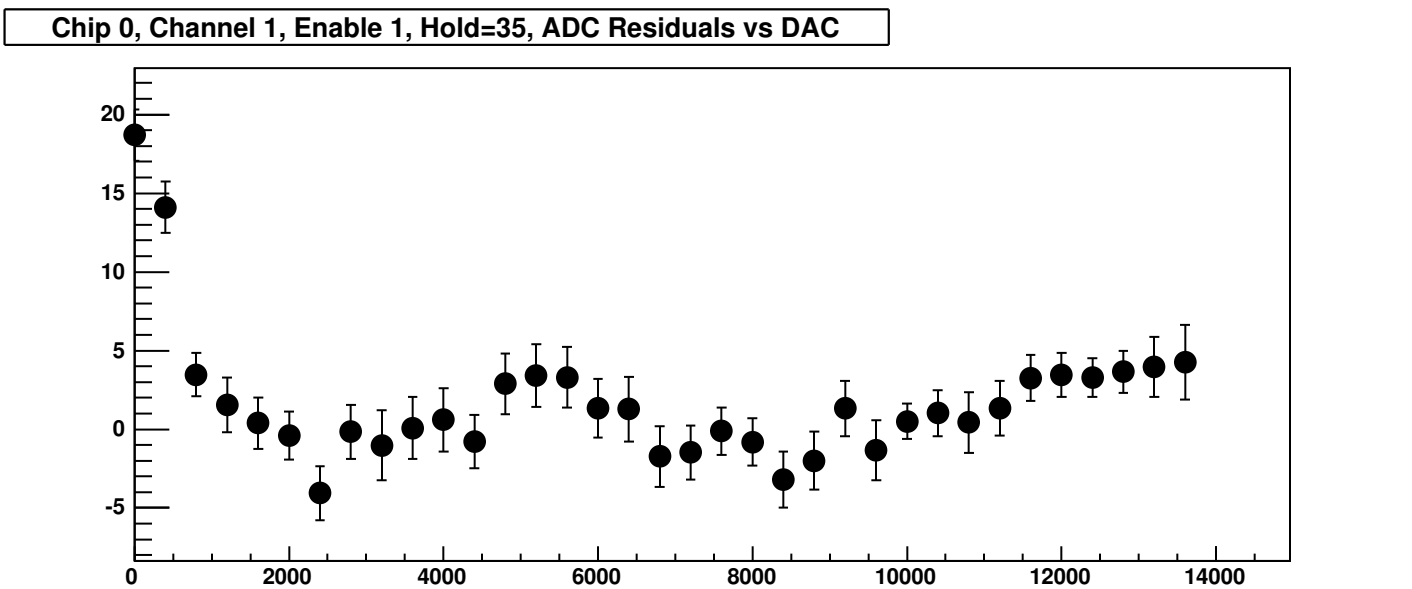

Chip 0, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 709.1 / 23
p0 -118.6 ± 1.315
p1 0.1771 ± 0.0002222
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

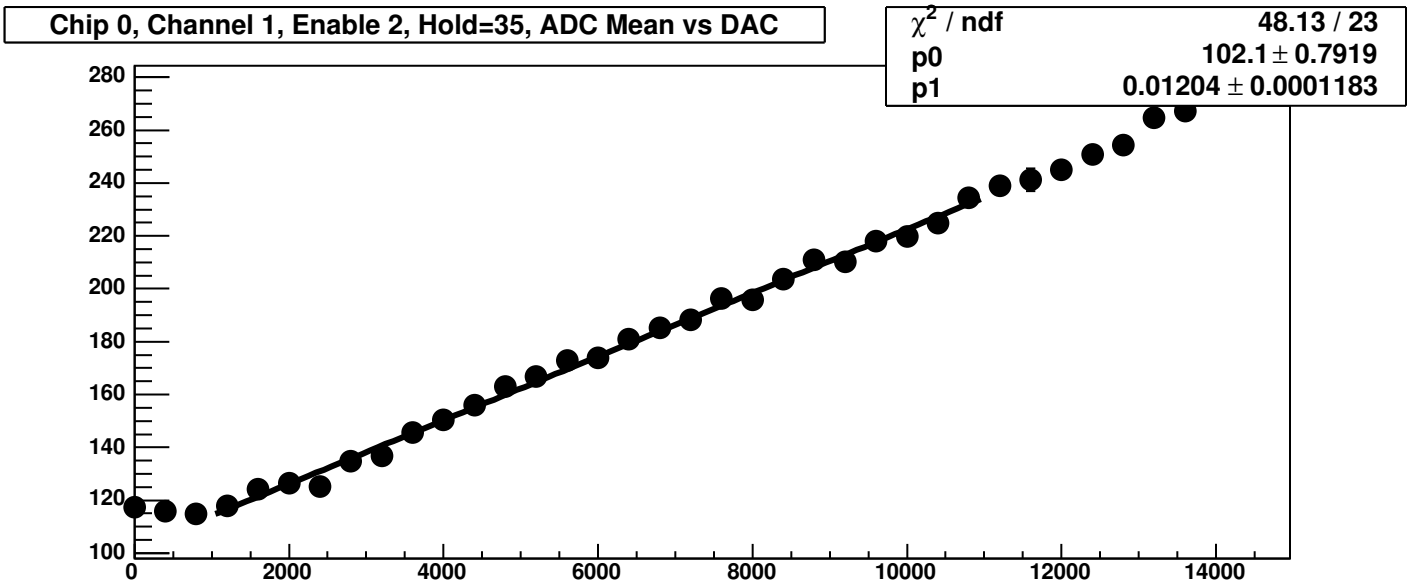

Chip 0, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

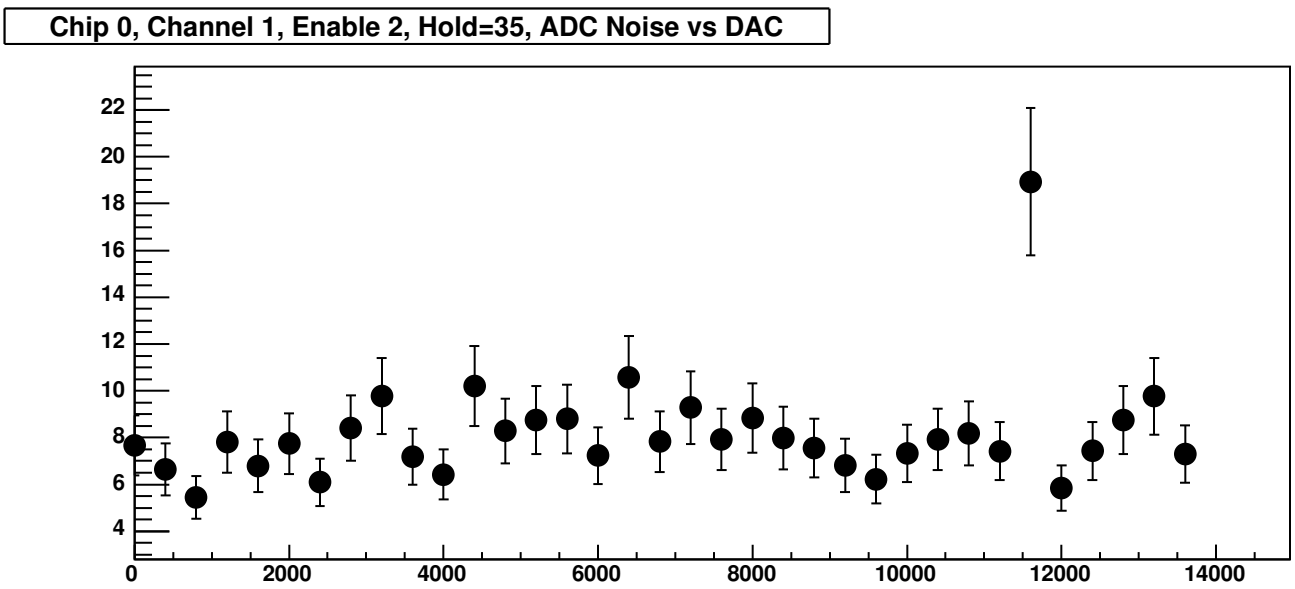

Chip 0, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

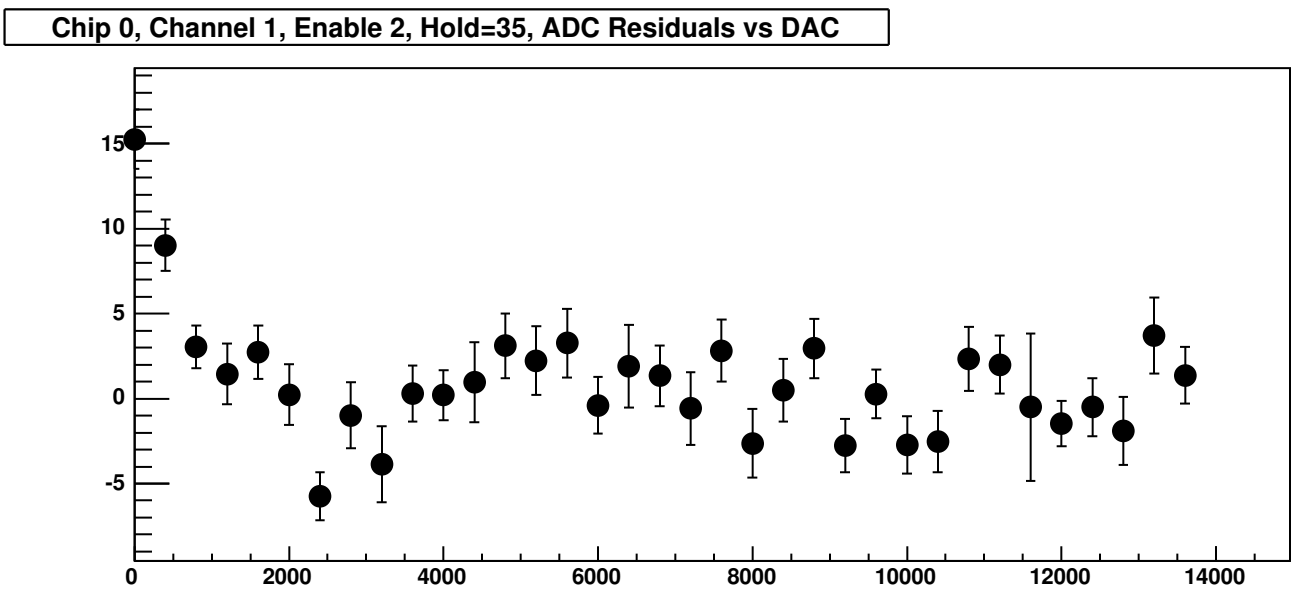

Chip 0, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 24.03 / 23
p0 97.73 ± 0.7943
p1 0.01726 ± 0.0001142
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

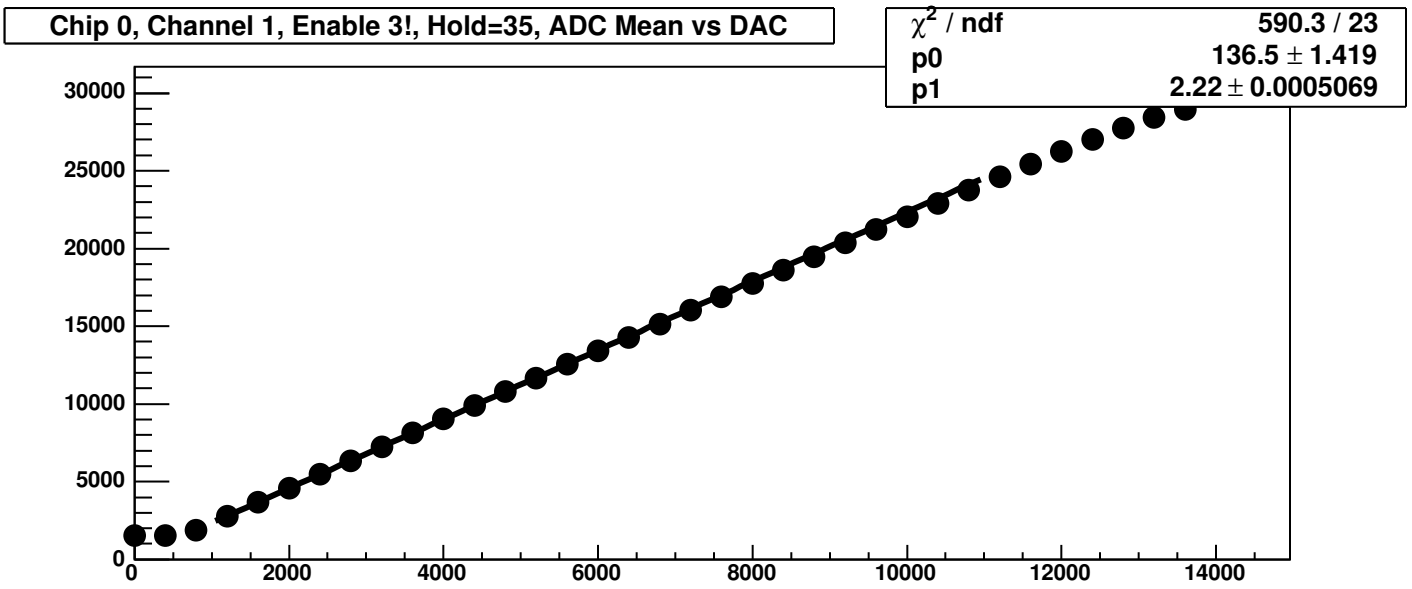

Chip 0, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

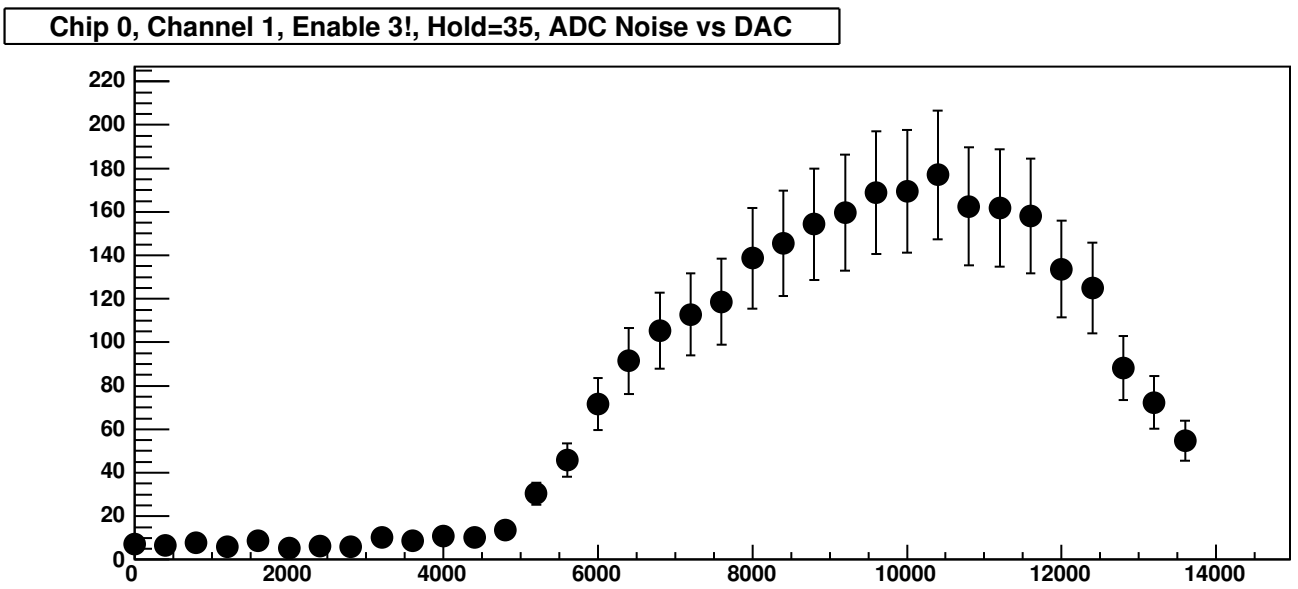

Chip 0, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

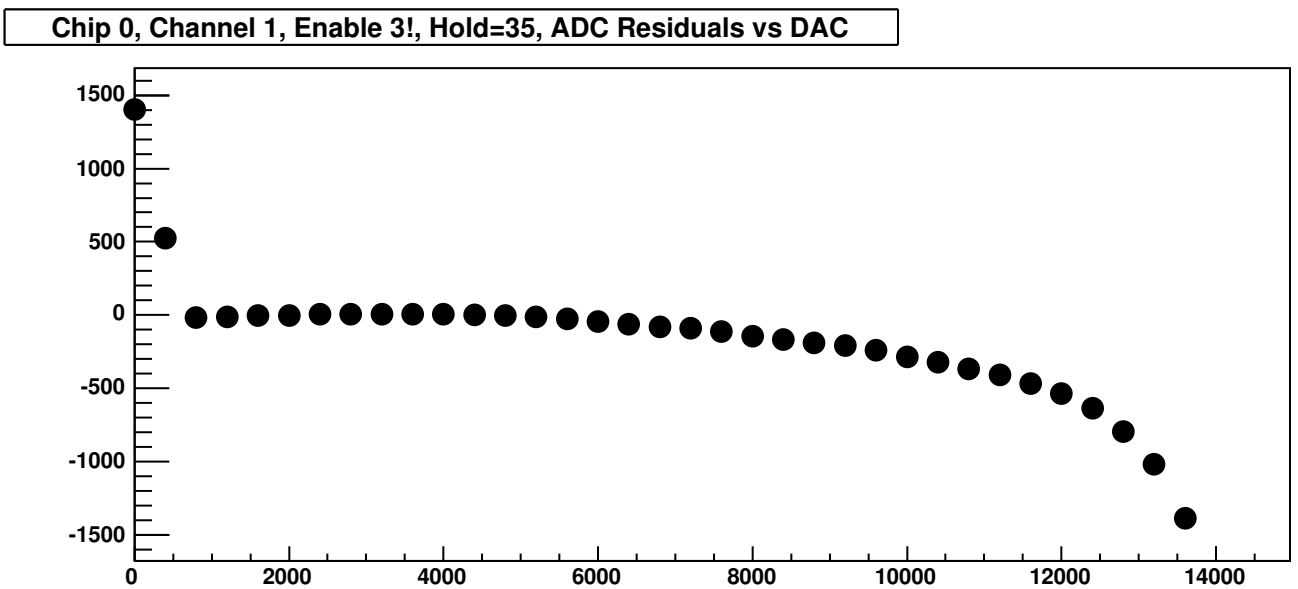

Chip 0, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 48.13 / 23
p0 102.1 ± 0.7919
p1 0.01204 ± 0.0001183
280
260
240
220
200
180
160
140
120
100
0
2000
4000
6000
8000
10000
12000
14000

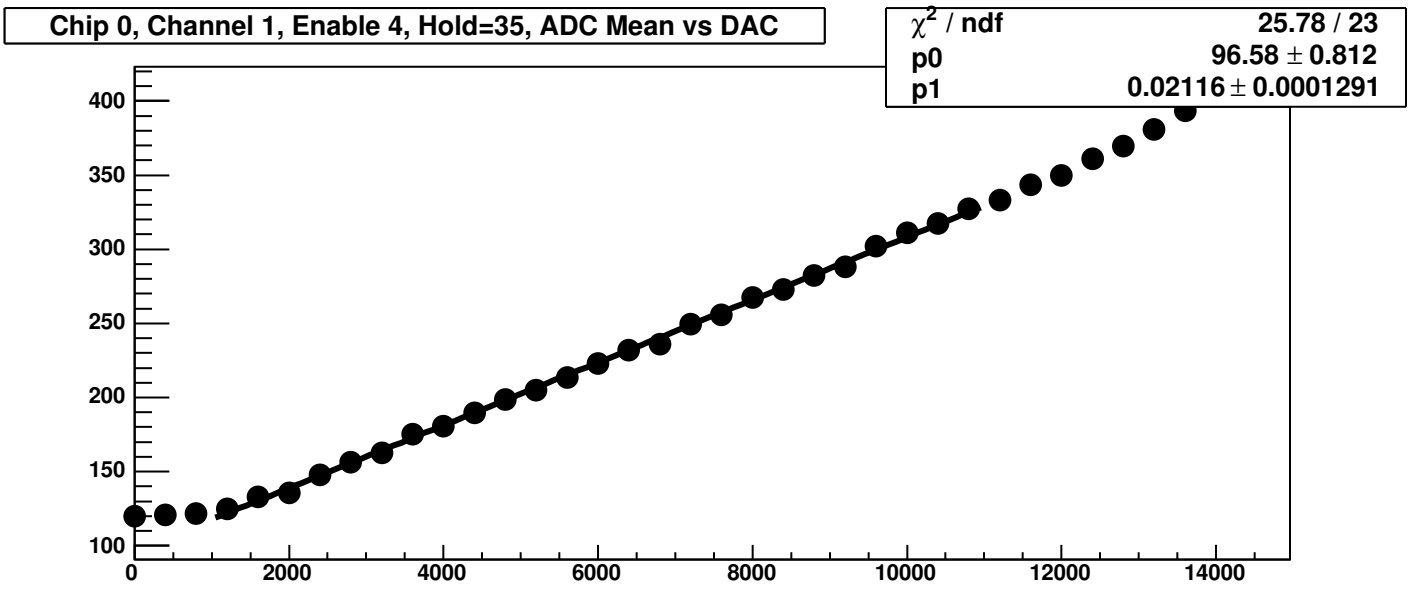

Chip 0, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

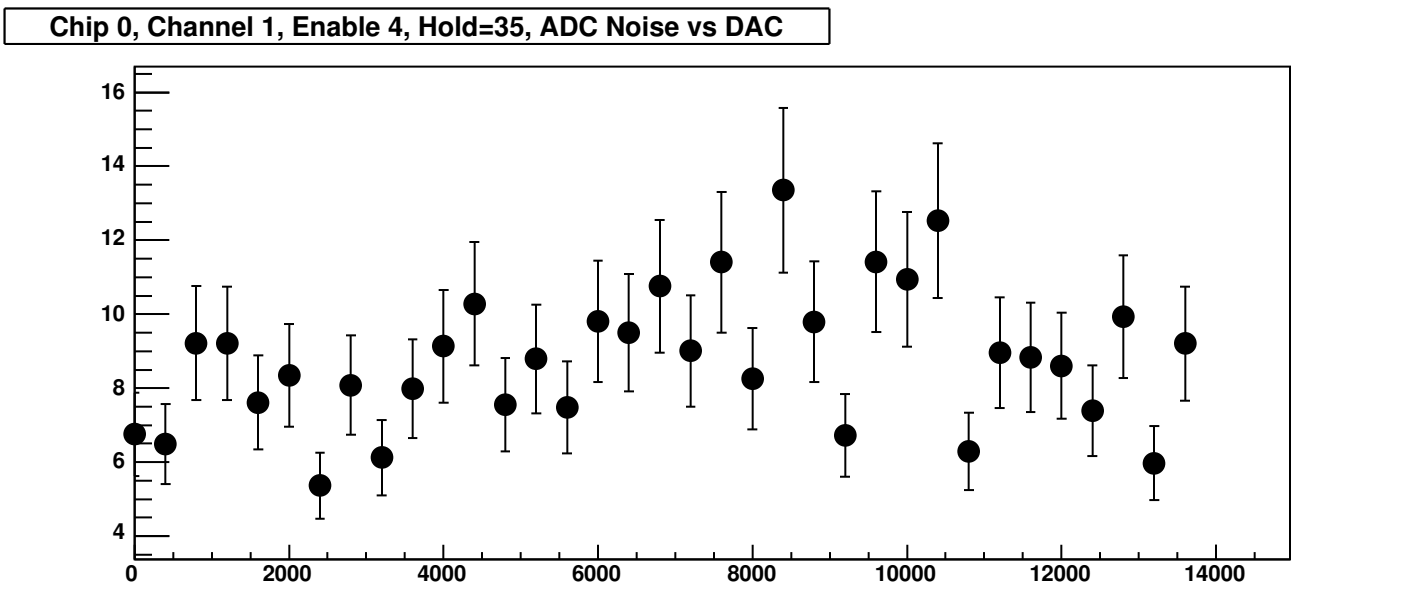

Chip 0, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

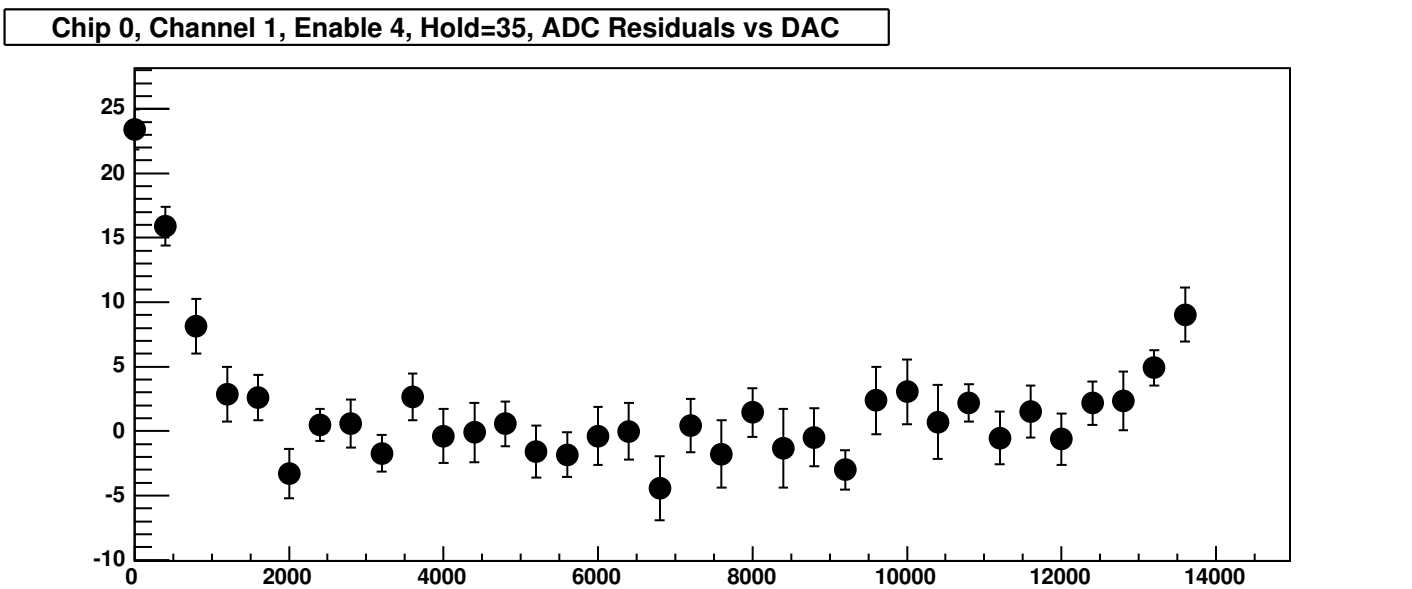

Chip 0, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 590.3 / 23
p0 136.5 ± 1.419
p1 2.22 ± 0.0005069
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

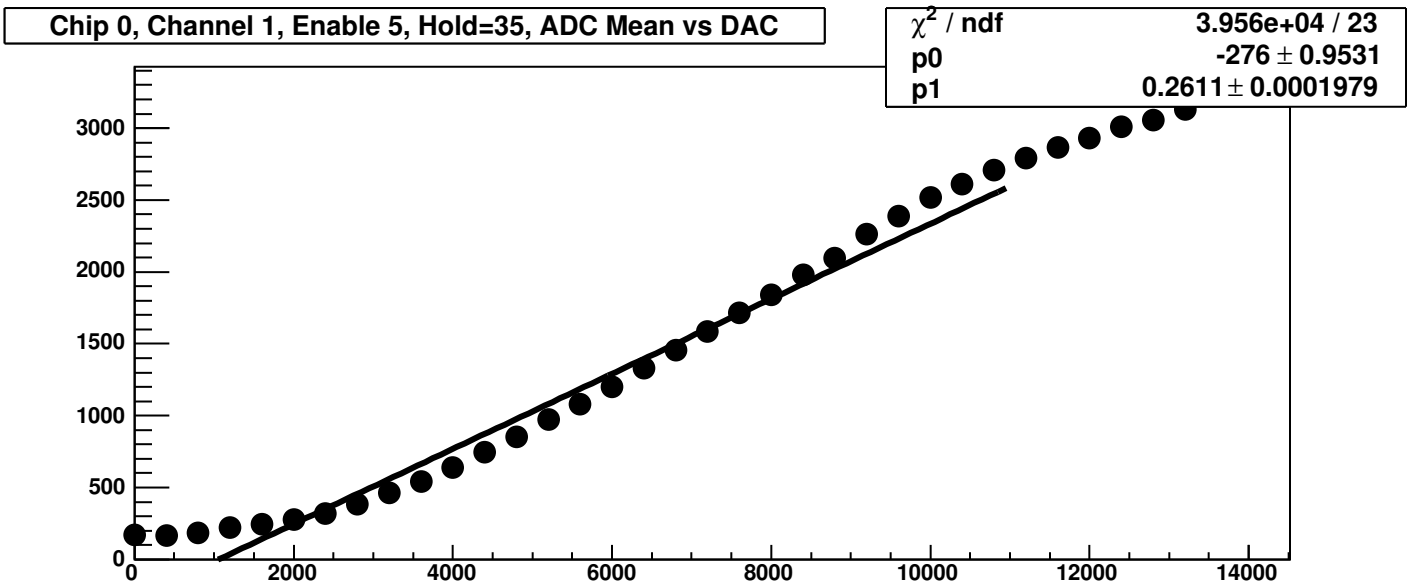

Chip 0, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

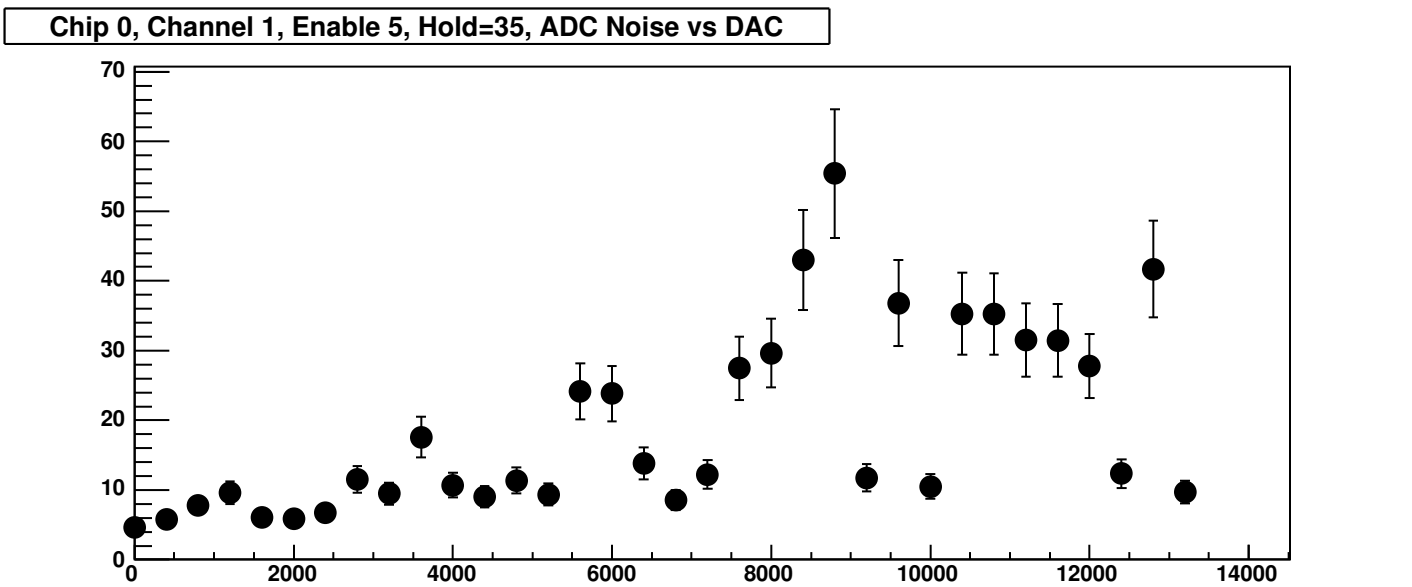

Chip 0, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

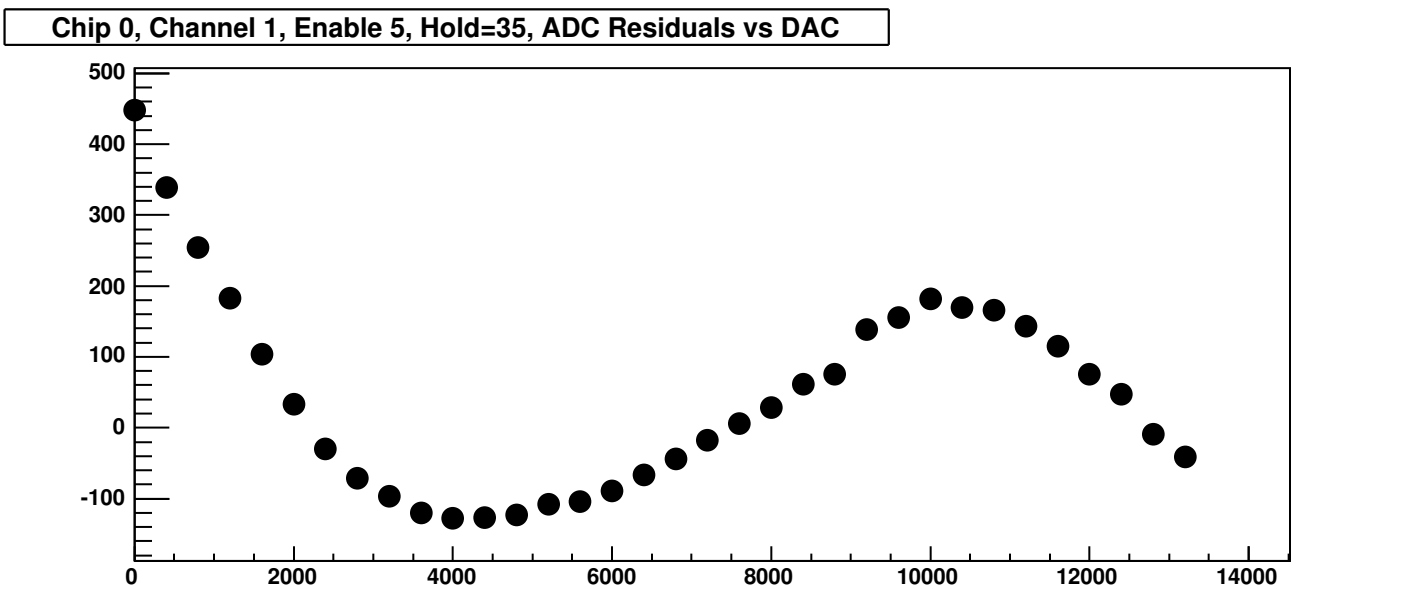

Chip 0, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 25.78 / 23
p0 96.58 ± 0.812
p1 0.02116 ± 0.0001291
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

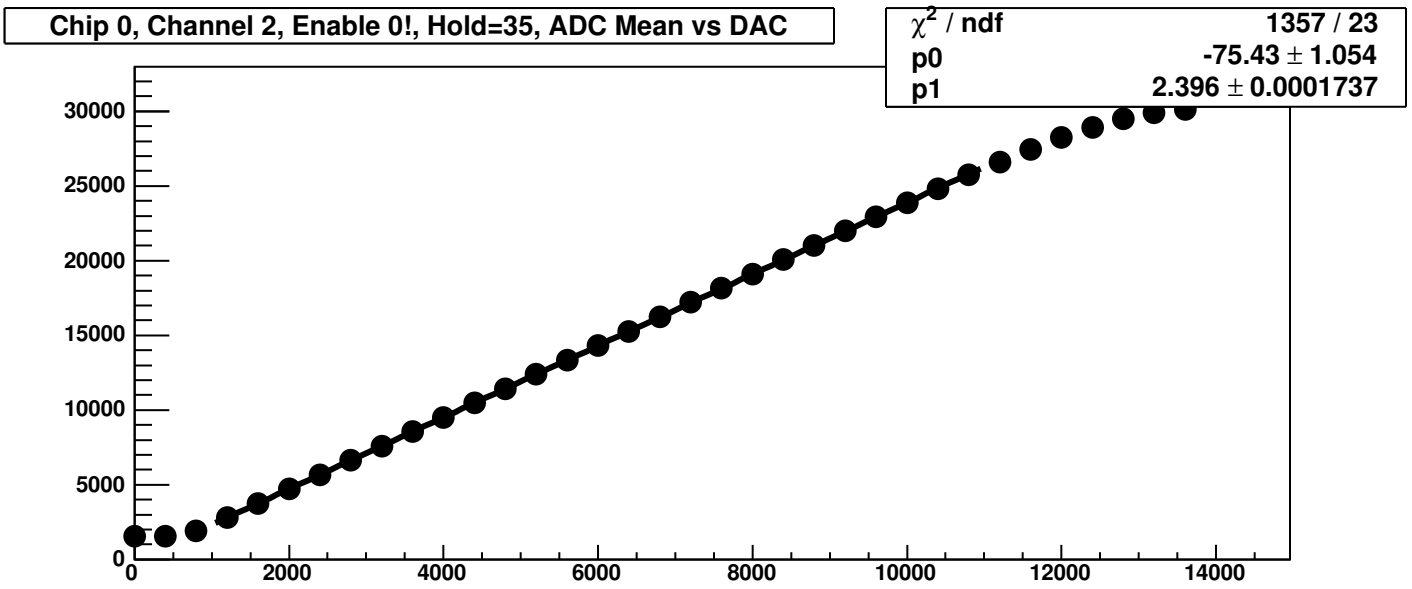

Chip 0, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

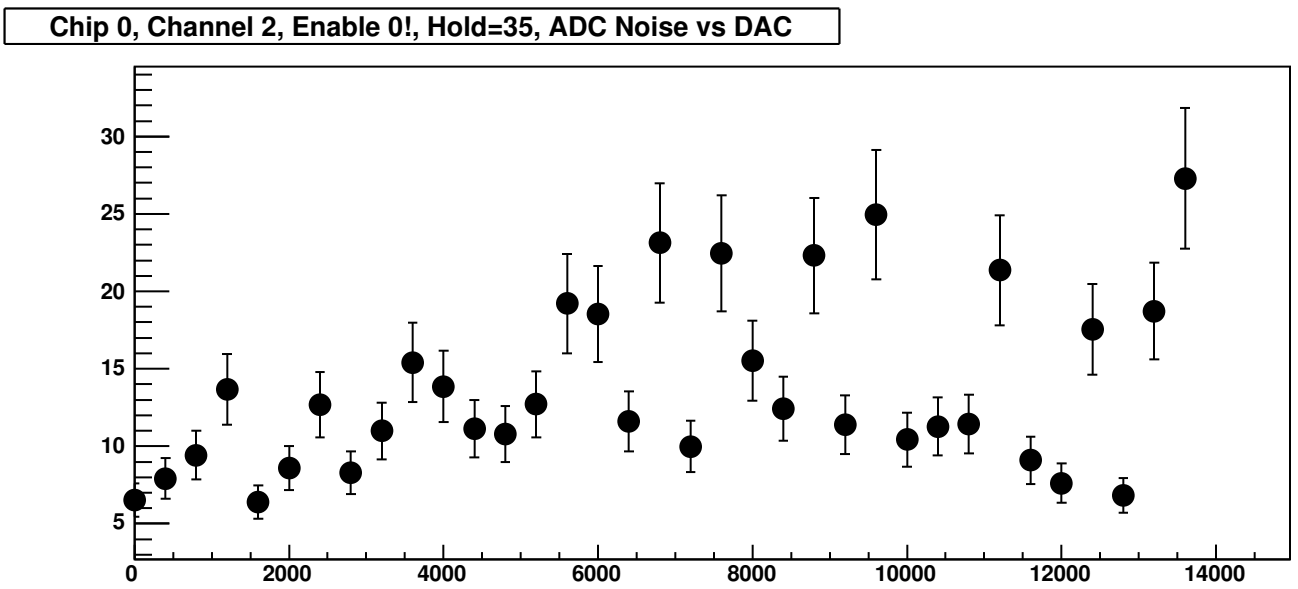

Chip 0, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

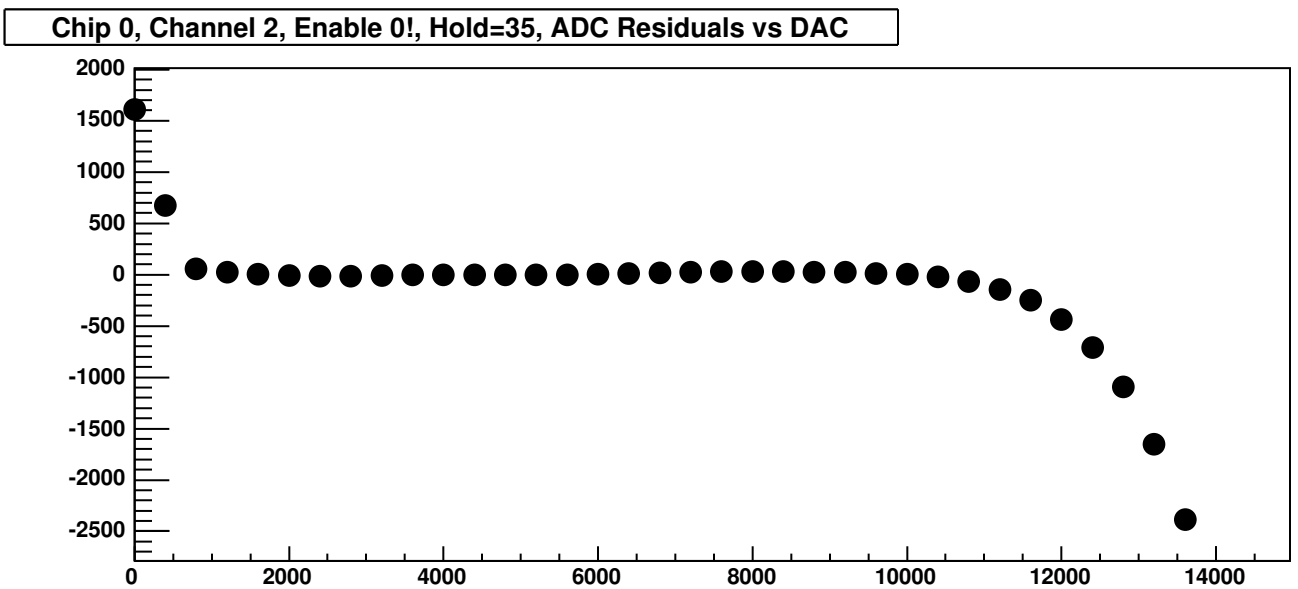

Chip 0, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.956e+04 / 23
p0 -276 ± 0.9531
p1 0.2611 ± 0.0001979
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

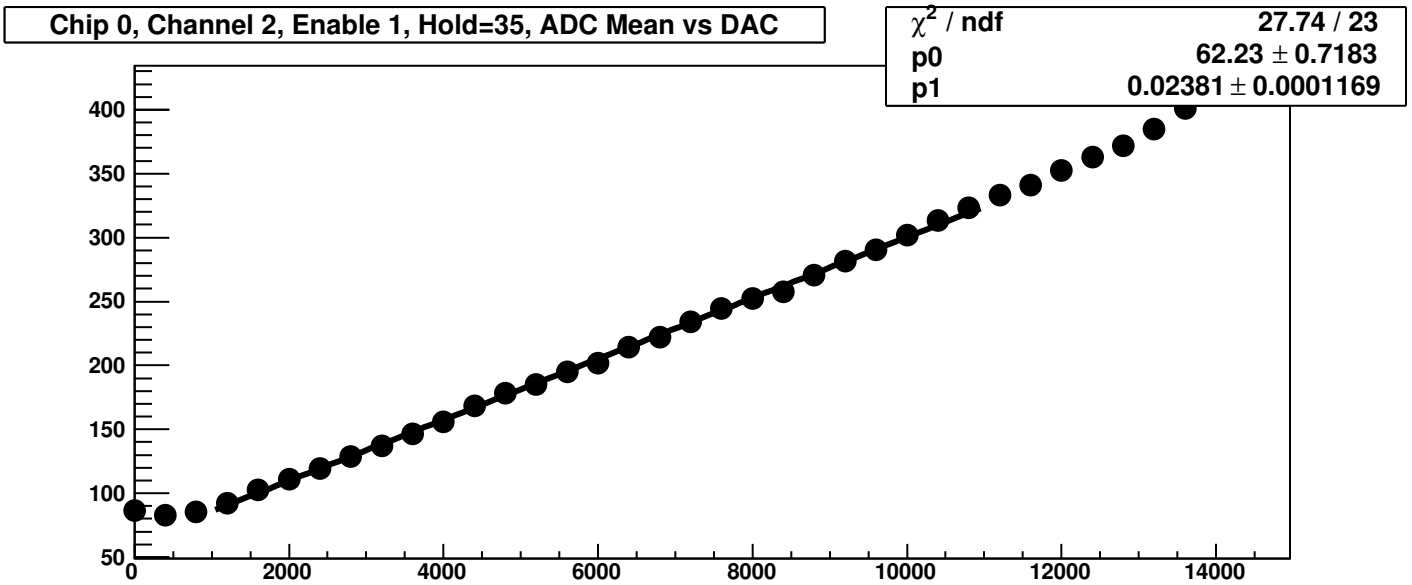

Chip 0, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

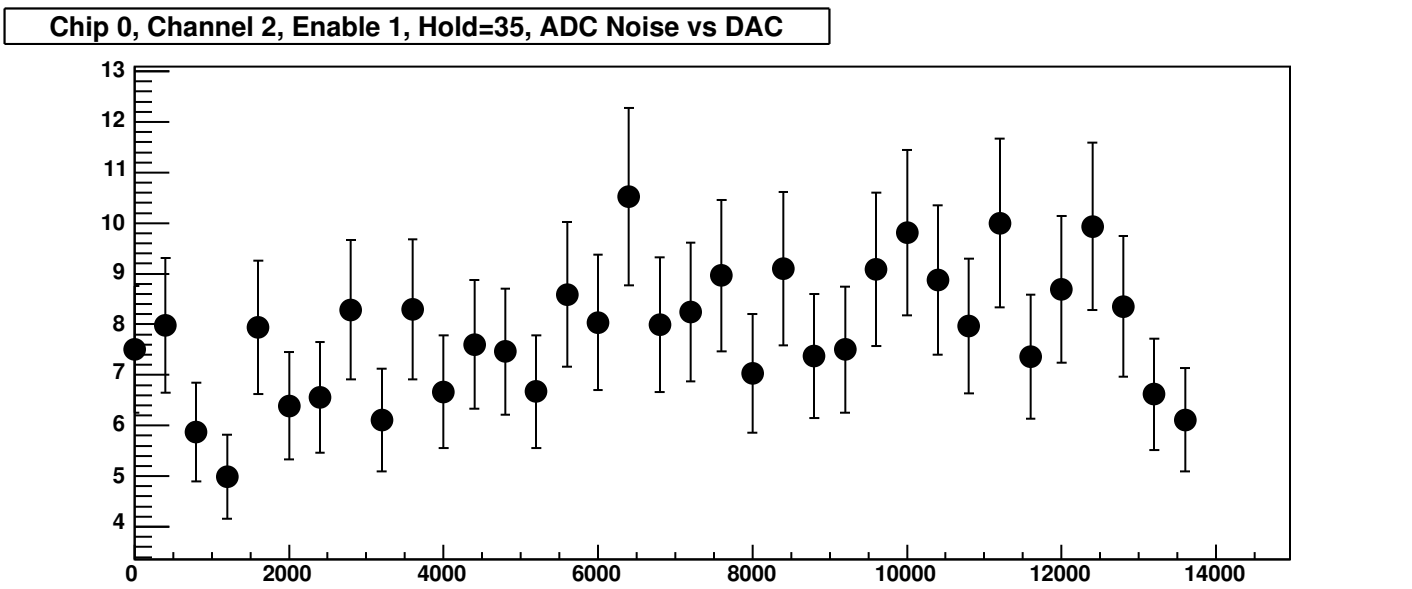

Chip 0, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

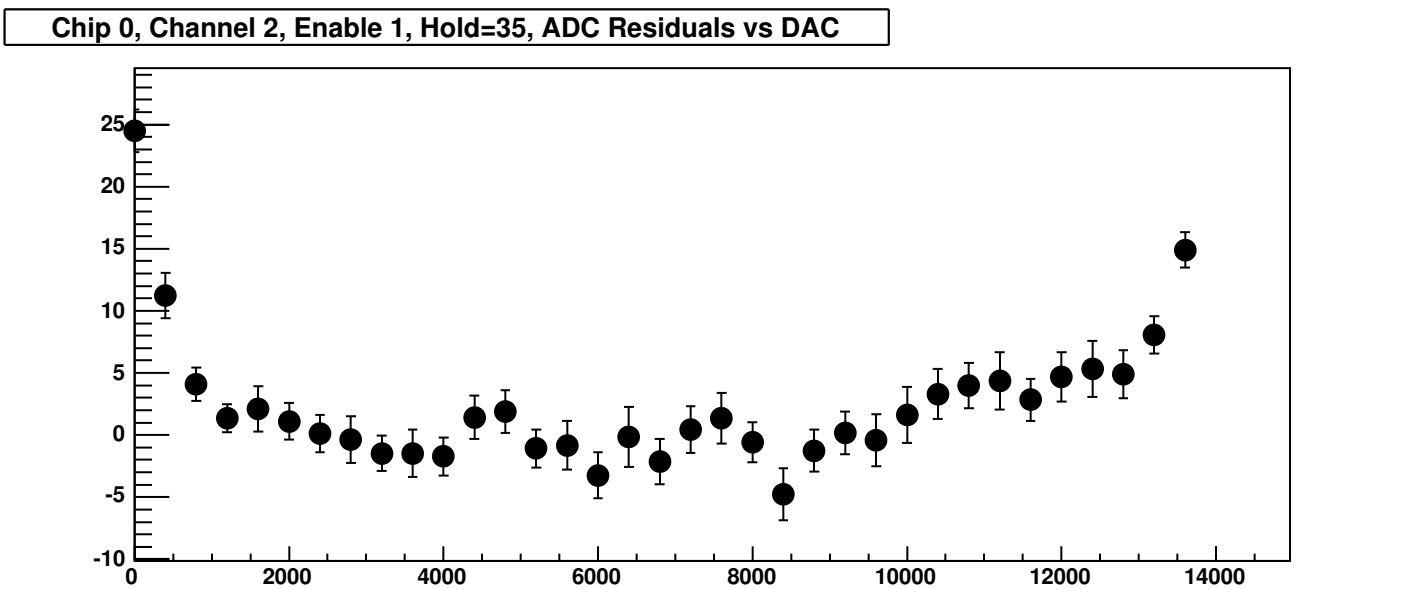

Chip 0, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1357 / 23
p0 -75.43 ± 1.054
p1 2.396 ± 0.0001737
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

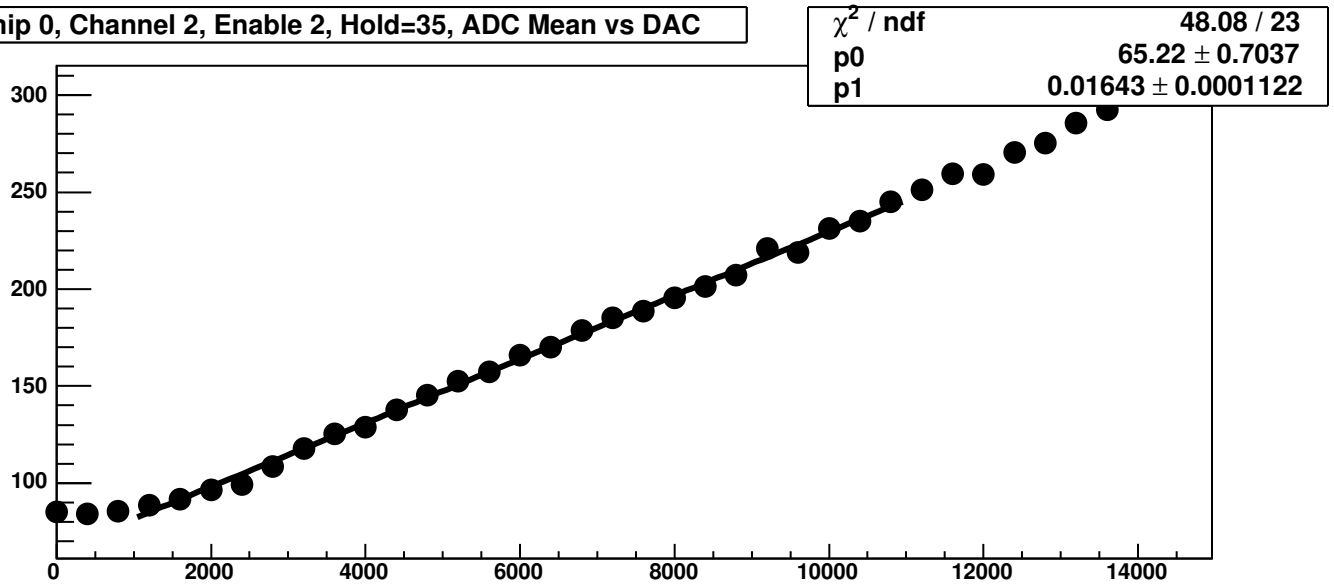

Chip 0, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 27.74 / 23
p0 62.23 ± 0.7183
p1 0.02381 ± 0.0001169
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

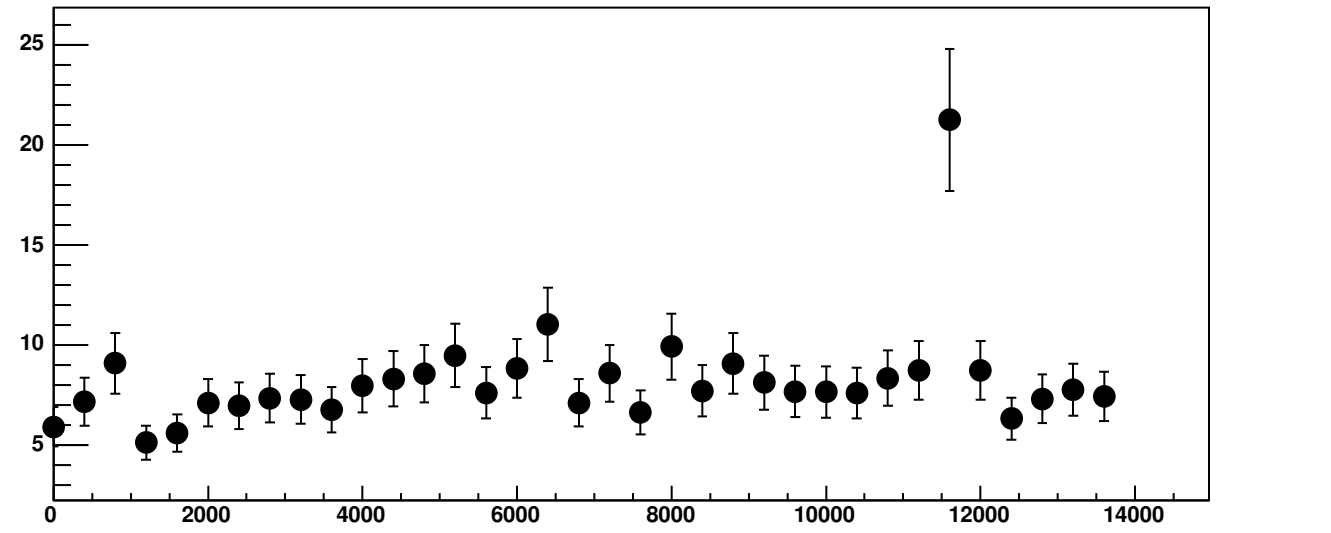

Chip 0, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

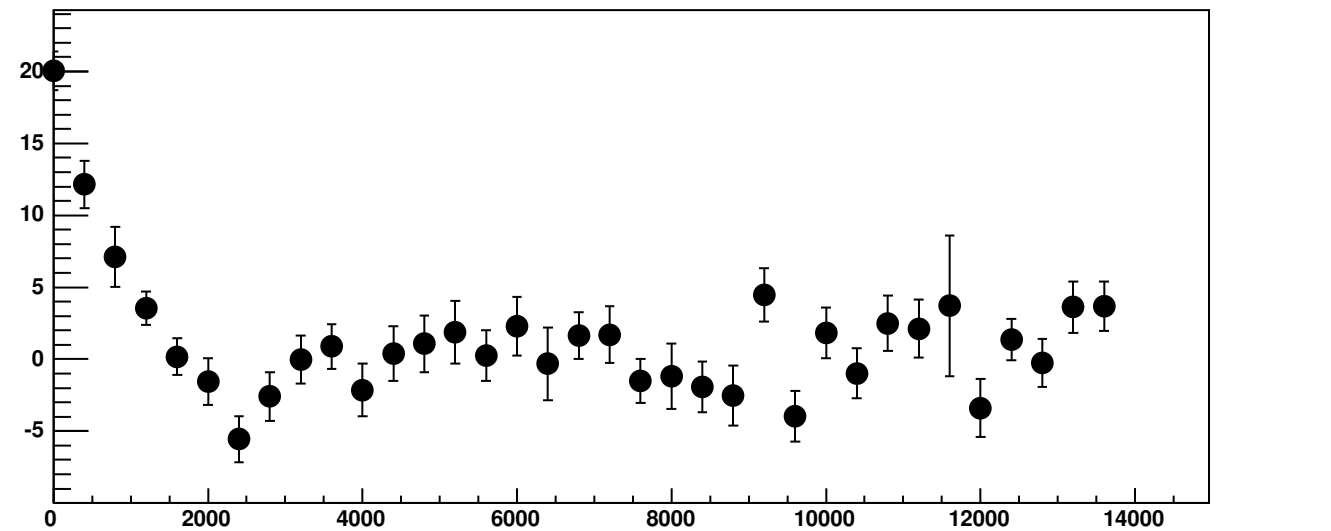
Chip 0, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

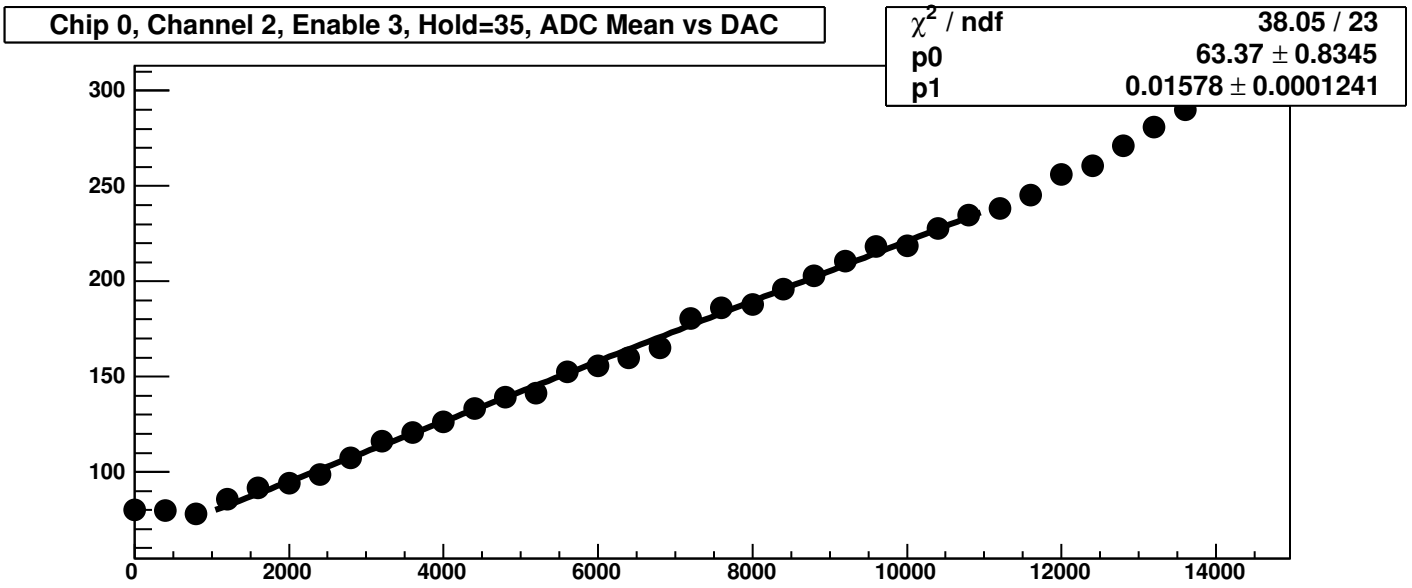


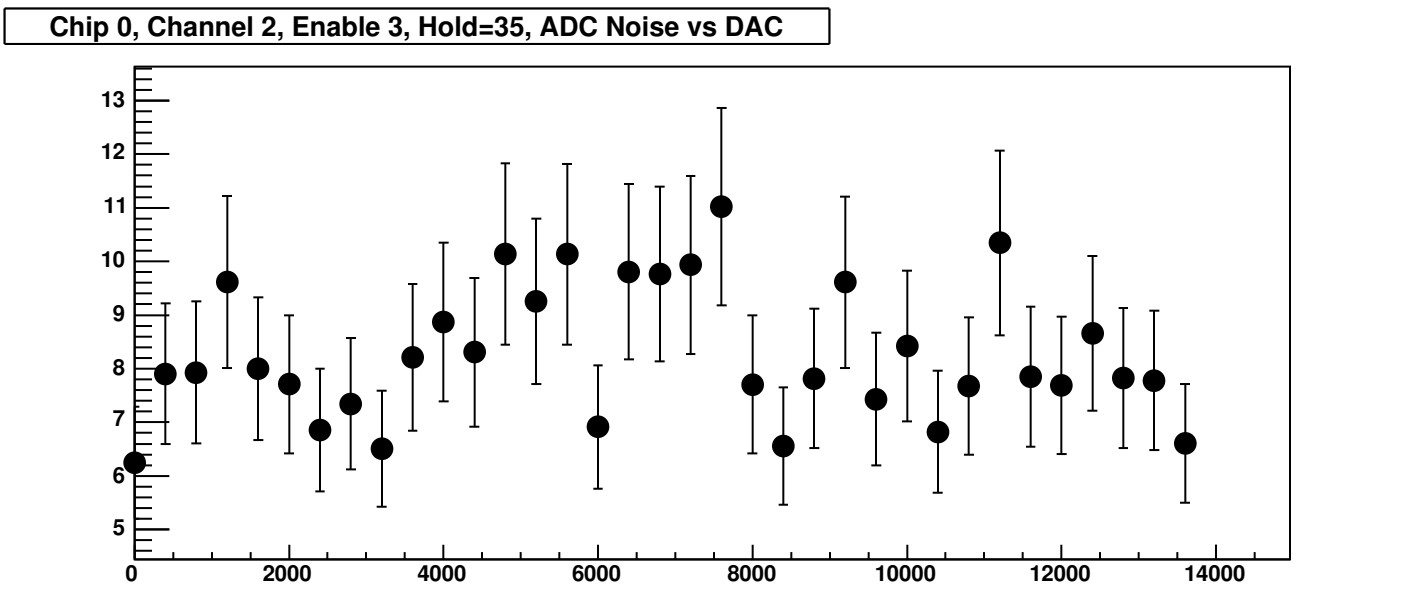
Chip 0, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

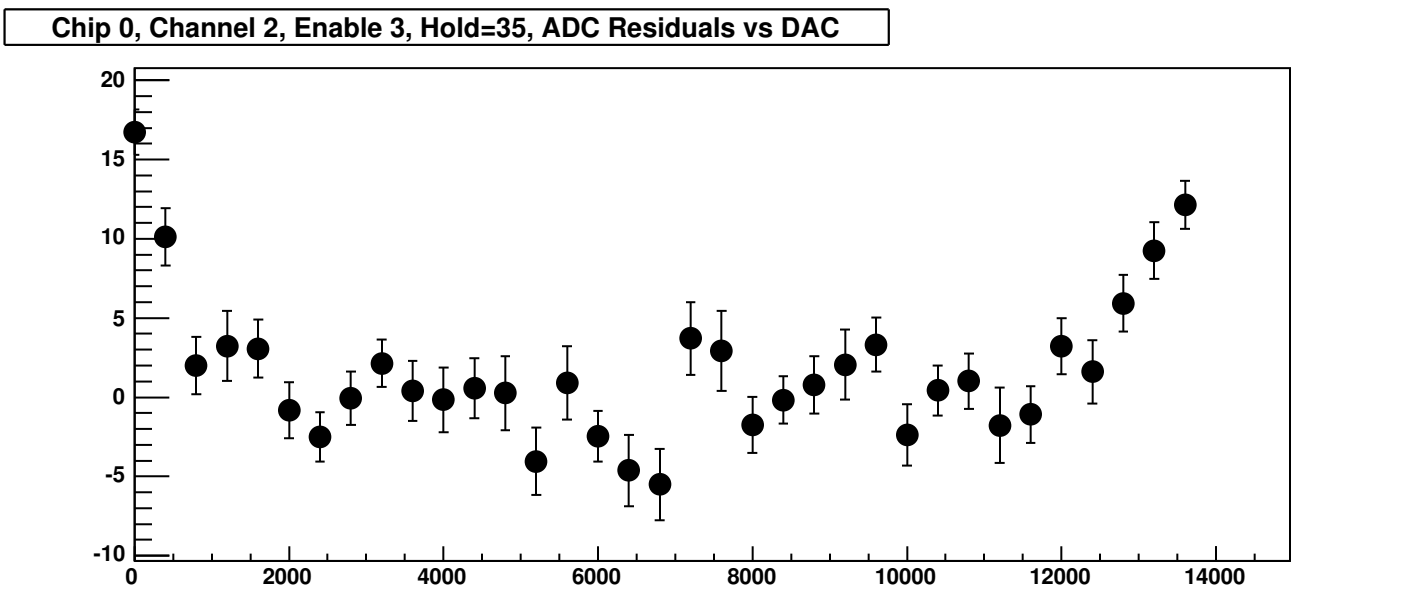


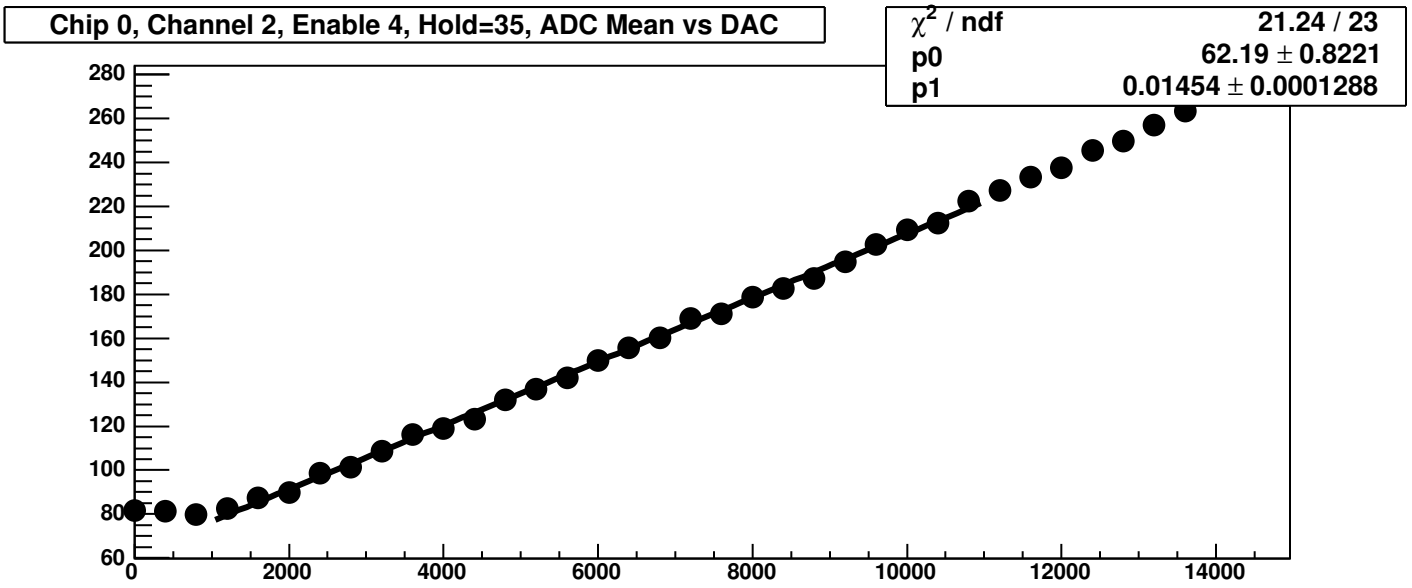
Chip 0, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

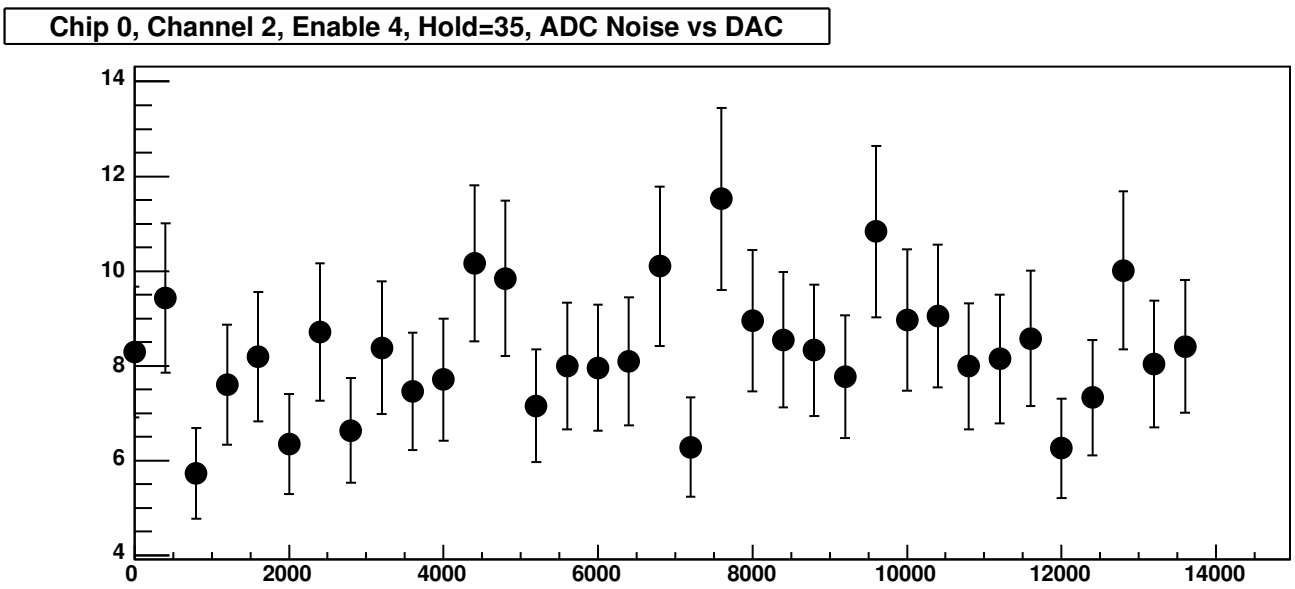


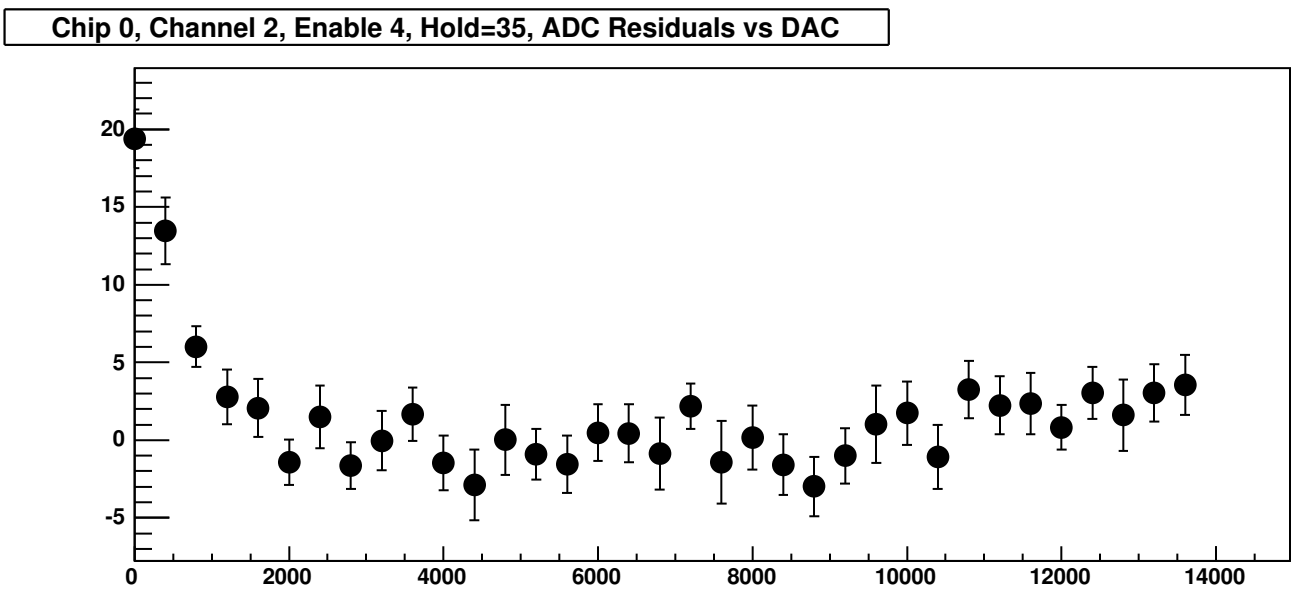

Chip 0, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.05 / 23
p0 63.37 ± 0.8345
p1 0.01578 ± 0.0001241
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

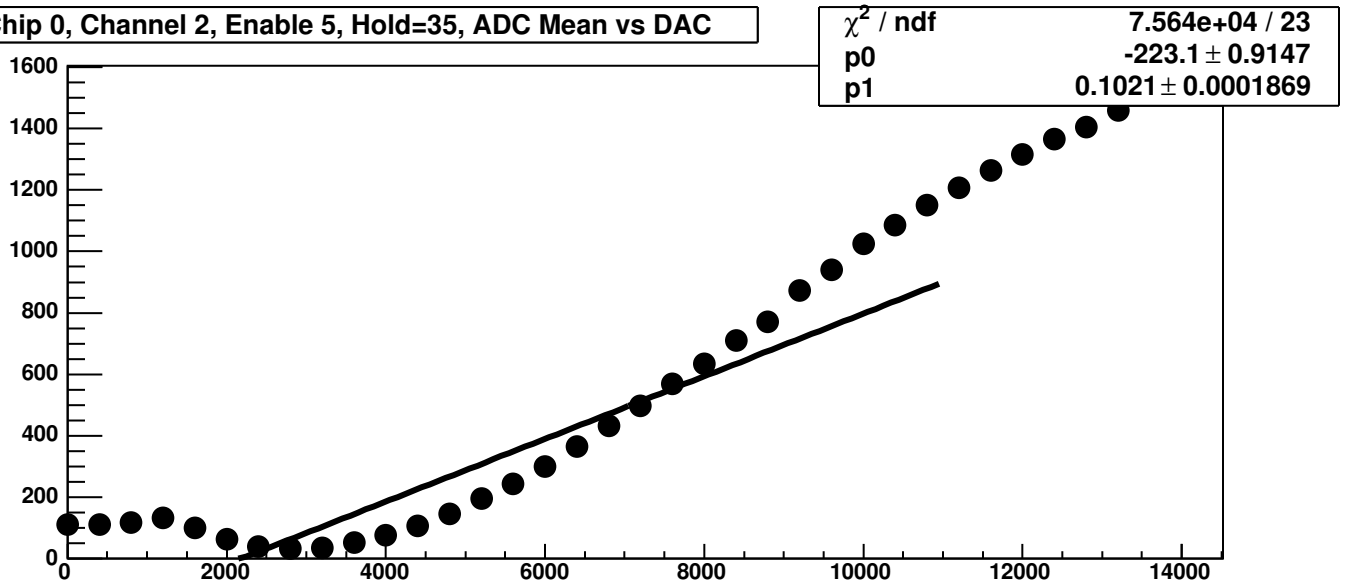

Chip 0, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 21.24 / 23
p0 62.19 ± 0.8221
p1 0.01454 ± 0.0001288
280
260
240
220
200
180
160
140
120
100
80
60
0
2000
4000
6000
8000
10000
12000
14000

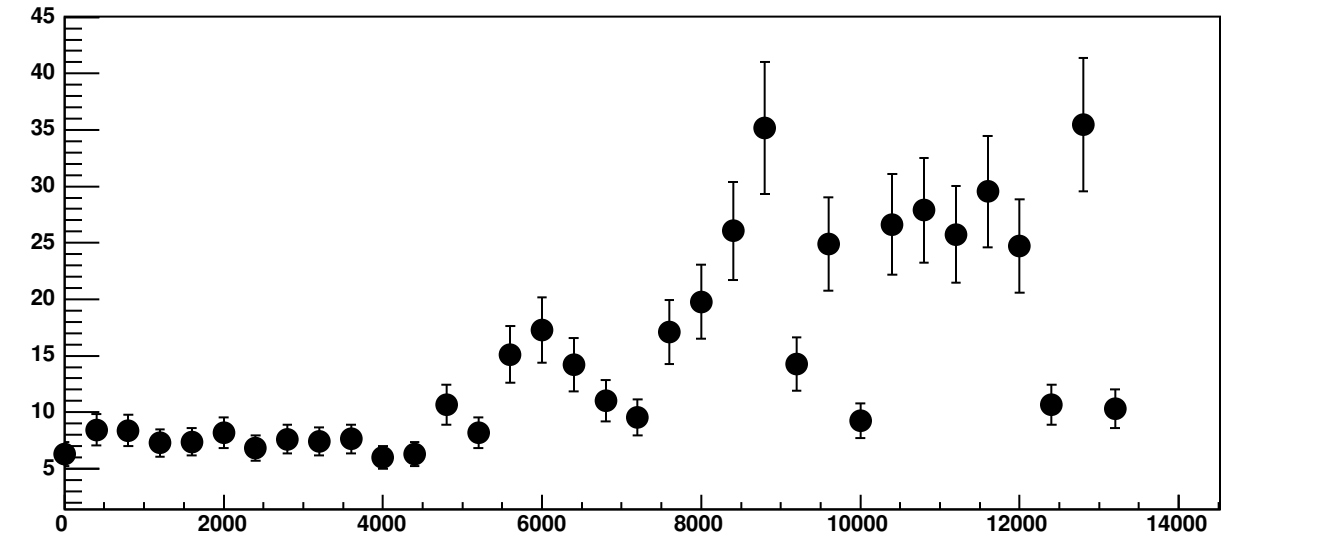

Chip 0, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

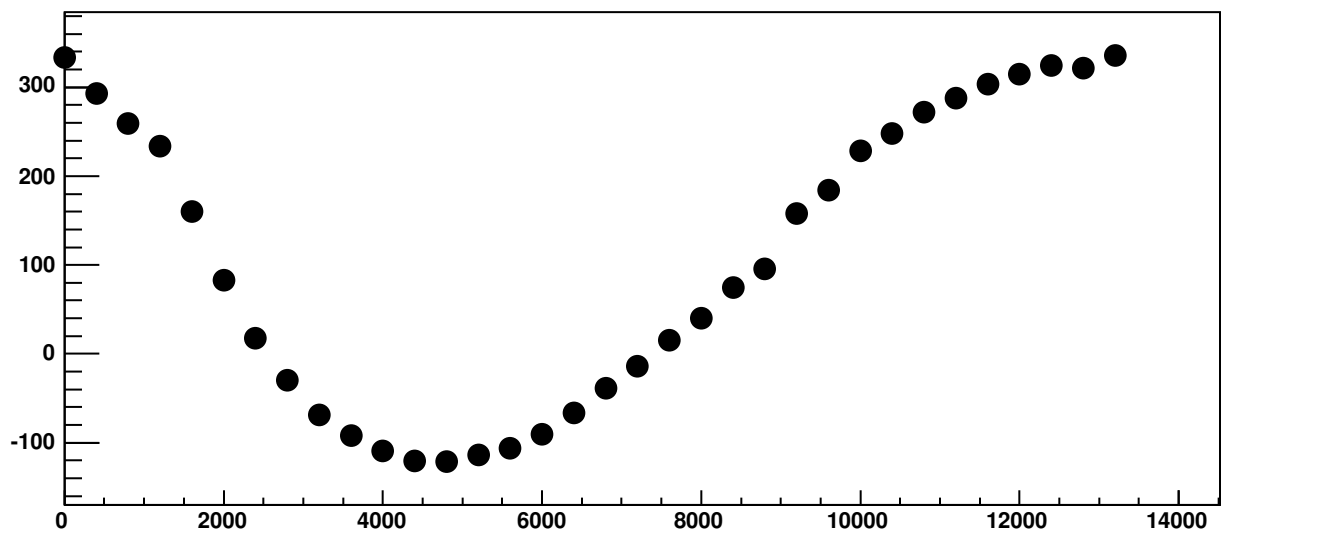
Chip 0, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC

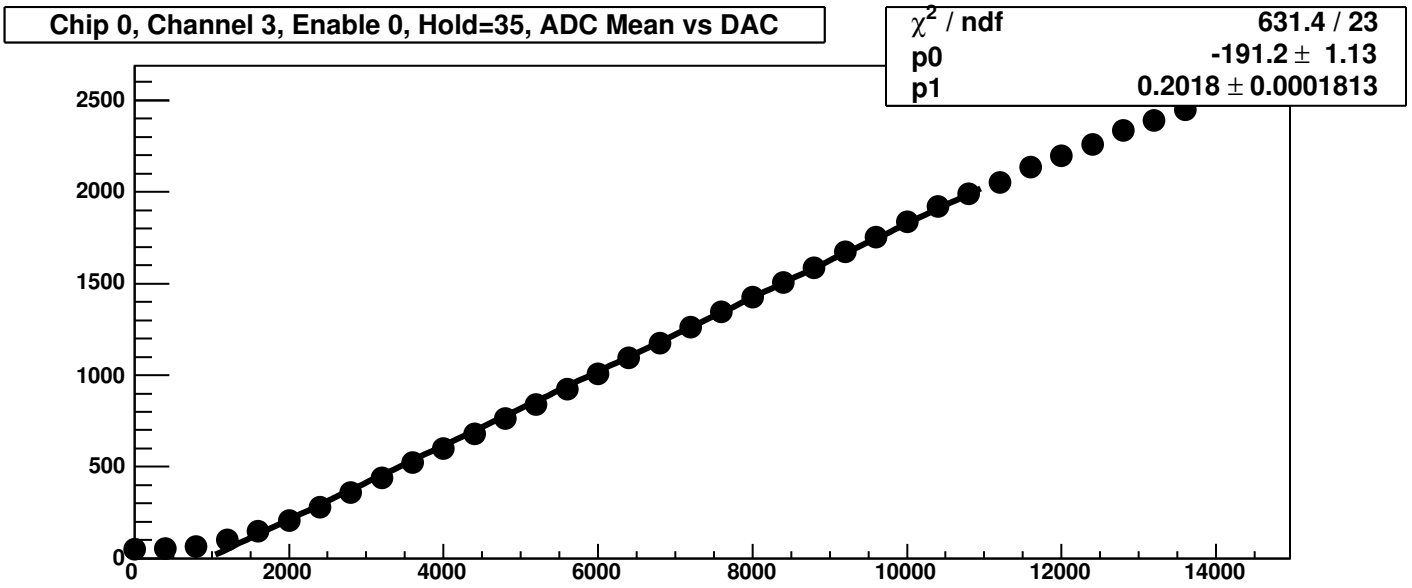


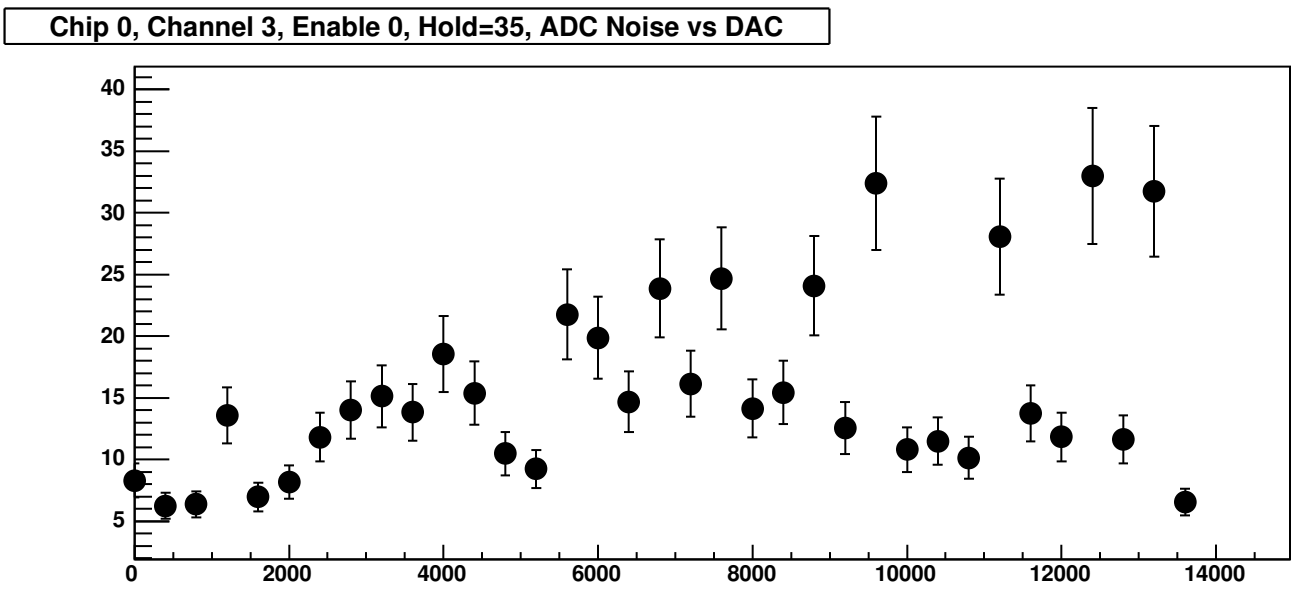
Chip 0, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC

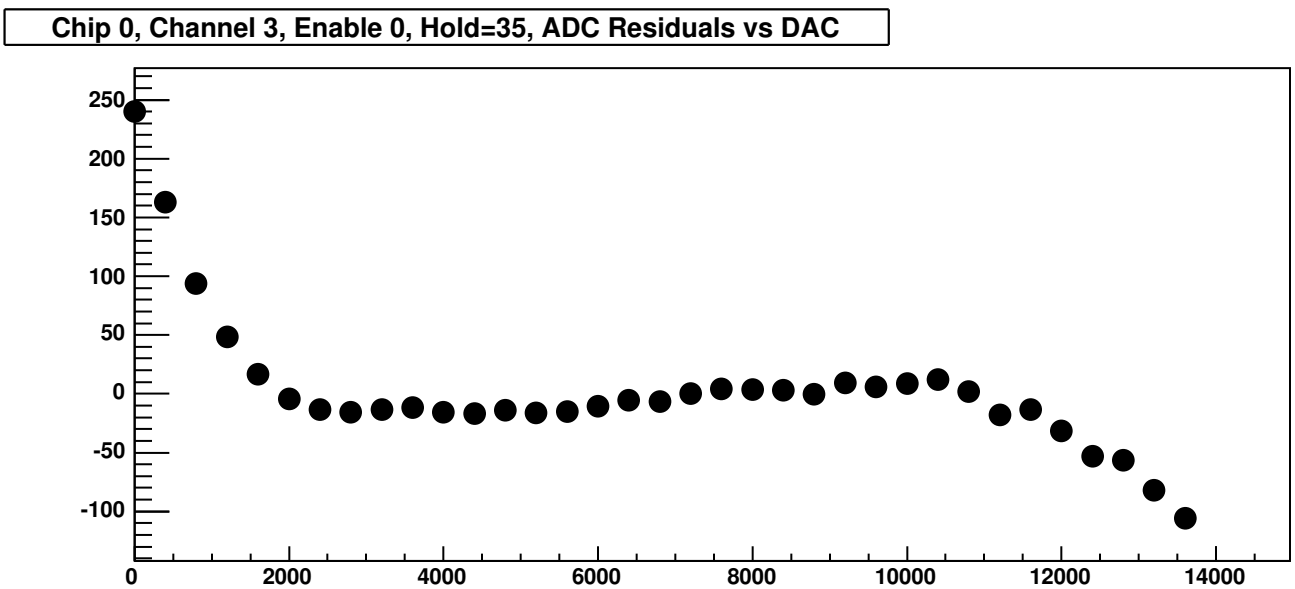


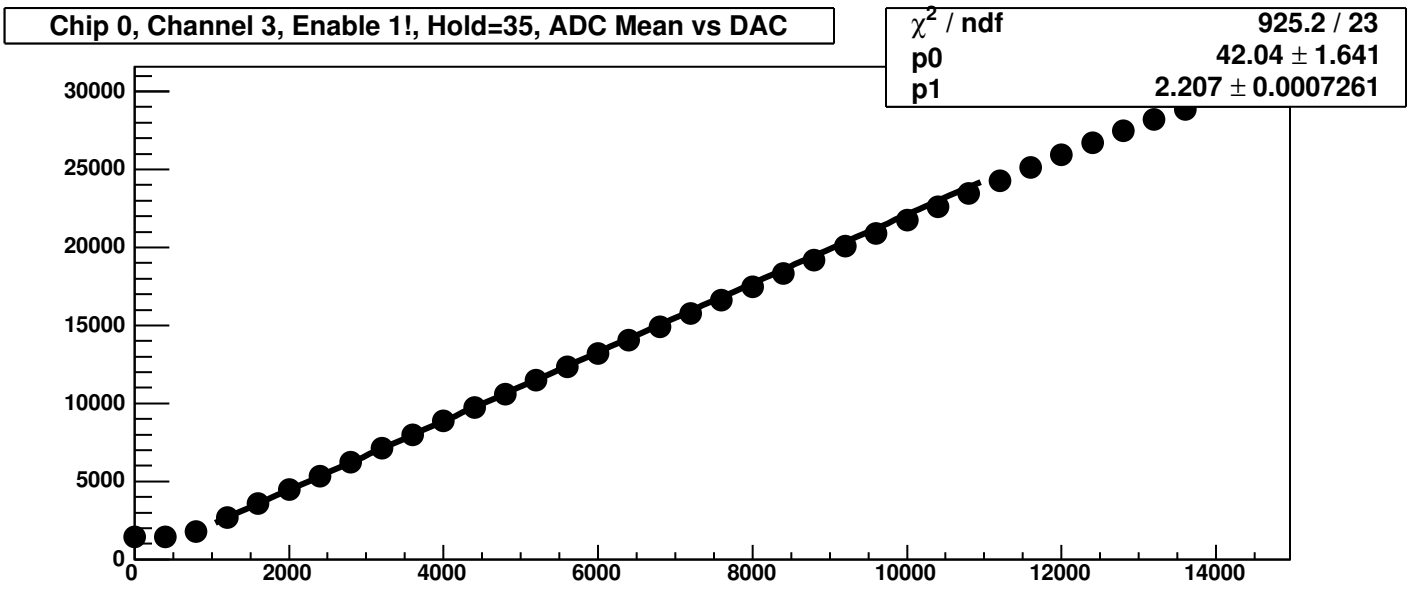
Chip 0, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC

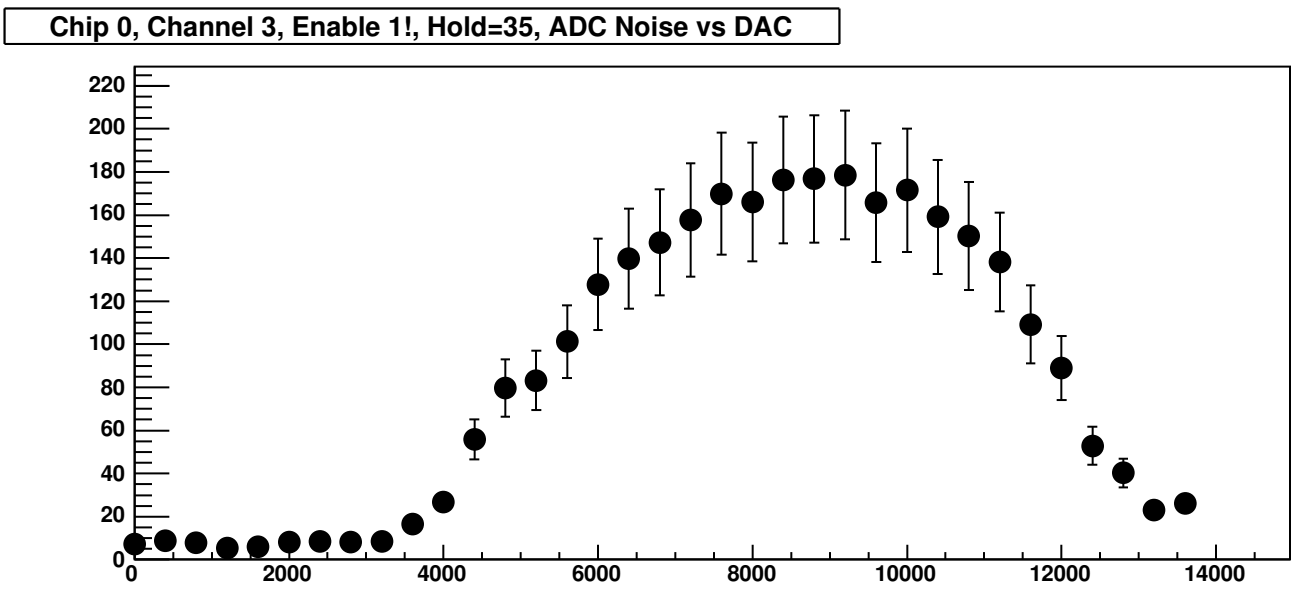


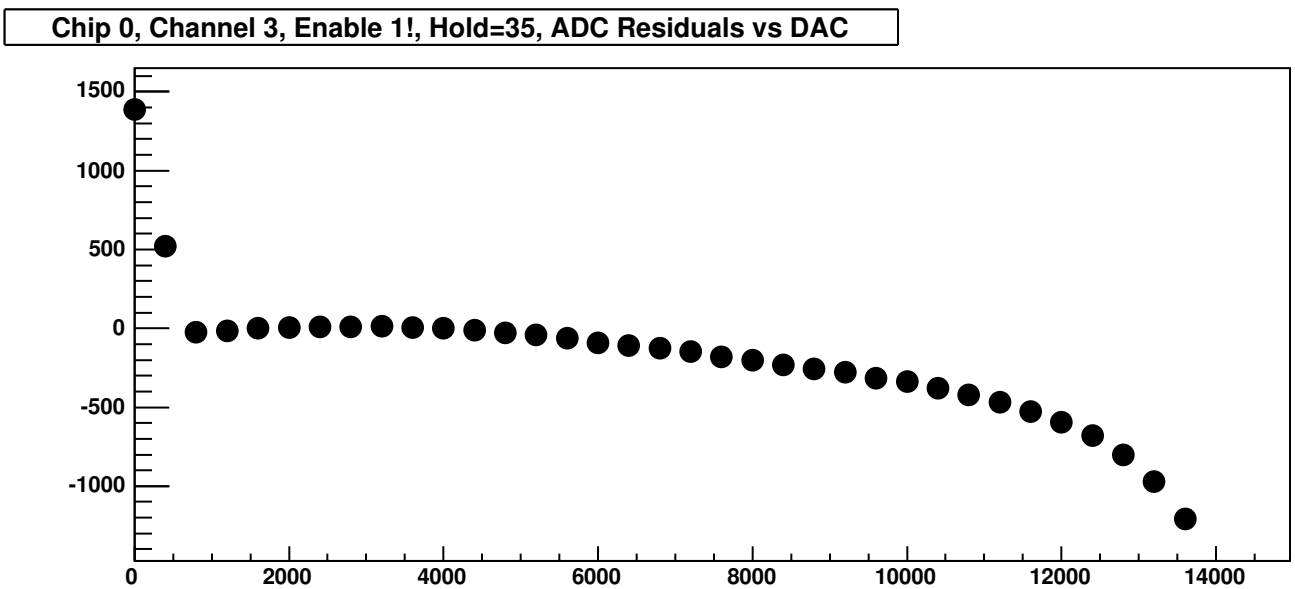

Chip 0, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 631.4 / 23
p0 -191.2 ± 1.13
p1 0.2018 ± 0.0001813
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

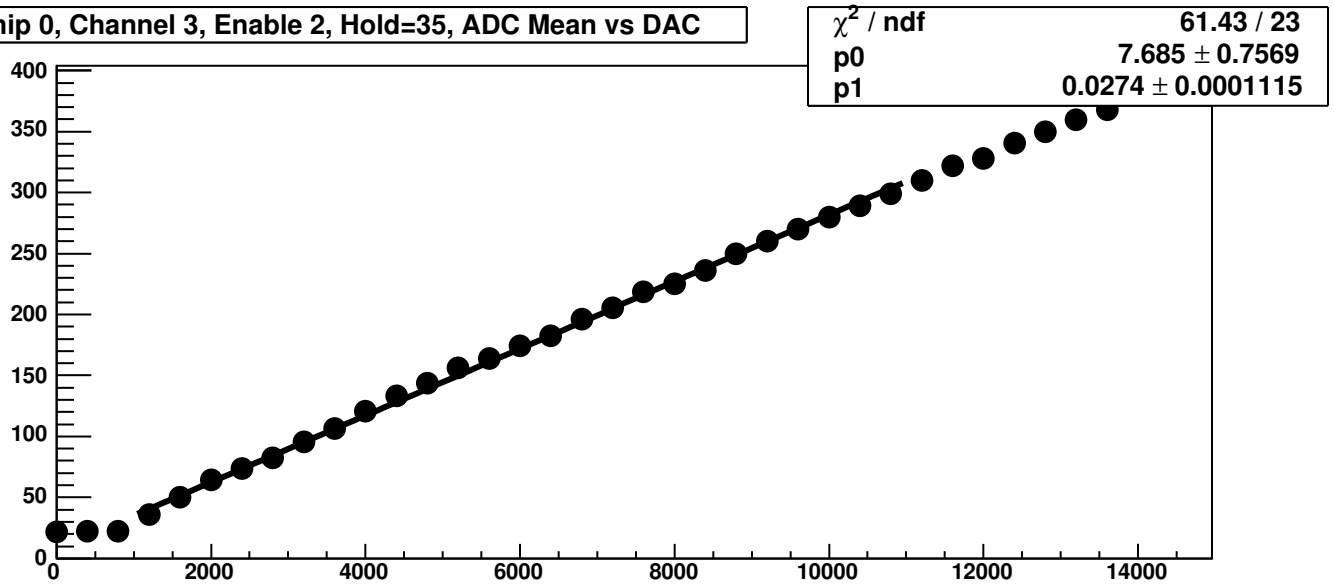

Chip 0, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 925.2 / 23
p0 42.04 ± 1.641
p1 2.207 ± 0.0007261
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

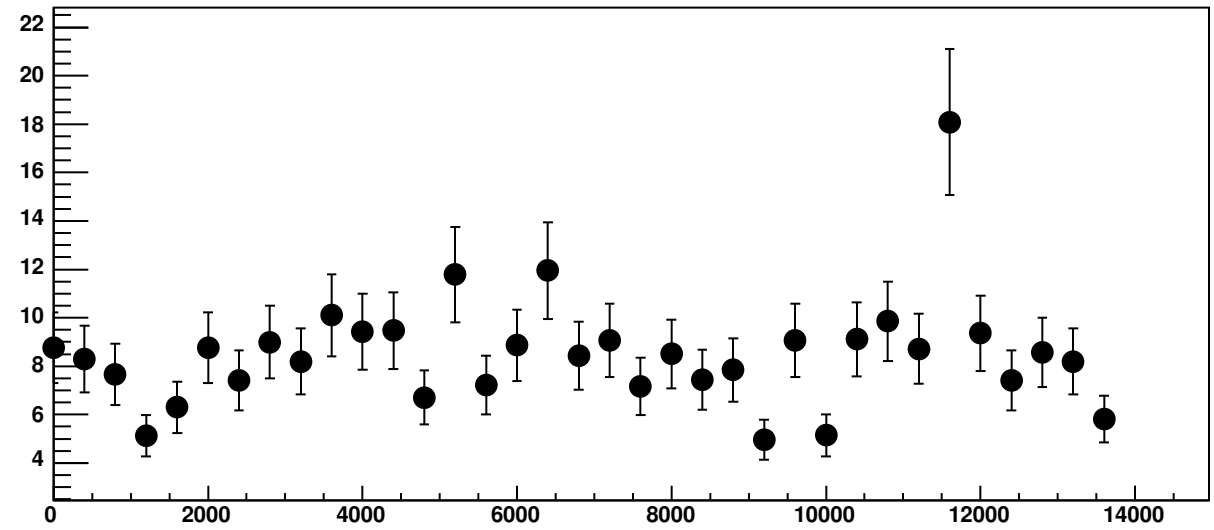

Chip 0, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

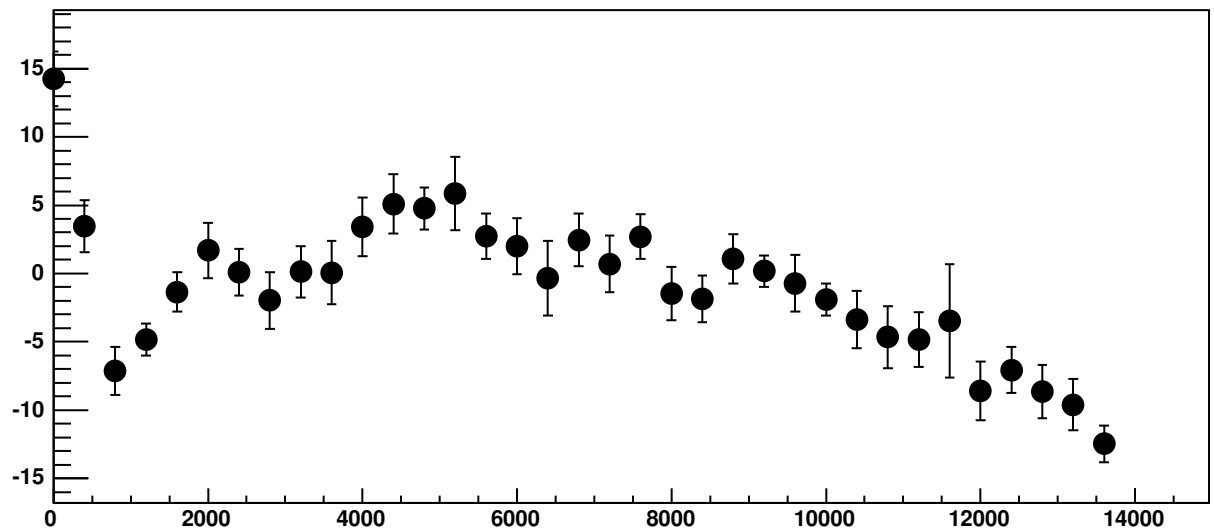
Chip 0, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

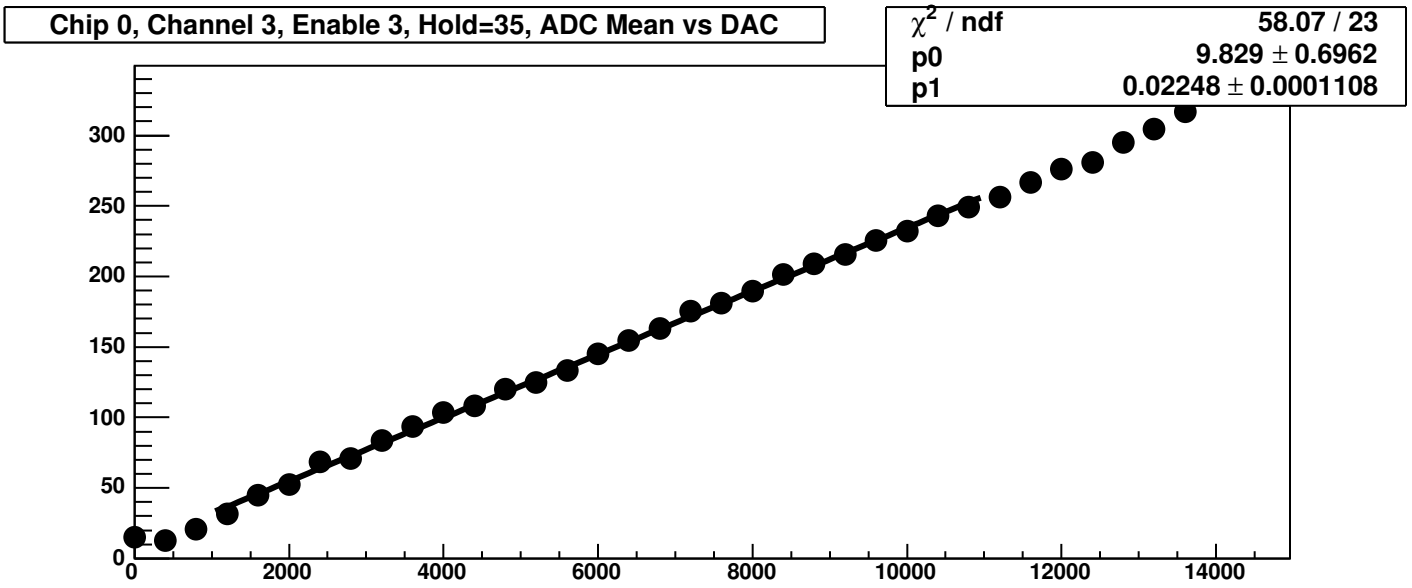


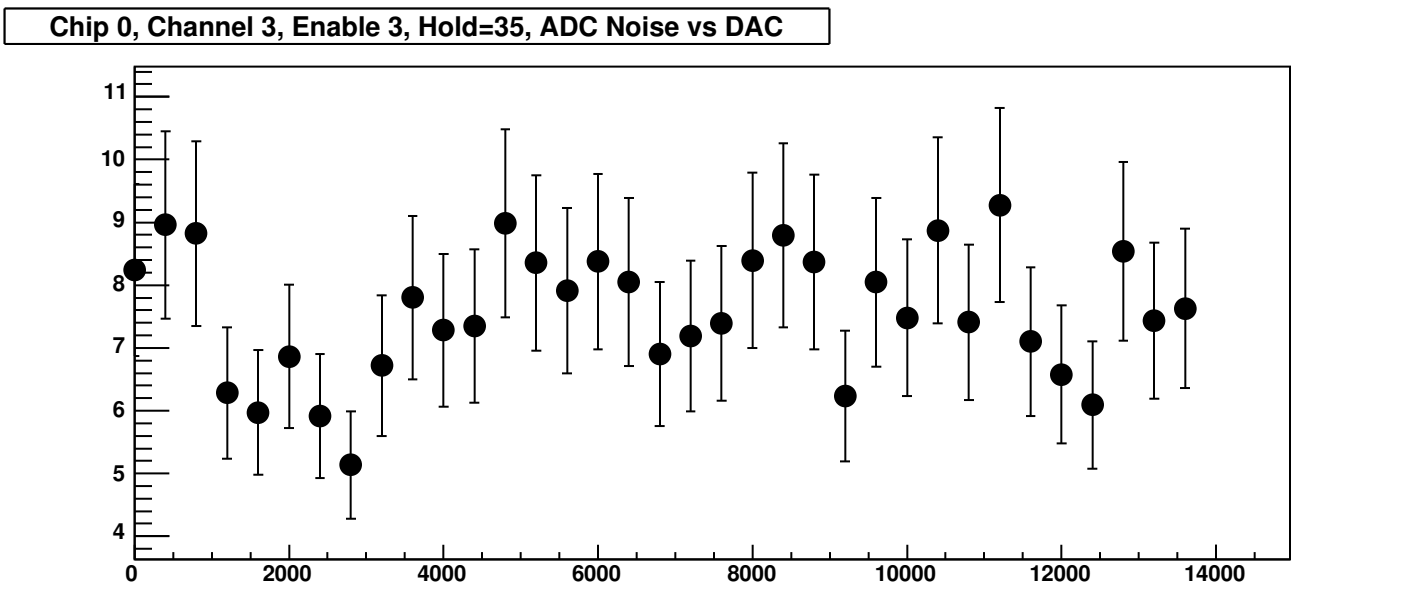
Chip 0, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC

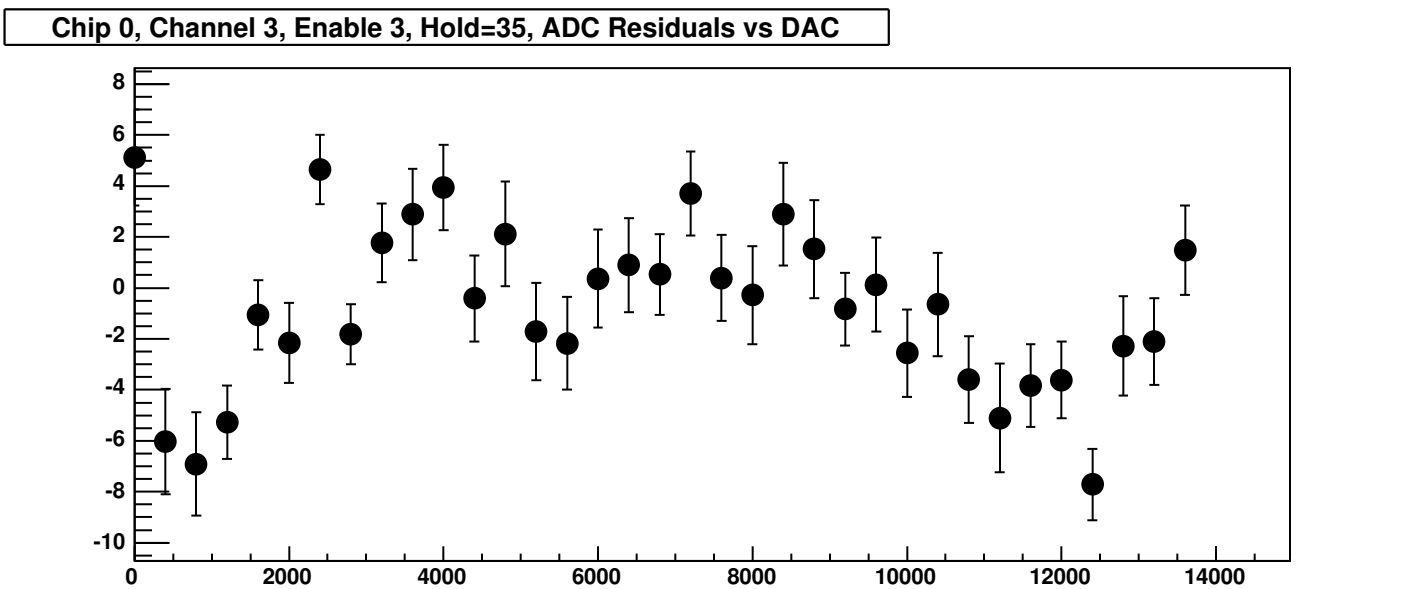


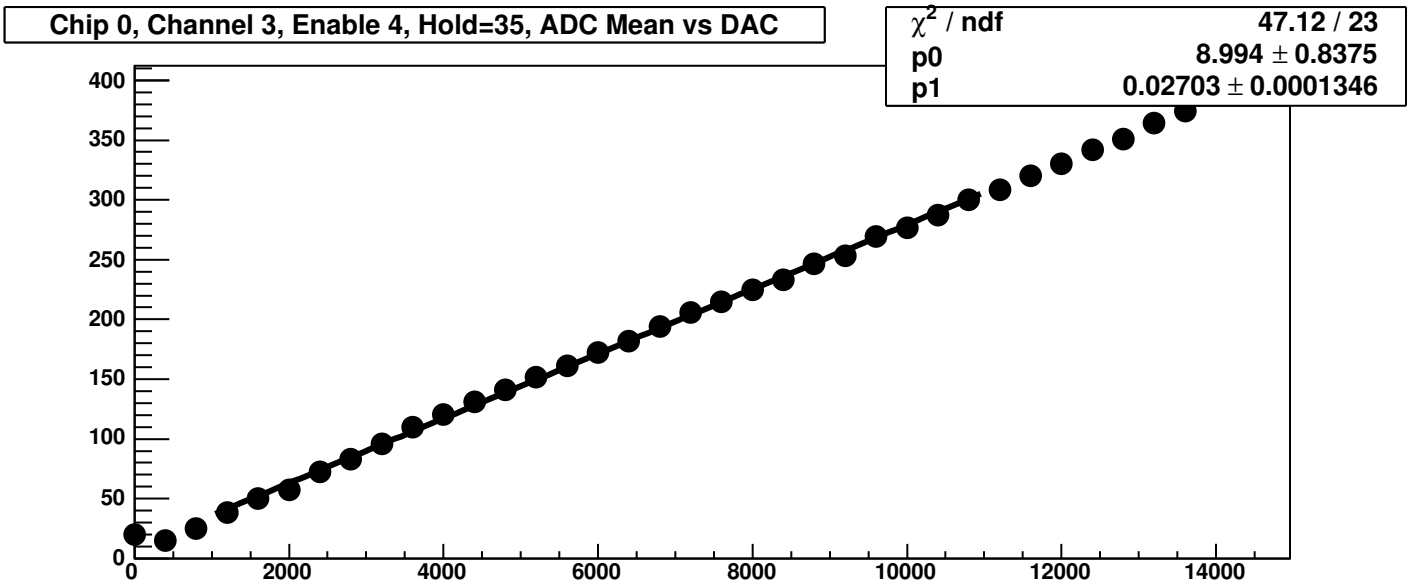
Chip 0, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

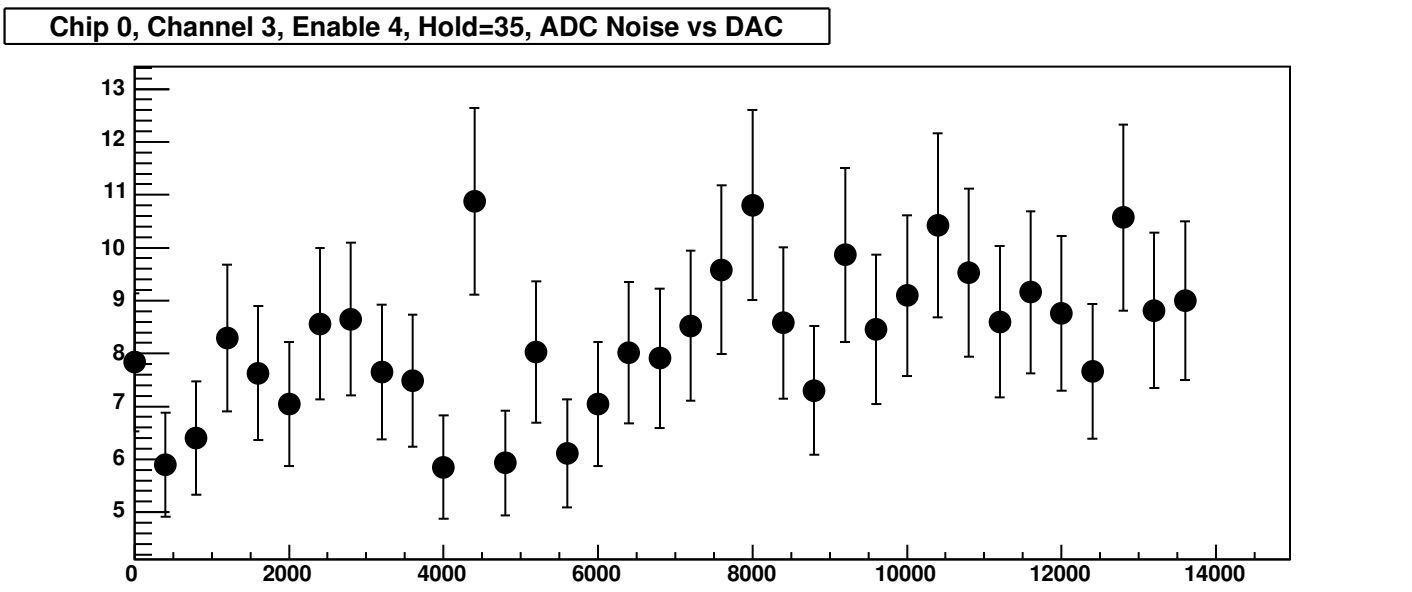


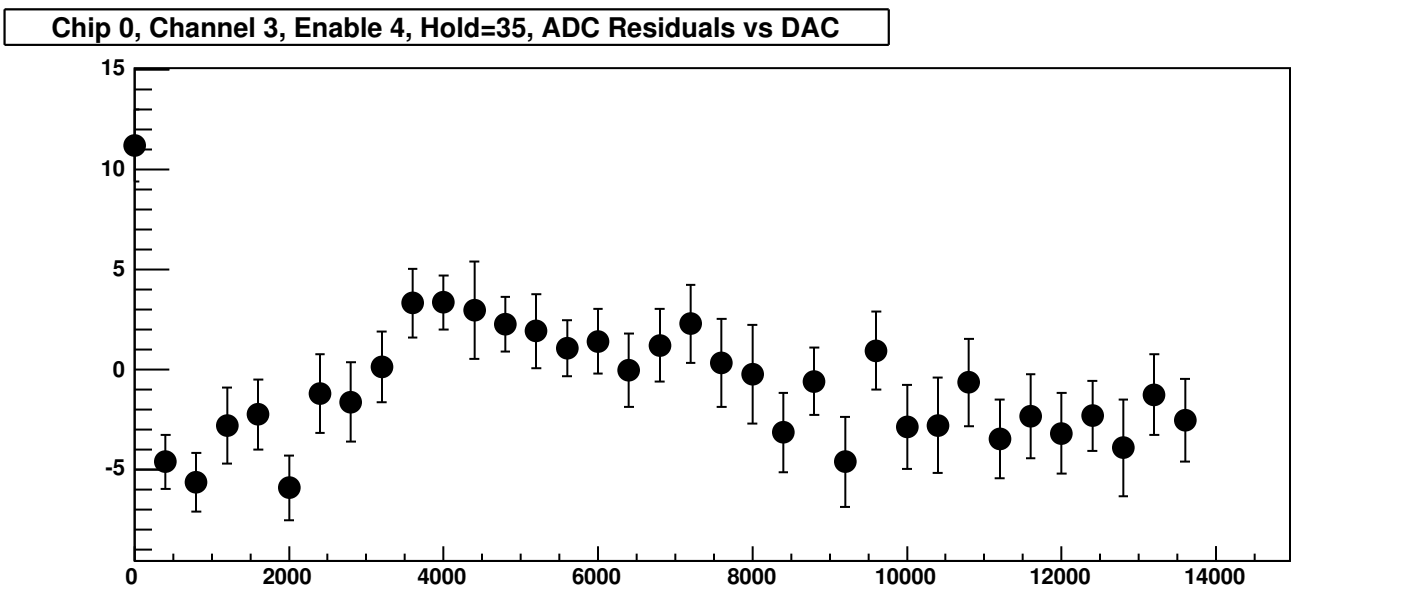

Chip 0, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 58.07 / 23
p0 9.829 ± 0.6962
p1 0.02248 ± 0.0001108
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

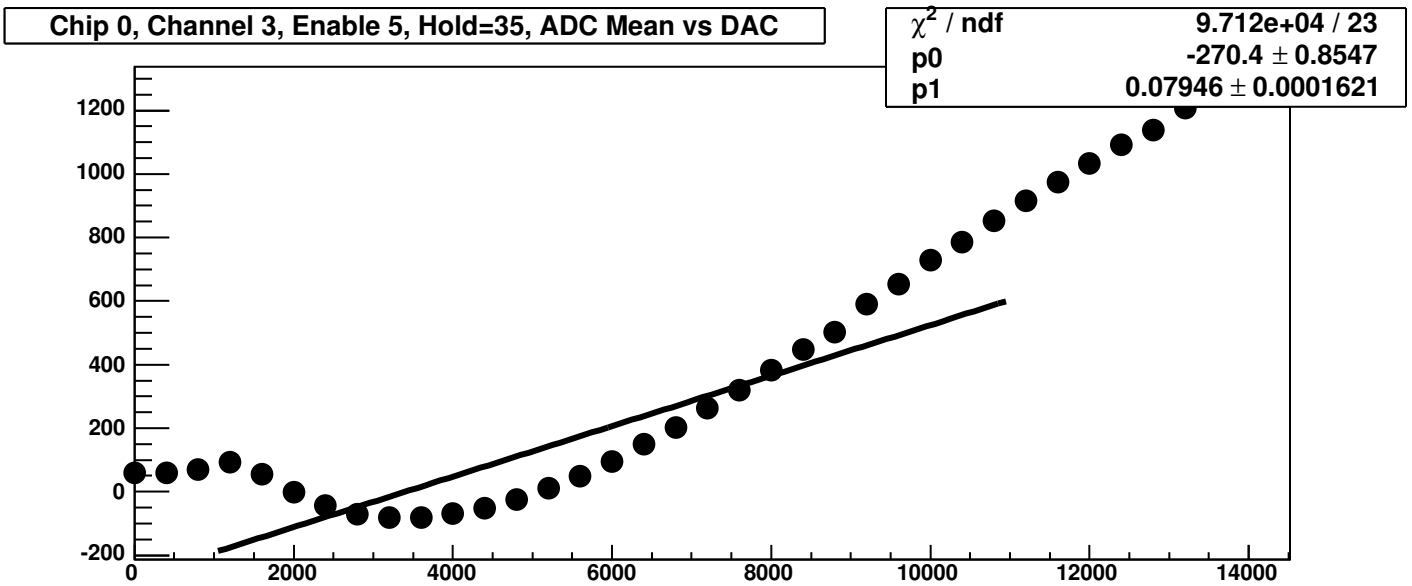

Chip 0, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

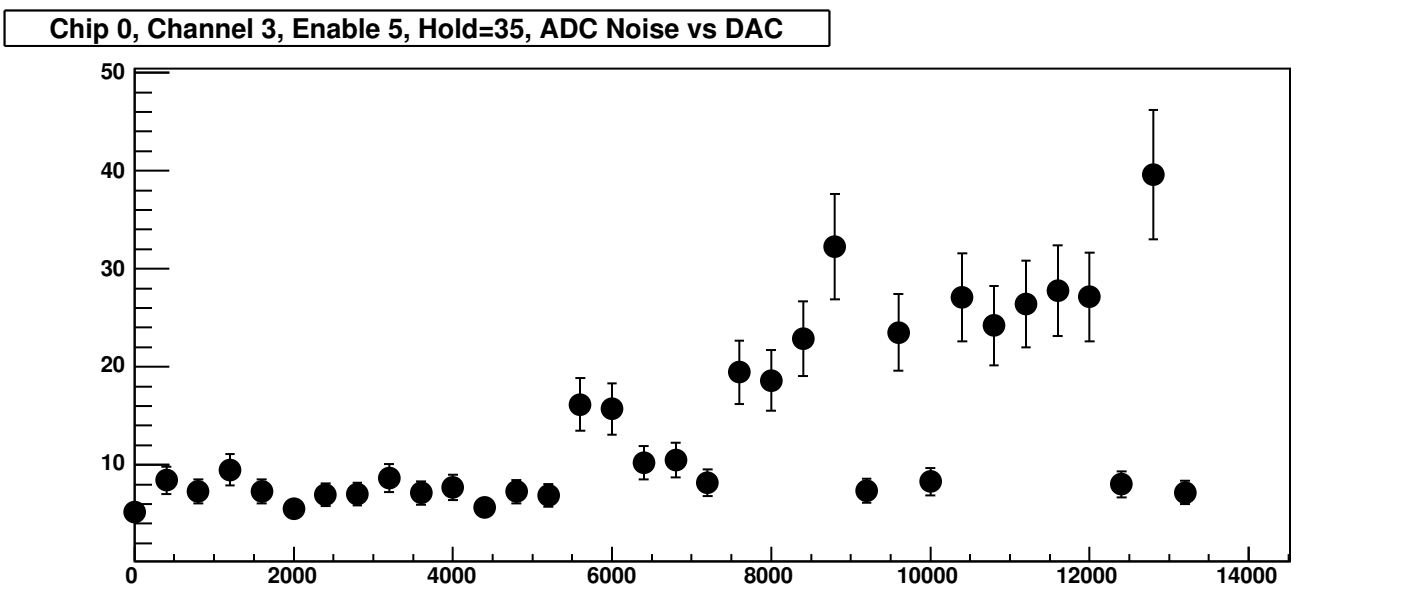

Chip 0, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
0
2000
4000
6000
8000
10000
12000
14000

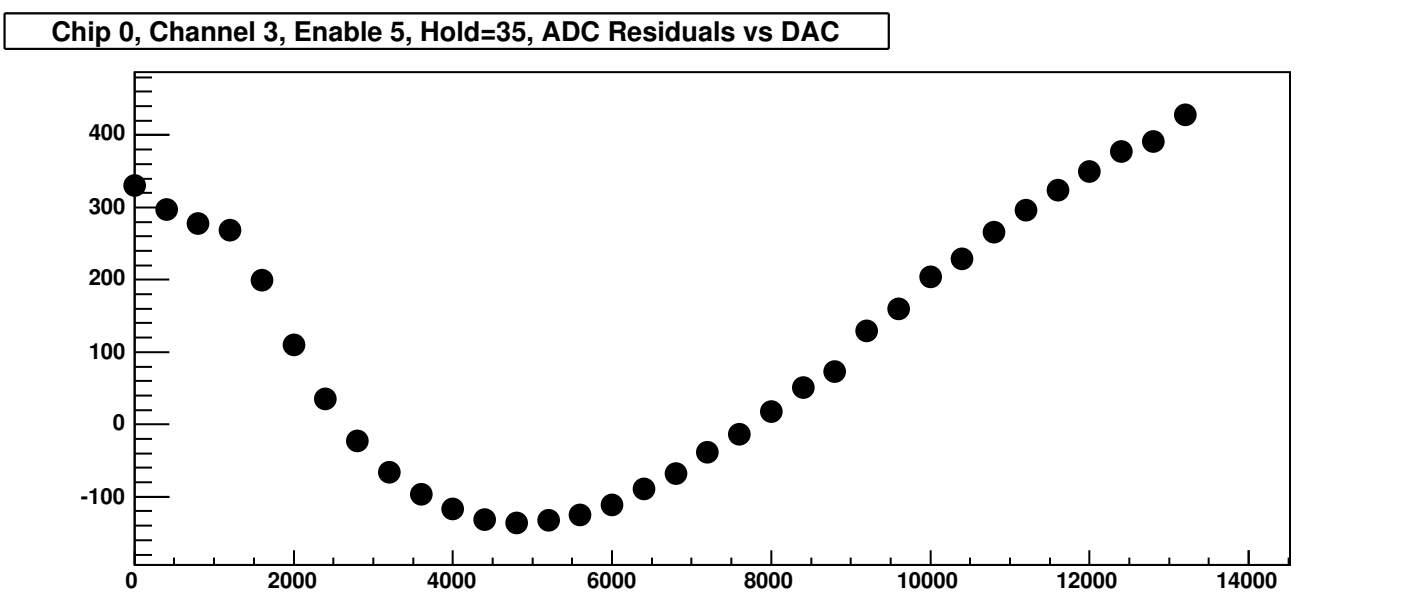

Chip 0, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 47.12 / 23
p0 8.994 ± 0.8375
p1 0.02703 ± 0.0001346
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

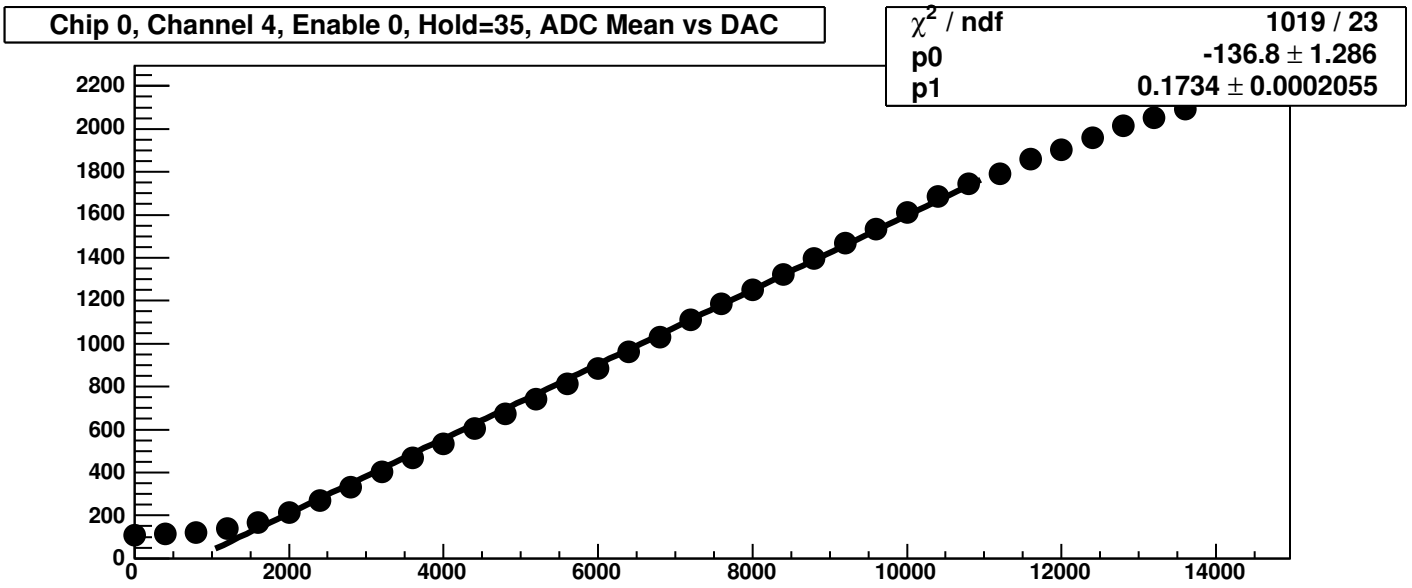

Chip 0, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

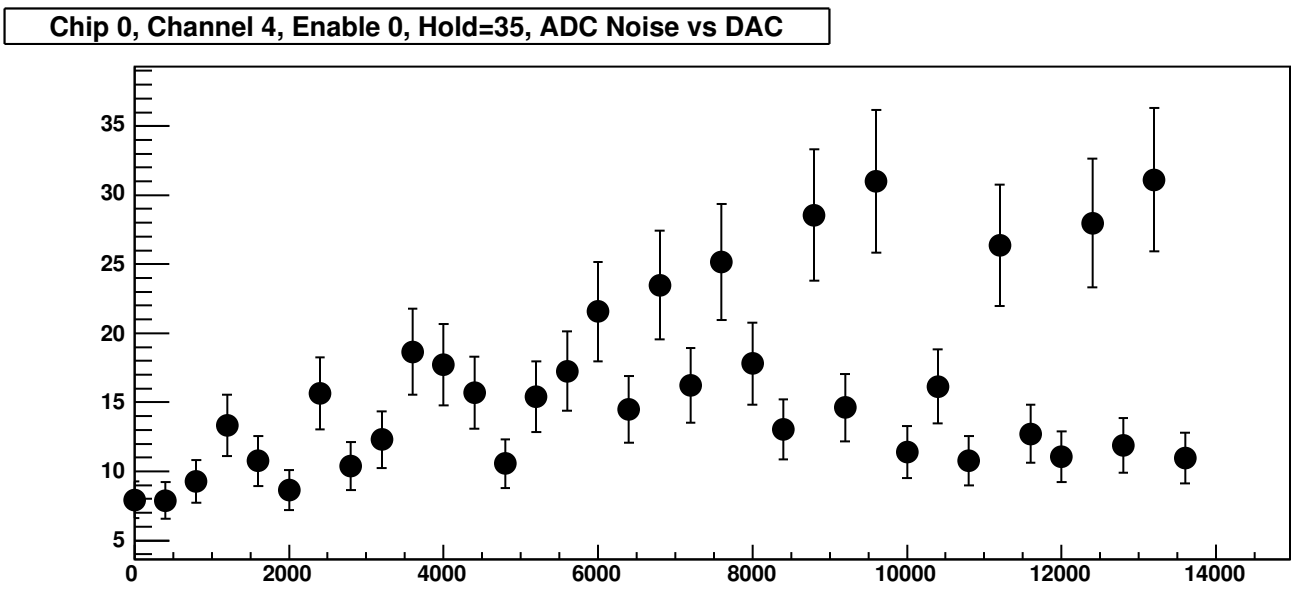

Chip 0, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

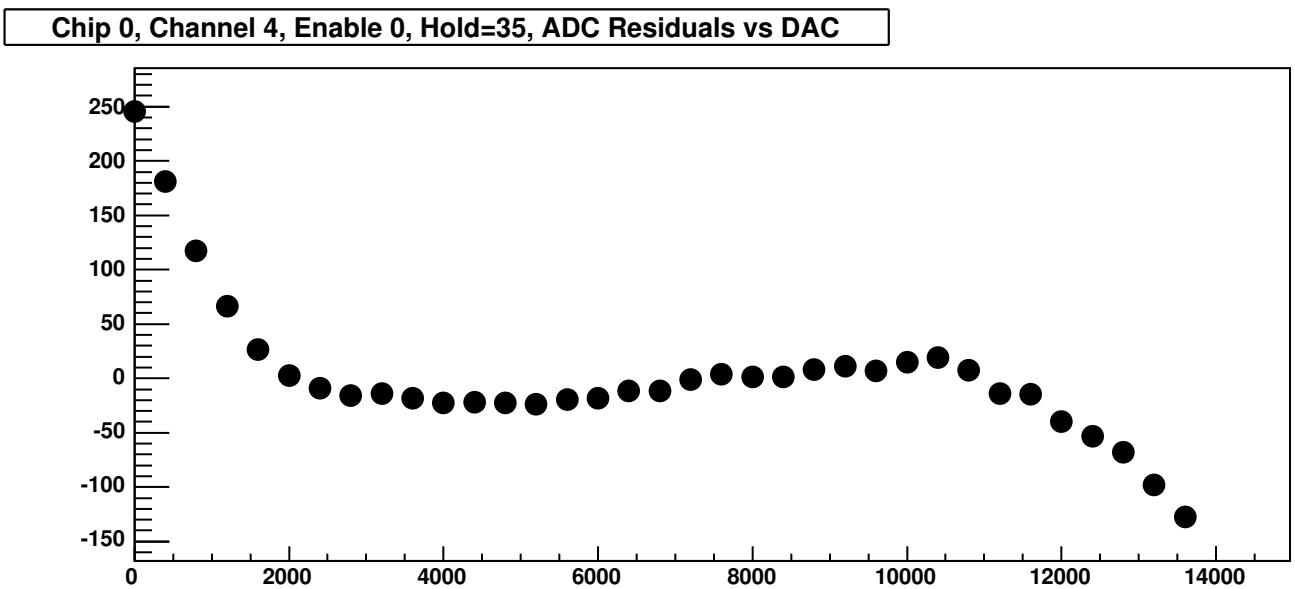

Chip 0, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 9.712e+04 / 23
p0 -270.4 ± 0.8547
p1 0.07946 ± 0.0001621
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

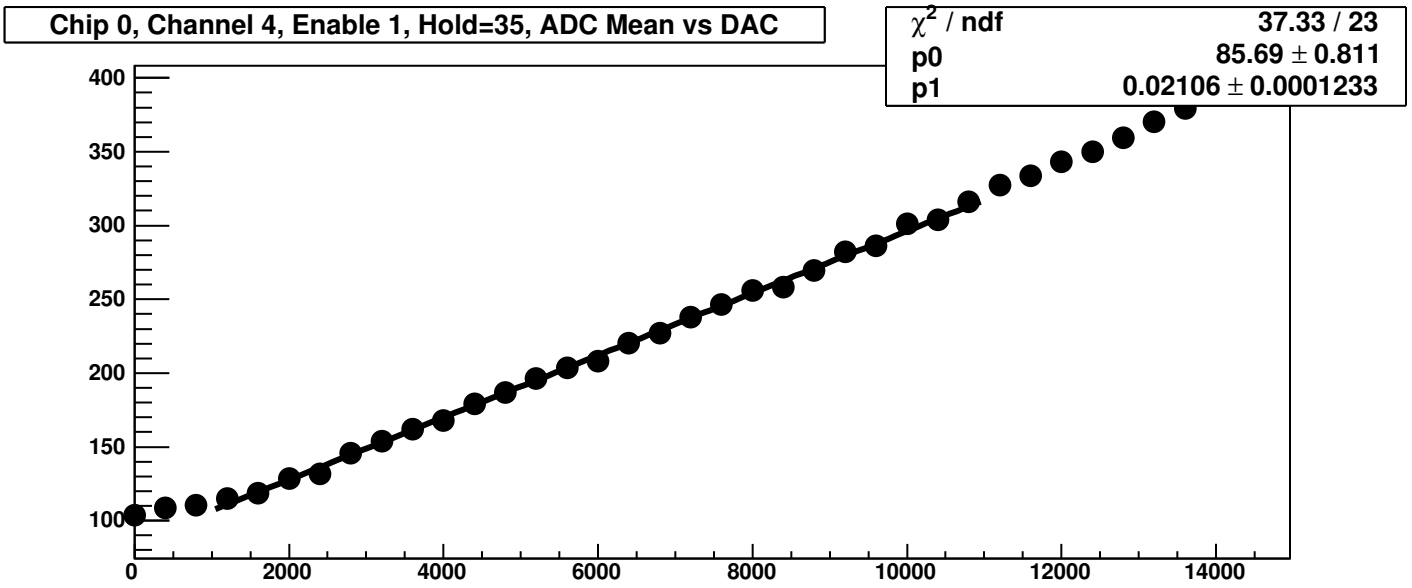

Chip 0, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

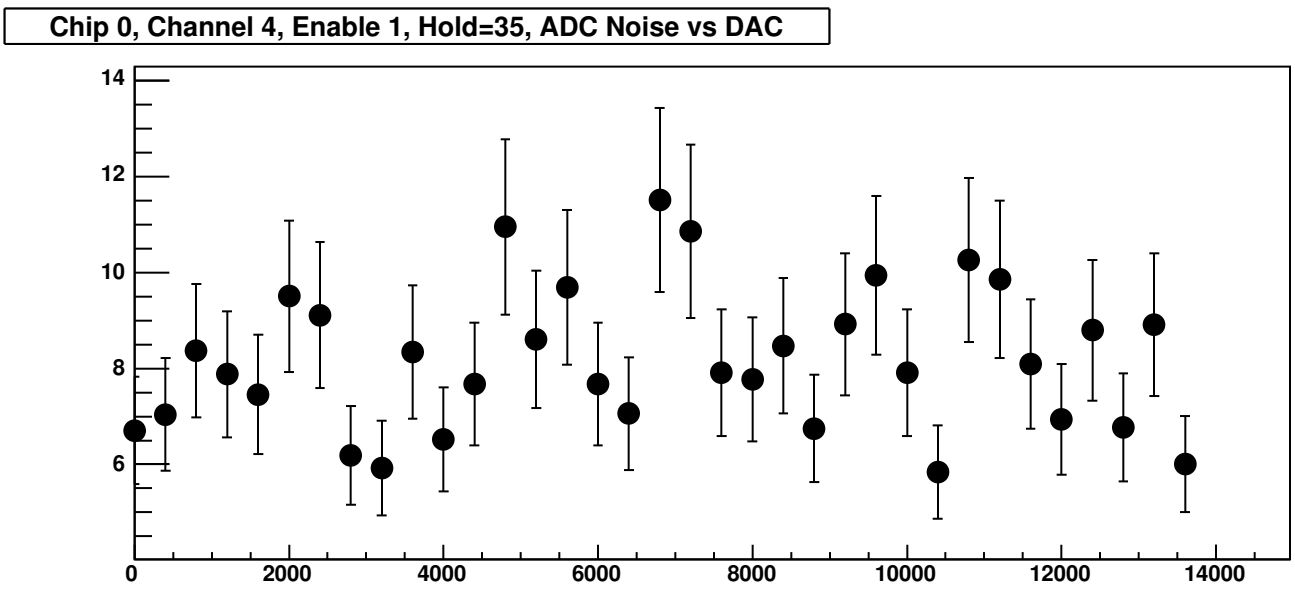

Chip 0, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

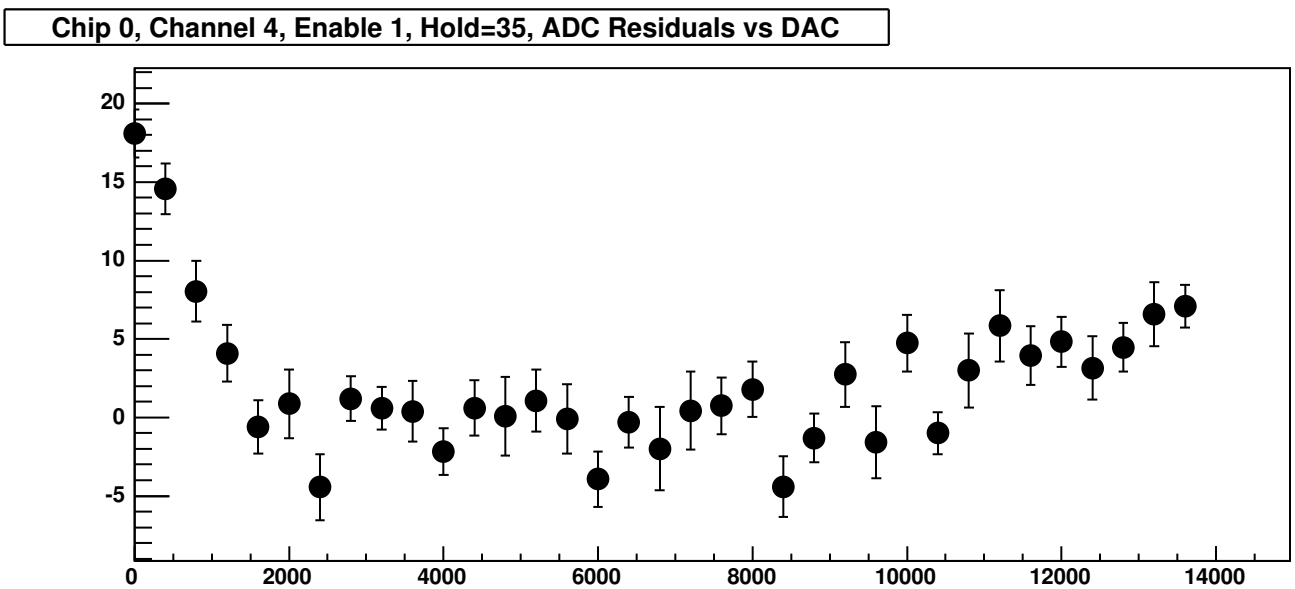

Chip 0, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1019 / 23
p0 -136.8 ± 1.286
p1 0.1734 ± 0.0002055
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

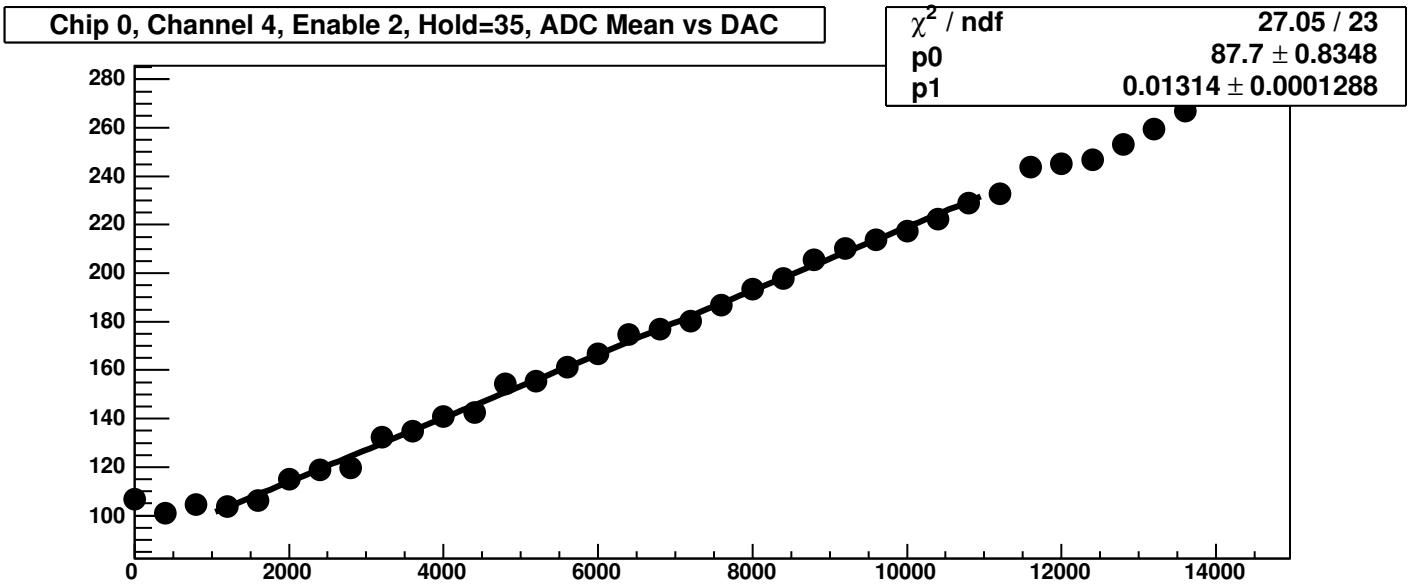

Chip 0, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

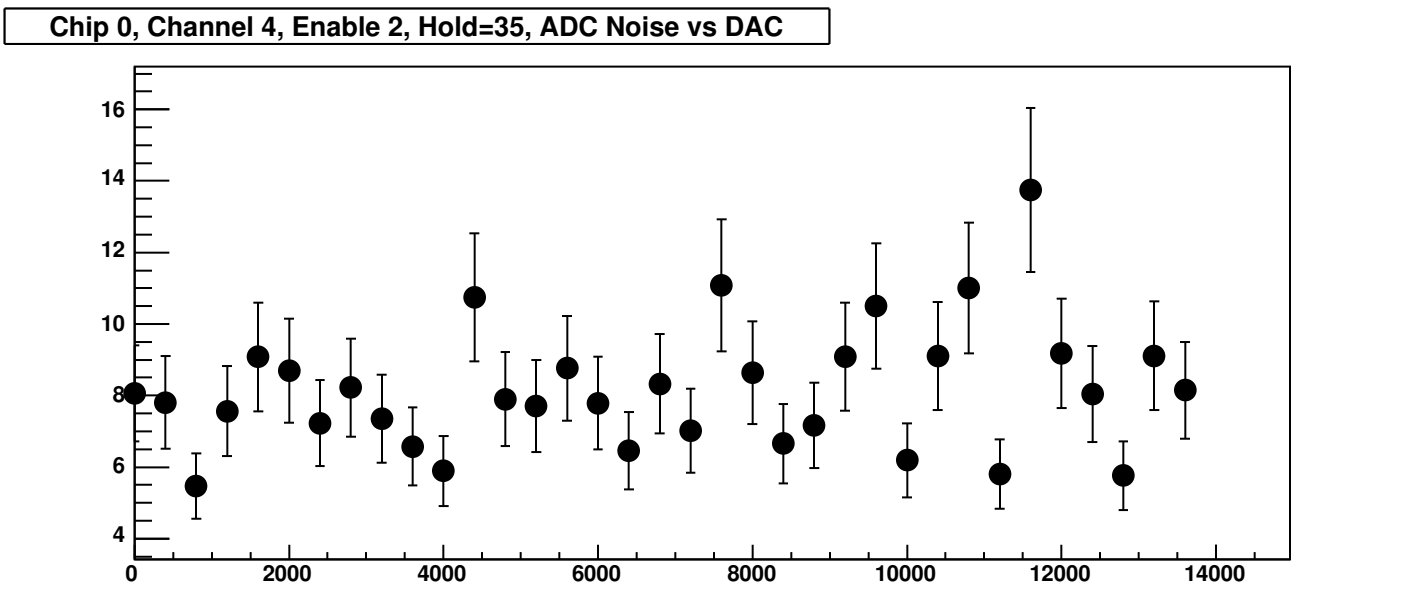

Chip 0, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

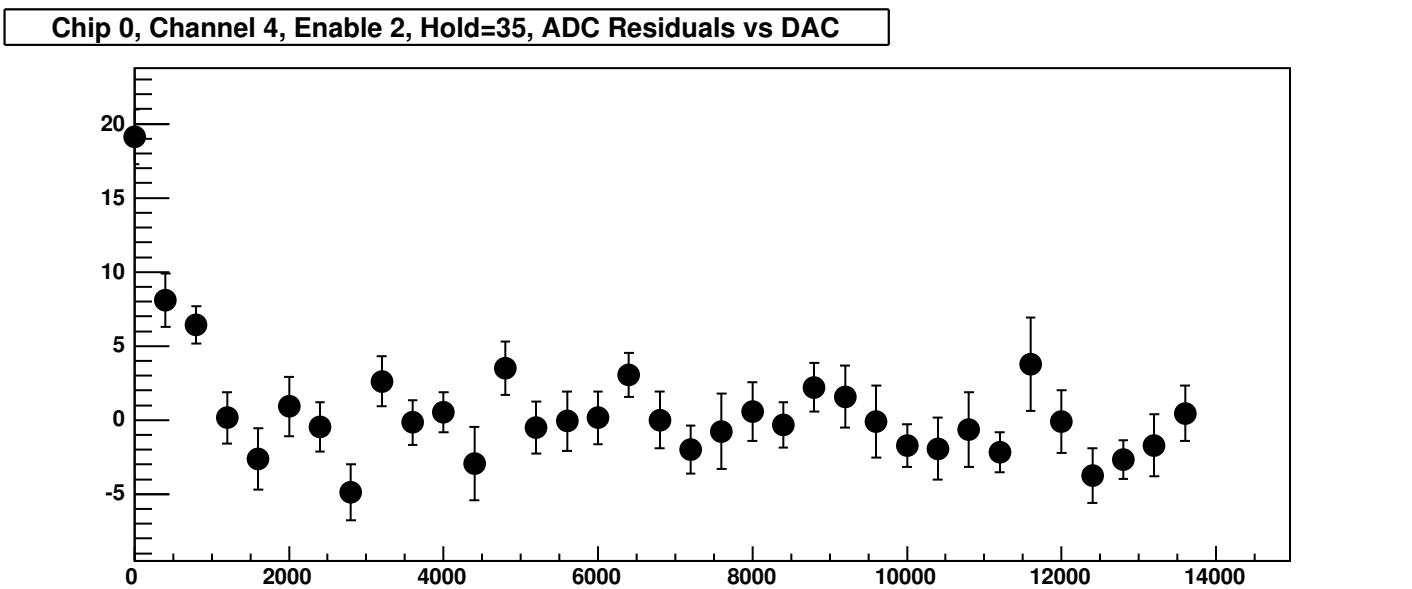

Chip 0, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 37.33 / 23
p0 85.69 ± 0.811
p1 0.02106 ± 0.0001233
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

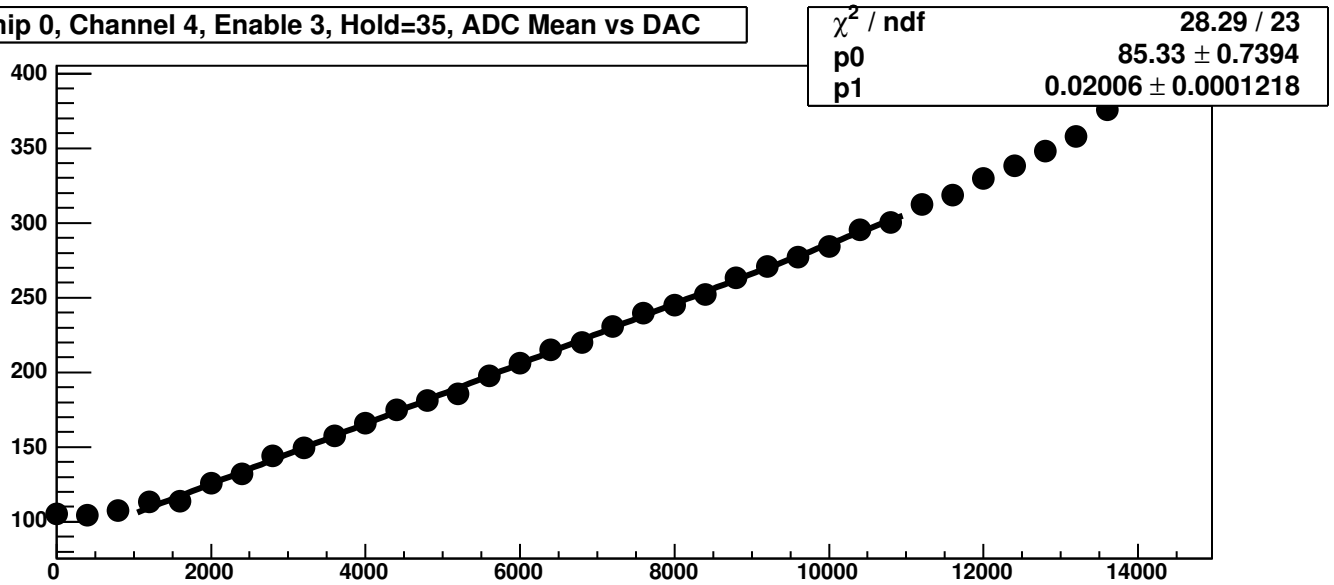

Chip 0, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 27.05 / 23
p0 87.7 ± 0.8348
p1 0.01314 ± 0.0001288
280
260
240
220
200
180
160
140
120
100
0
2000
4000
6000
8000
10000
12000
14000

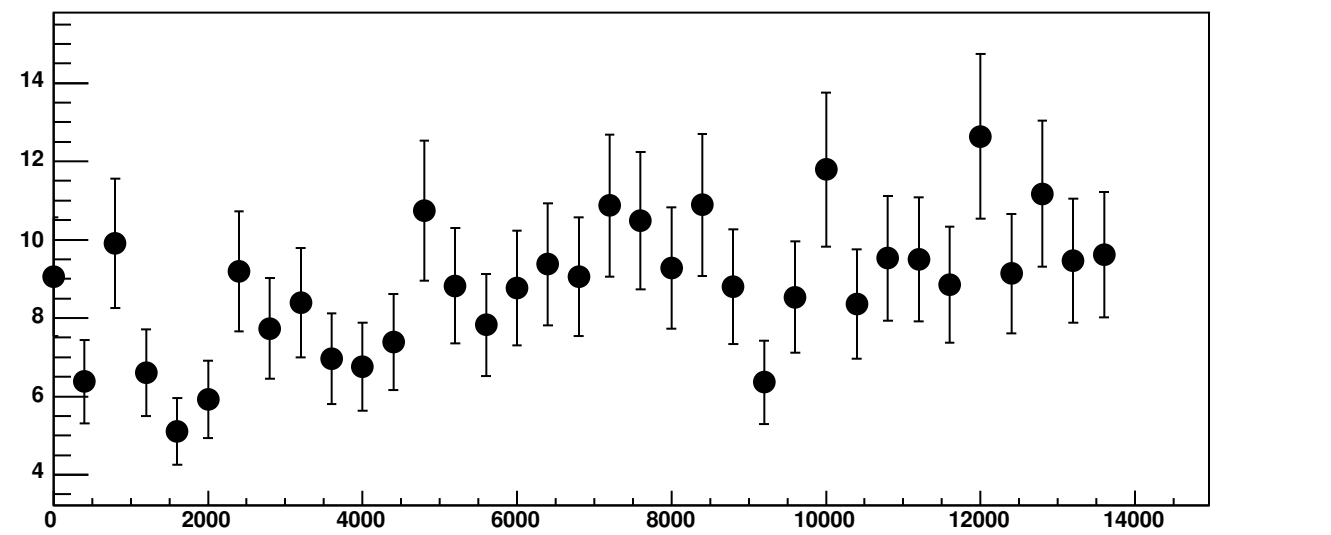

Chip 0, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

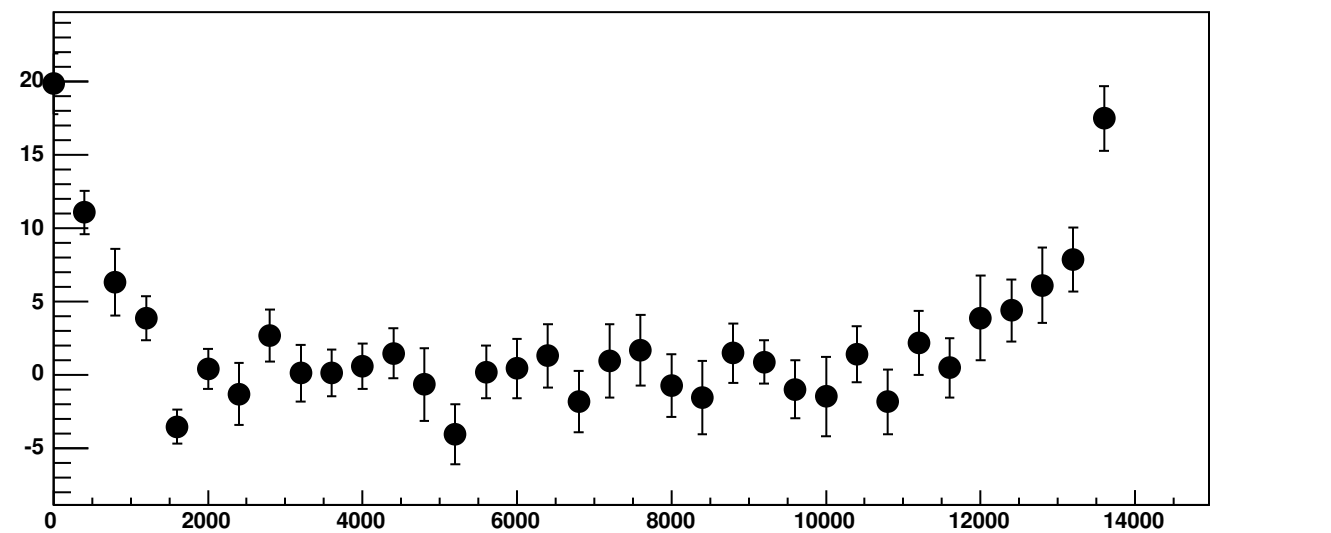
Chip 0, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

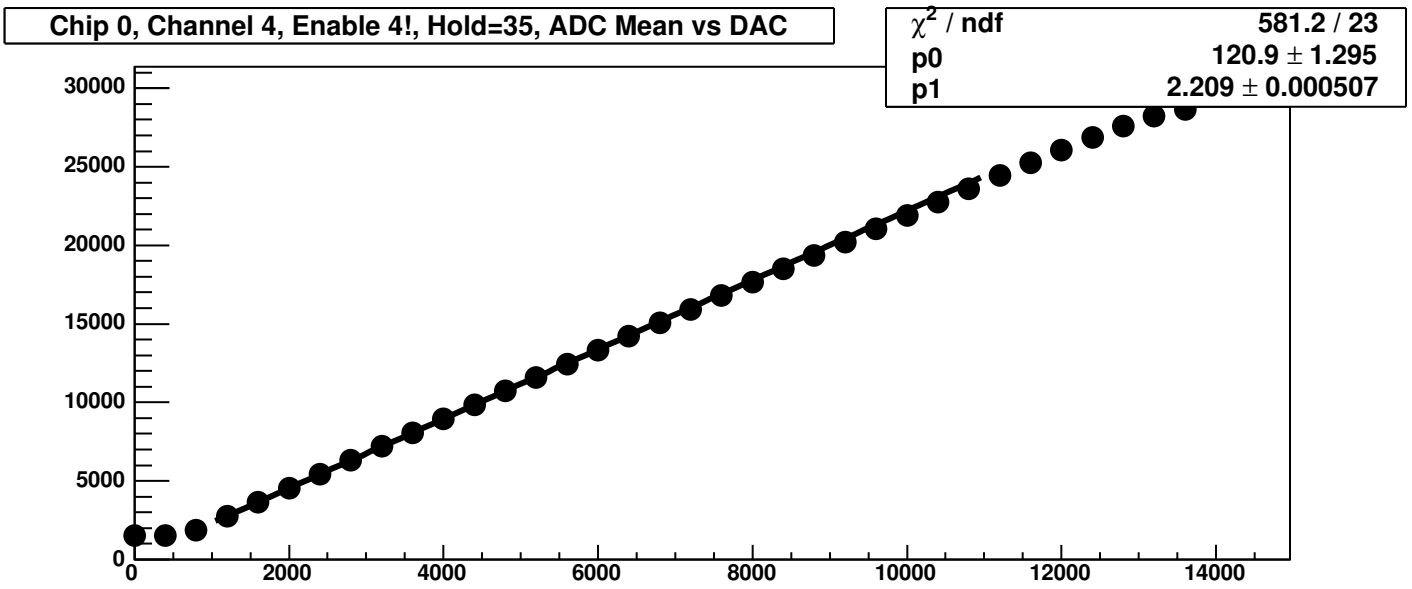


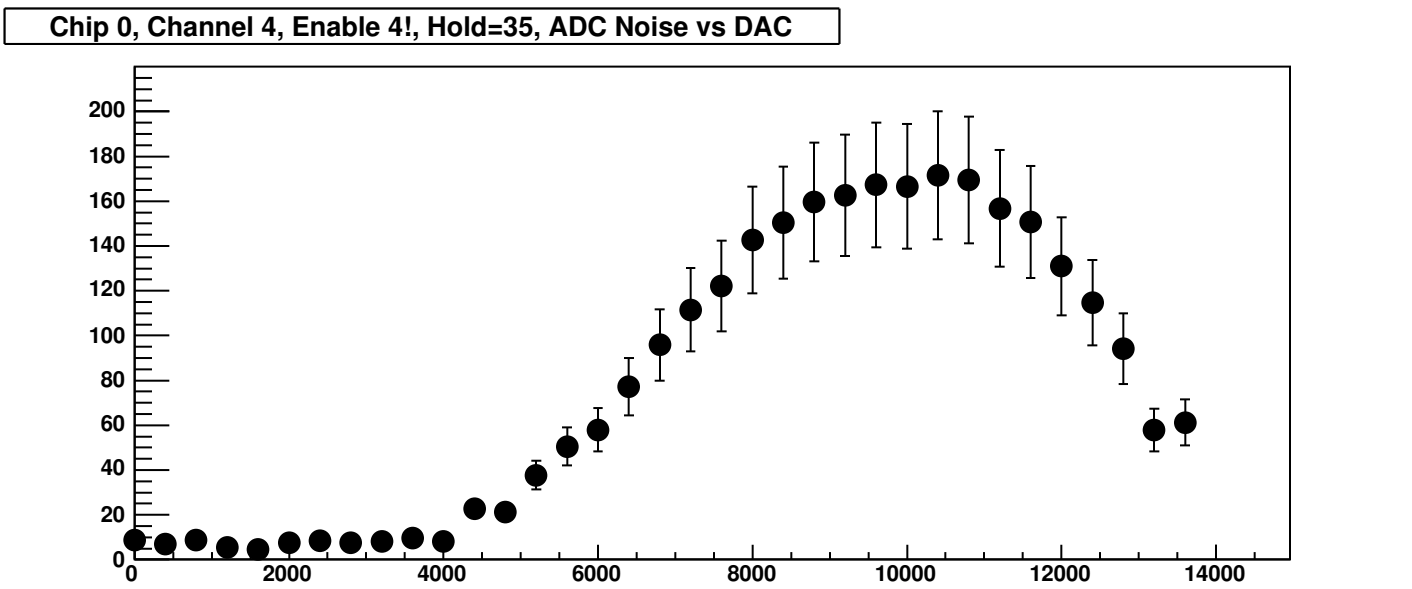
Chip 0, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC

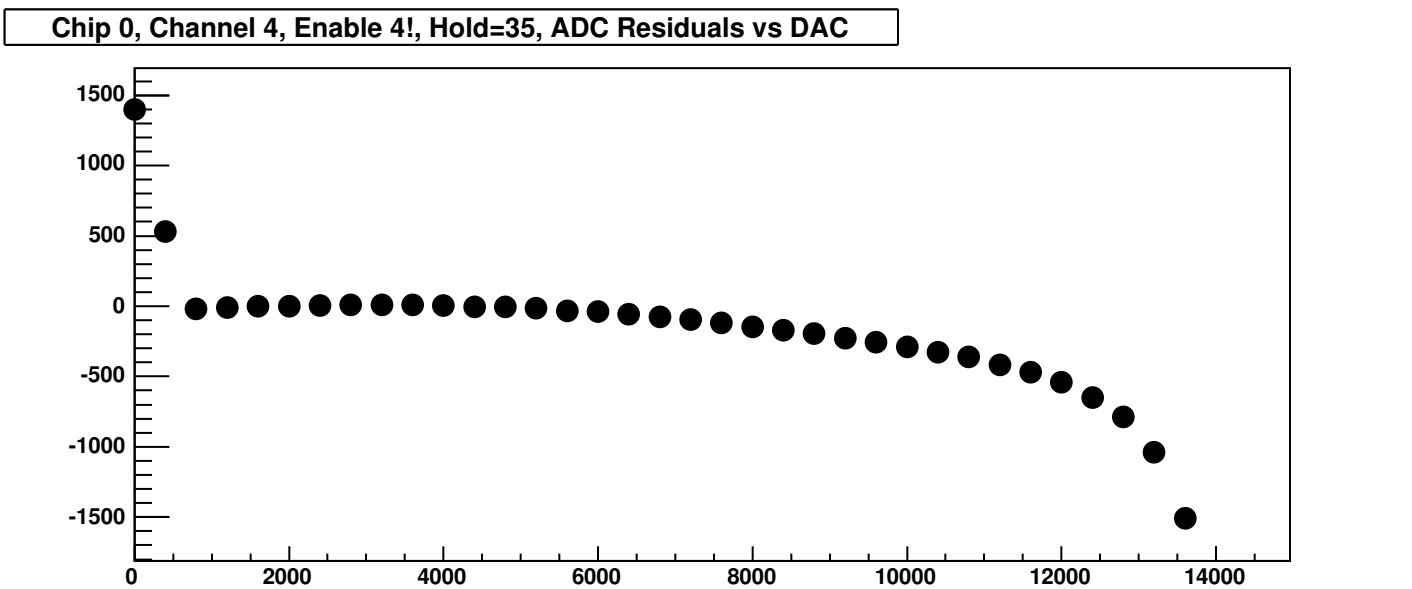


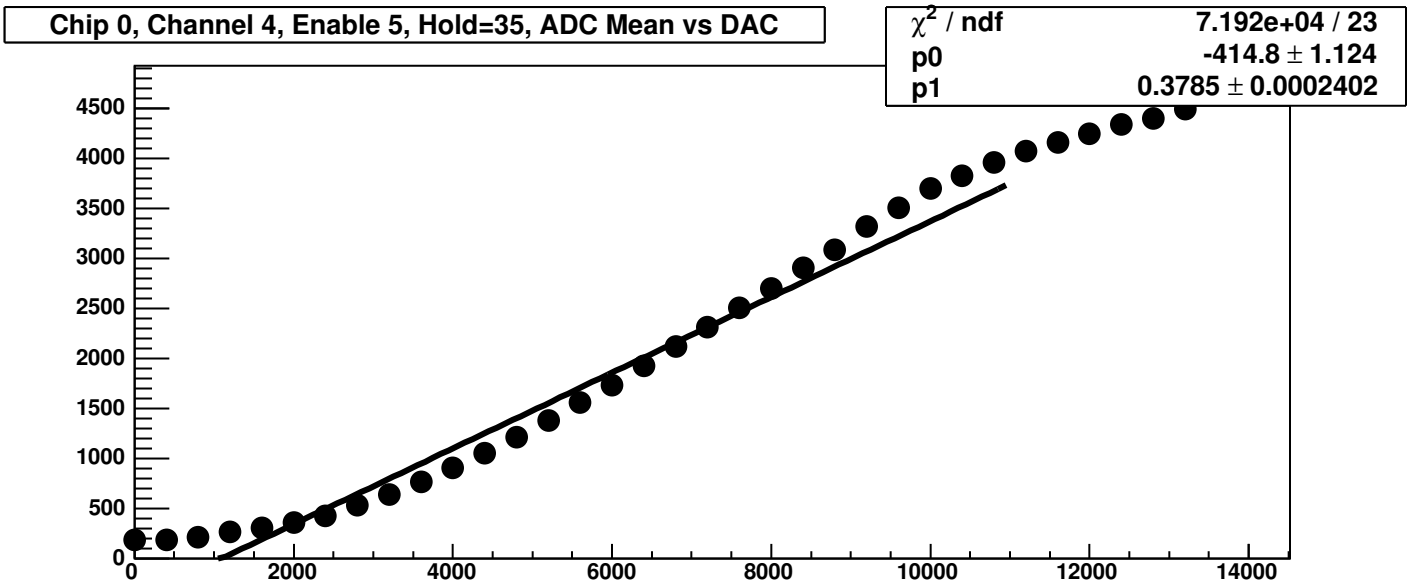
Chip 0, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

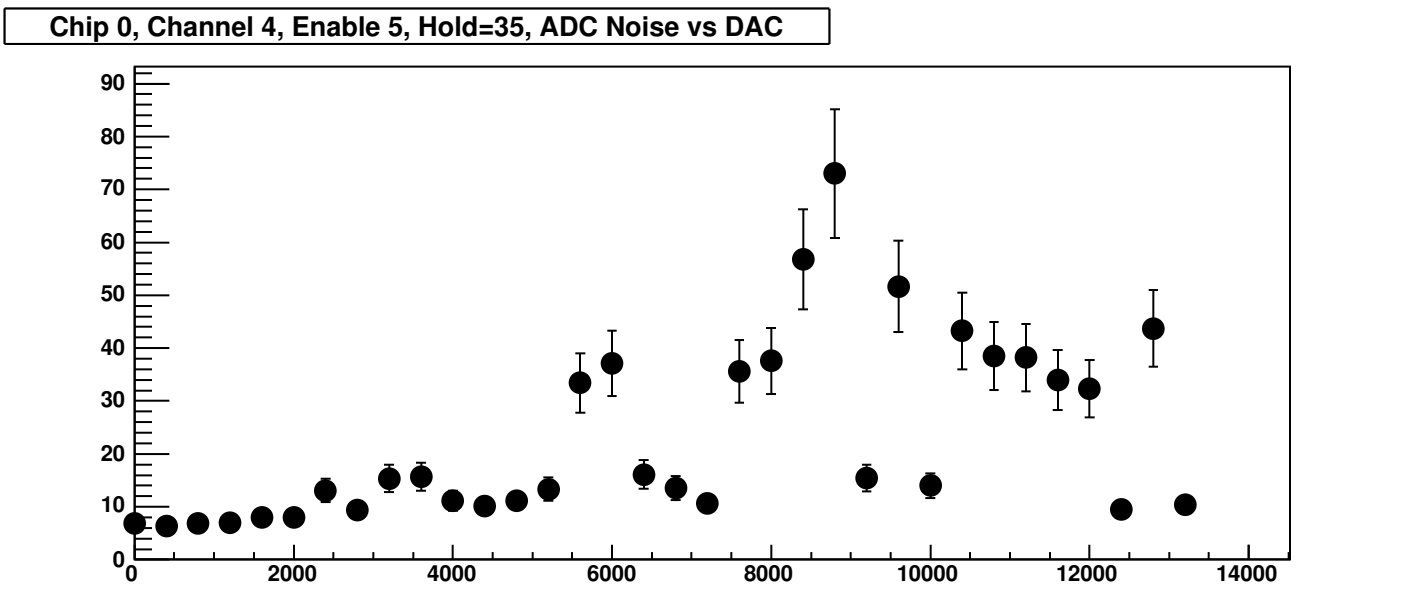


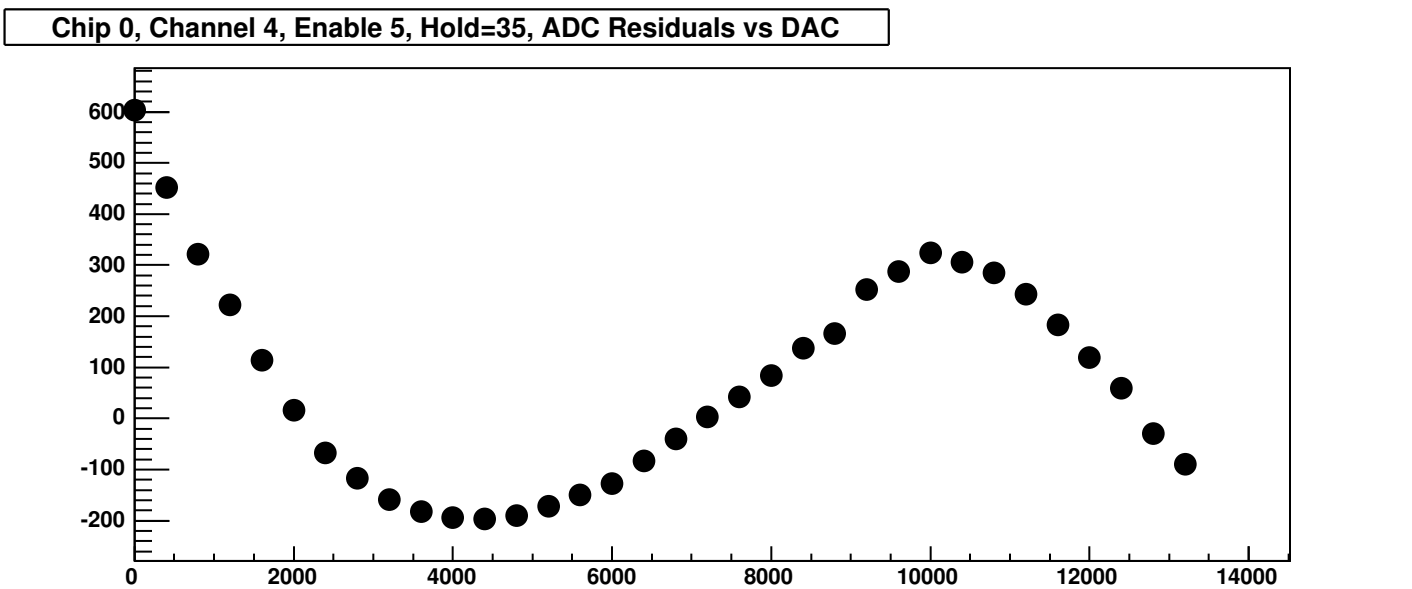

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 581.2 / 23
p0 120.9 ± 1.295
p1 2.209 ± 0.000507
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

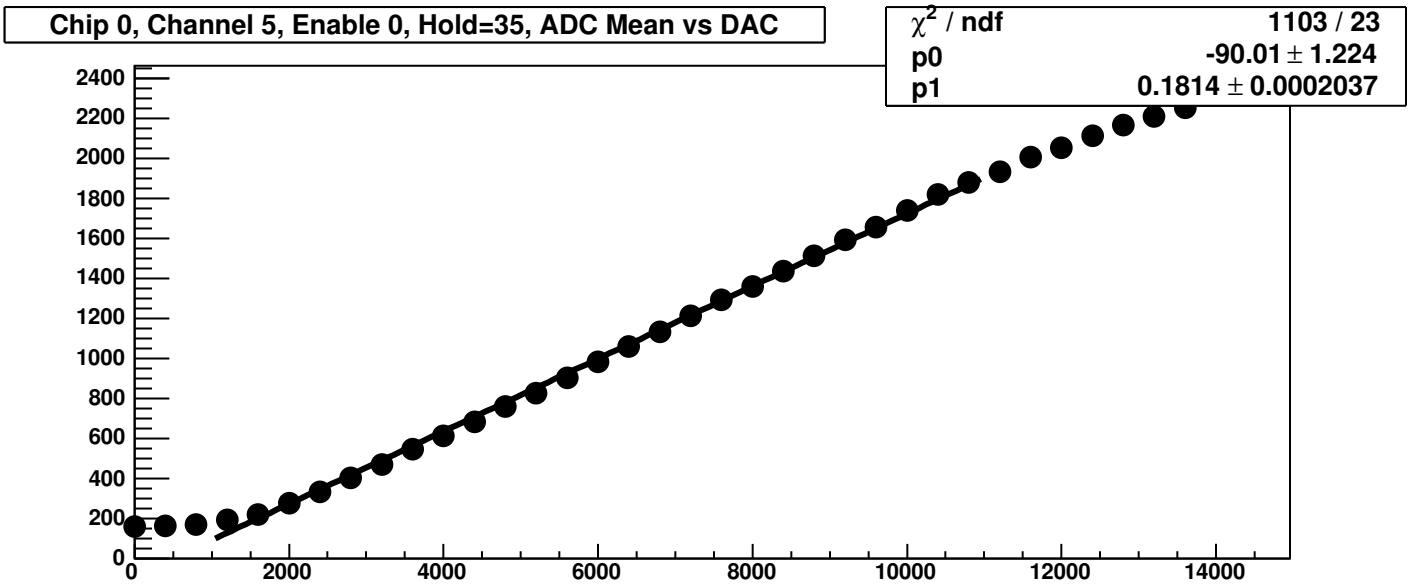

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

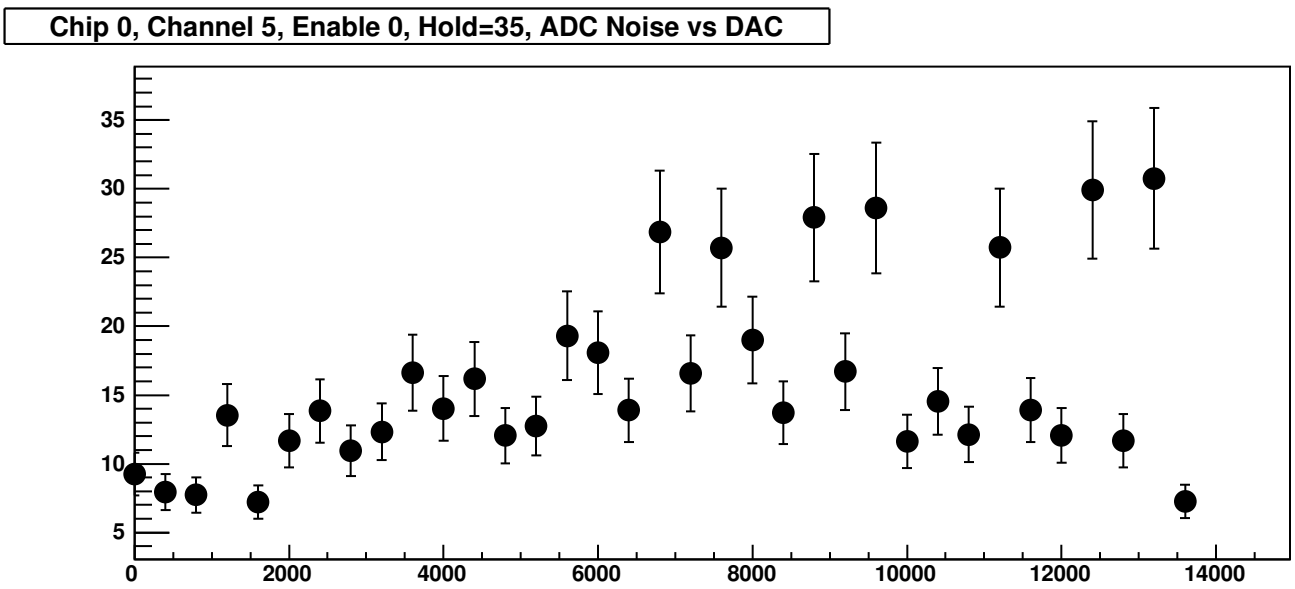

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

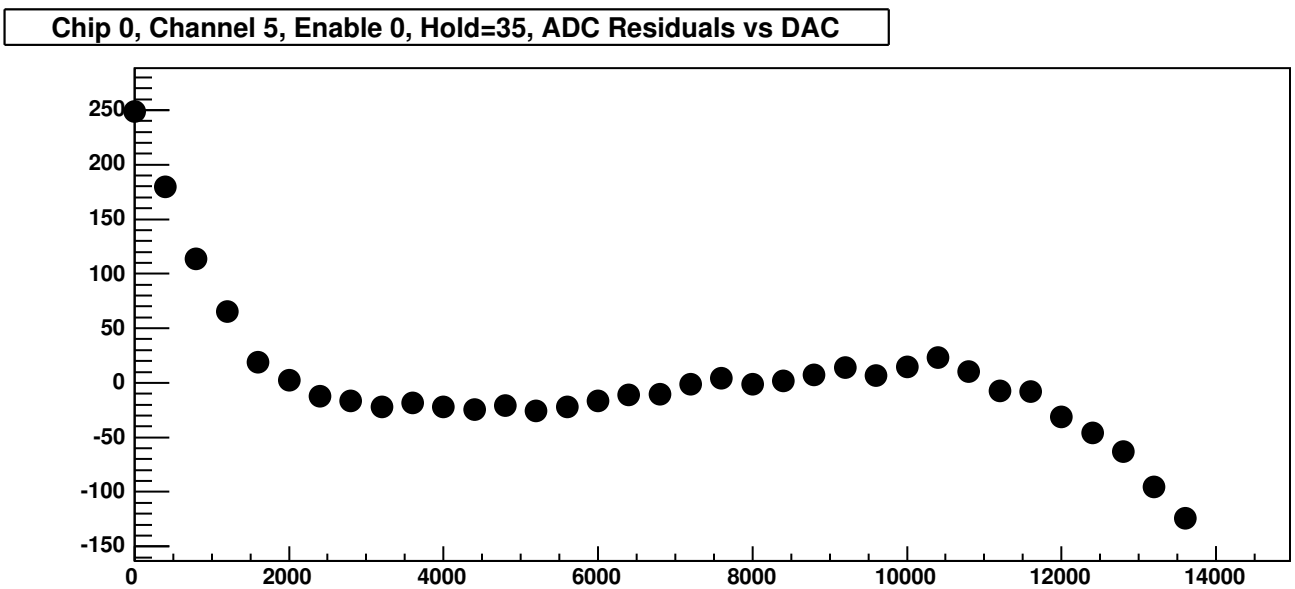

Chip 0, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 7.192e+04 / 23
p0 -414.8 ± 1.124
p1 0.3785 ± 0.0002402
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

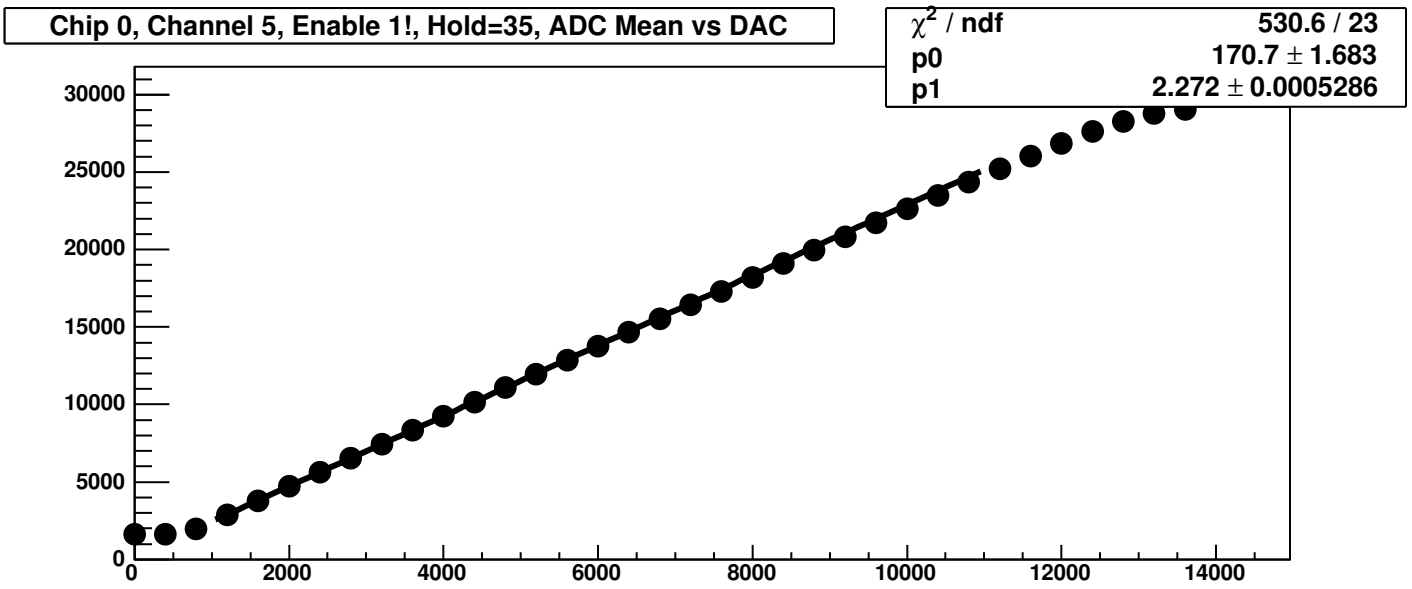

Chip 0, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

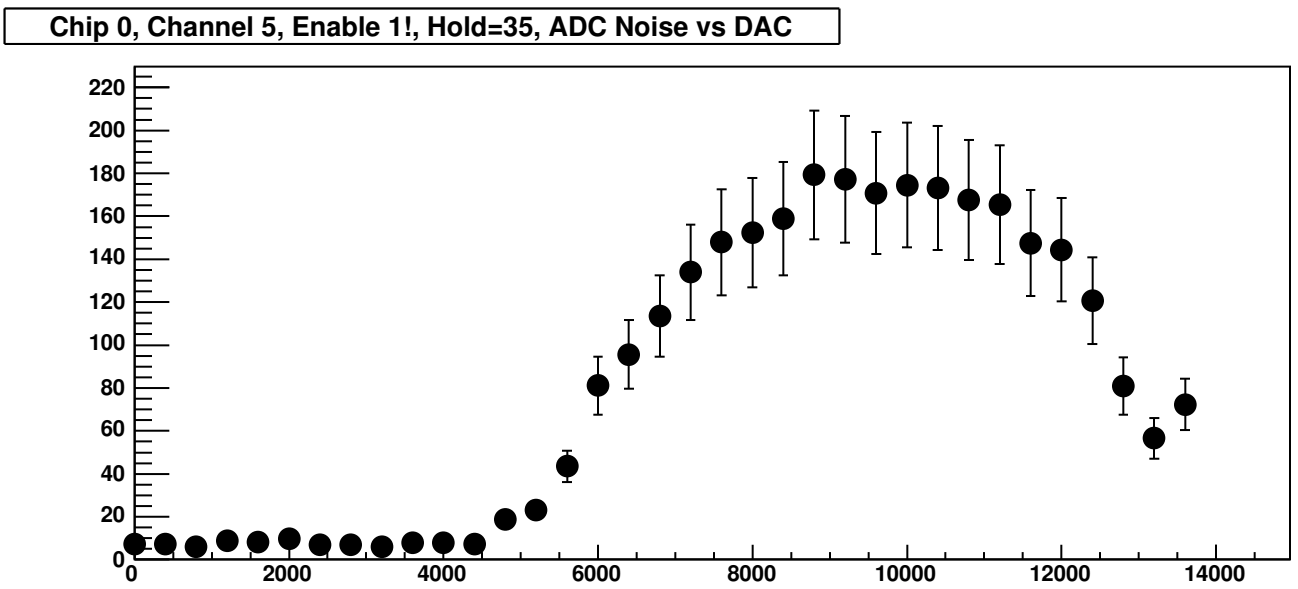

Chip 0, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

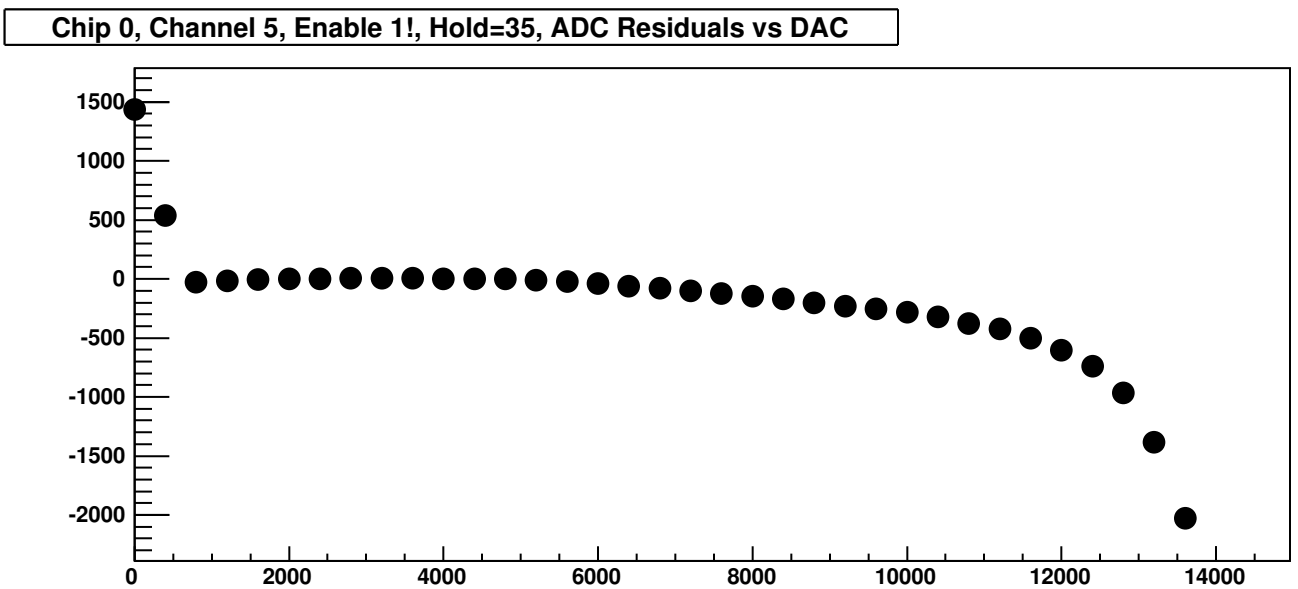

Chip 0, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1103 / 23
p0 -90.01 ± 1.224
p1 0.1814 ± 0.0002037
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

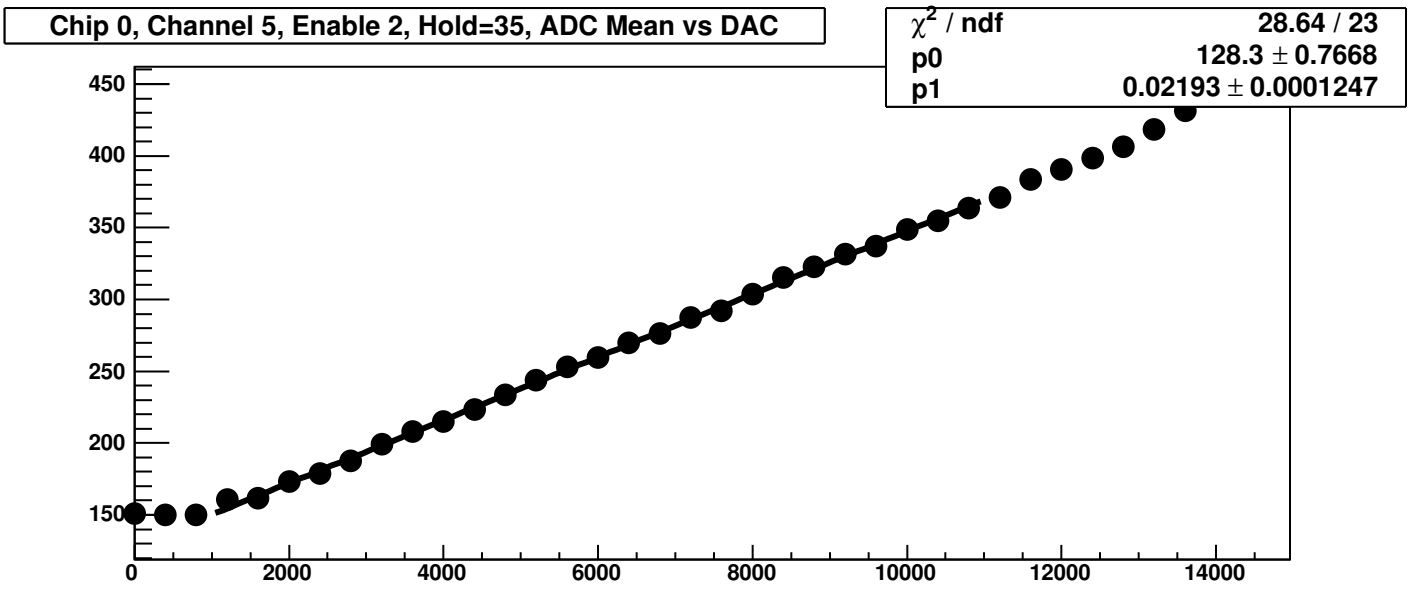

Chip 0, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

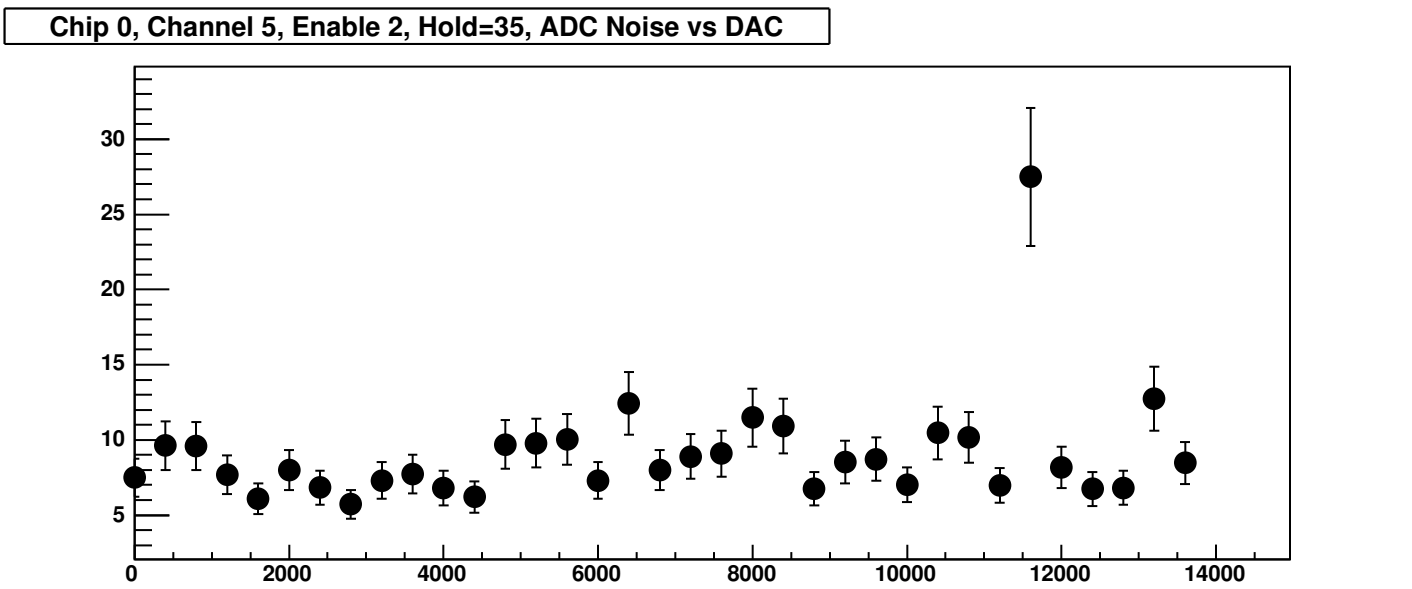

Chip 0, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

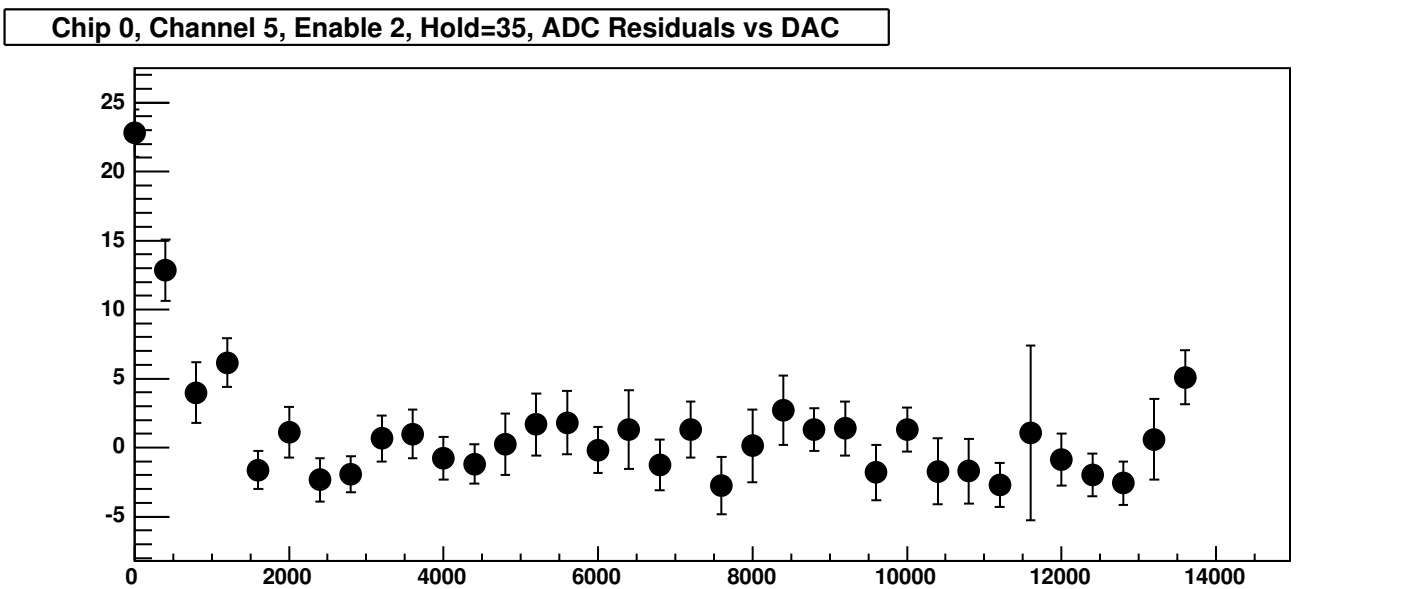

Chip 0, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 530.6 / 23
p0 170.7 ± 1.683
p1 2.272 ± 0.0005286
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

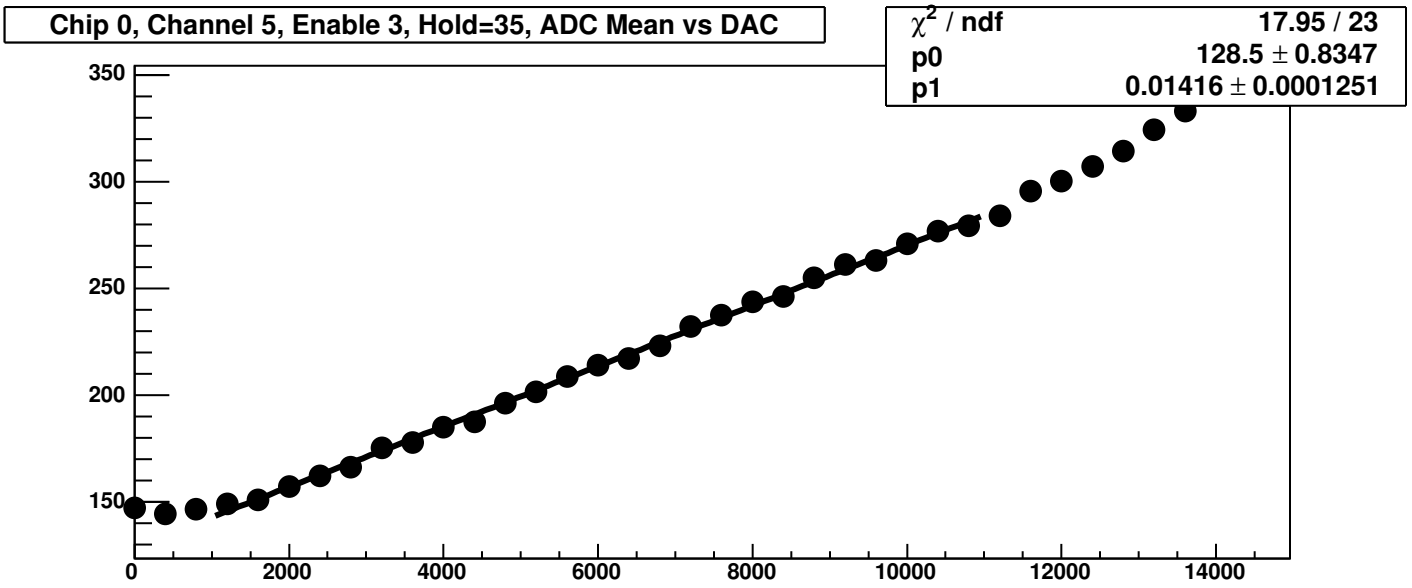

Chip 0, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

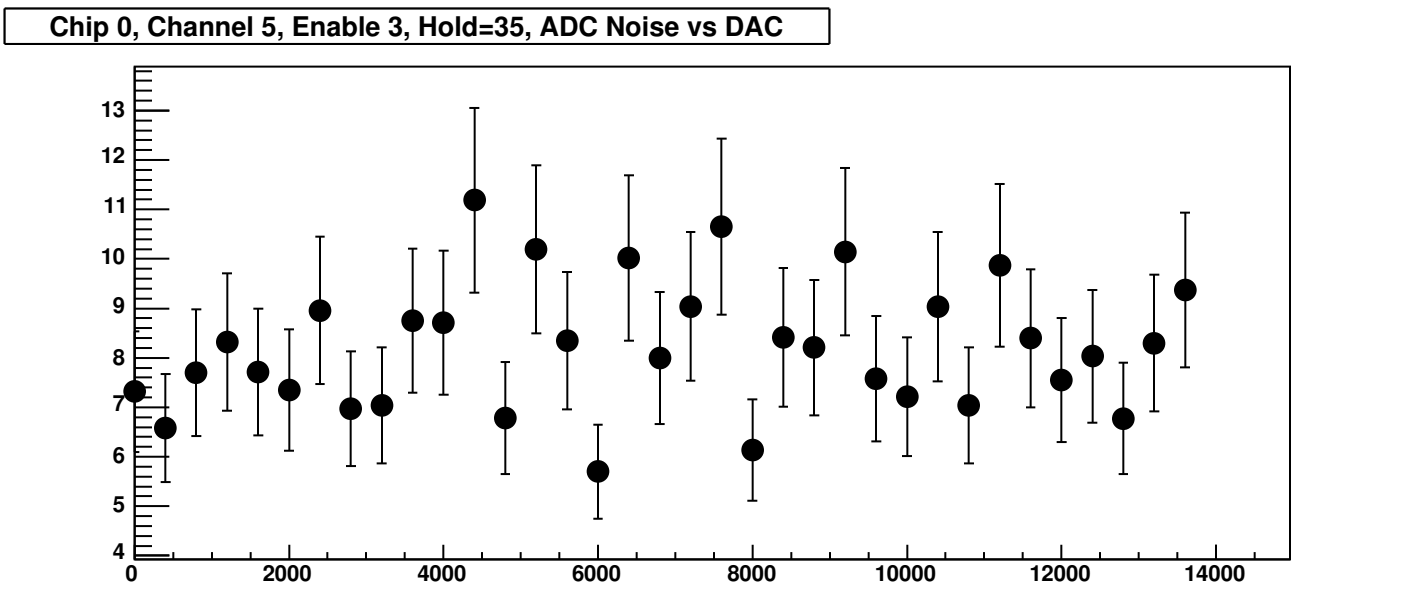

Chip 0, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

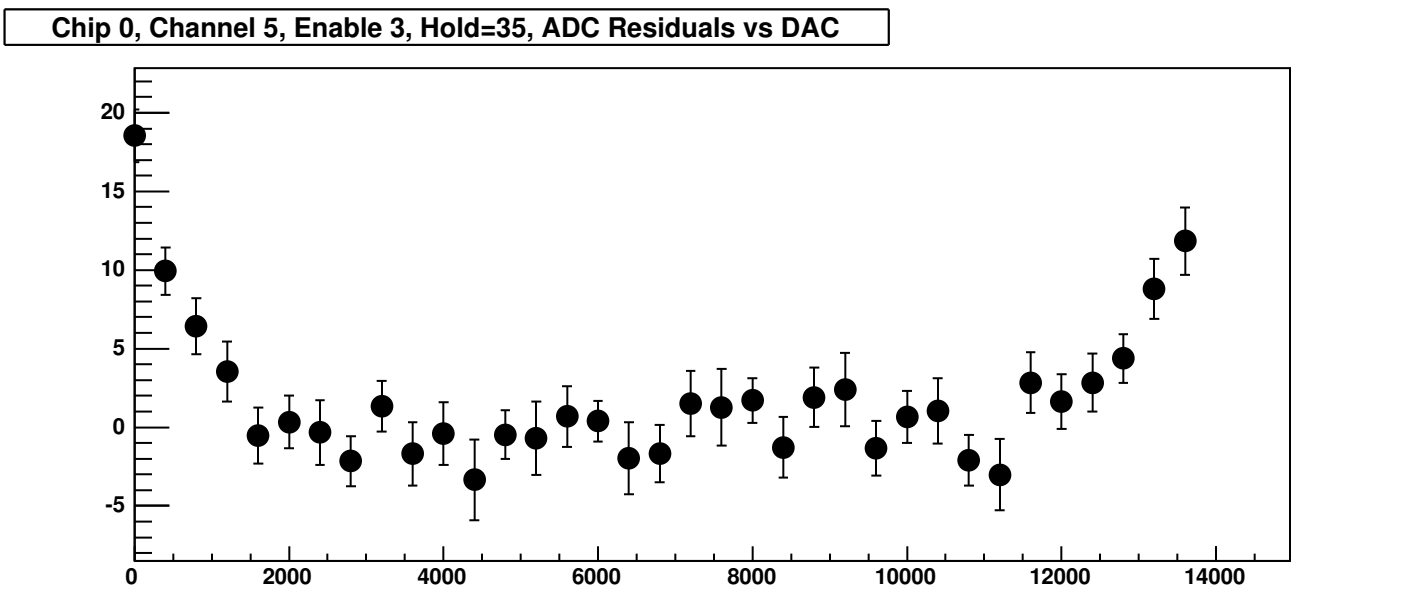

Chip 0, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 28.64 / 23
p0 128.3 ± 0.7668
p1 0.02193 ± 0.0001247
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

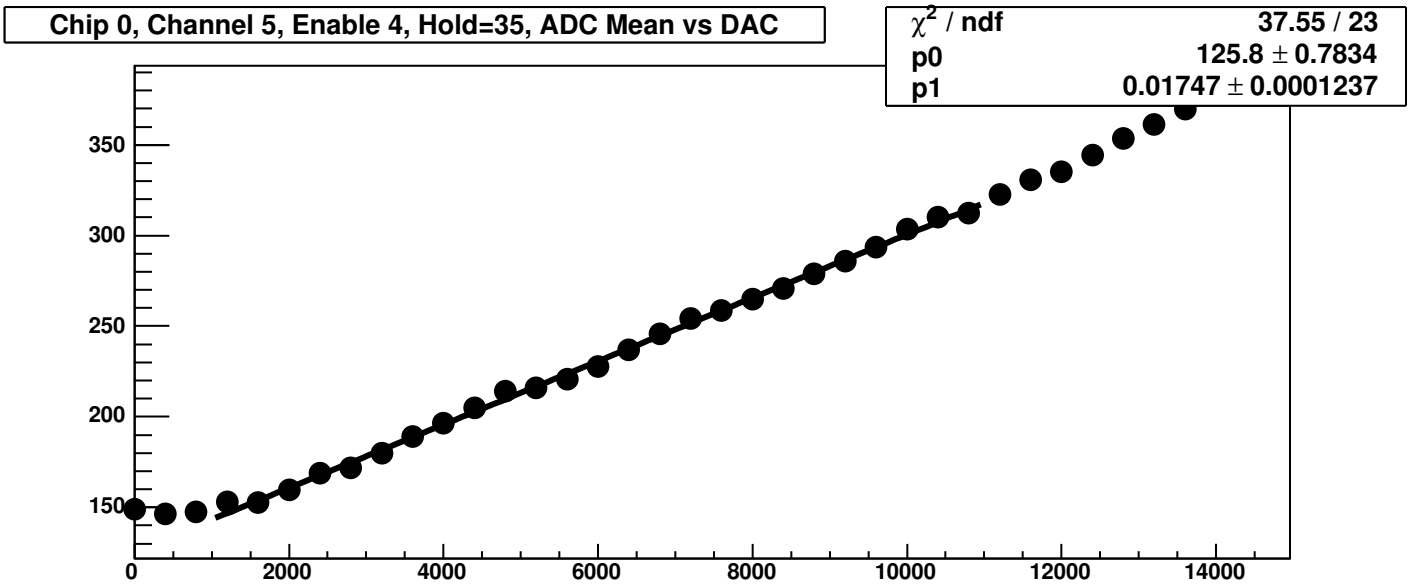

Chip 0, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

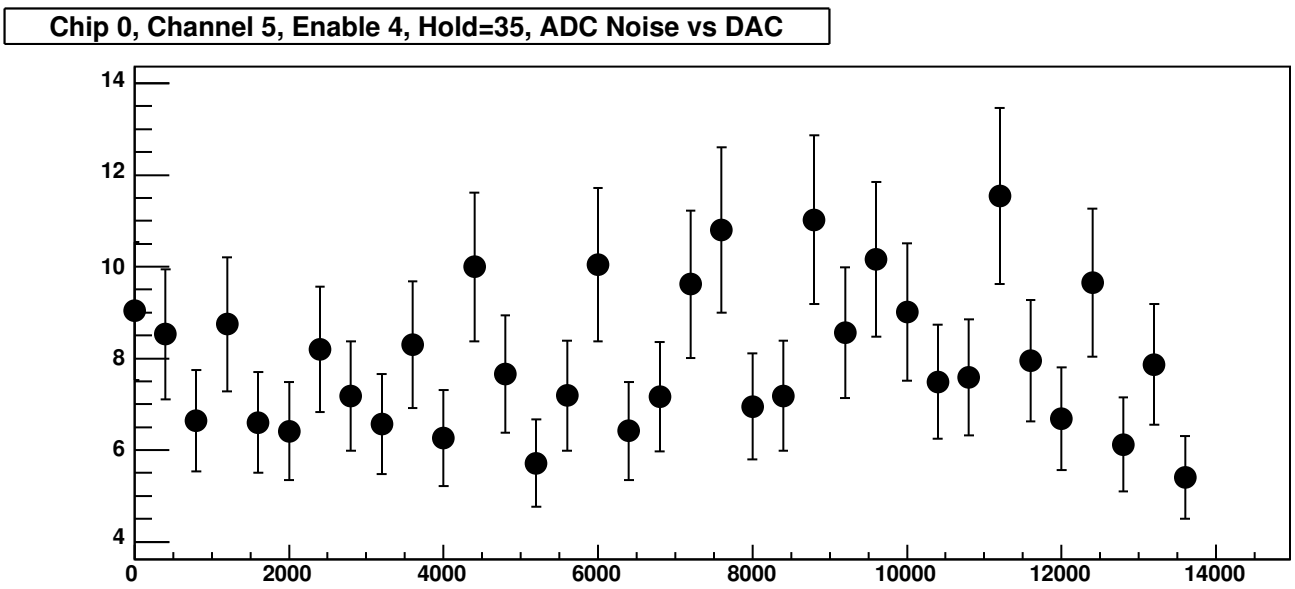

Chip 0, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

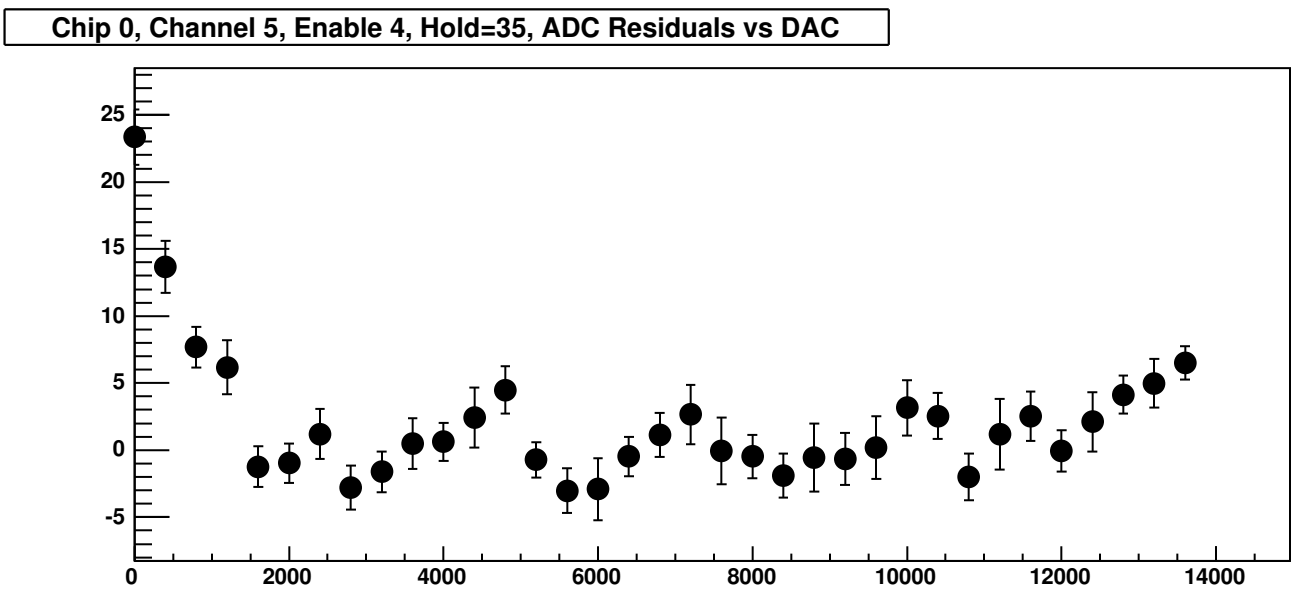

Chip 0, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 17.95 / 23
p0 128.5 ± 0.8347
p1 0.01416 ± 0.0001251
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

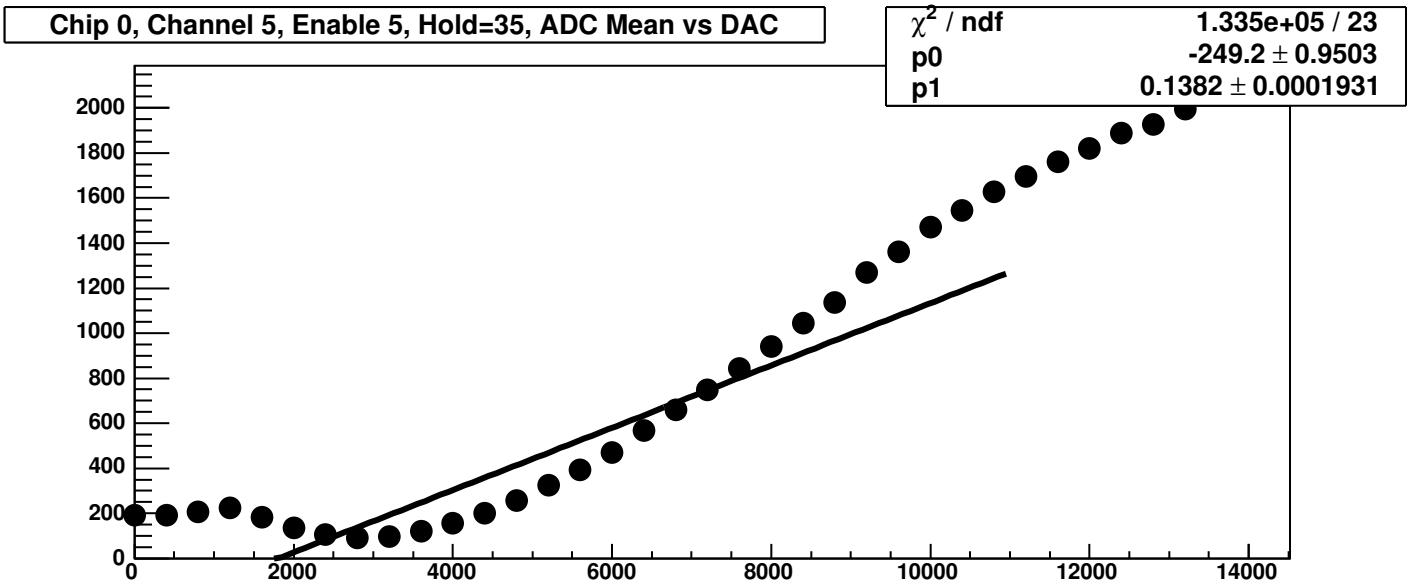

Chip 0, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

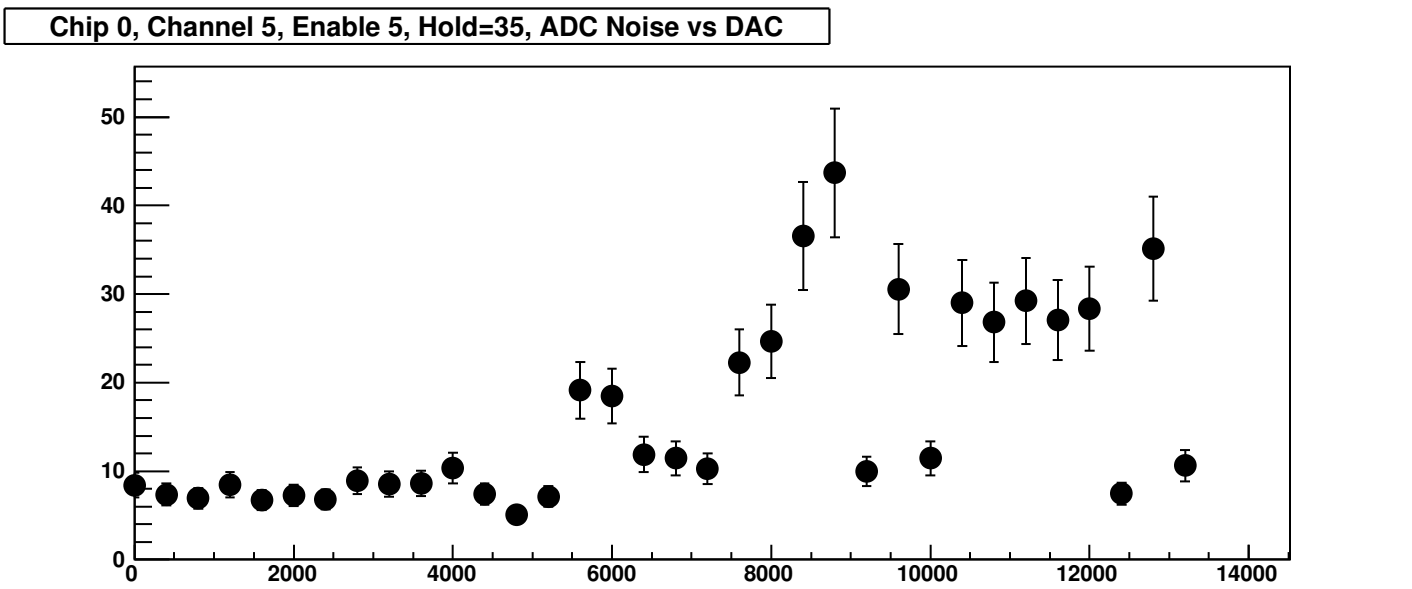

Chip 0, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

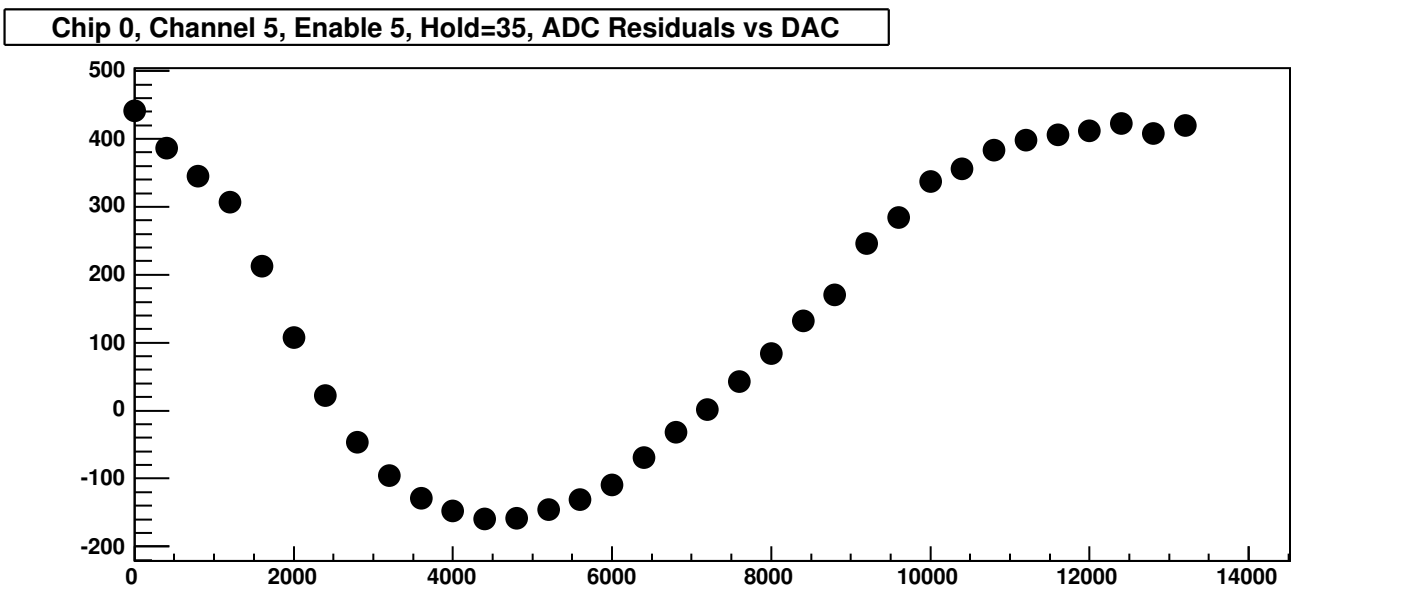

Chip 0, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 37.55 / 23
p0 125.8 ± 0.7834
p1 0.01747 ± 0.0001237
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

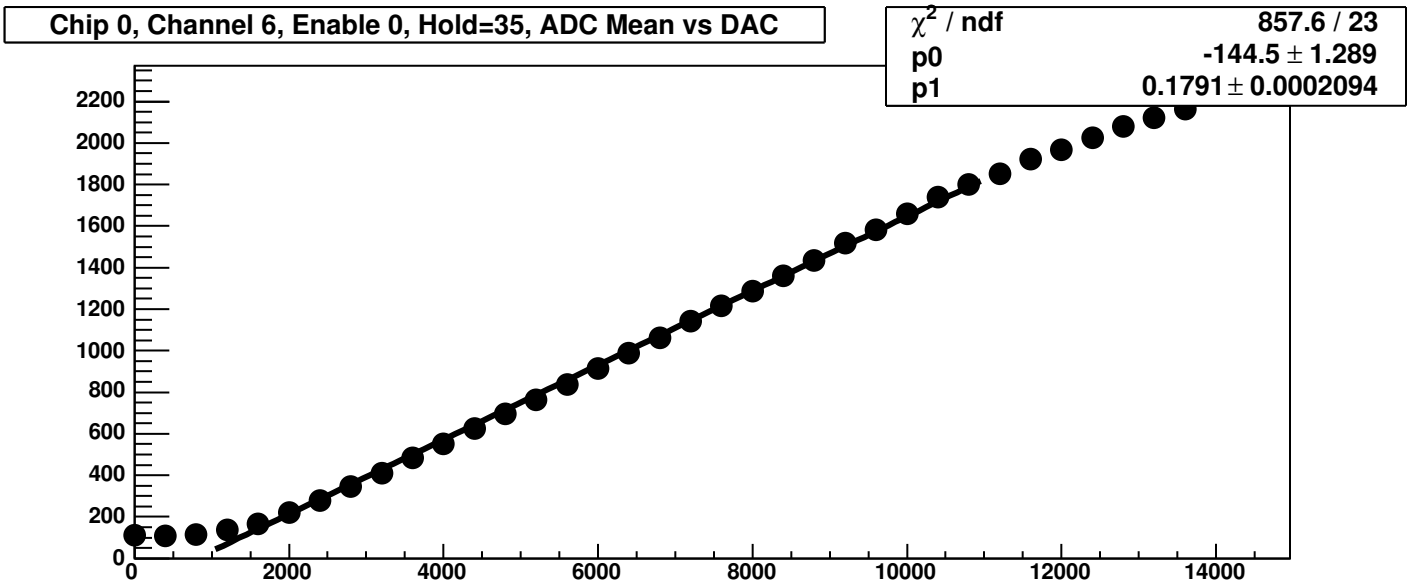

Chip 0, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

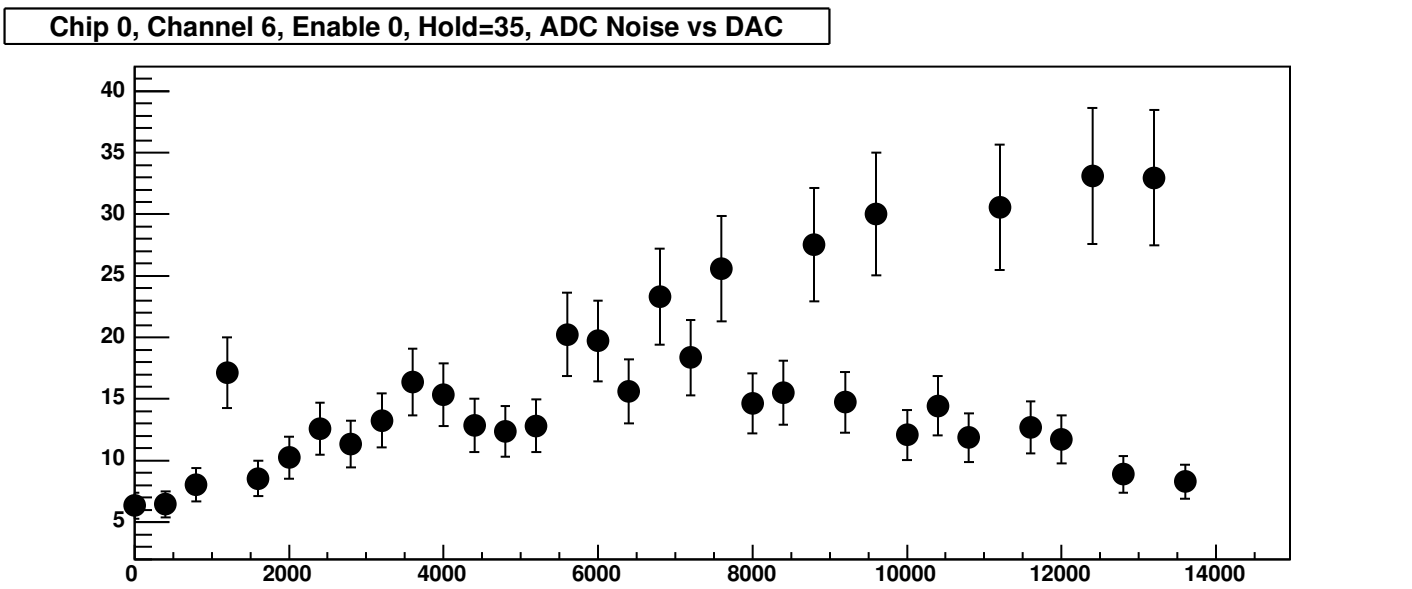

Chip 0, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

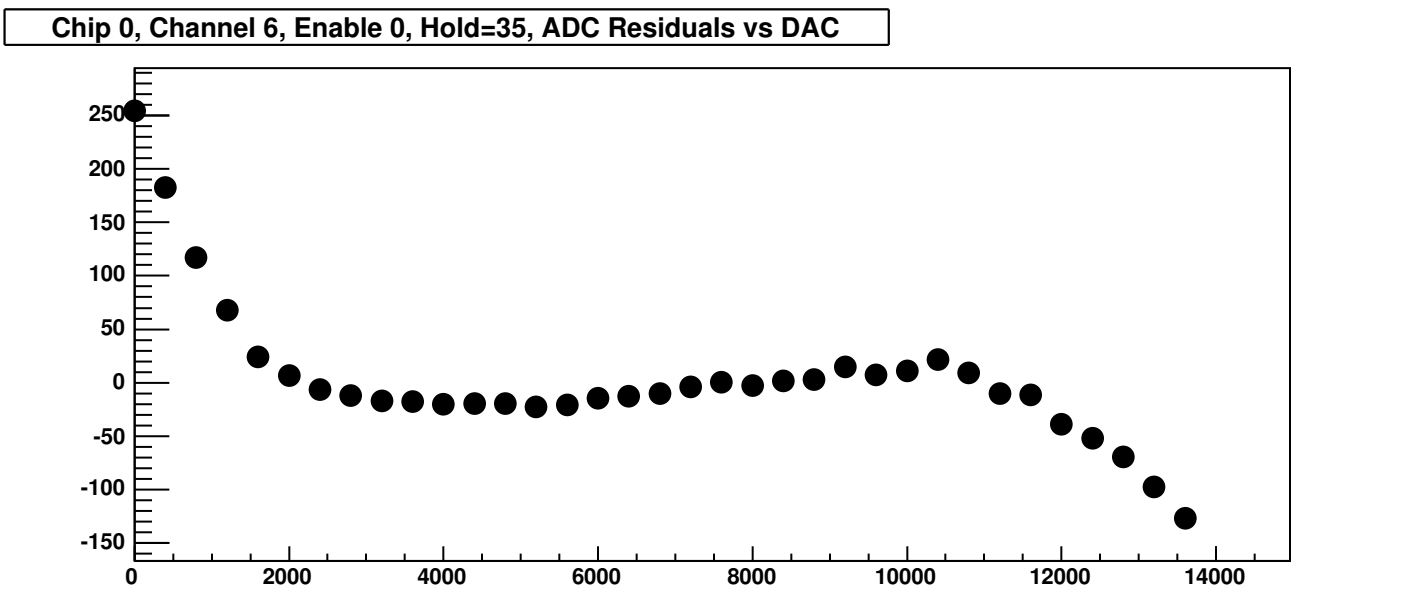

Chip 0, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.335e+05 / 23
p0 -249.2 ± 0.9503
p1 0.1382 ± 0.0001931
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

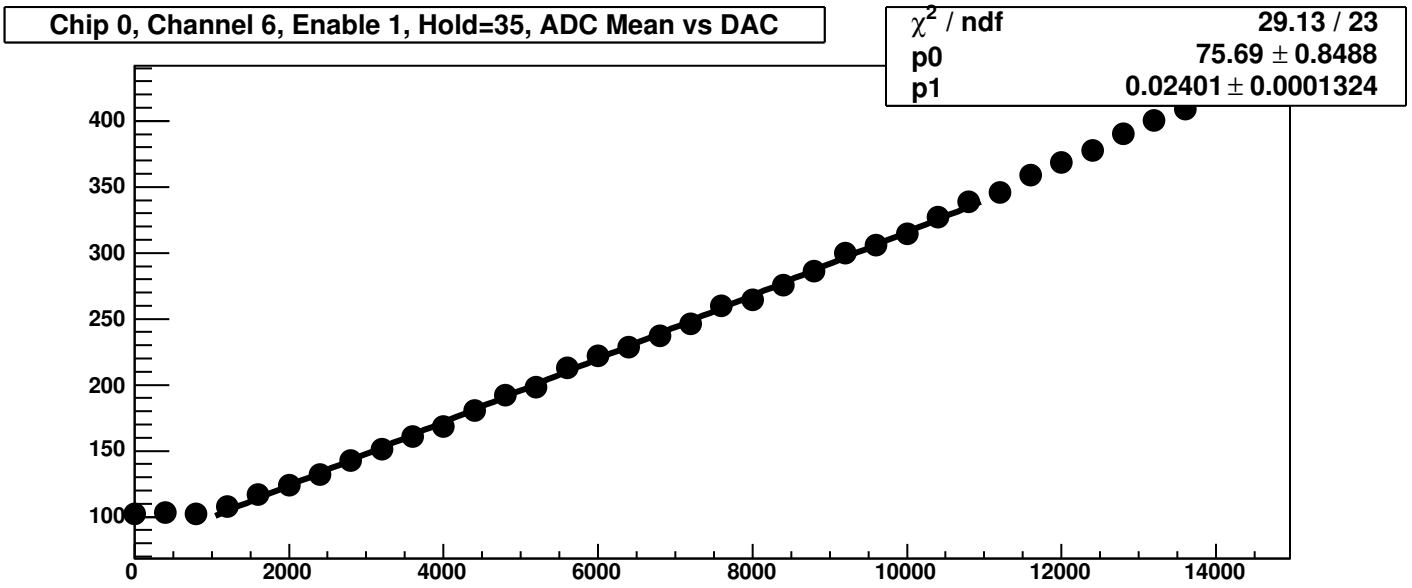

Chip 0, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

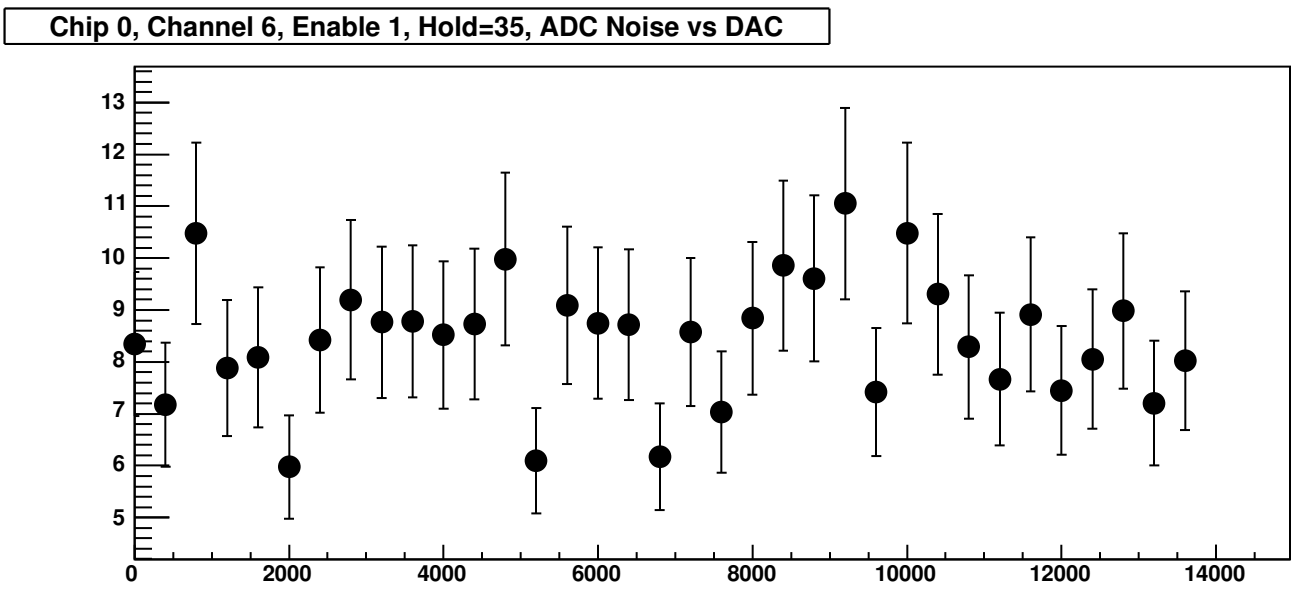

Chip 0, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

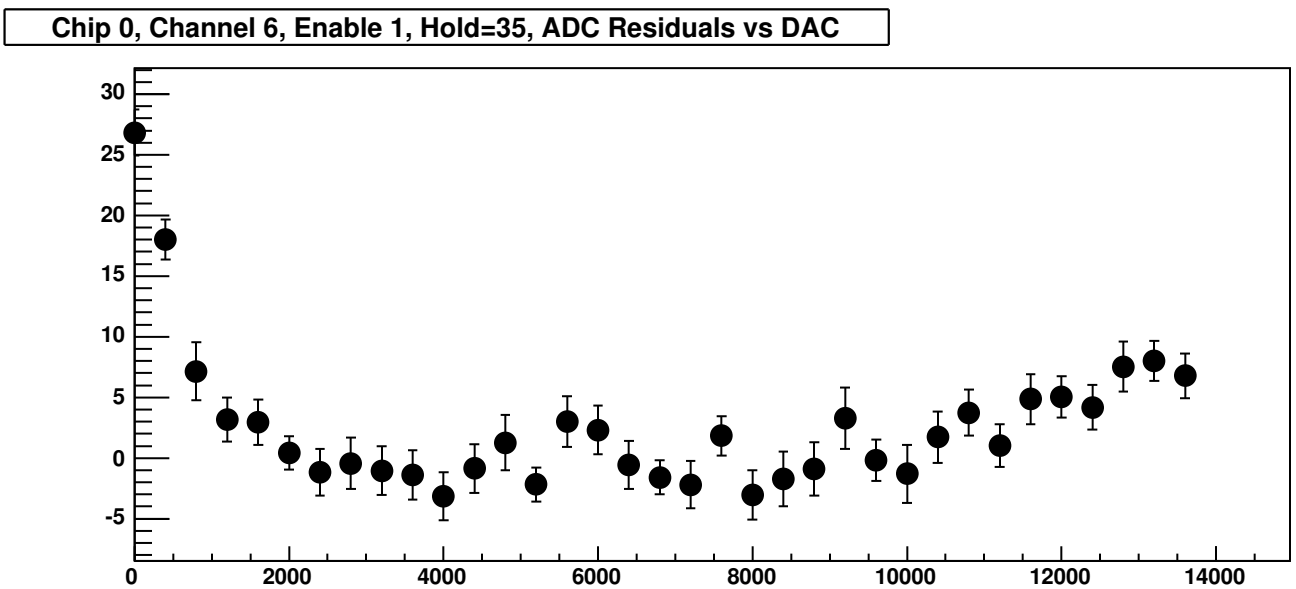

Chip 0, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 857.6 / 23
p0 -144.5 ± 1.289
p1 0.1791 ± 0.0002094
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

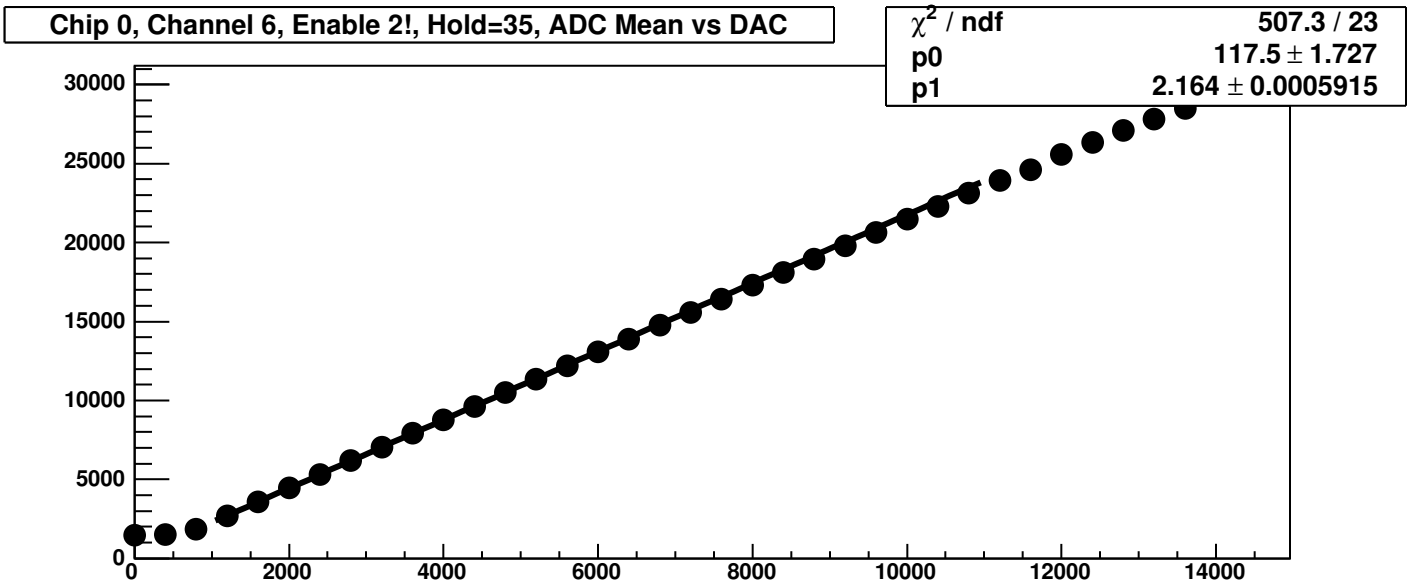

Chip 0, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

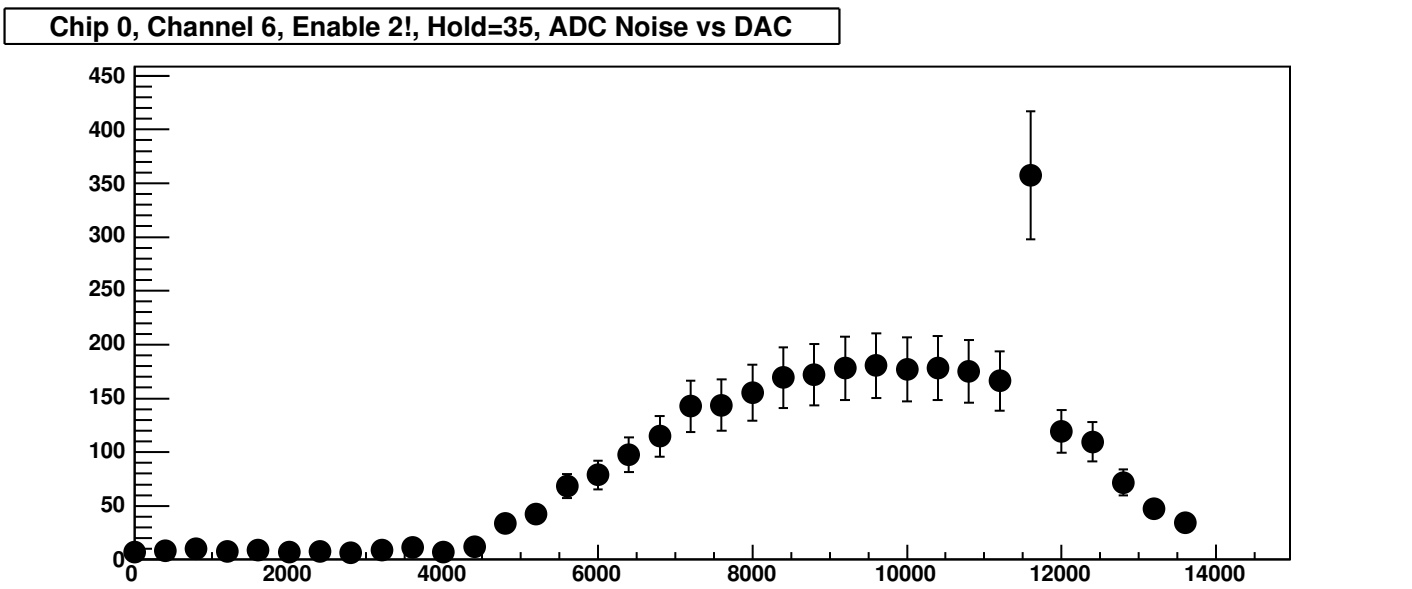

Chip 0, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

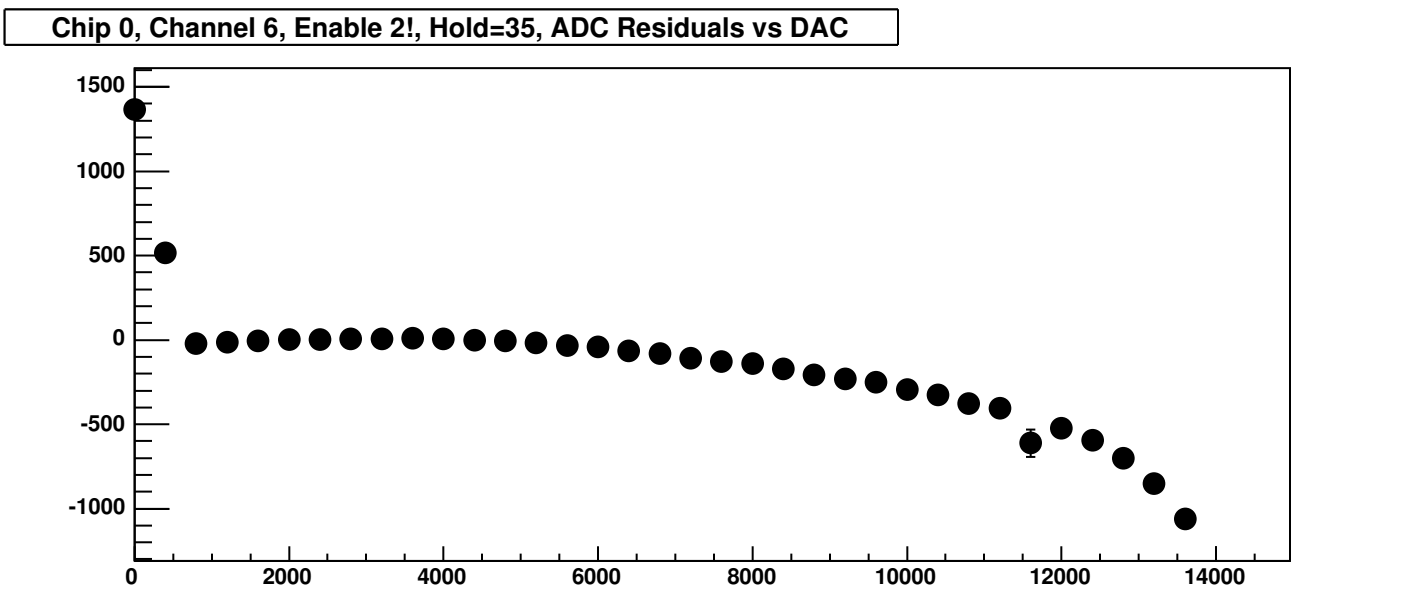

Chip 0, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 29.13 / 23
p0 75.69 ± 0.8488
p1 0.02401 ± 0.0001324
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

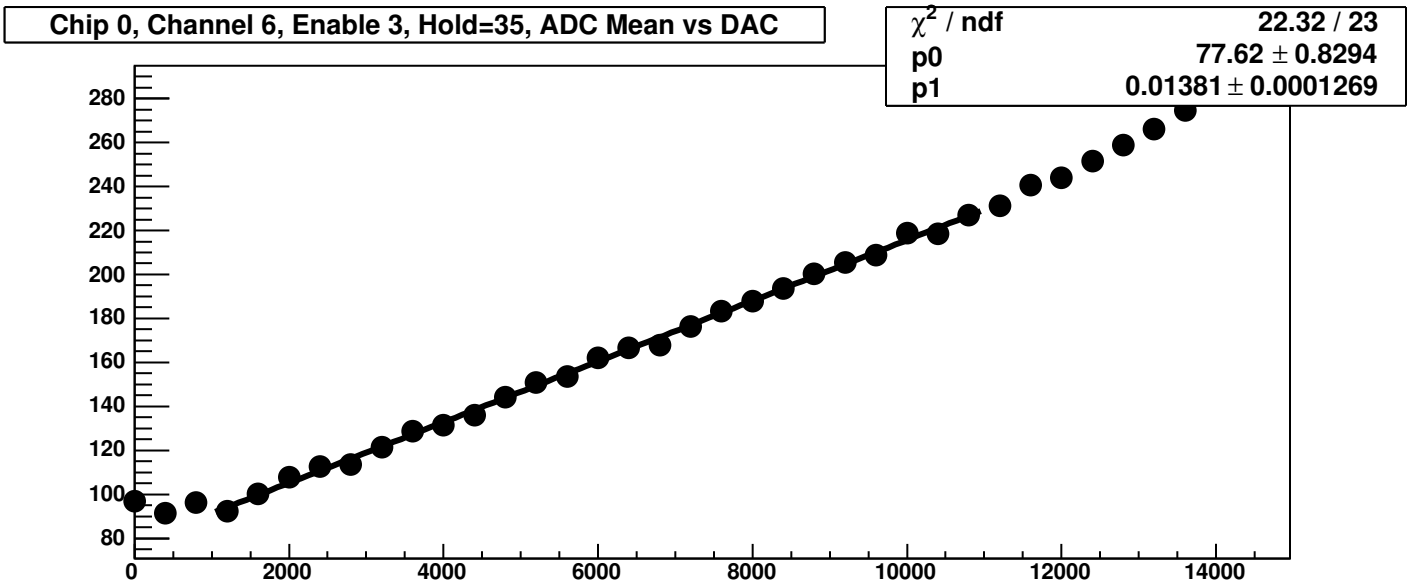

Chip 0, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

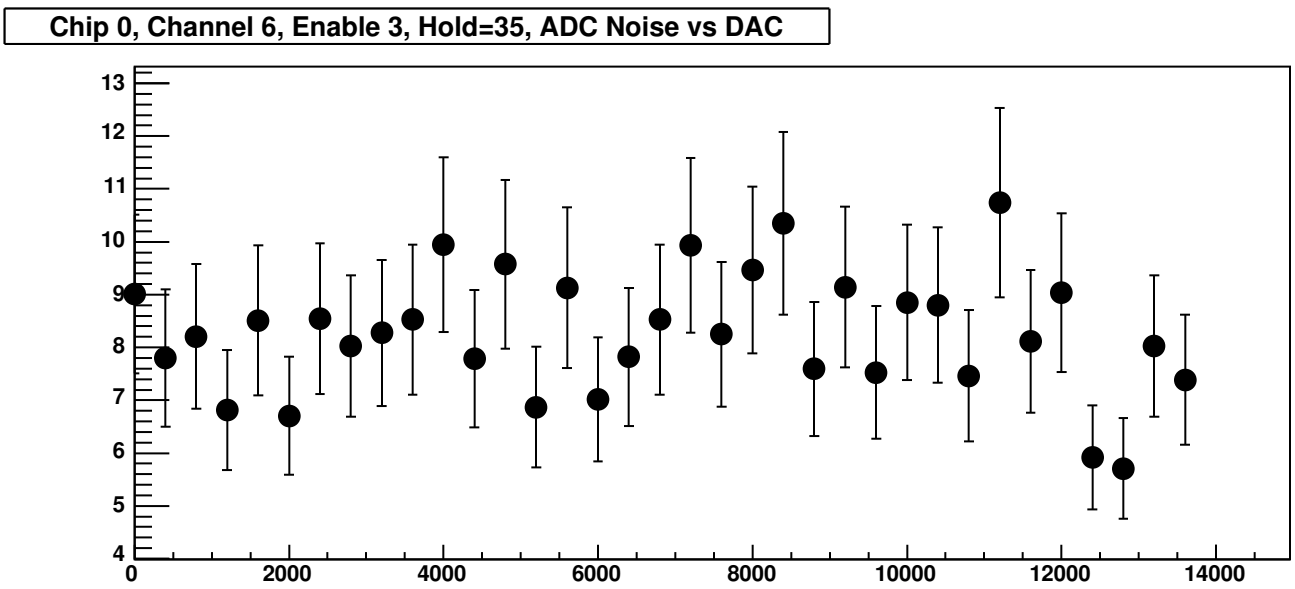

Chip 0, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

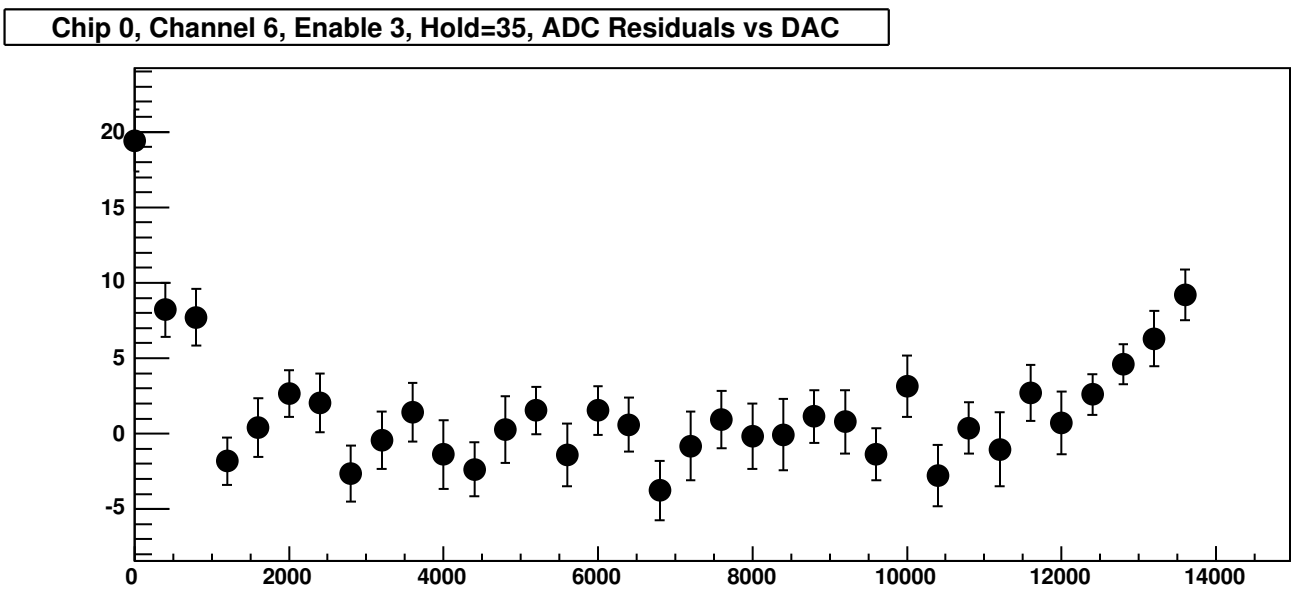

Chip 0, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 507.3 / 23
p0 117.5 ± 1.727
p1 2.164 ± 0.0005915
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

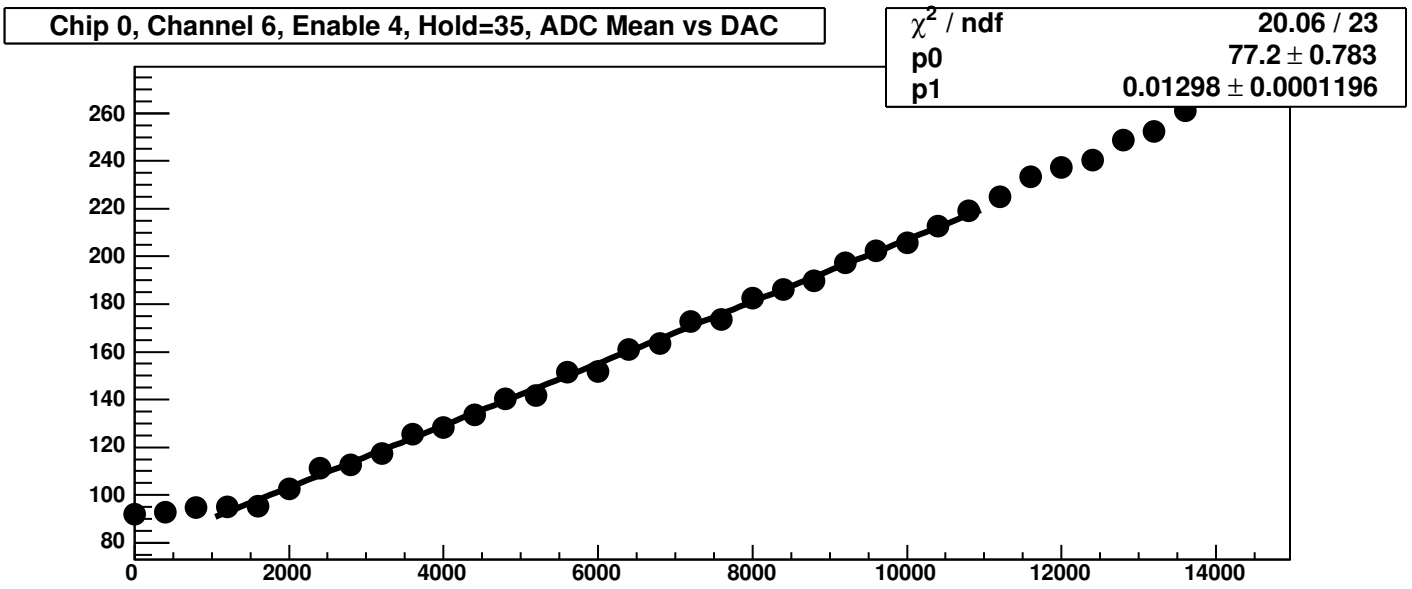

Chip 0, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

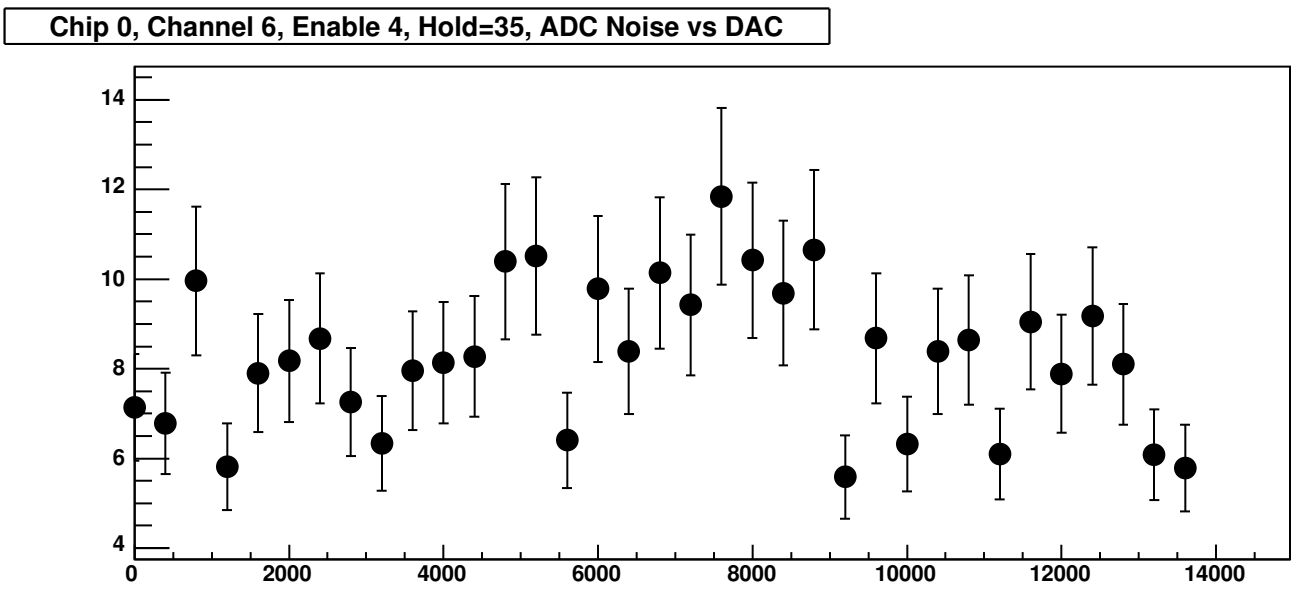

Chip 0, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

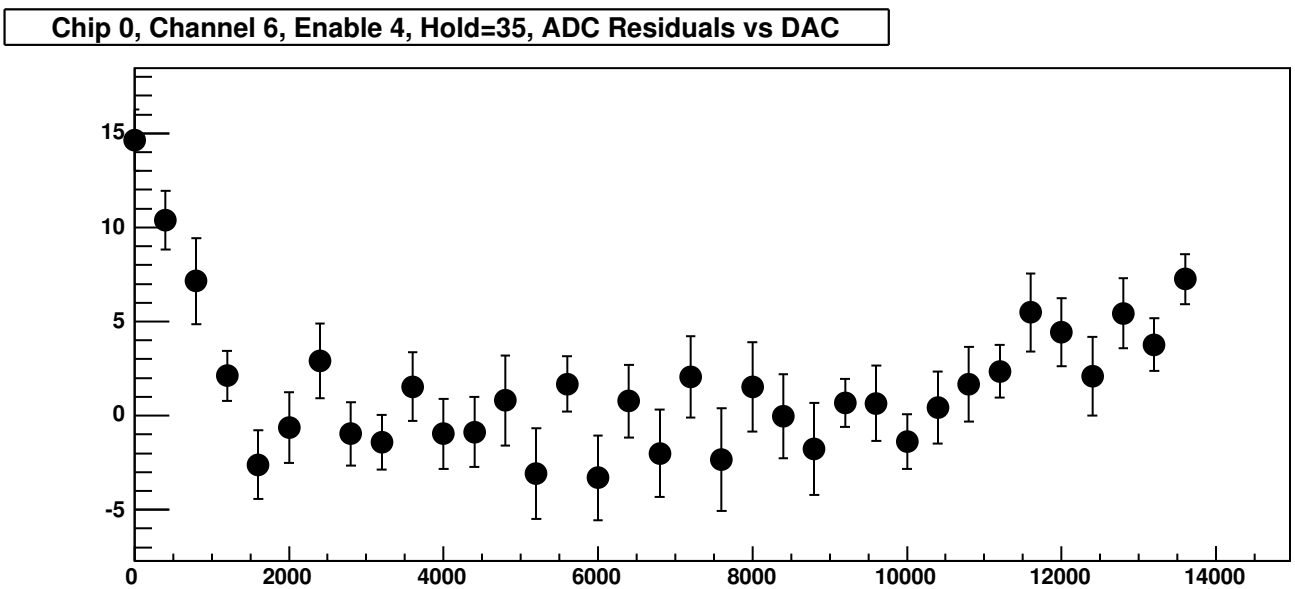

Chip 0, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 22.32 / 23
p0 77.62 ± 0.8294
p1 0.01381 ± 0.0001269
280
260
240
220
200
180
160
140
120
100
80
0
2000
4000
6000
8000
10000
12000
14000

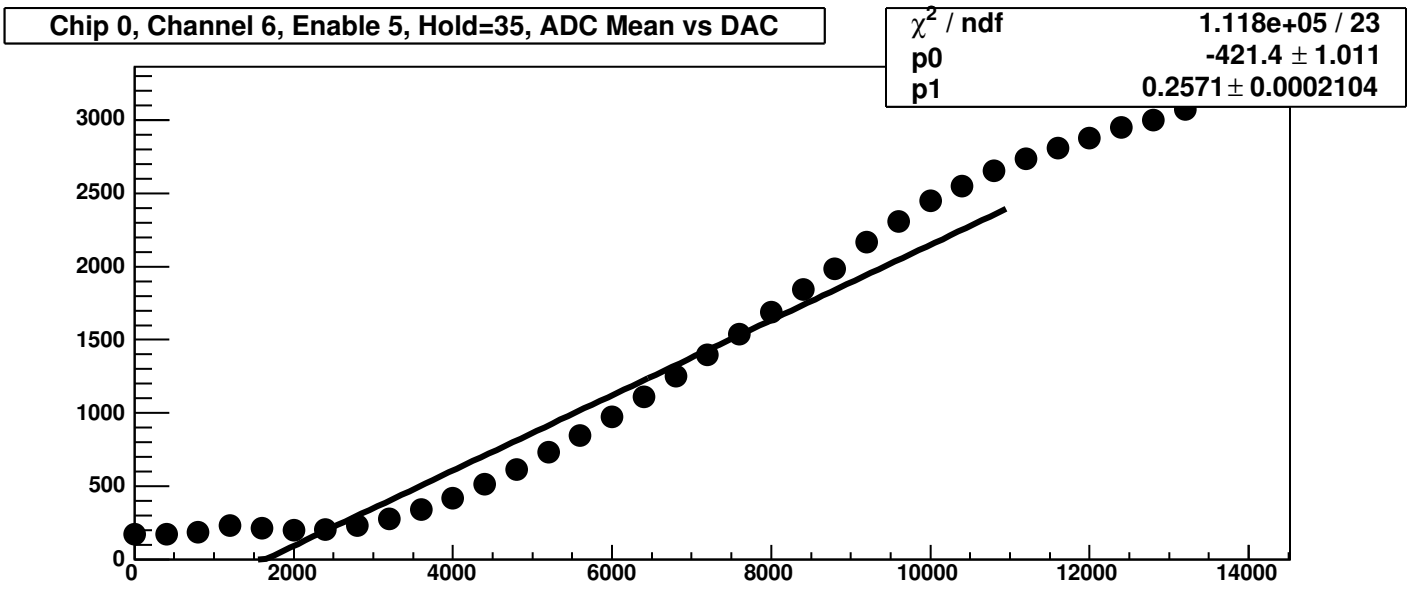

Chip 0, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

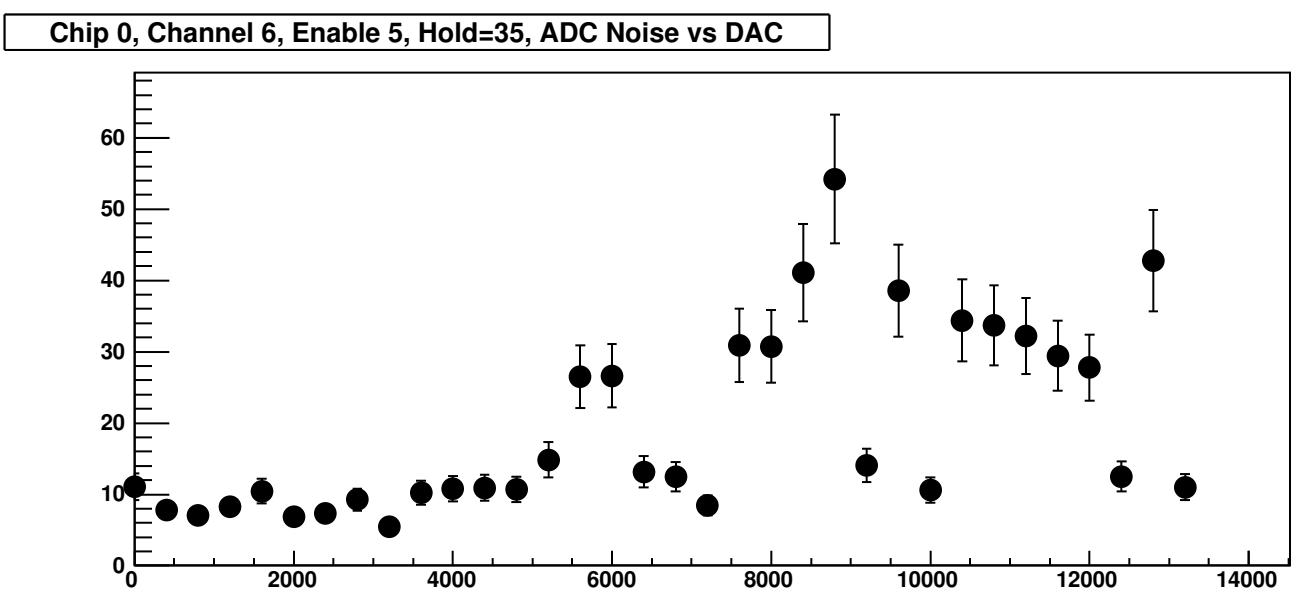

Chip 0, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

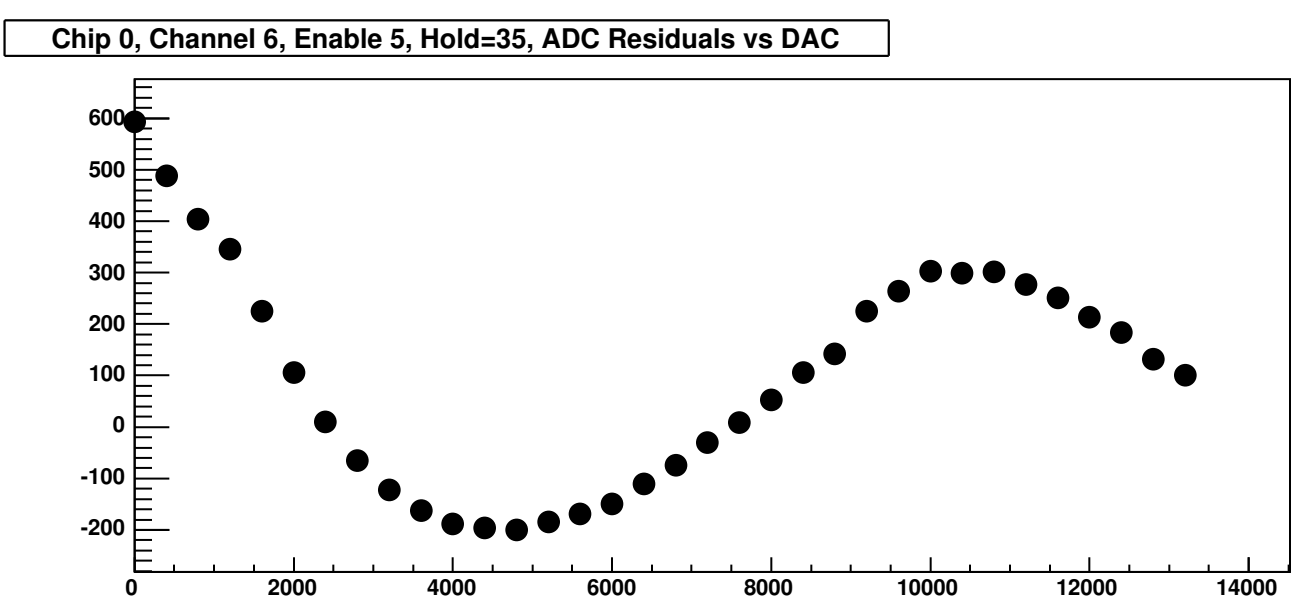

Chip 0, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 20.06 / 23
p0 77.2 ± 0.783
p1 0.01298 ± 0.0001196
260
240
220
200
180
160
140
120
100
80
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

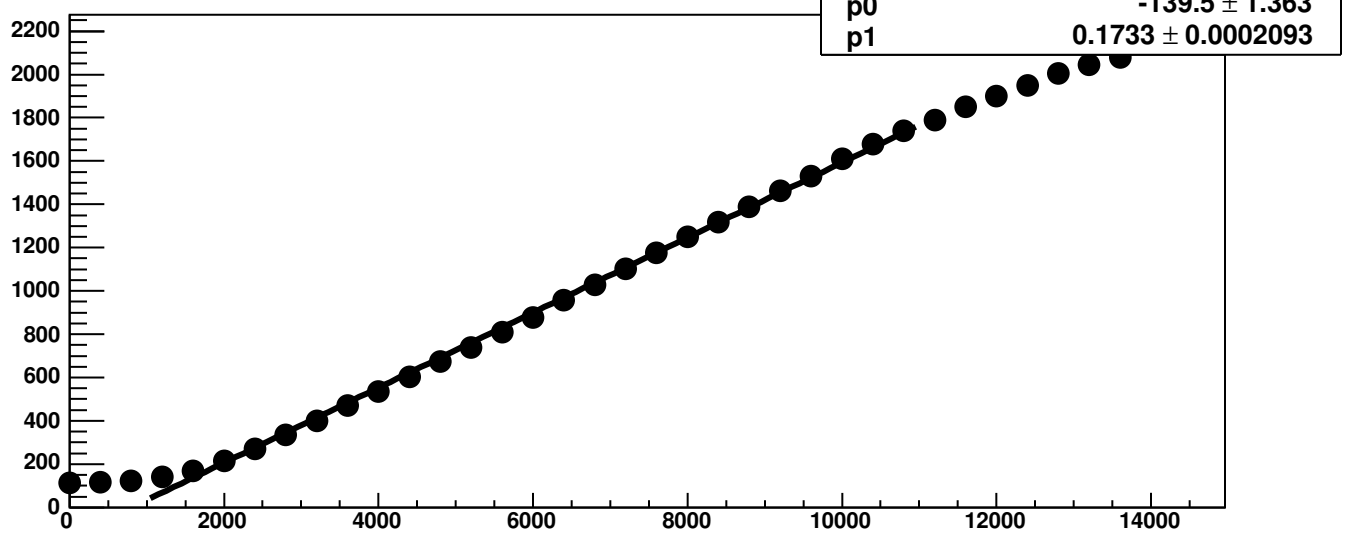

Chip 0, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.118e+05 / 23
p0 -421.4 ± 1.011
p1 0.2571 ± 0.0002104
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

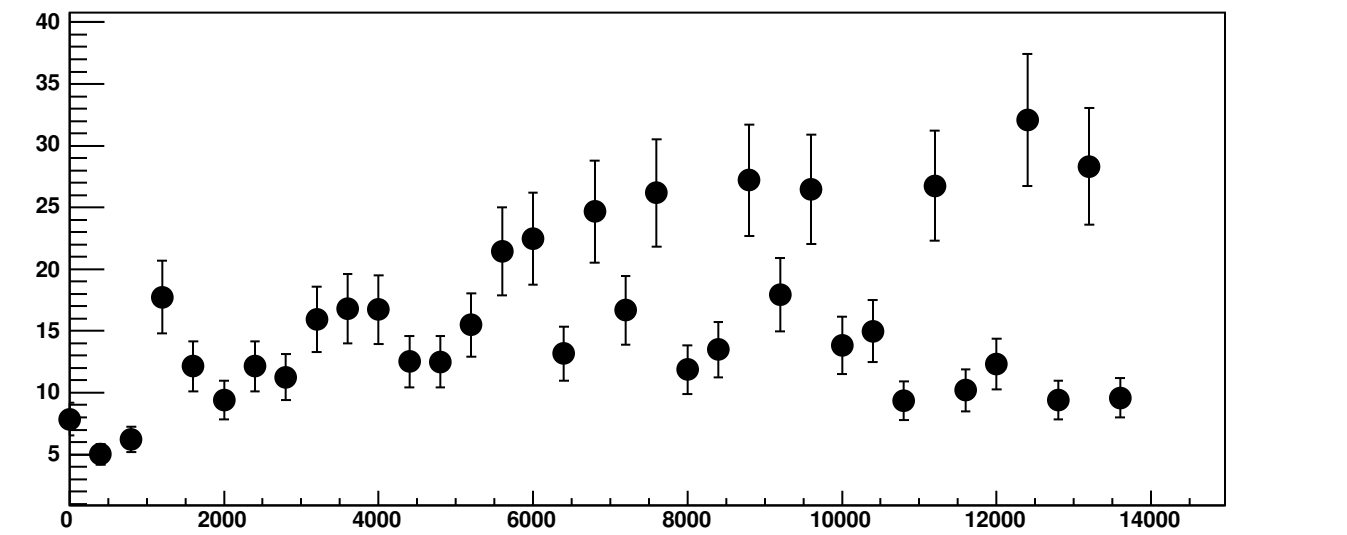

Chip 0, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

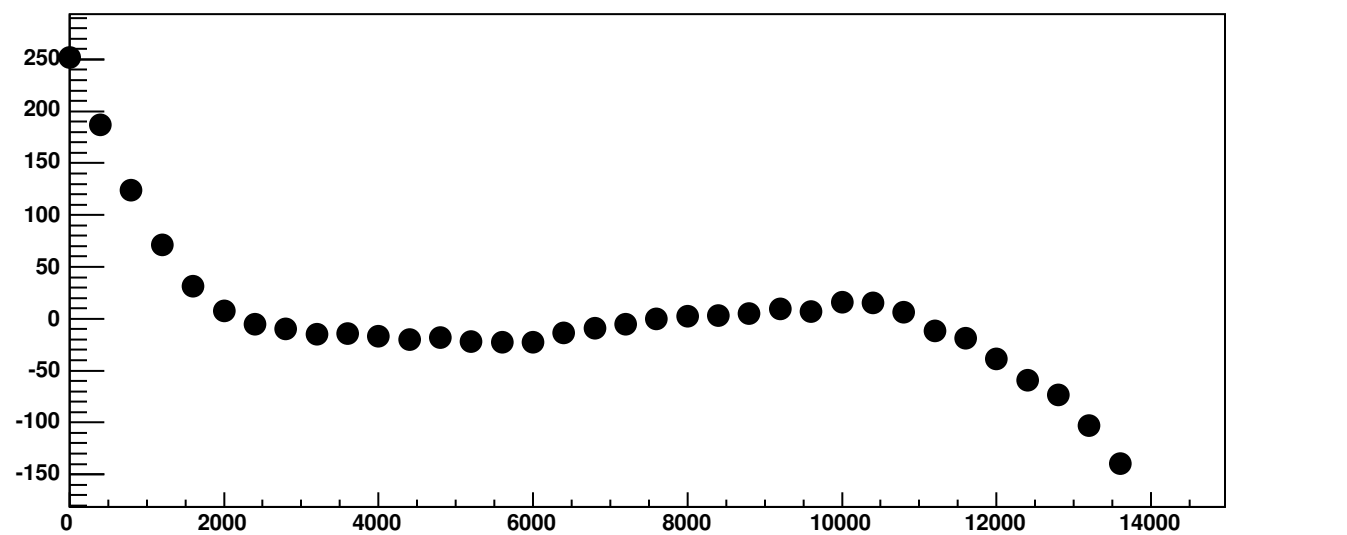
Chip 0, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



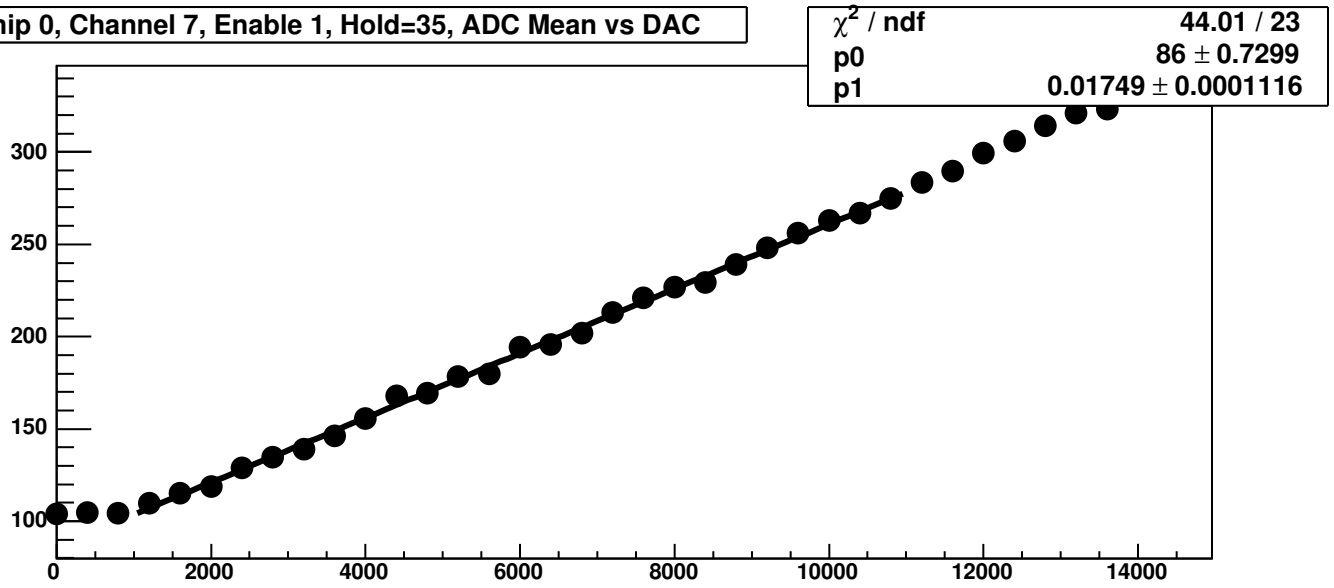
Chip 0, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



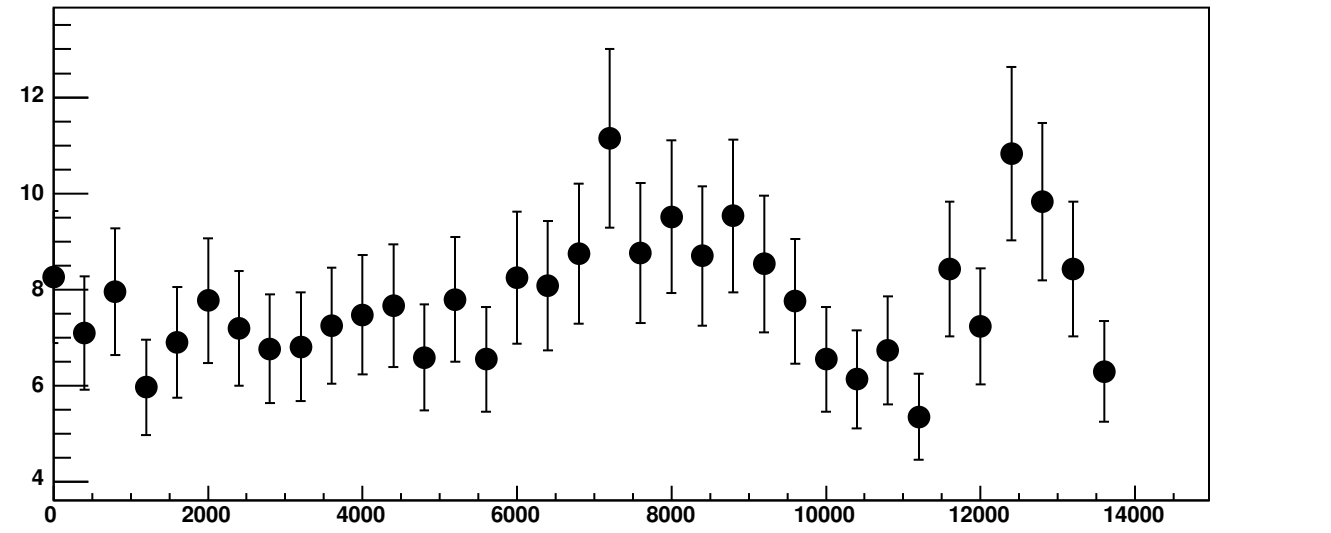
Chip 0, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



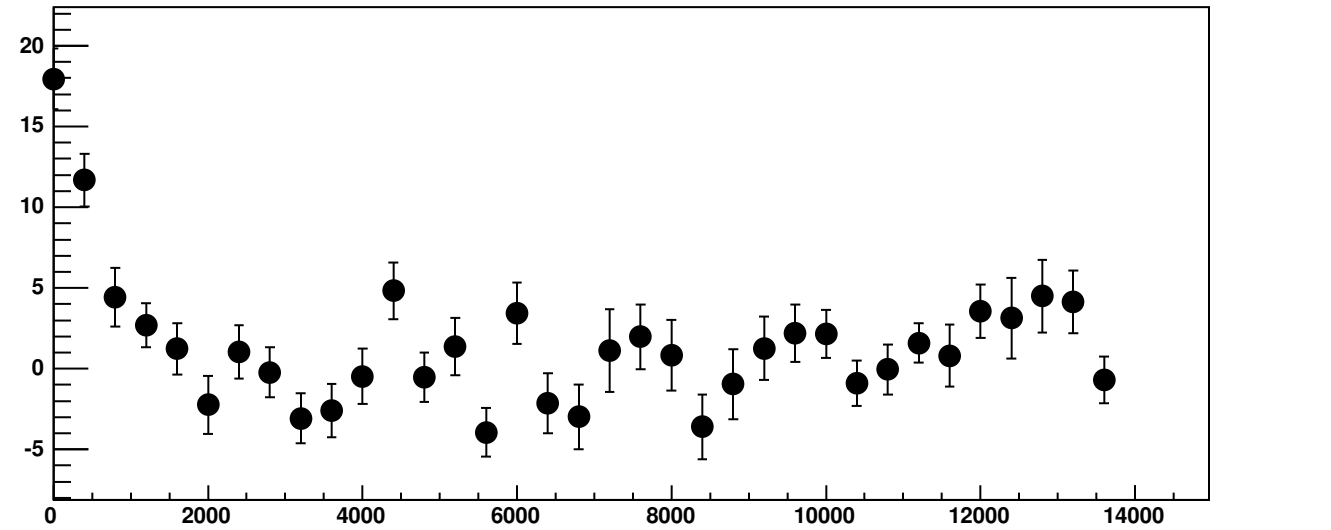
Chip 0, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

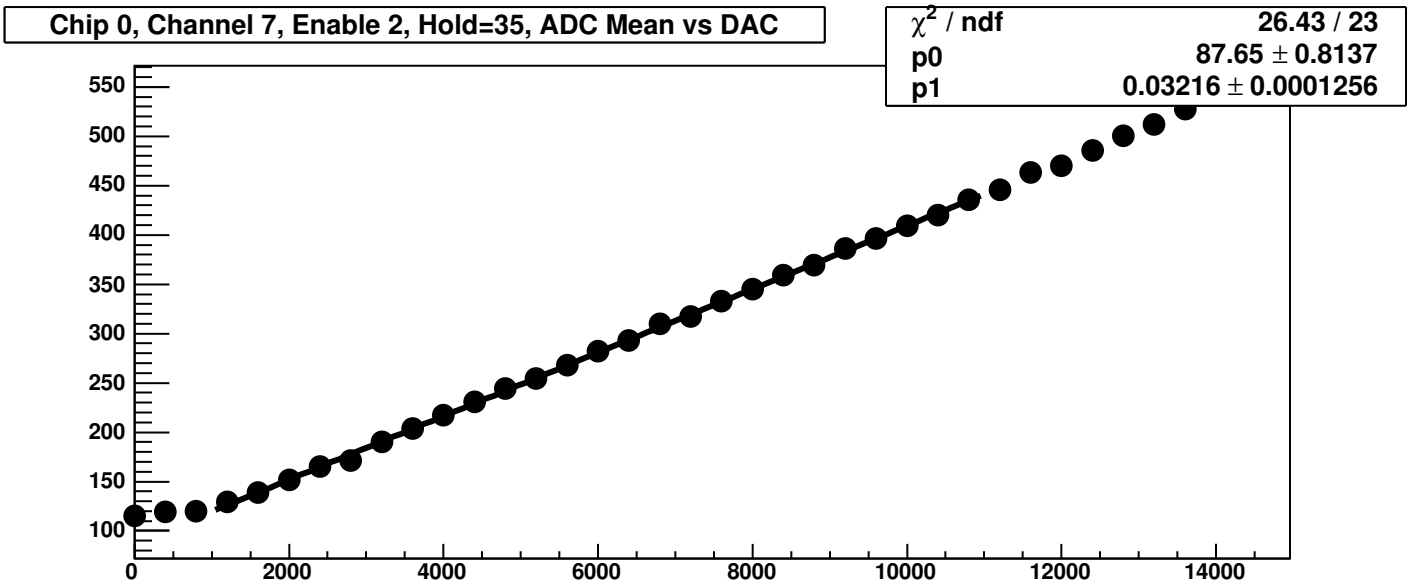


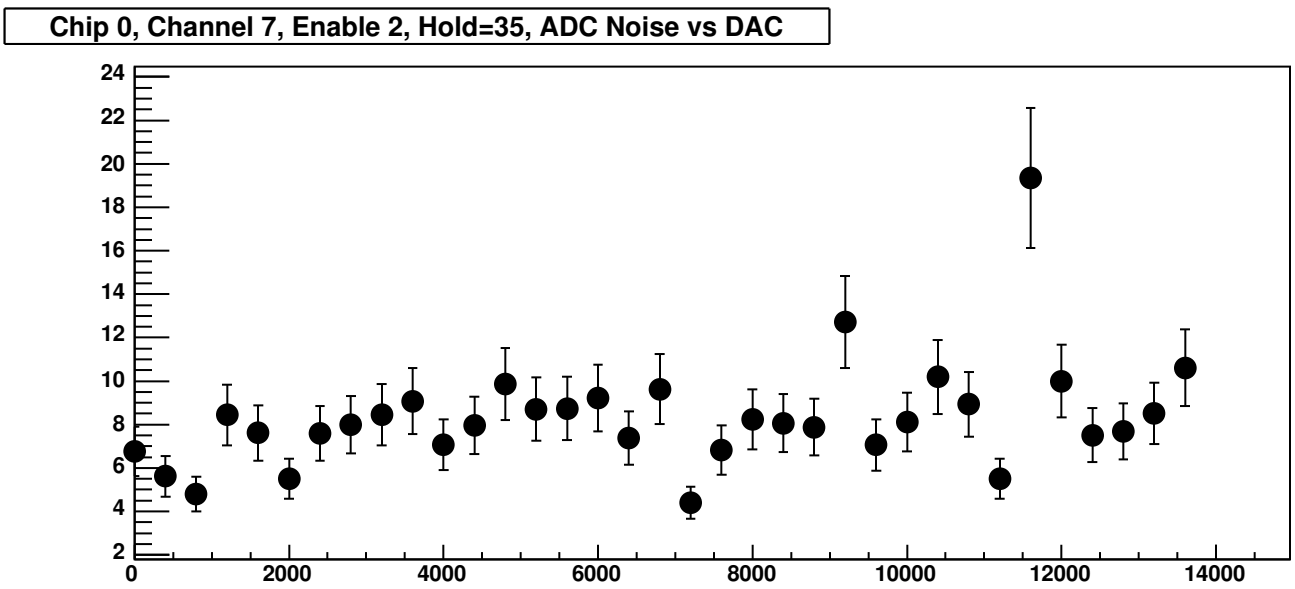
Chip 0, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC

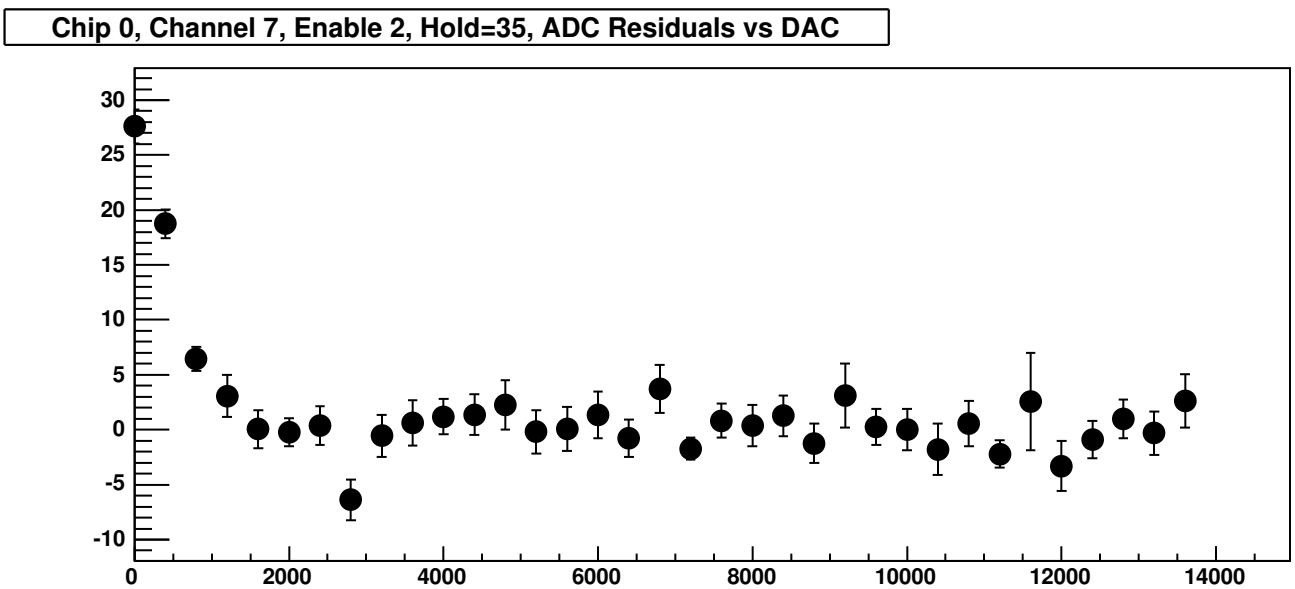


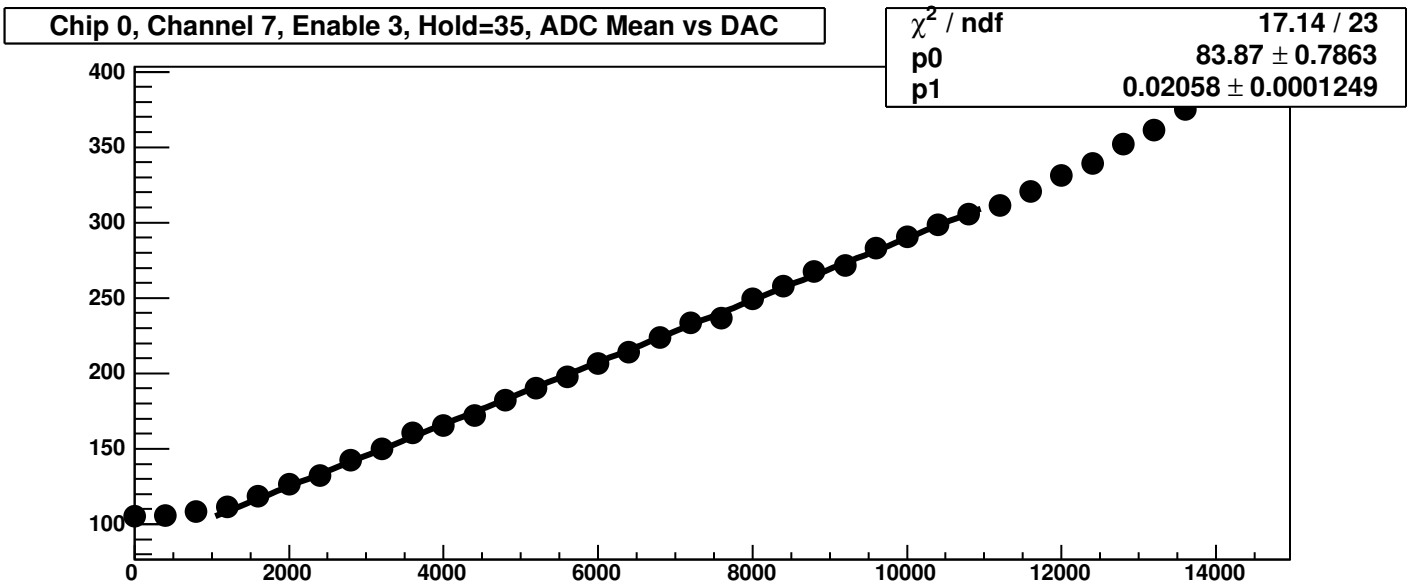
Chip 0, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

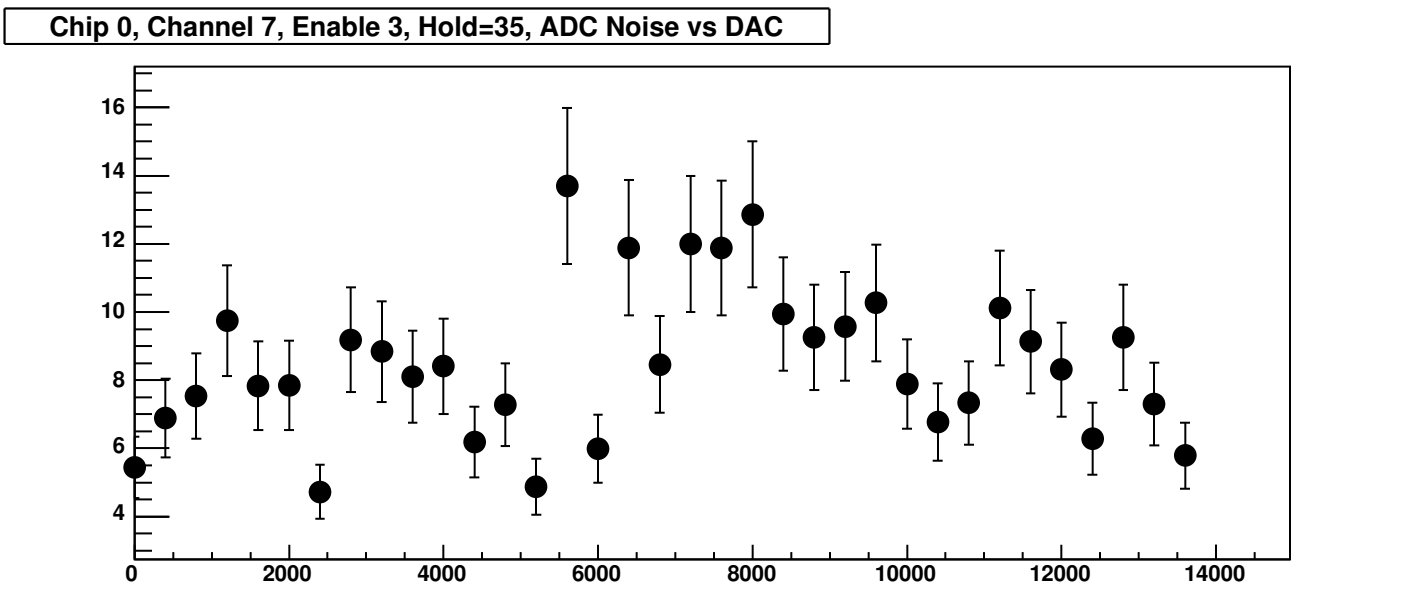


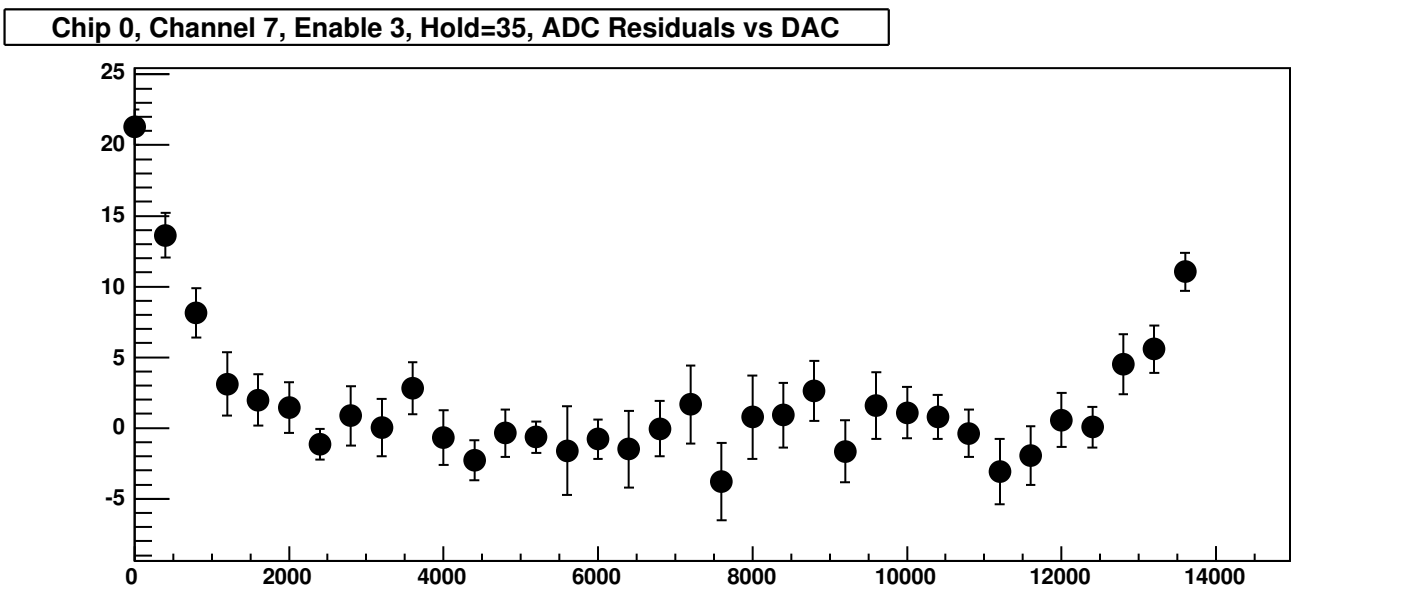

Chip 0, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 26.43 / 23
p0 87.65 ± 0.8137
p1 0.03216 ± 0.0001256
550
500
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
24
22
20
18
16
14
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

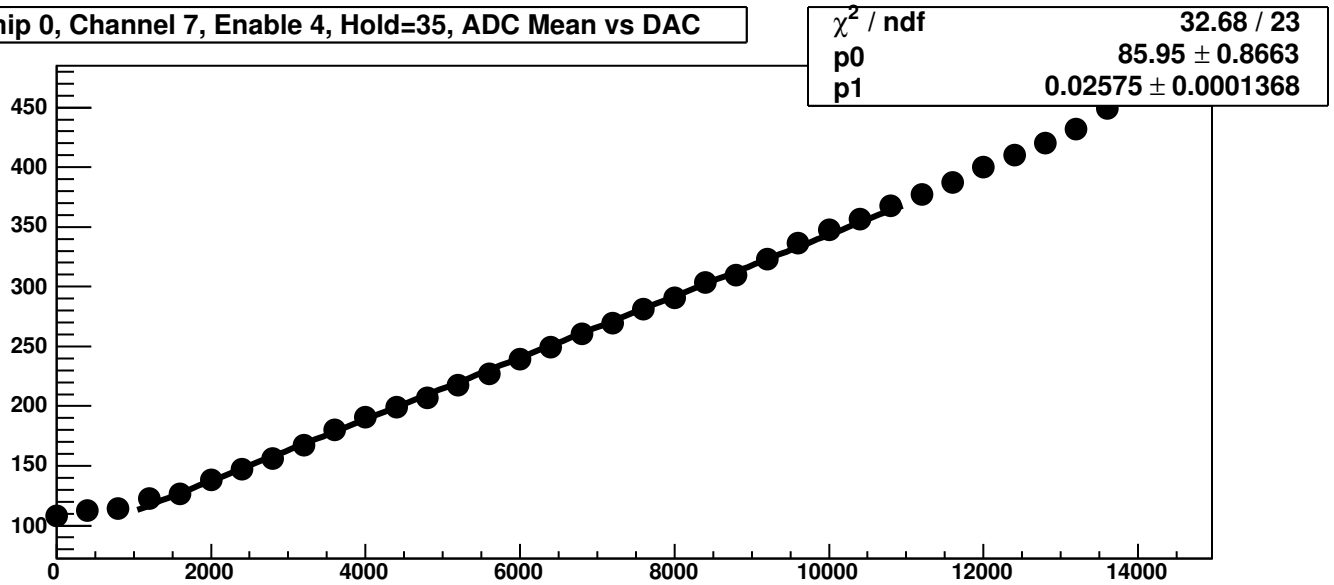

Chip 0, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 17.14 / 23
p0 83.87 ± 0.7863
p1 0.02058 ± 0.0001249
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

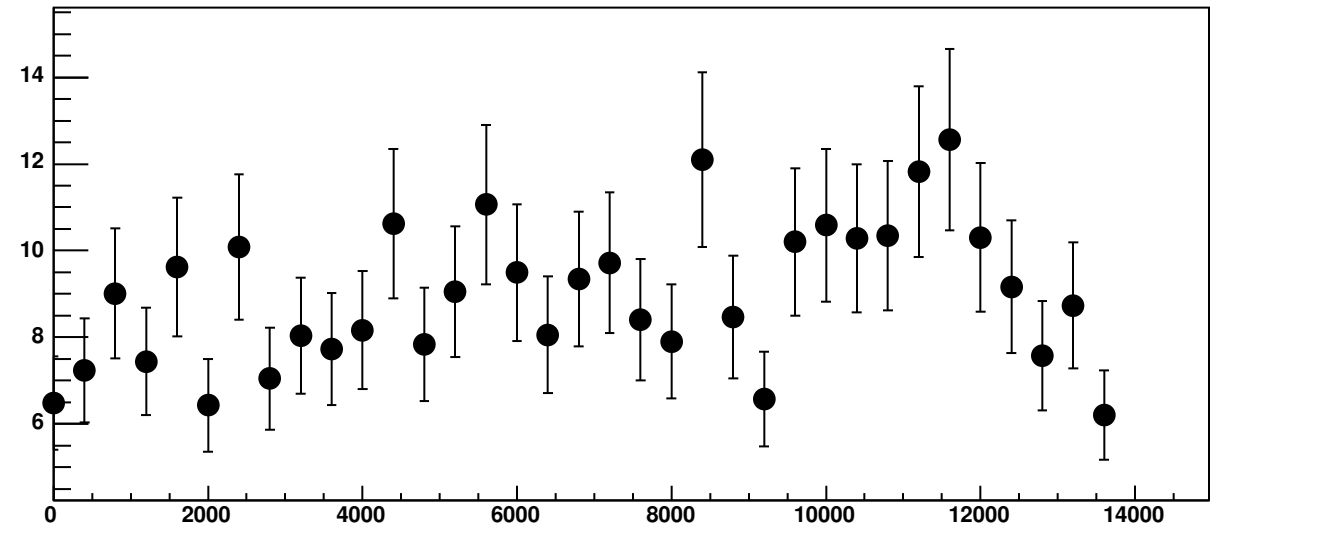

Chip 0, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

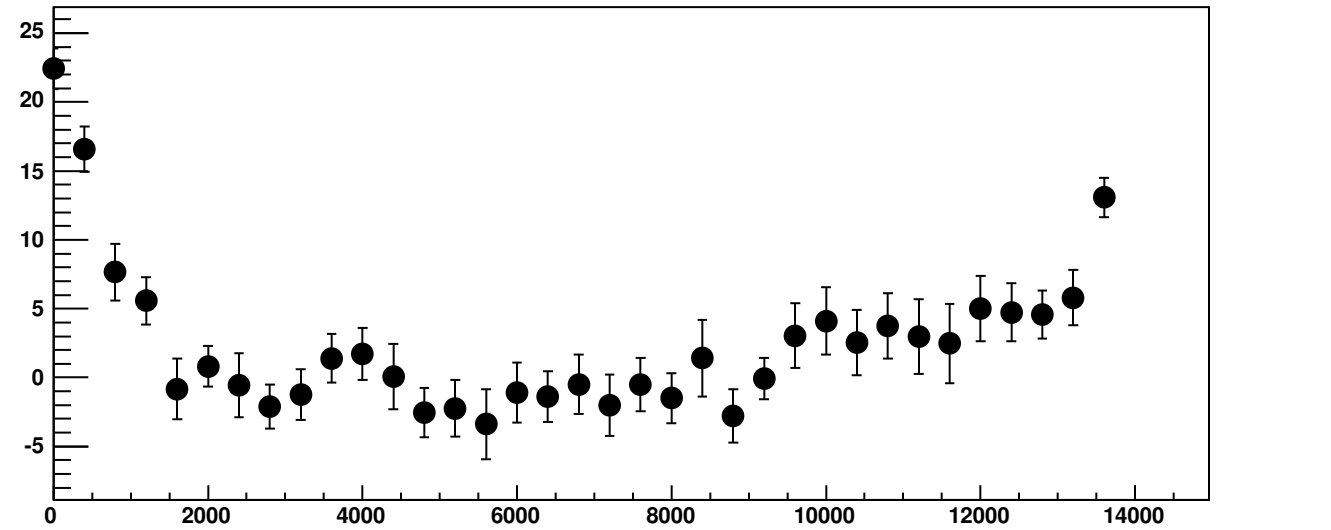
Chip 0, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

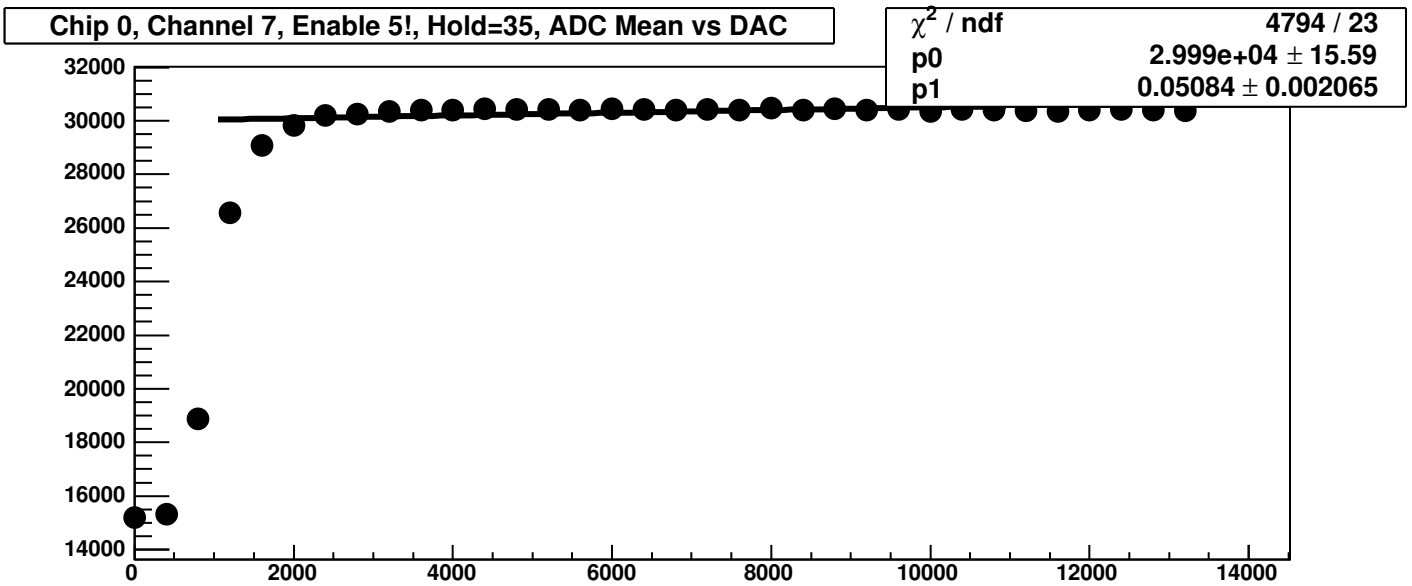


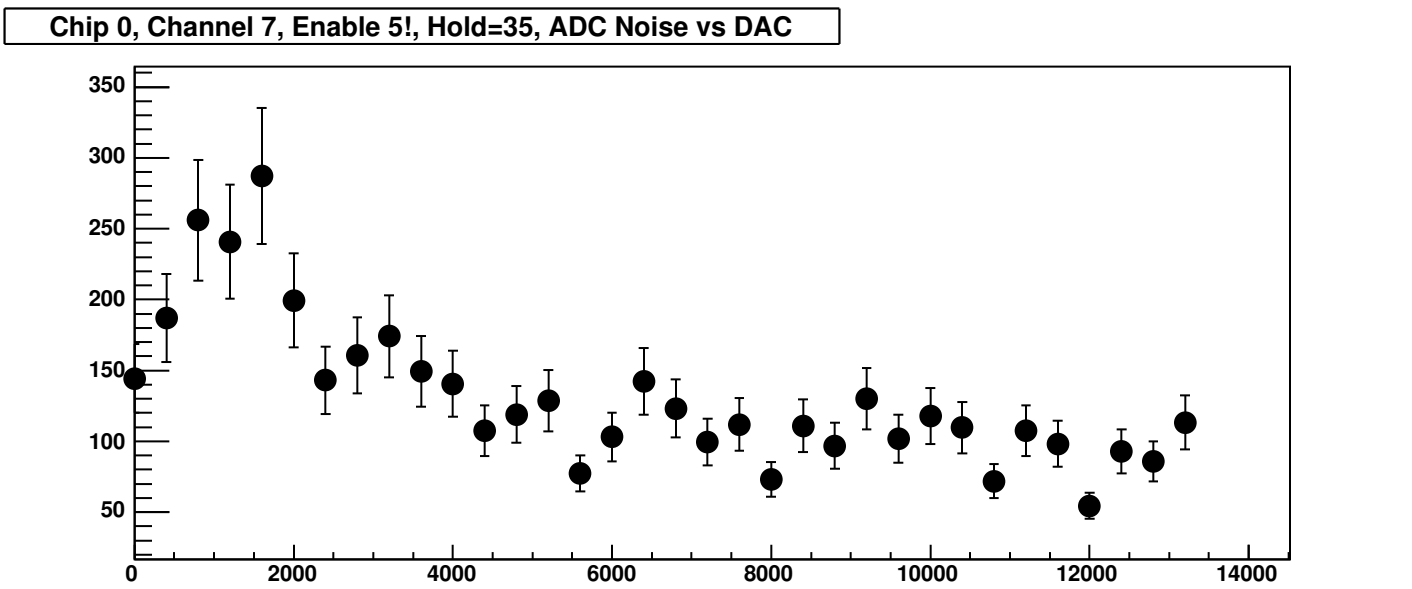
Chip 0, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

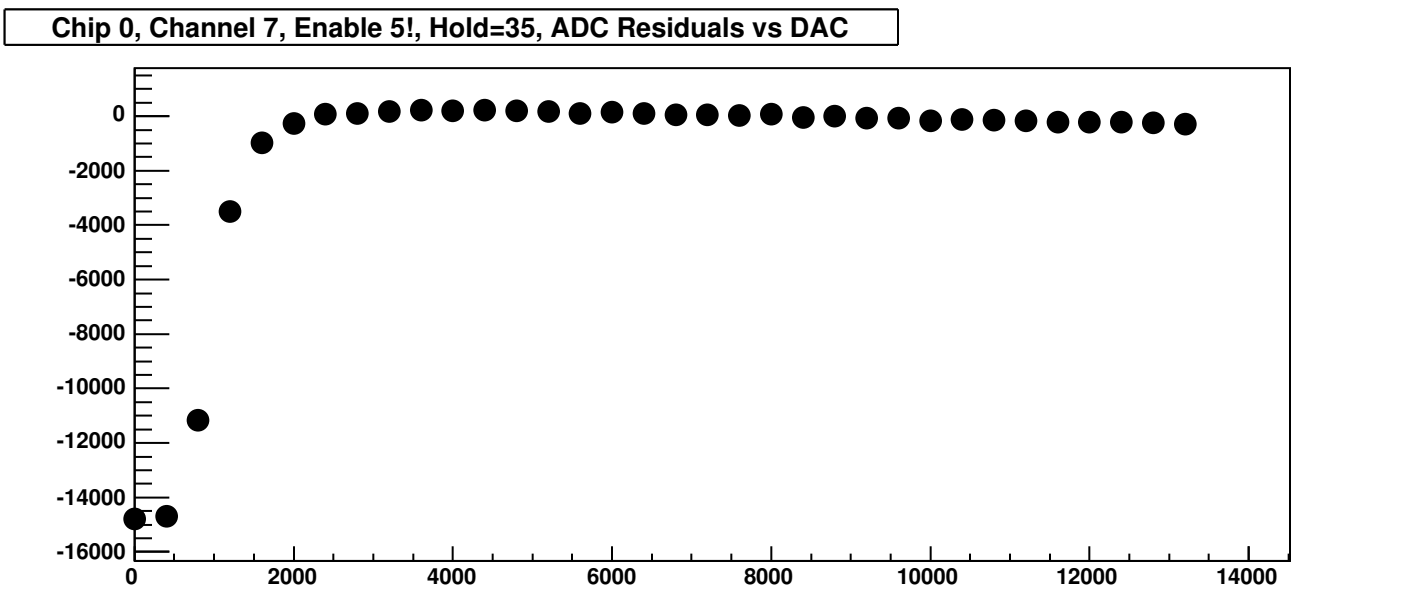


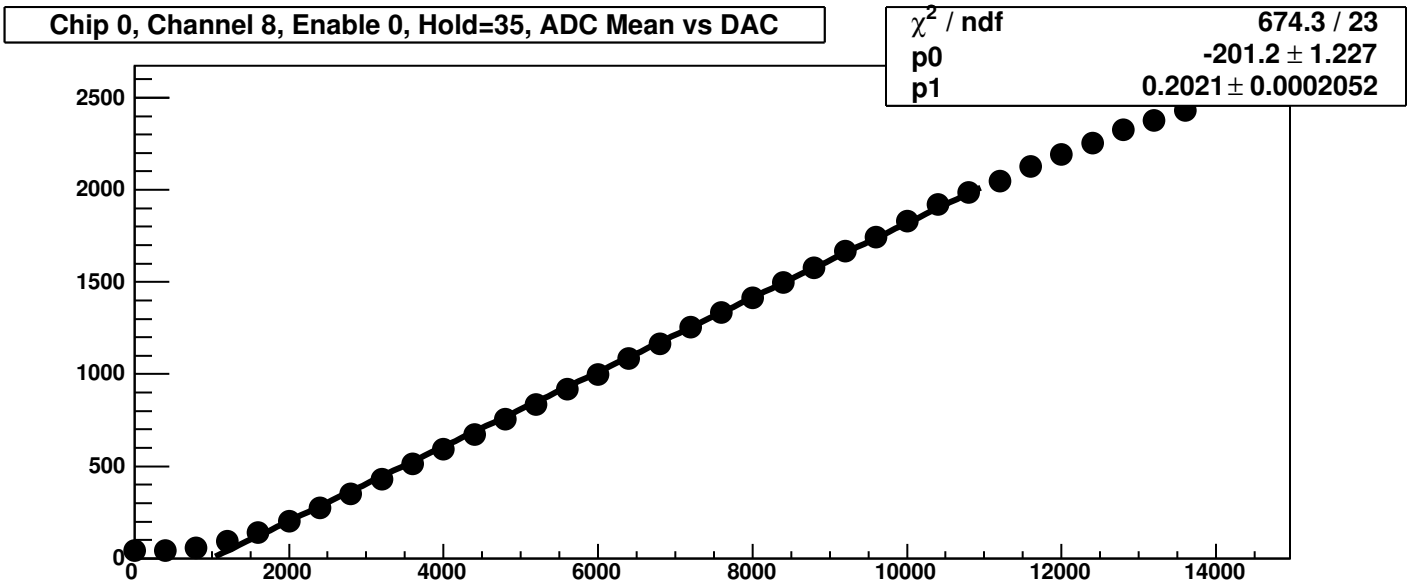
Chip 0, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

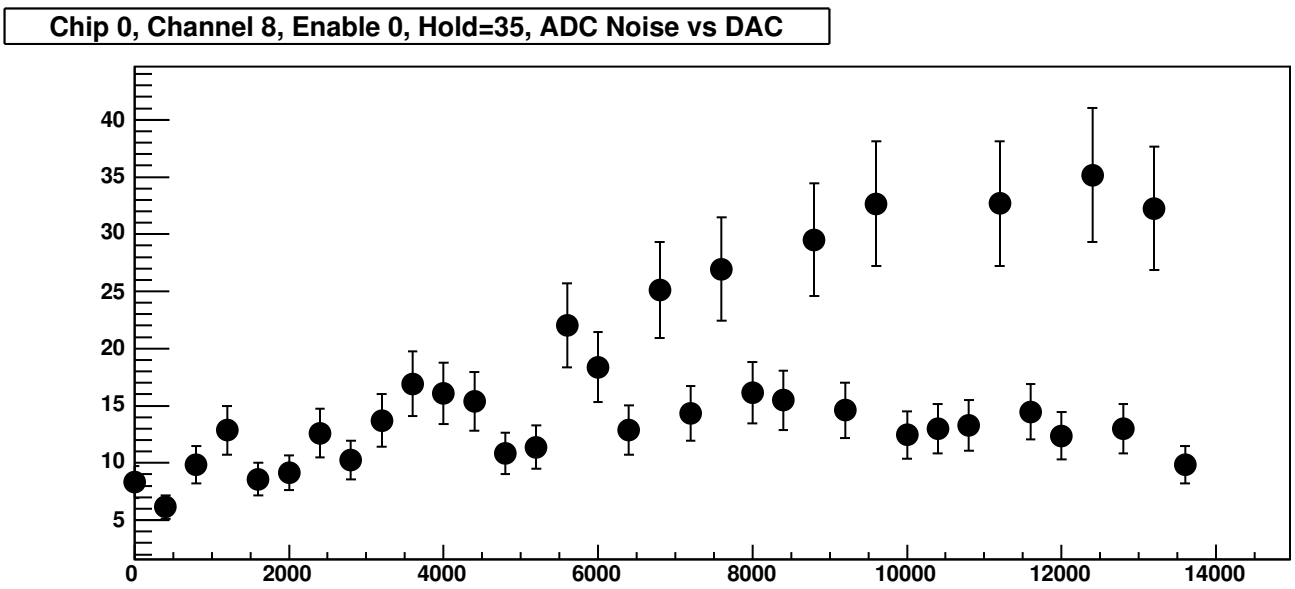


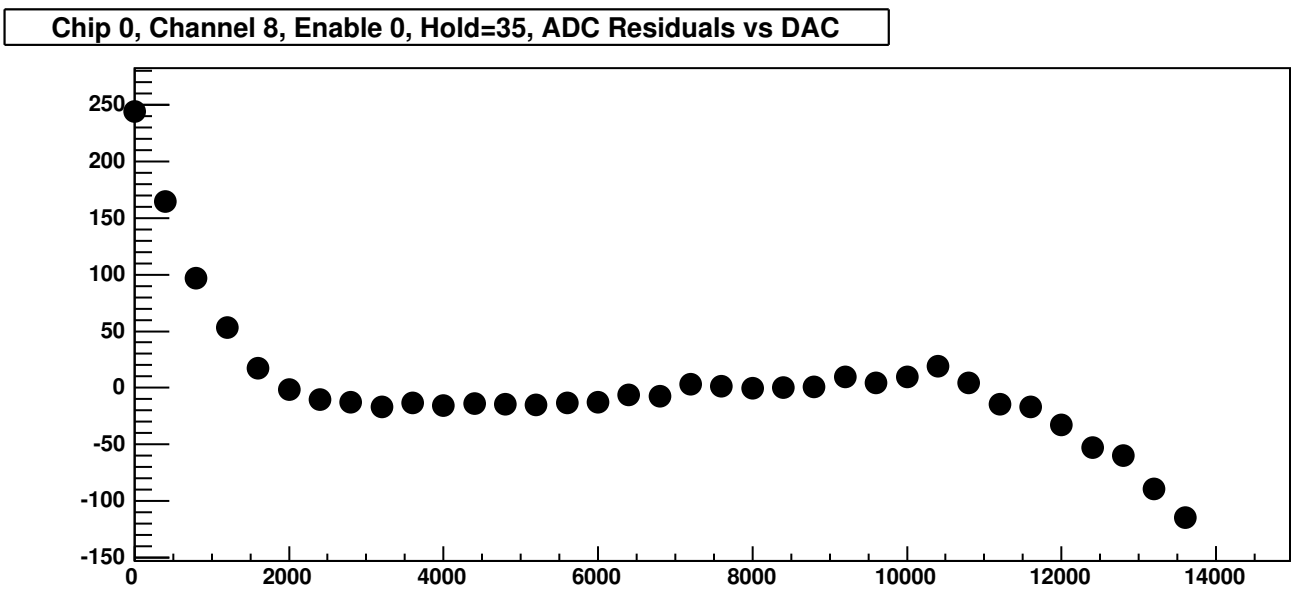

Chip 0, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4794 / 23
p0 2.999e+04 ± 15.59
p1 0.05084 ± 0.002065
32000
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

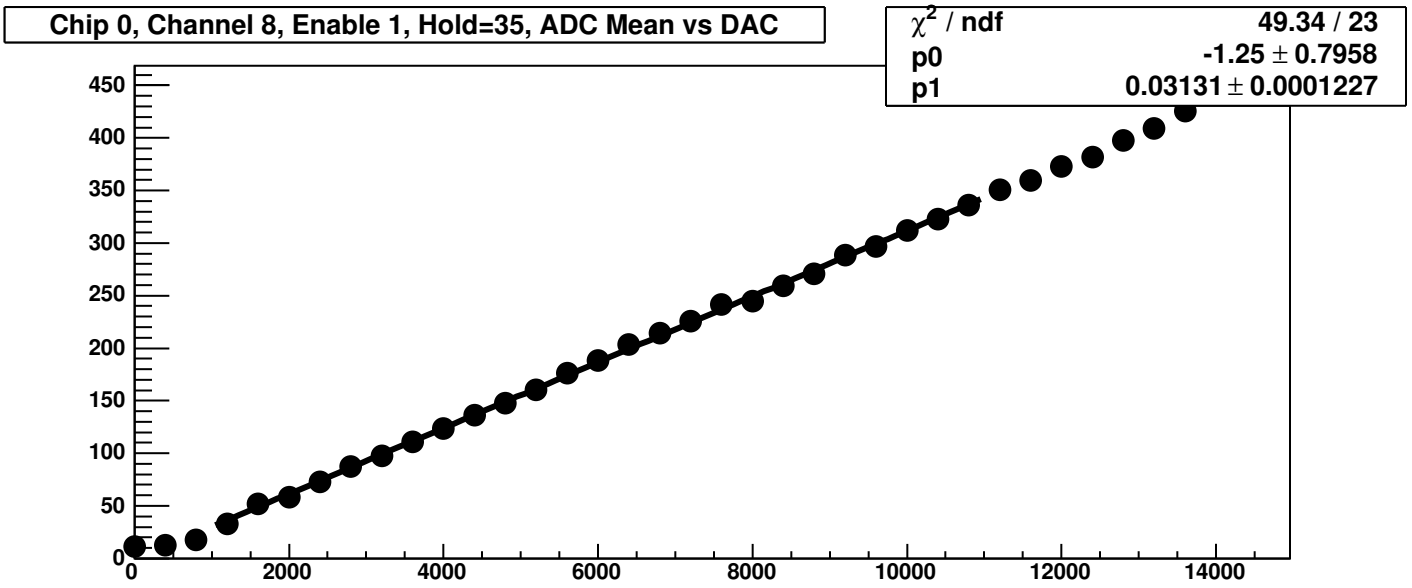

Chip 0, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

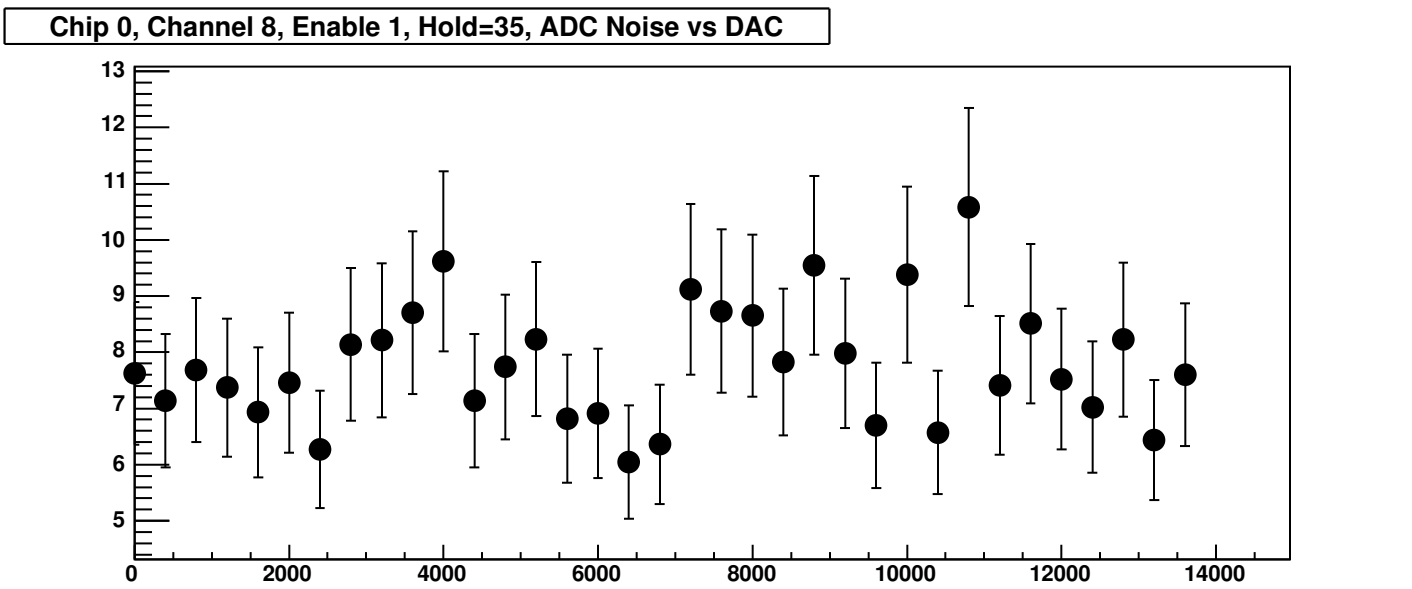

Chip 0, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

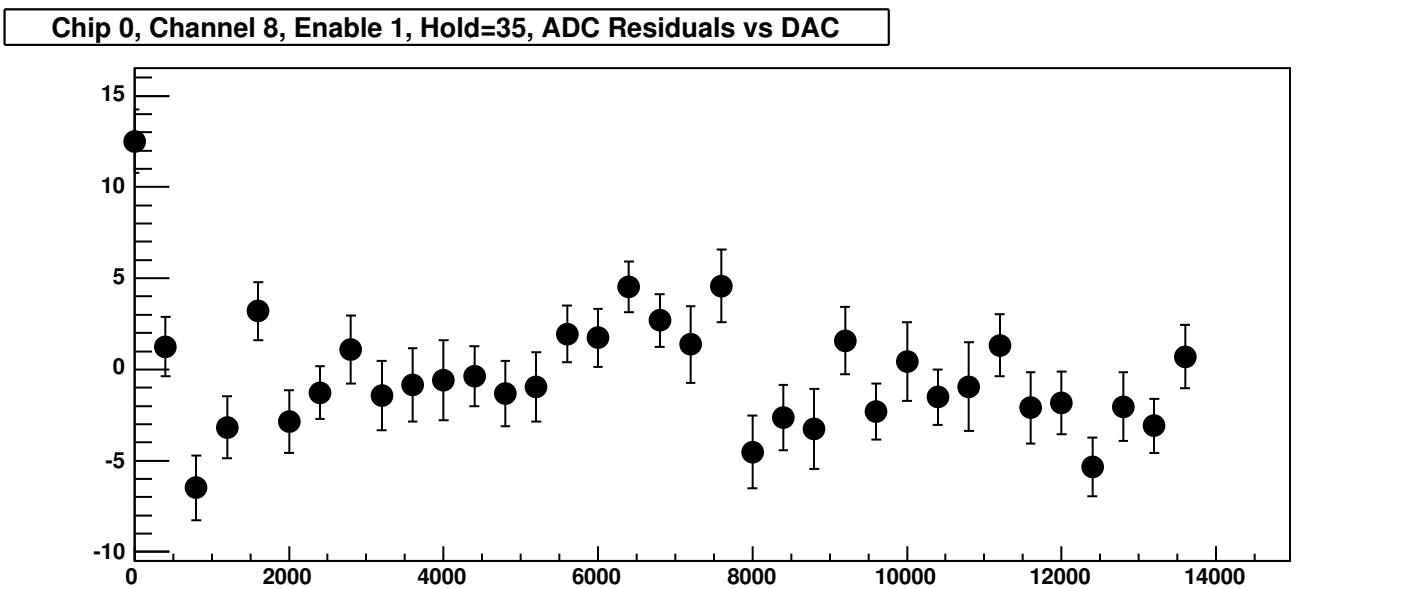

Chip 0, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 674.3 / 23
p0 -201.2 ± 1.227
p1 0.2021 ± 0.0002052
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

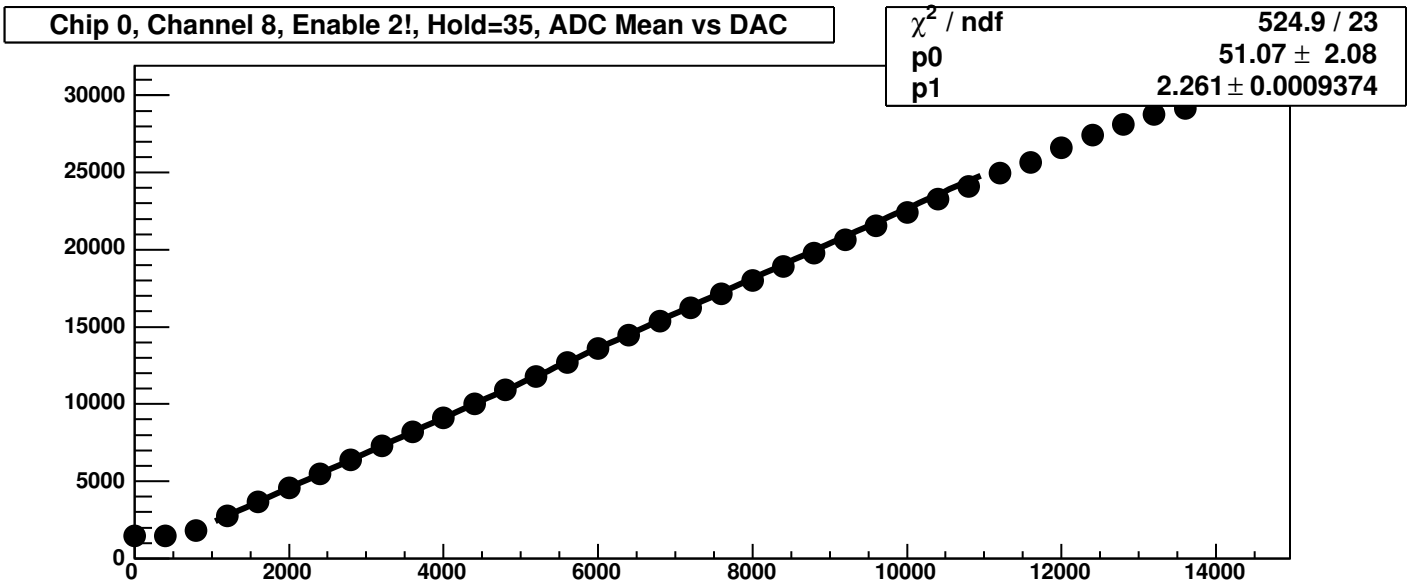

Chip 0, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

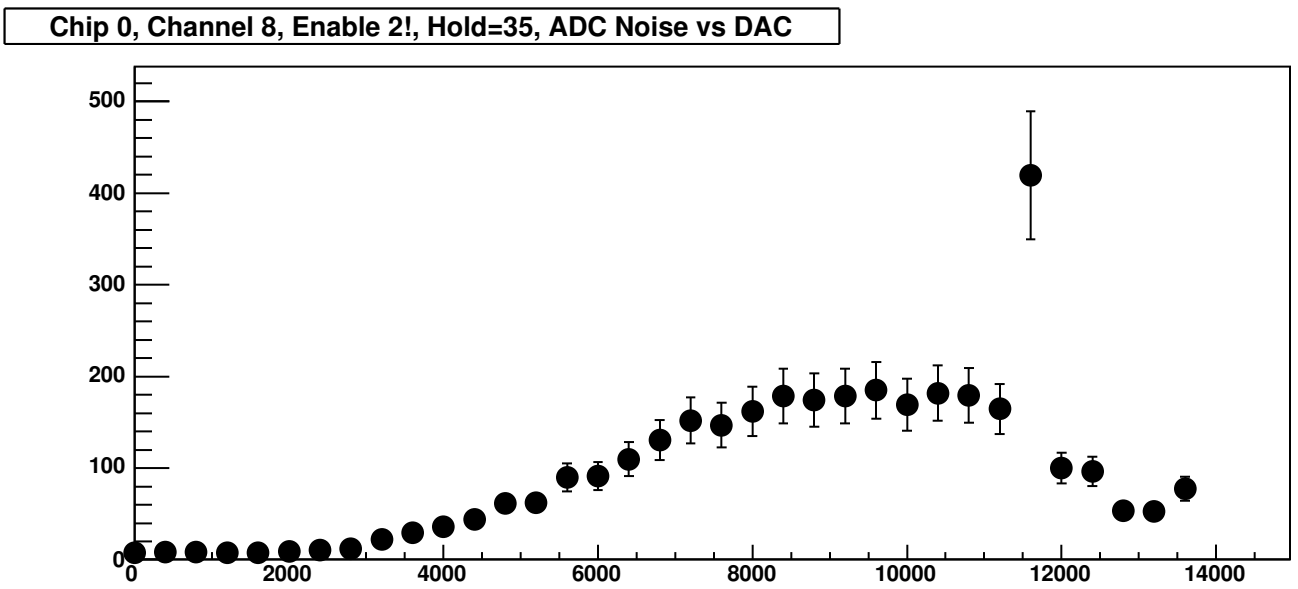

Chip 0, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

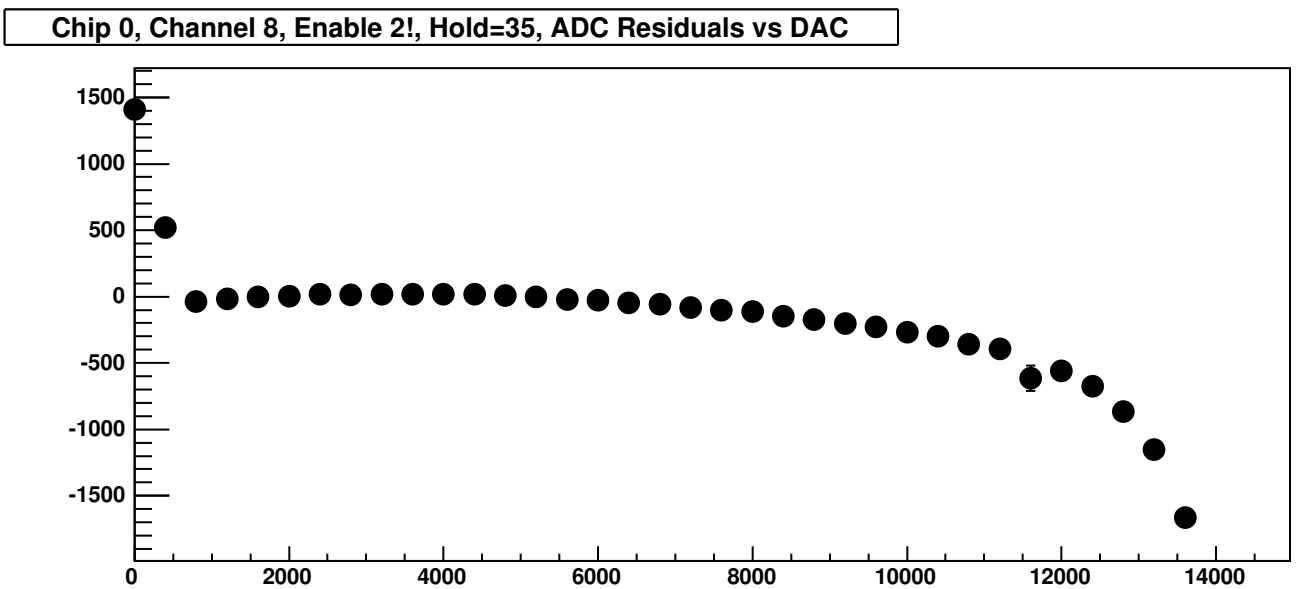

Chip 0, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 49.34 / 23
p0 -1.25 ± 0.7958
p1 0.03131 ± 0.0001227
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

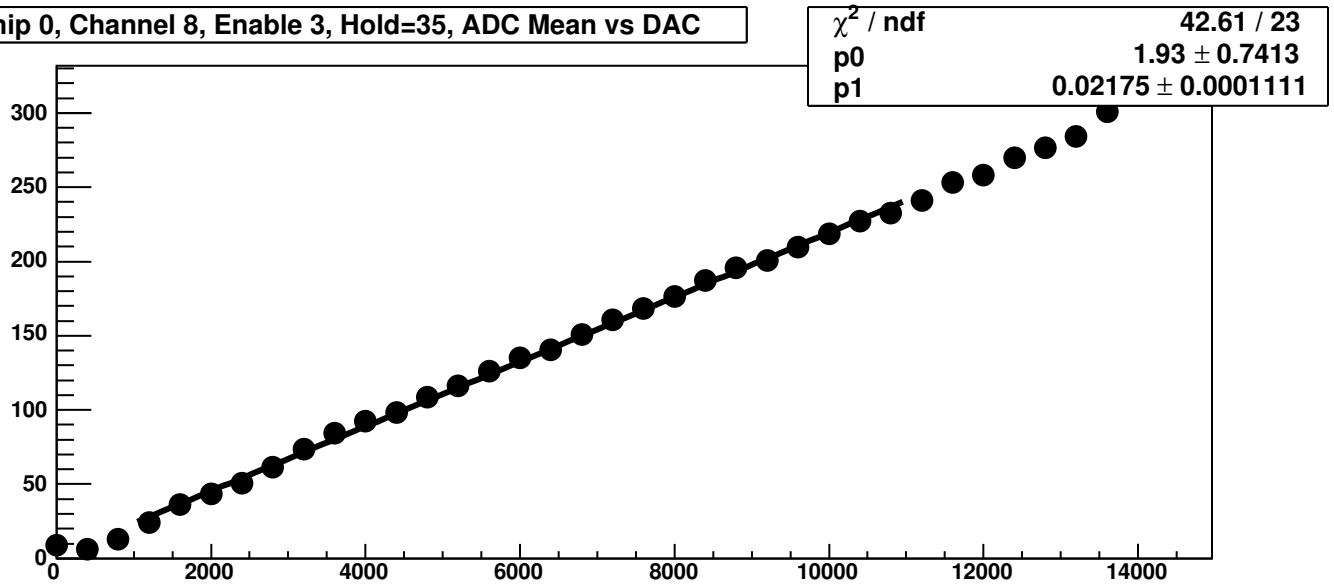

Chip 0, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 524.9 / 23
p0 51.07 ± 2.08
p1 2.261 ± 0.0009374
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

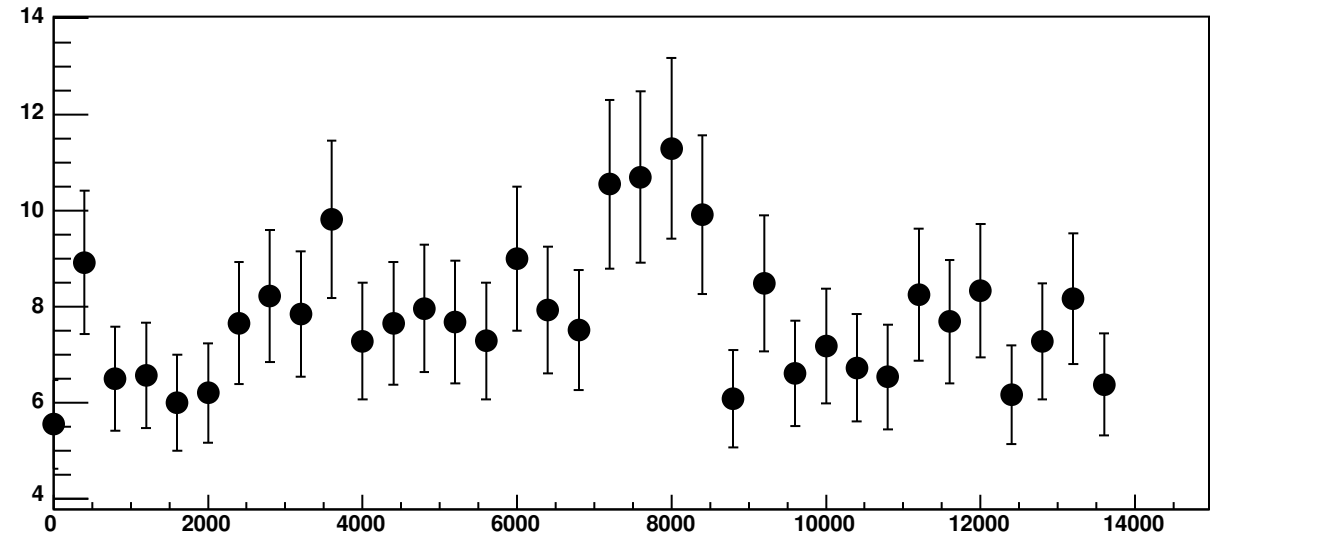

Chip 0, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

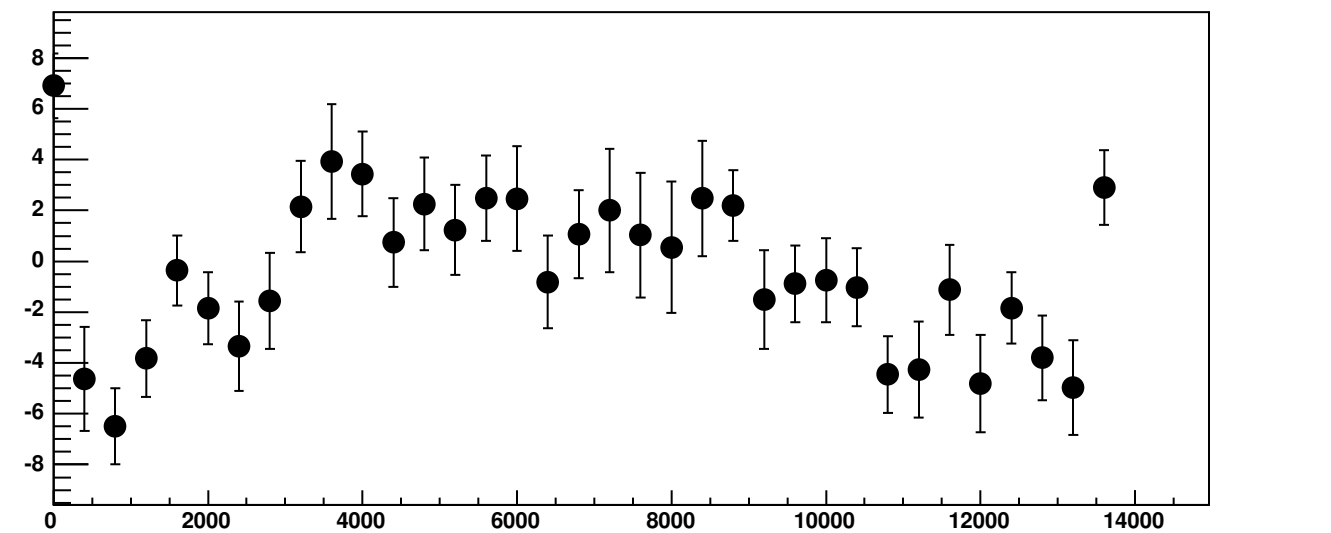
Chip 0, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

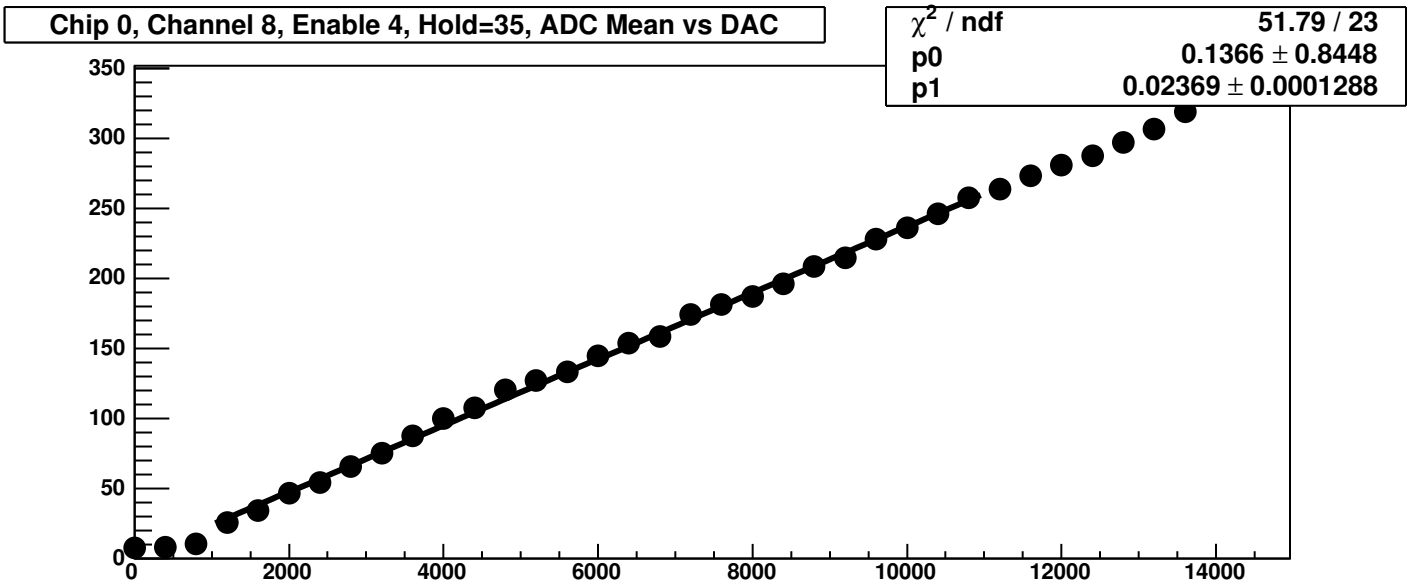


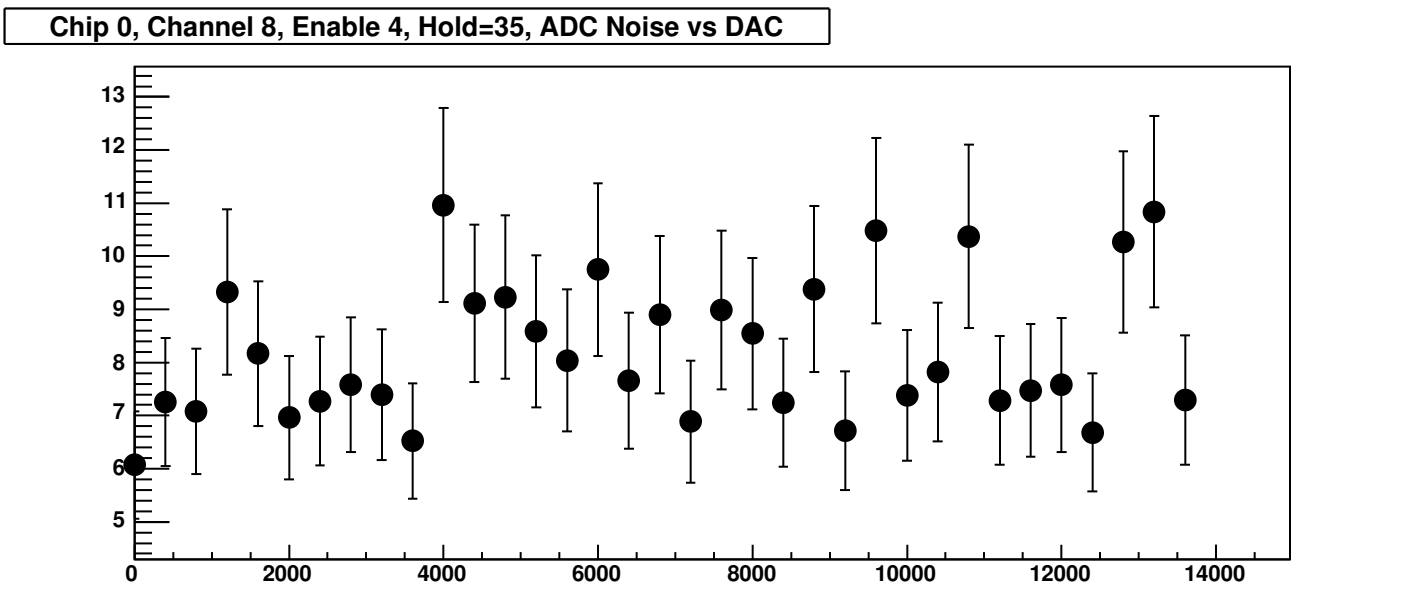
Chip 0, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC

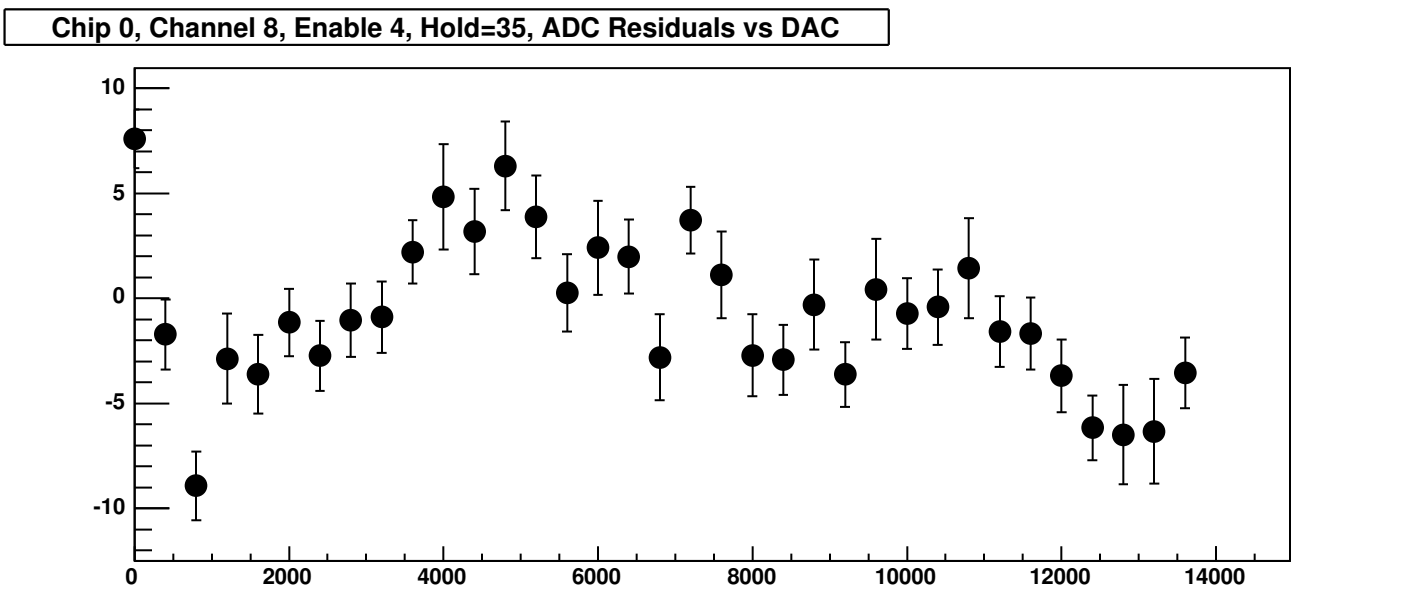


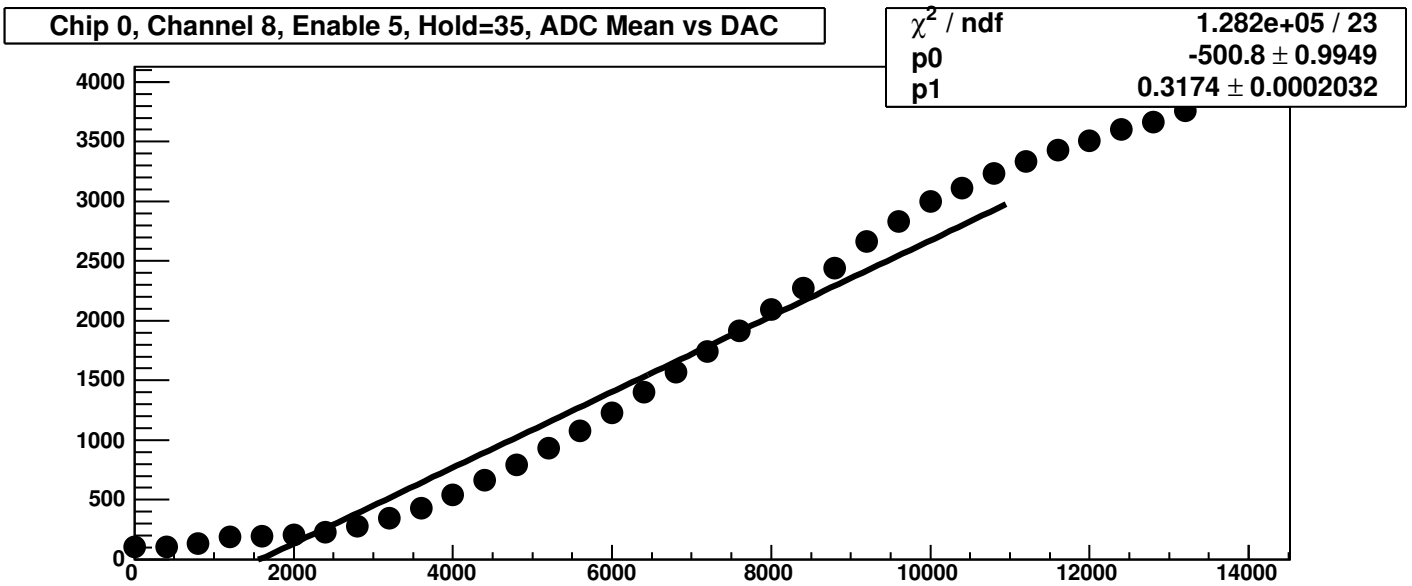
Chip 0, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

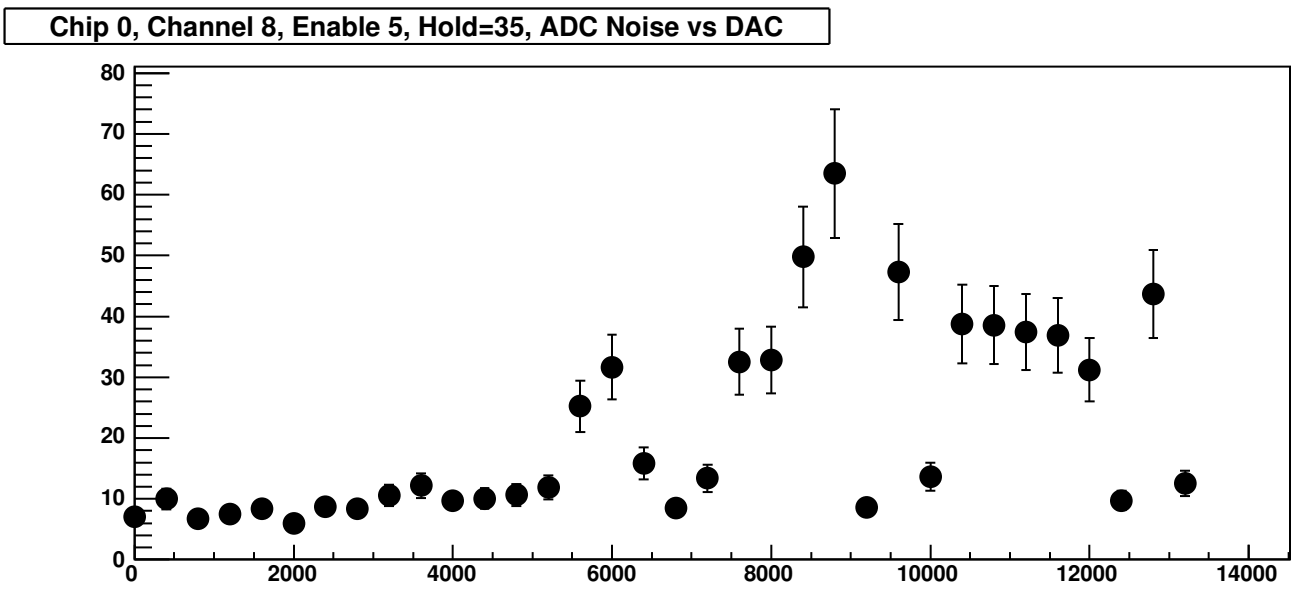


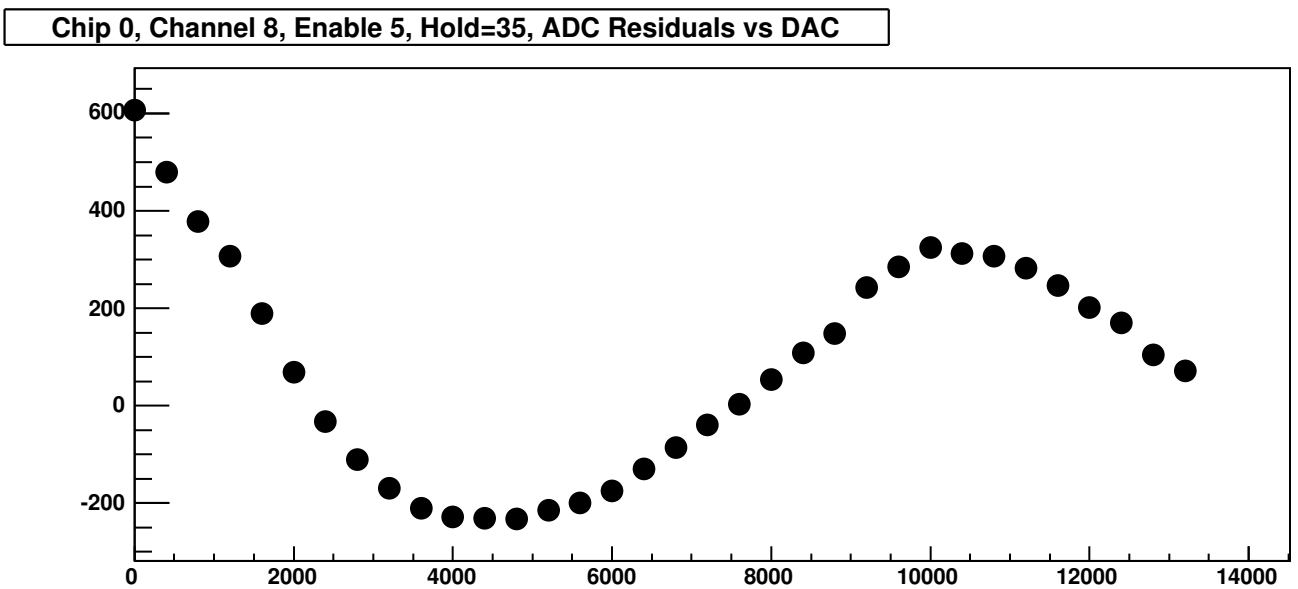

Chip 0, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 51.79 / 23
p0 0.1366 ± 0.8448
p1 0.02369 ± 0.0001288
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

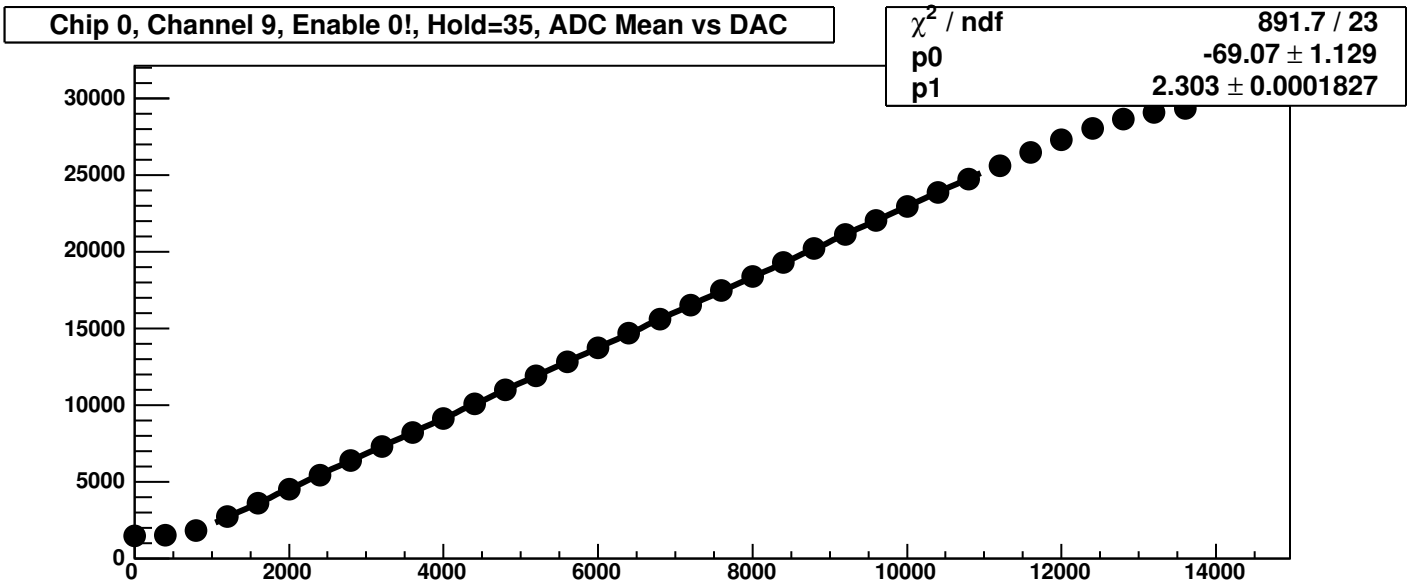

Chip 0, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

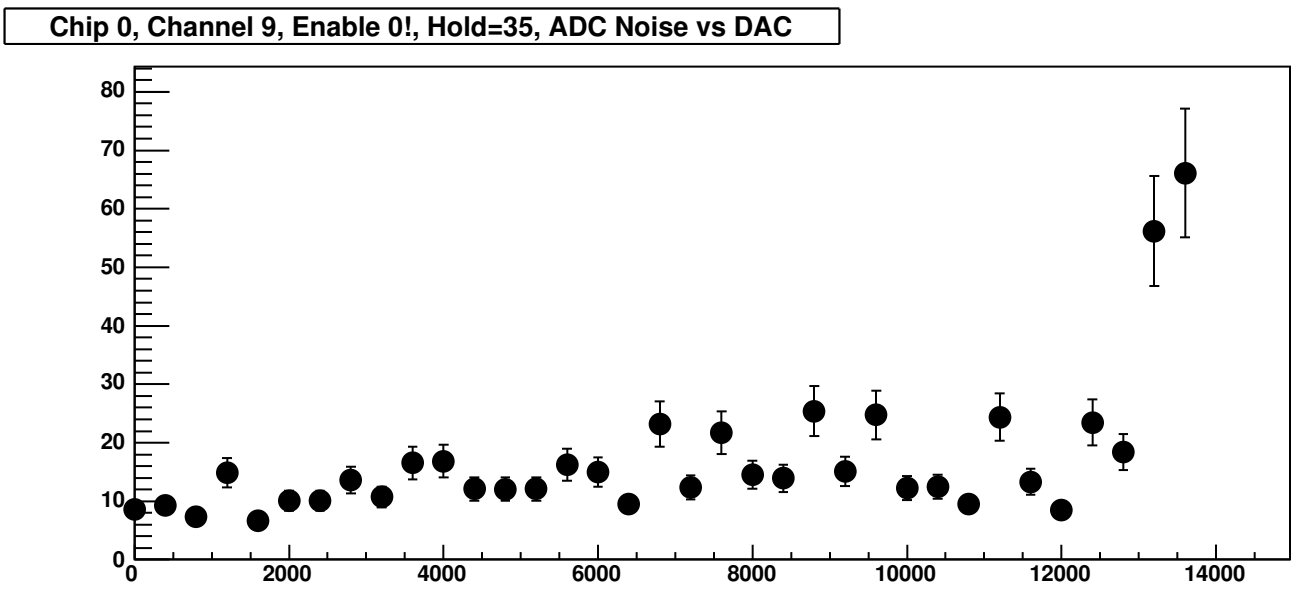

Chip 0, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

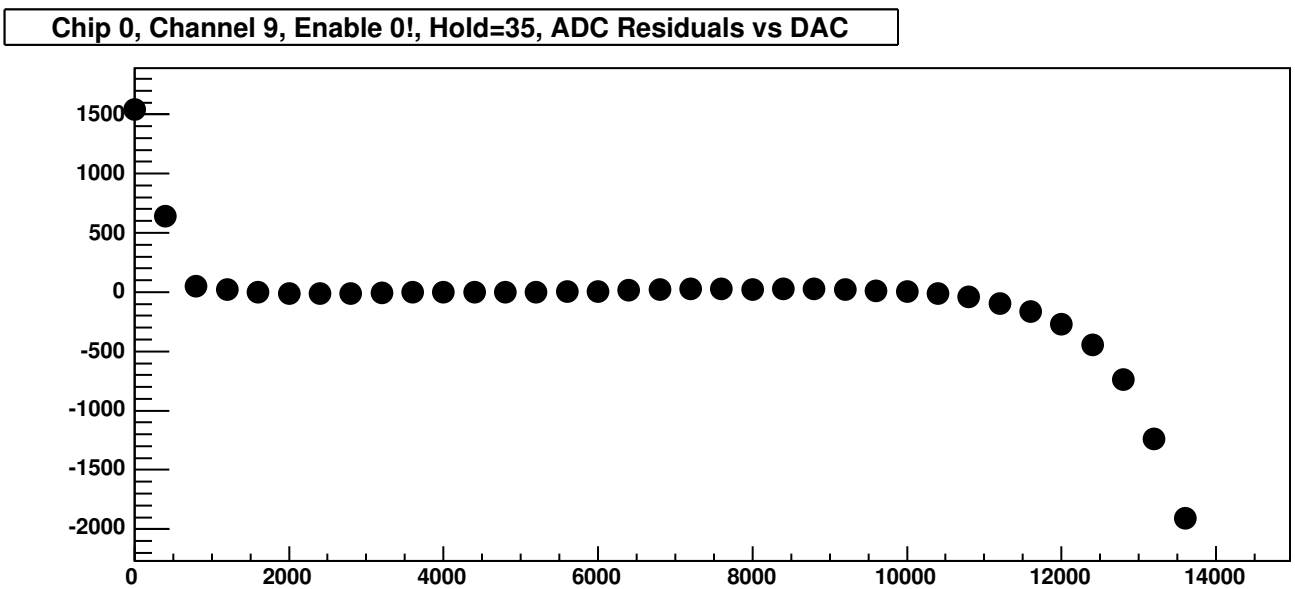

Chip 0, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.282e+05 / 23
p0 -500.8 ± 0.9949
p1 0.3174 ± 0.0002032
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

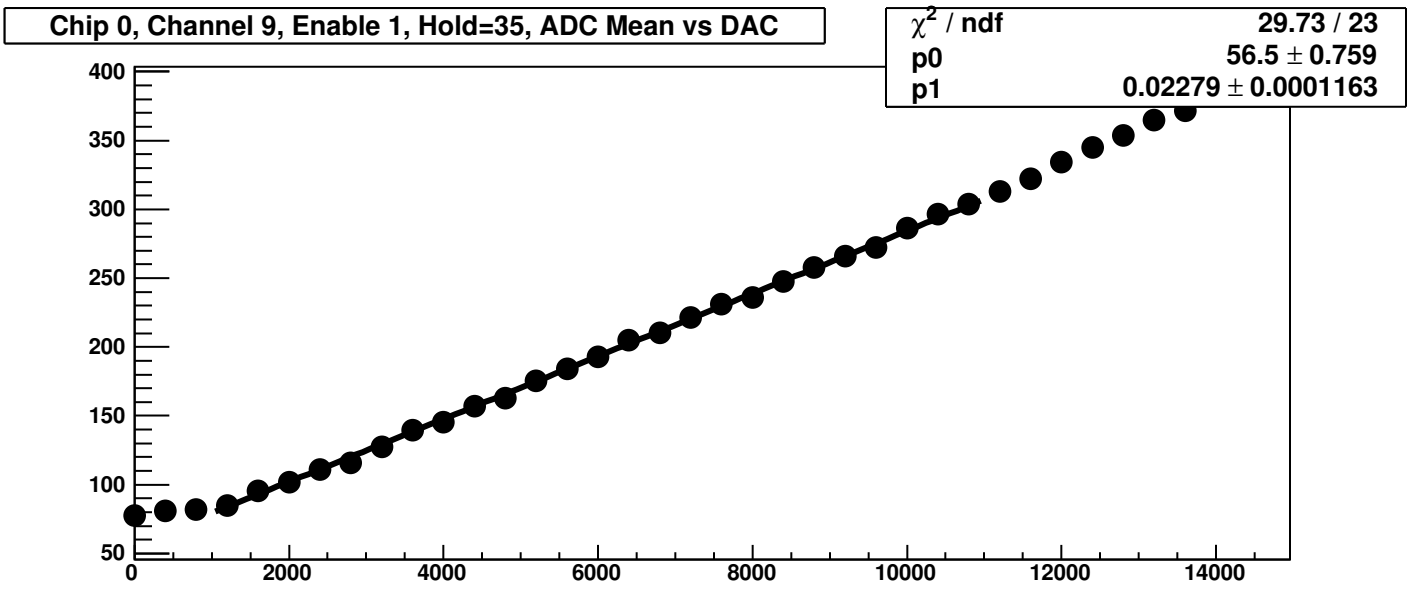

Chip 0, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

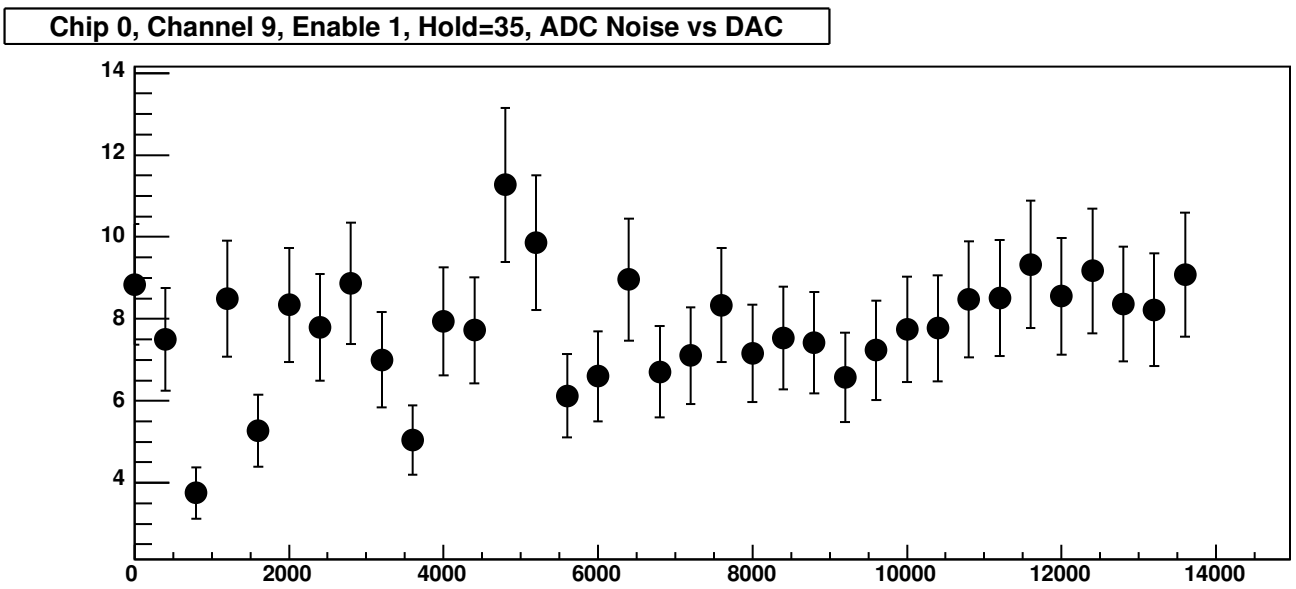

Chip 0, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

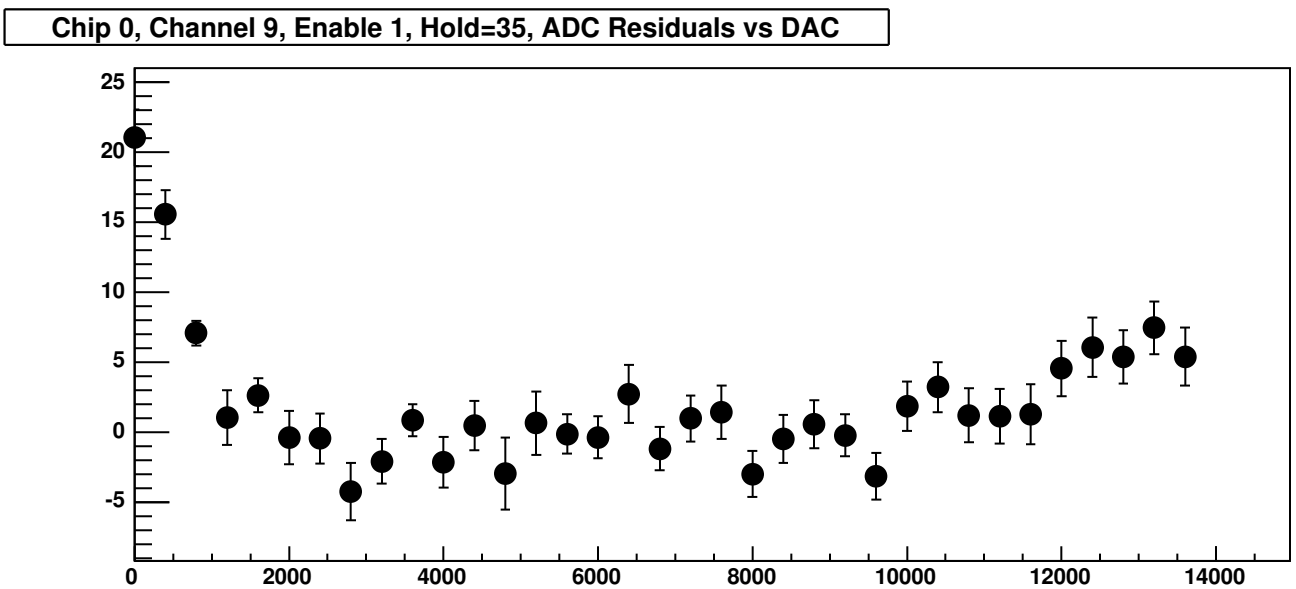

Chip 0, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 891.7 / 23
p0 -69.07 ± 1.129
p1 2.303 ± 0.0001827
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

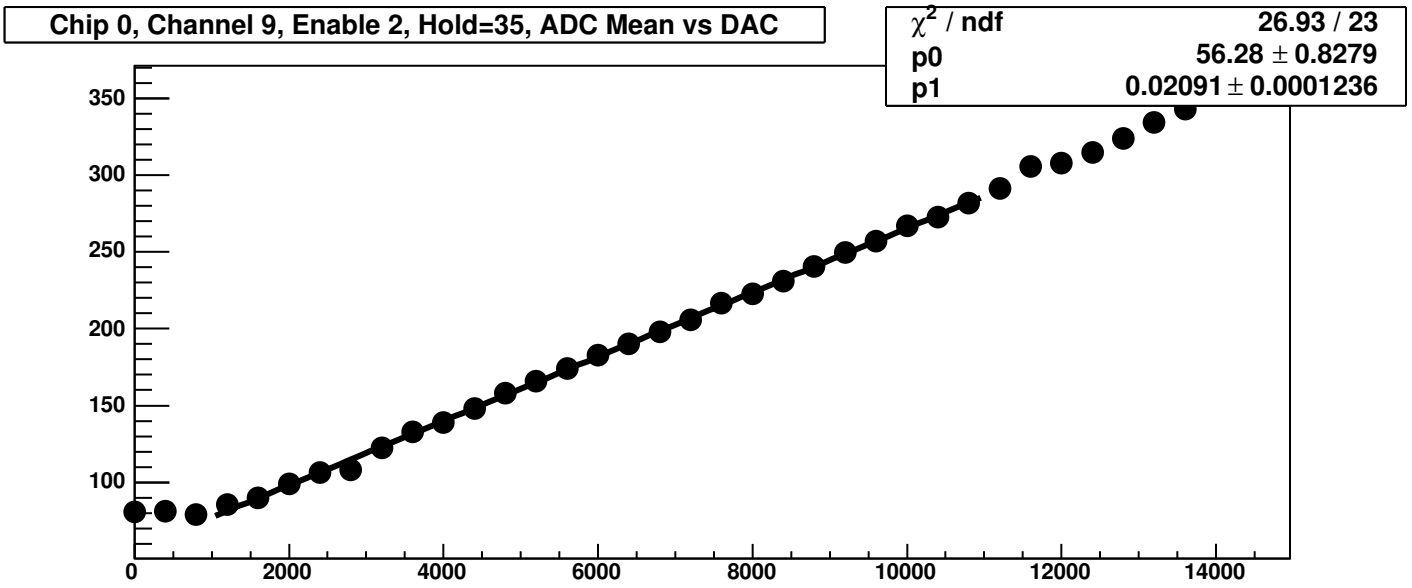

Chip 0, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

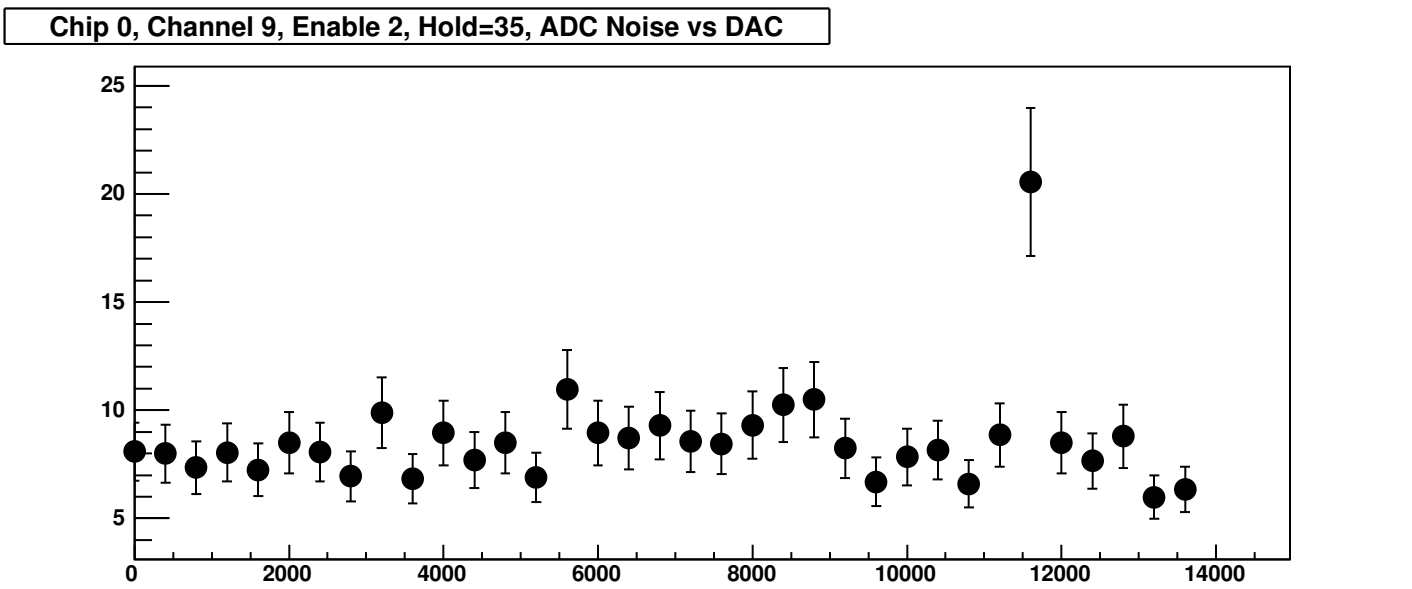

Chip 0, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

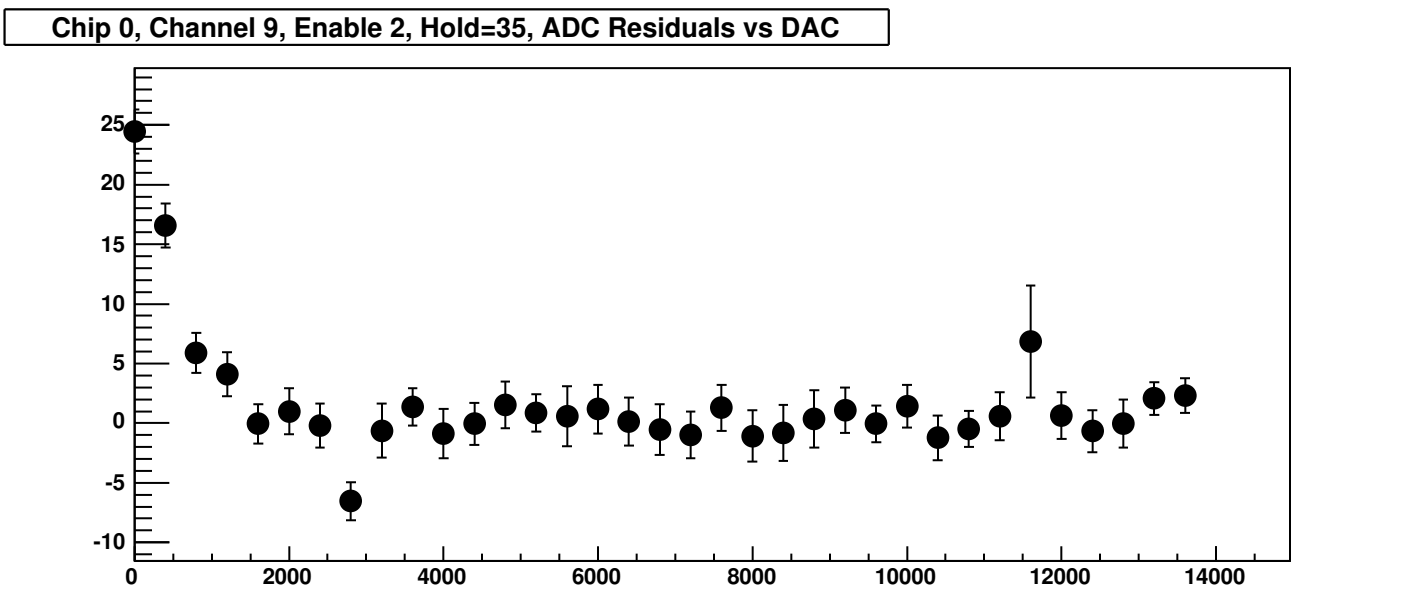

Chip 0, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 29.73 / 23
p0 56.5 ± 0.759
p1 0.02279 ± 0.0001163
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

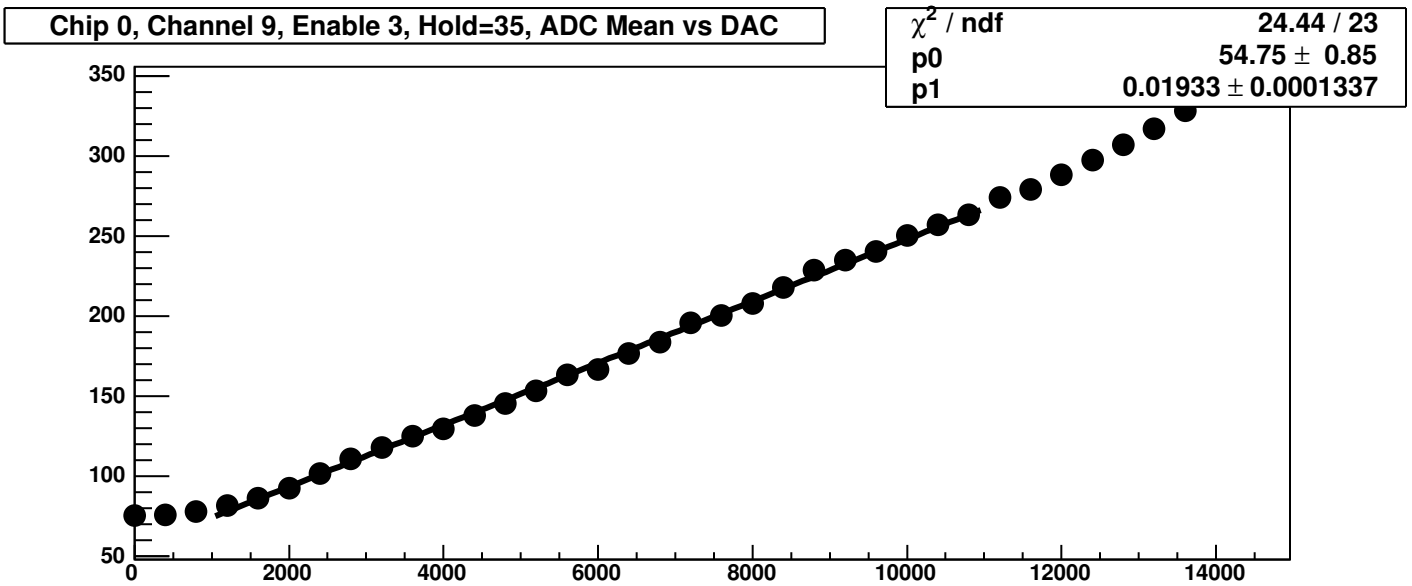

Chip 0, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

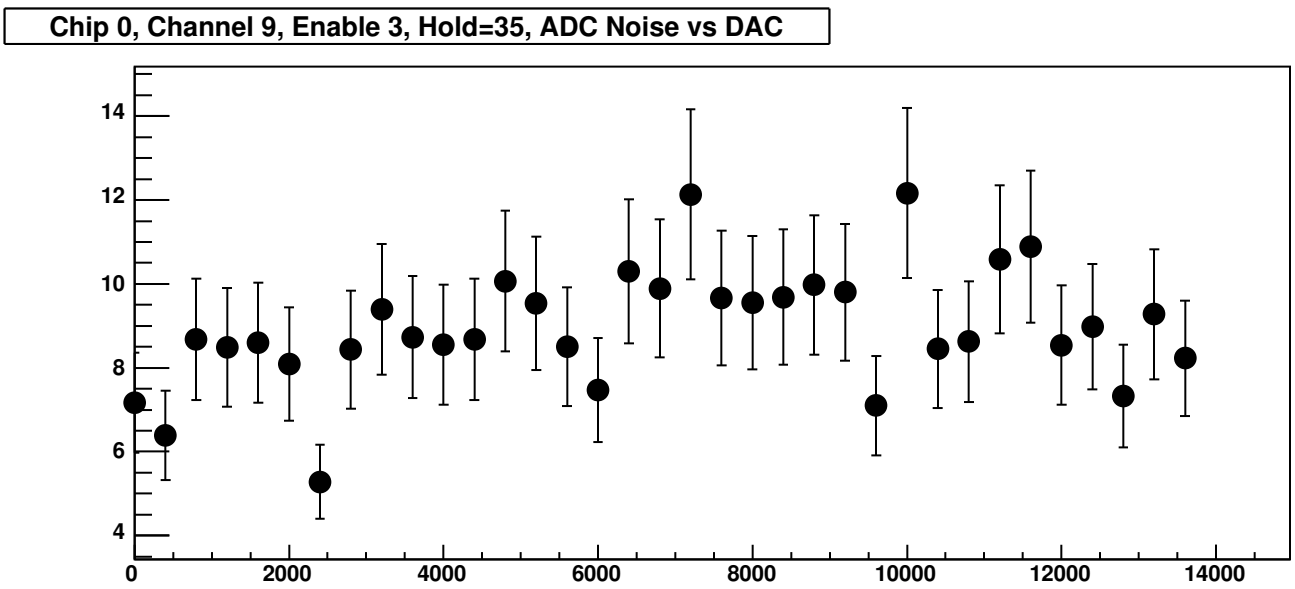

Chip 0, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

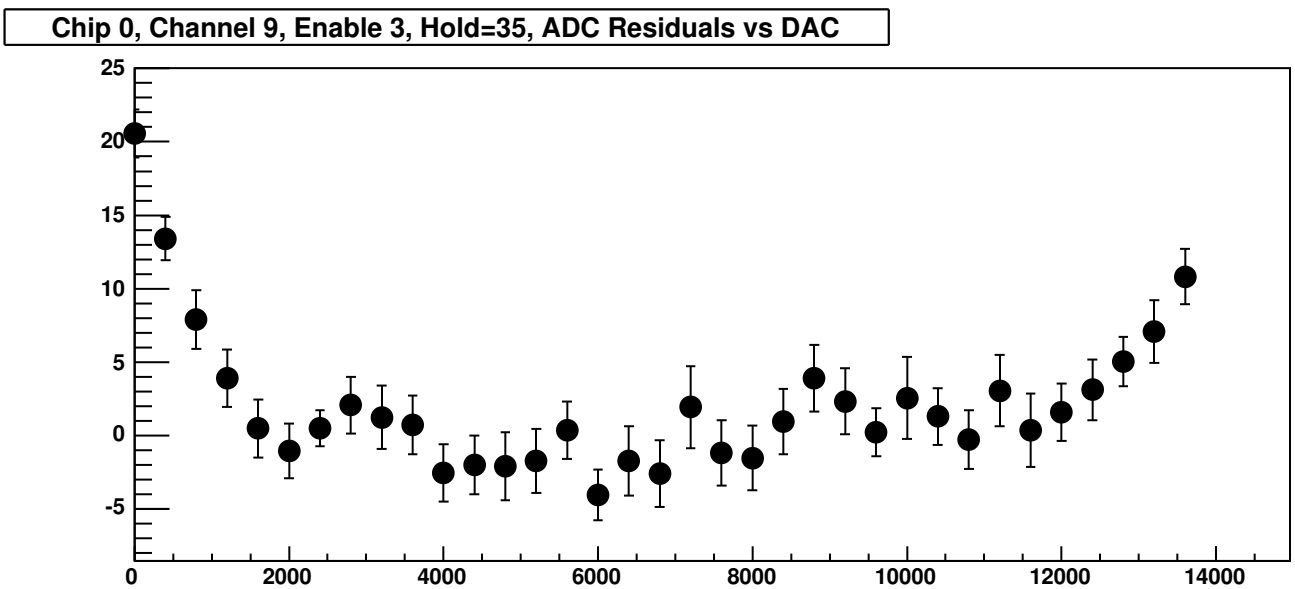

Chip 0, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 26.93 / 23
p0 56.28 ± 0.8279
p1 0.02091 ± 0.0001236
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

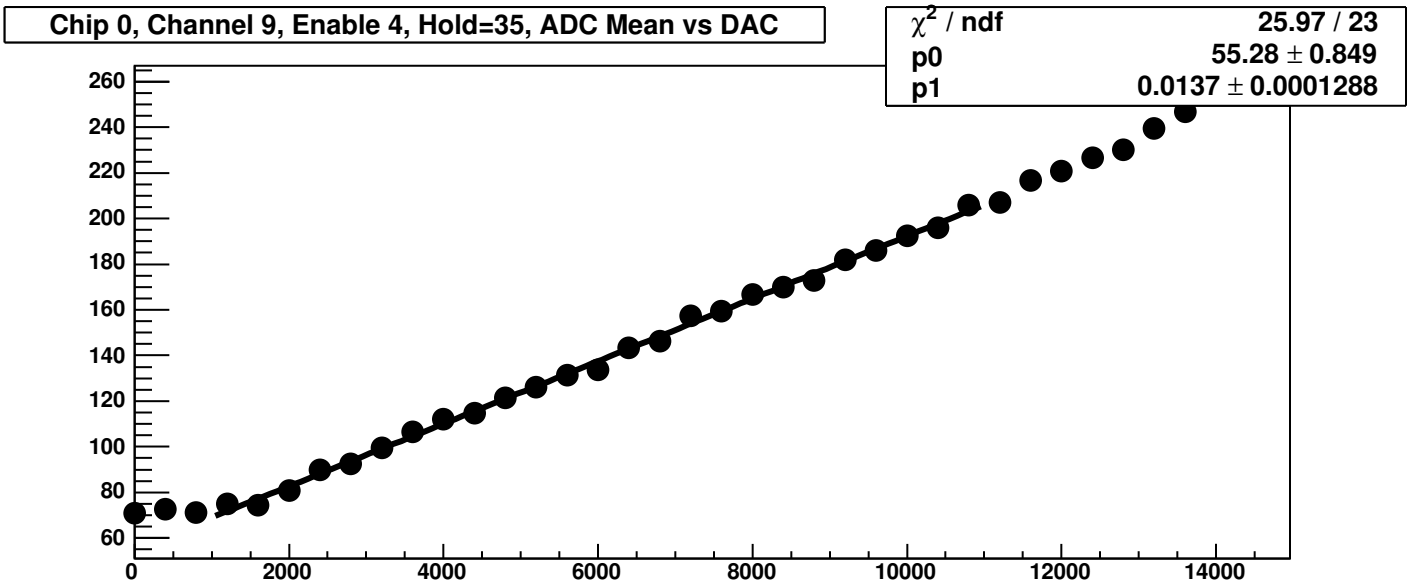

Chip 0, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

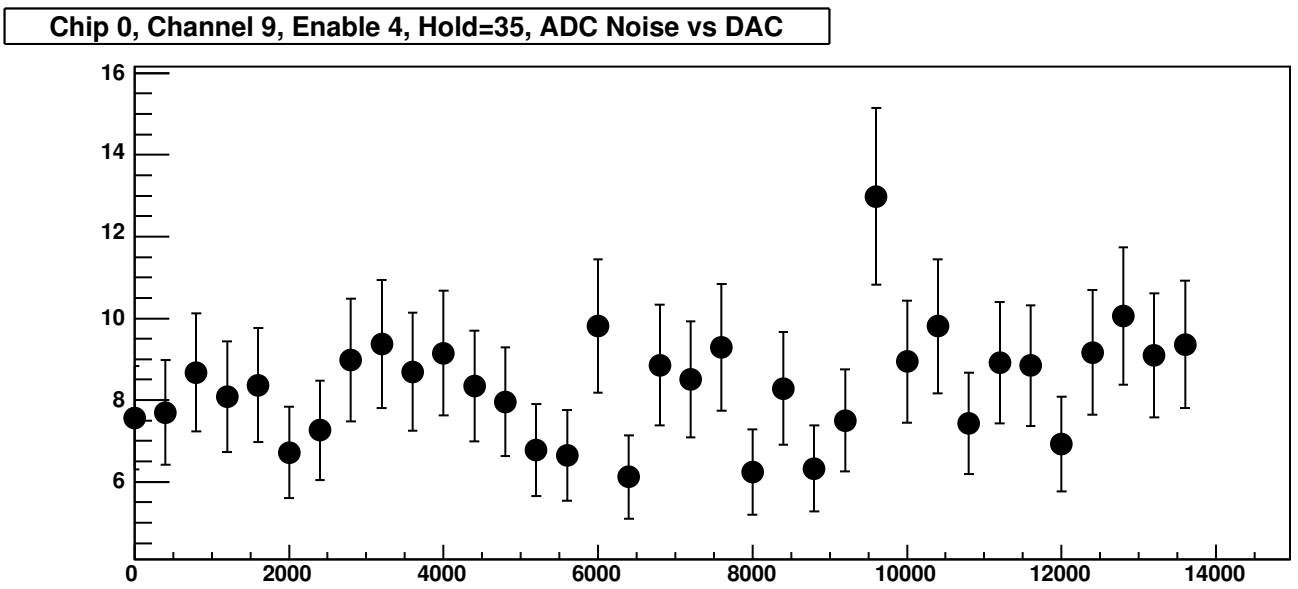

Chip 0, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

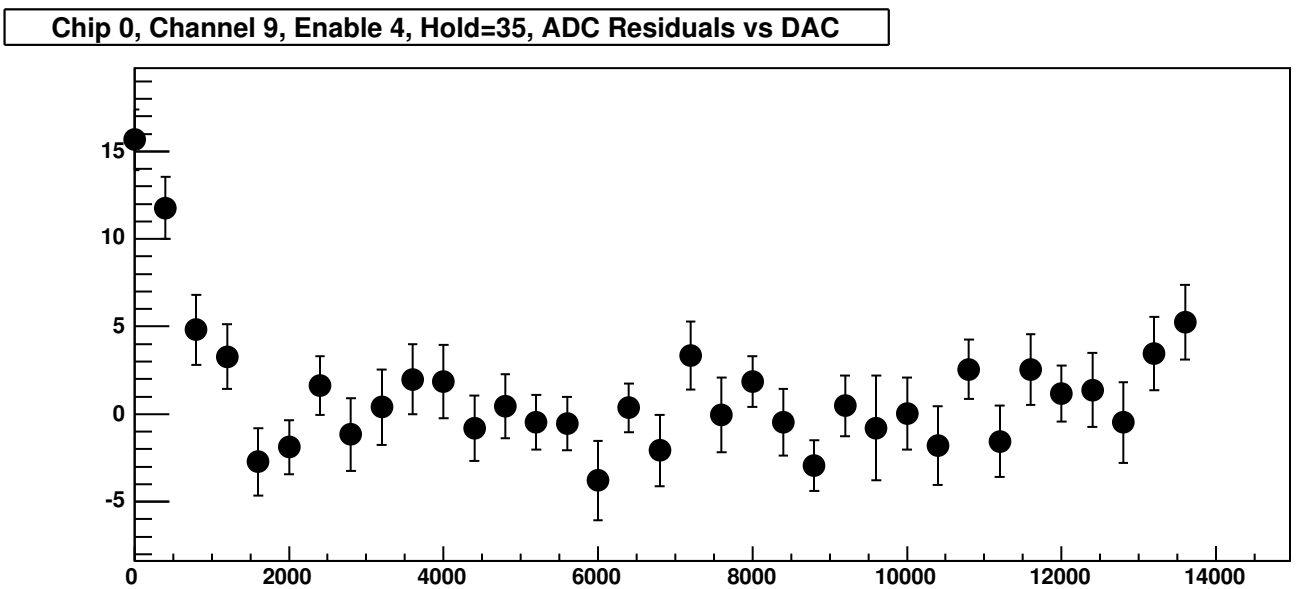

Chip 0, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.44 / 23
p0 54.75 ± 0.85
p1 0.01933 ± 0.0001337
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

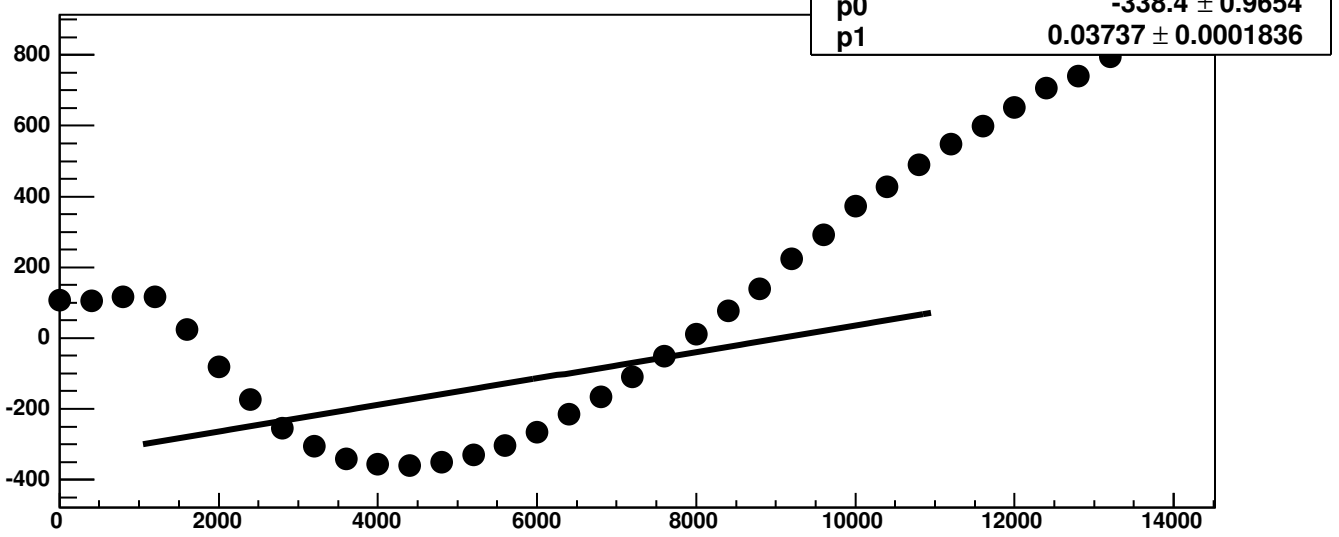

Chip 0, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 25.97 / 23
p0 55.28 ± 0.849
p1 0.0137 ± 0.0001288
260
240
220
200
180
160
140
120
100
80
60
0
2000
4000
6000
8000
10000
12000
14000

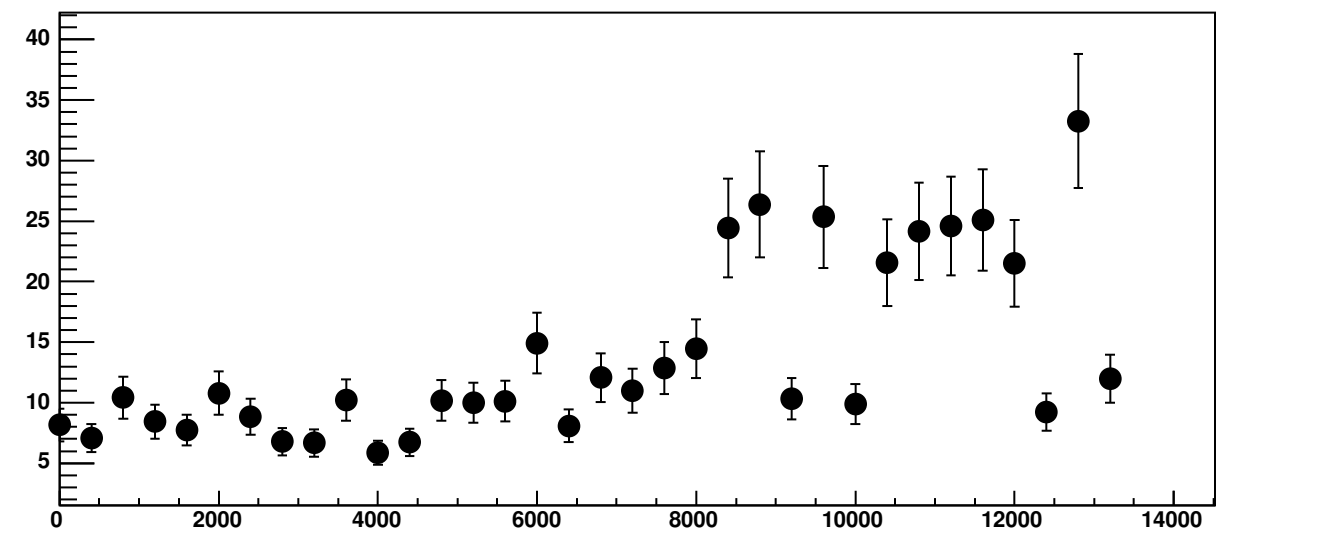

Chip 0, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

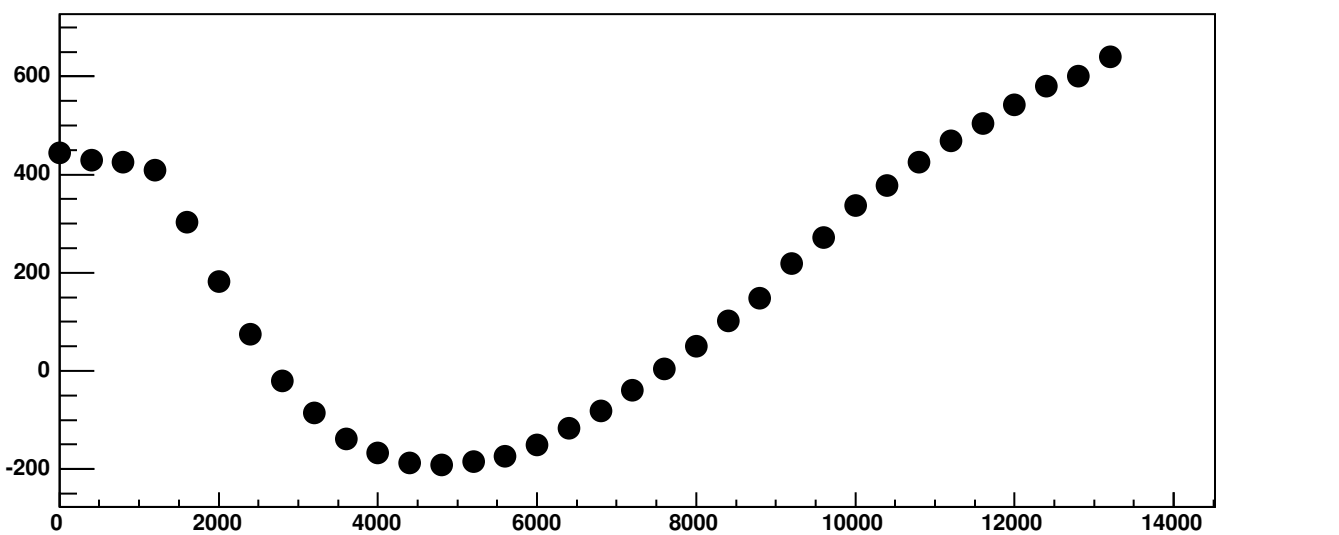
Chip 0, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

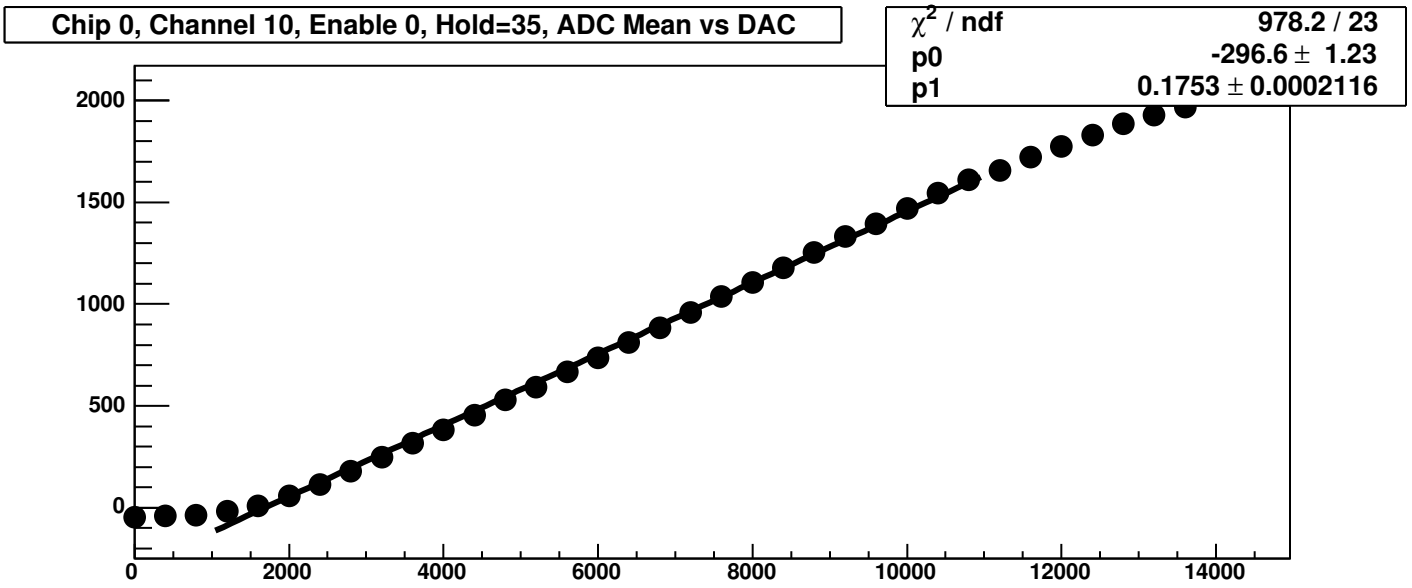


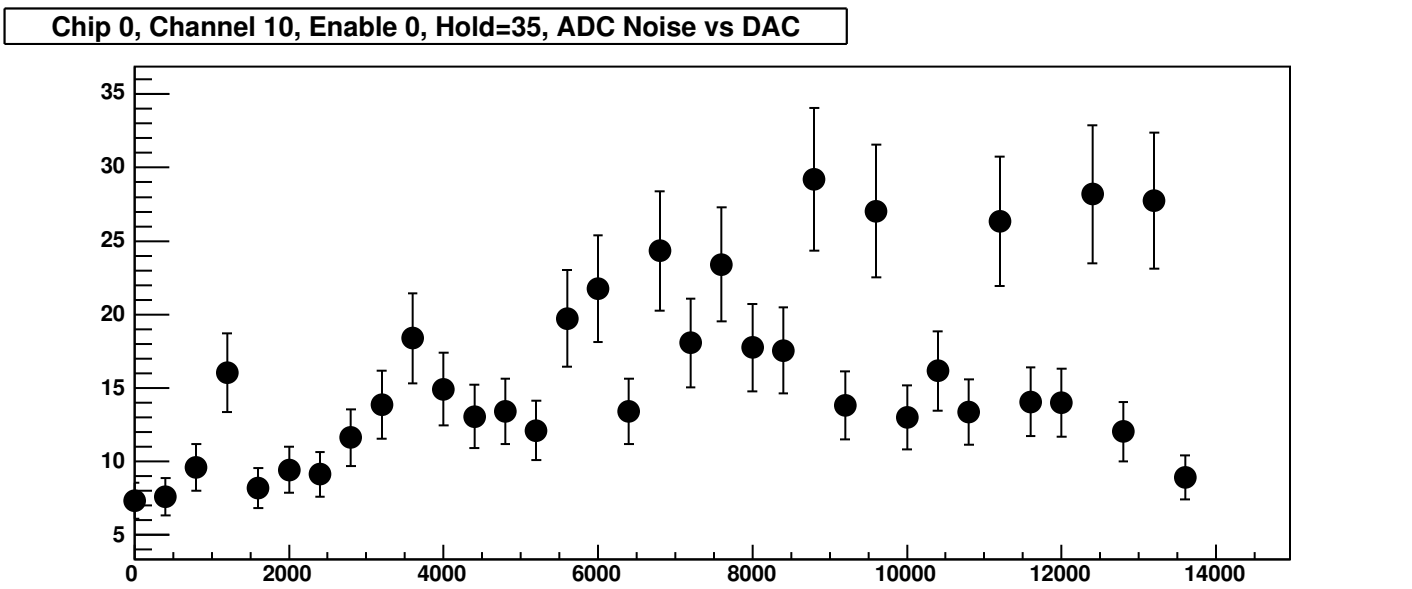
Chip 0, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

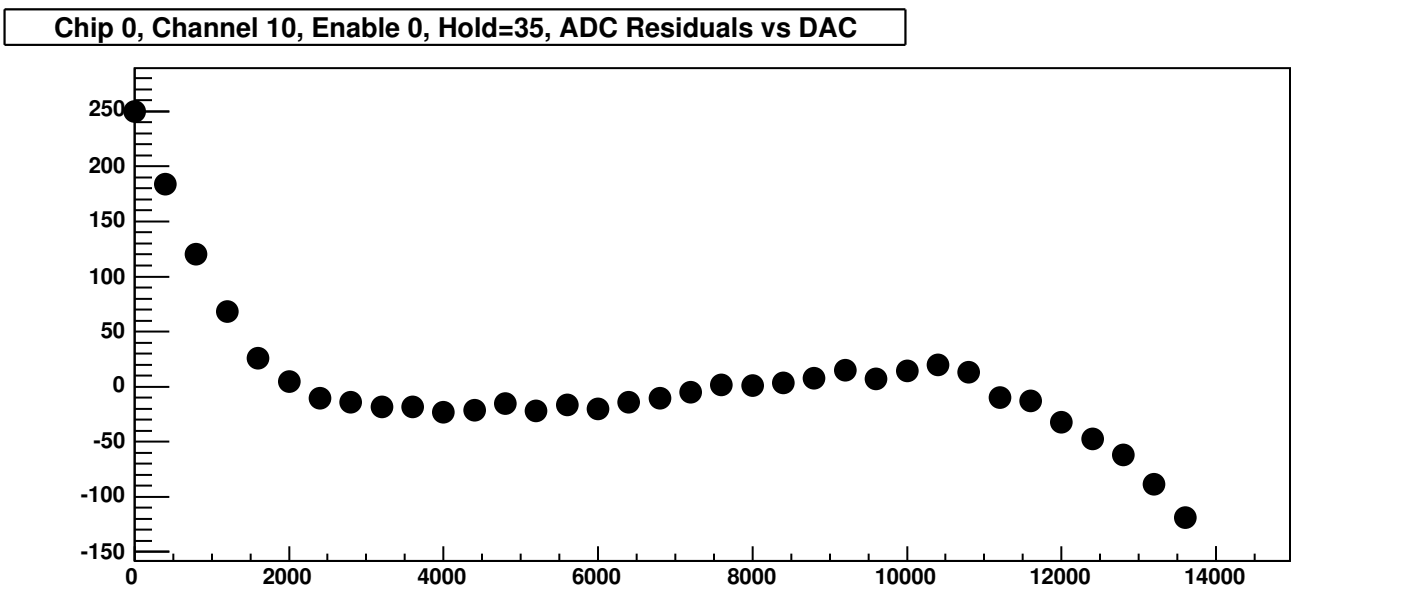


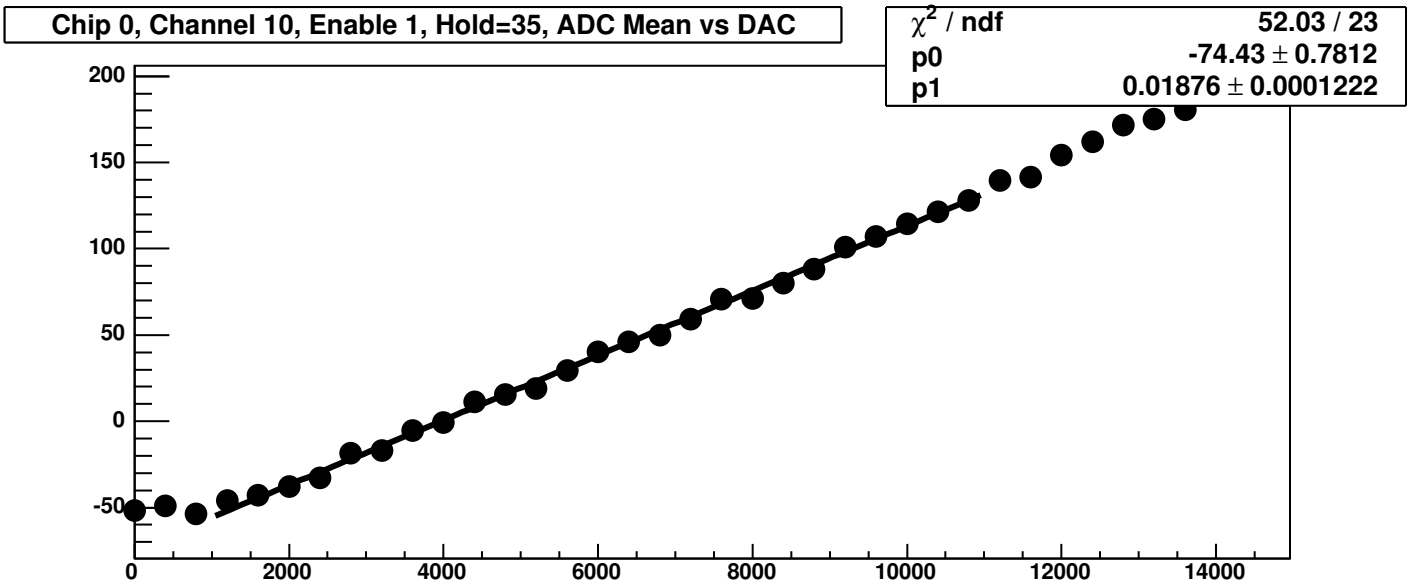
Chip 0, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

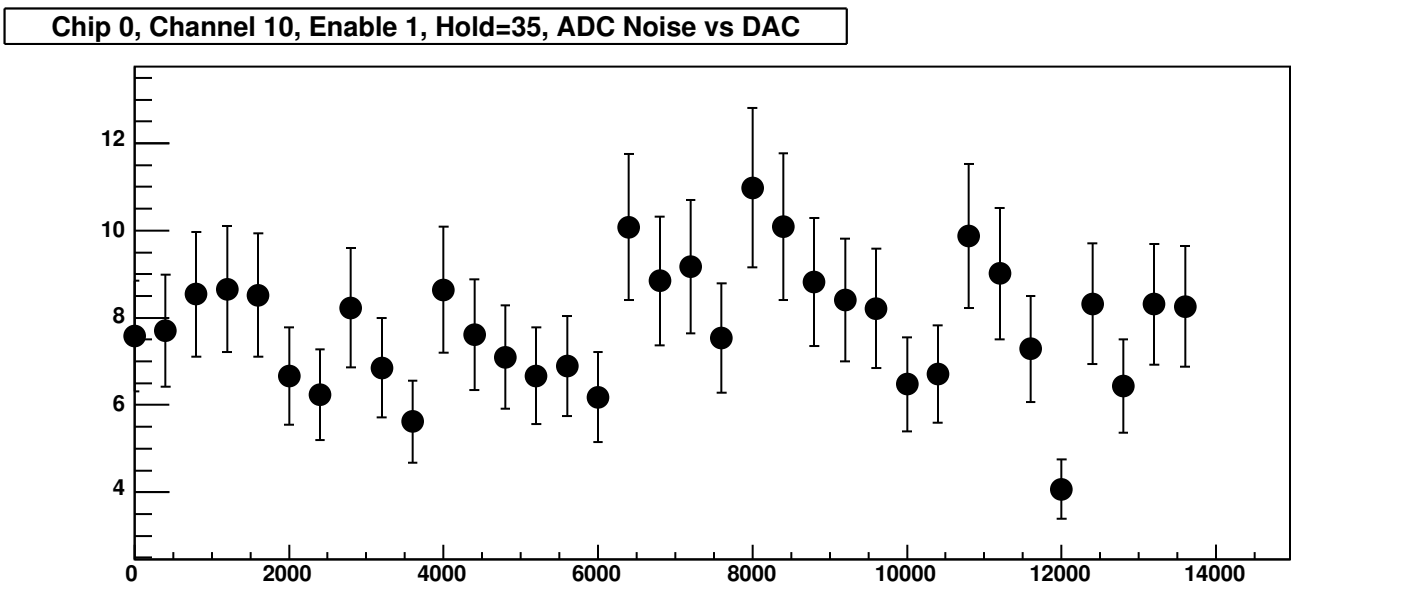


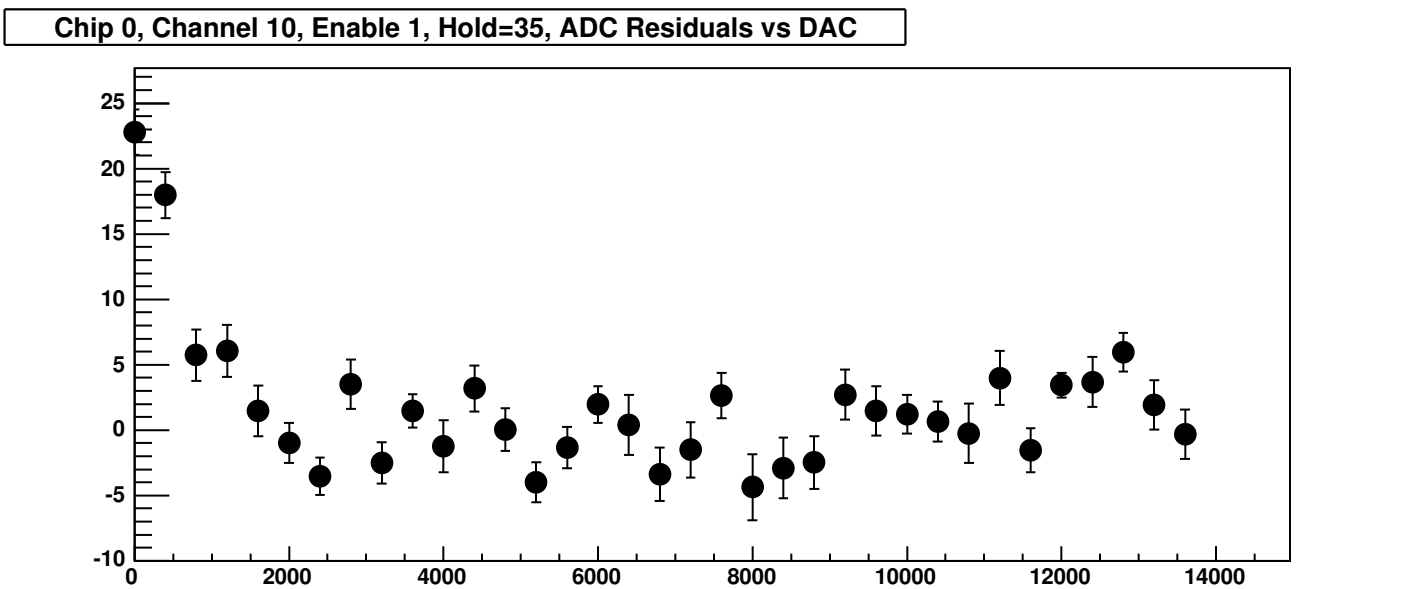

Chip 0, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 978.2 / 23
p0 -296.6 ± 1.23
p1 0.1753 ± 0.0002116
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

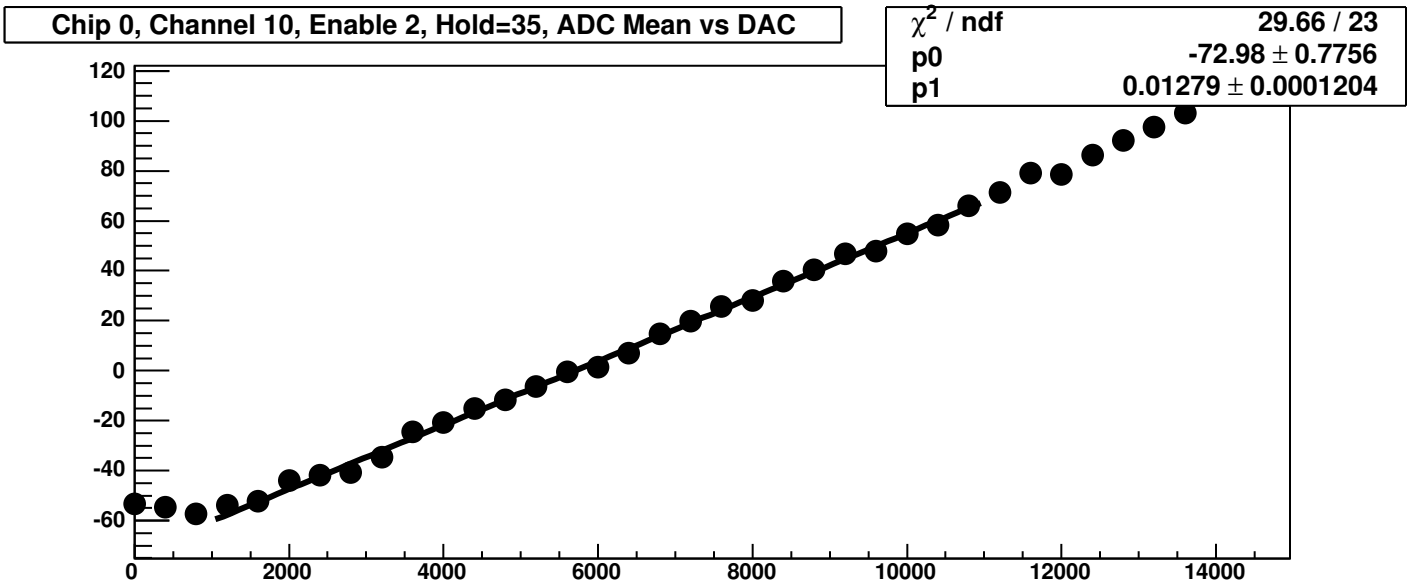

Chip 0, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

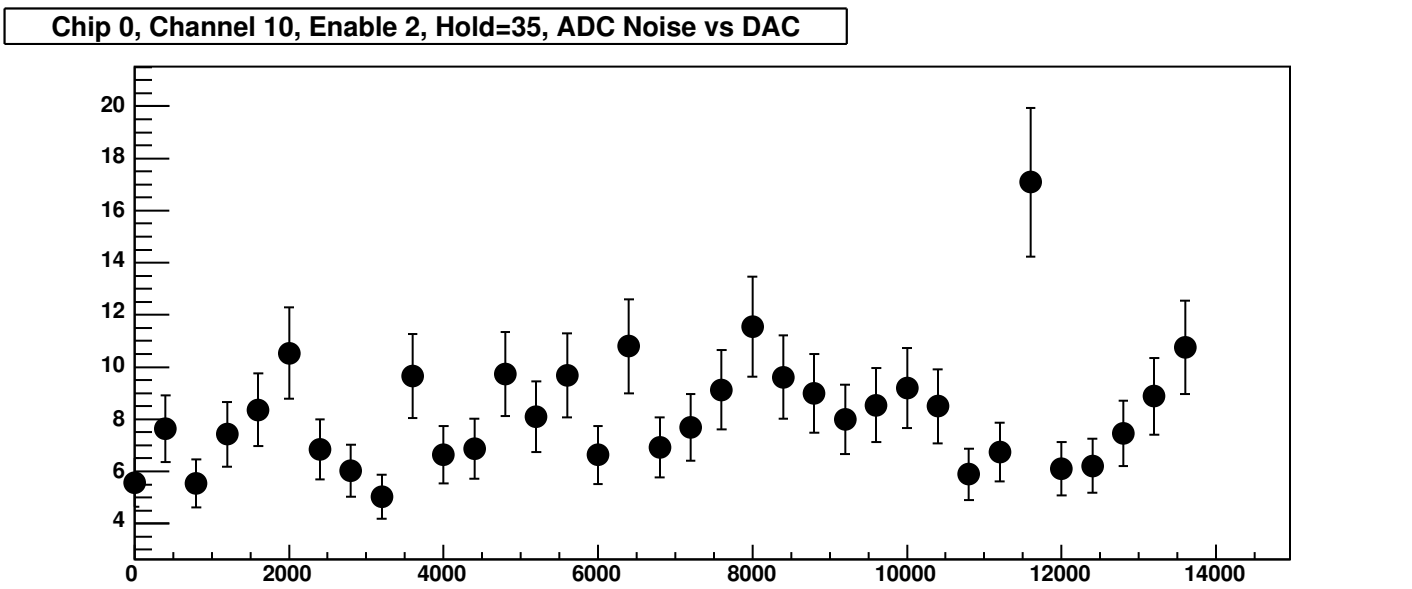

Chip 0, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

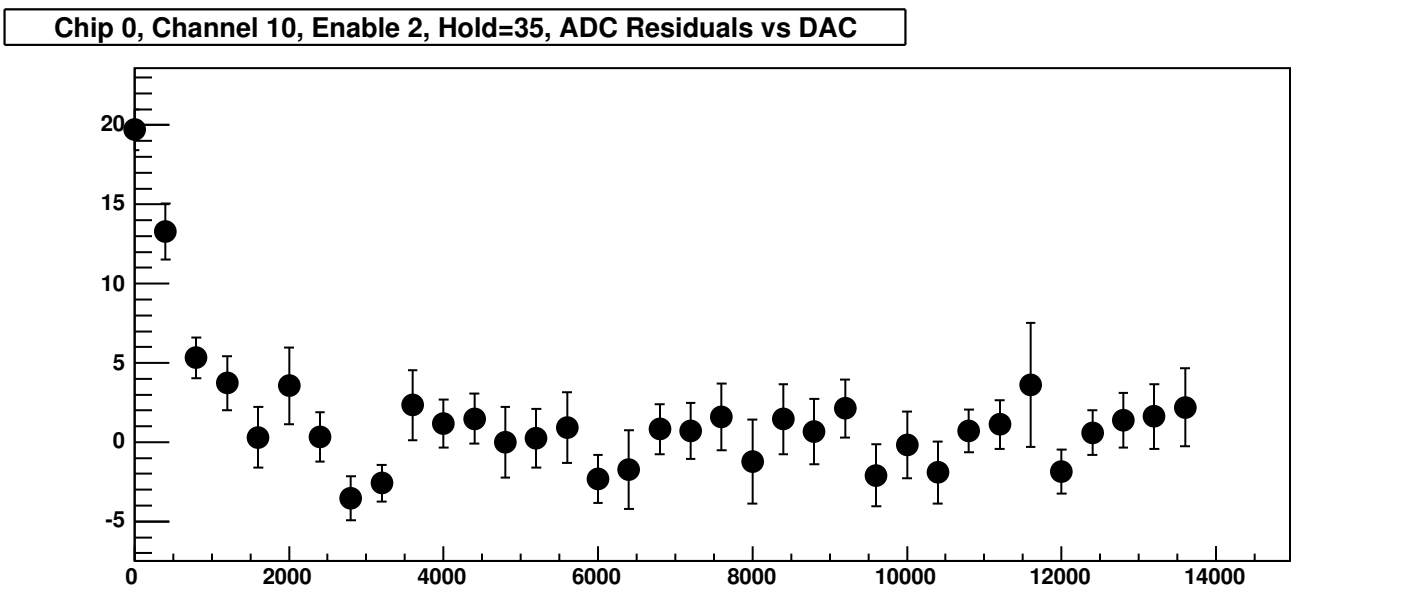

Chip 0, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 52.03 / 23
p0 -74.43 ± 0.7812
p1 0.01876 ± 0.0001222
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

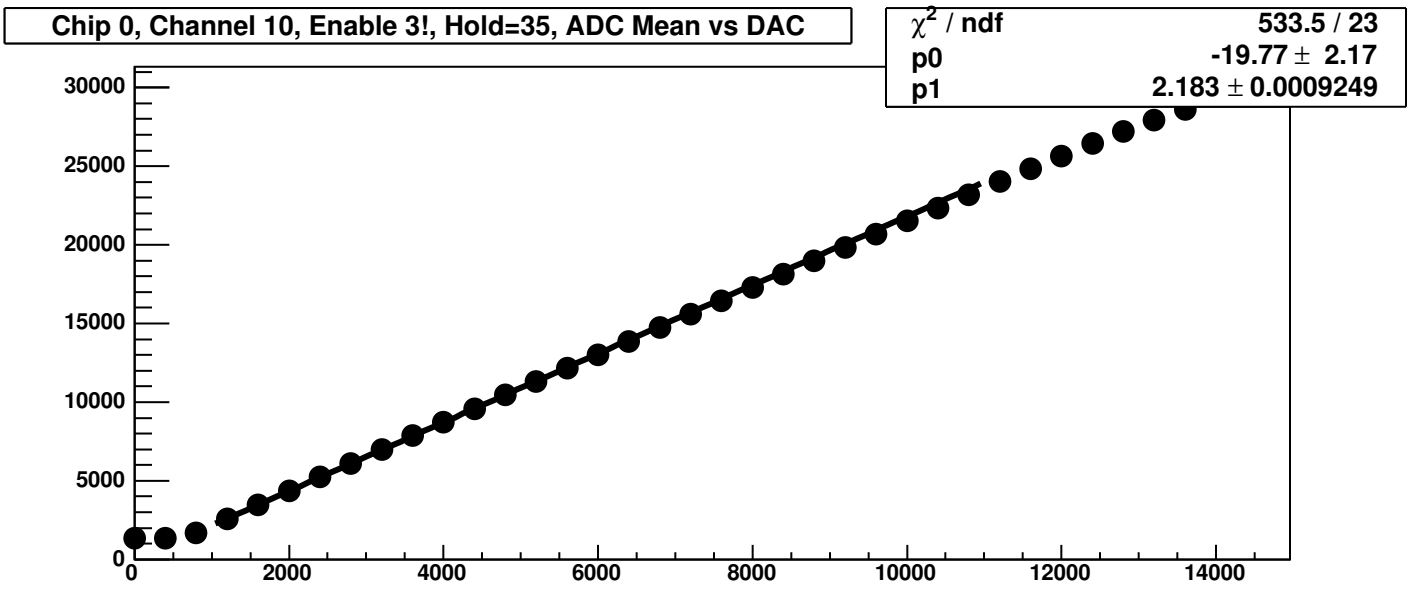

Chip 0, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

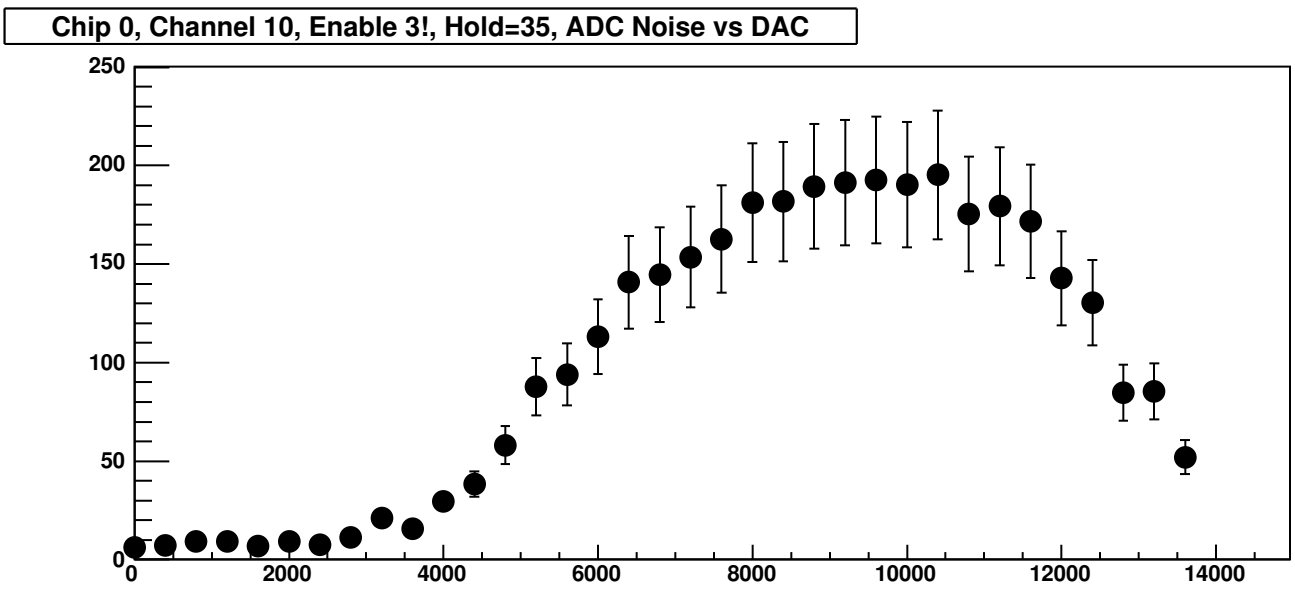

Chip 0, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

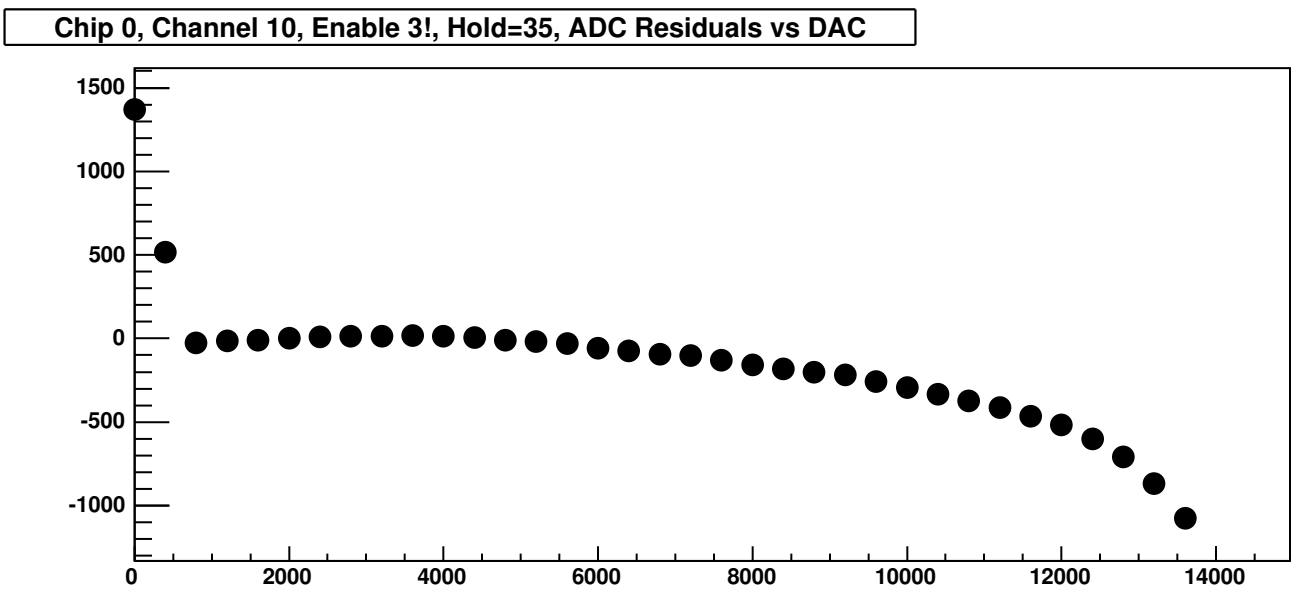

Chip 0, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 29.66 / 23
p0 -72.98 ± 0.7756
p1 0.01279 ± 0.0001204
120
100
80
60
40
20
0
-20
-40
-60
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

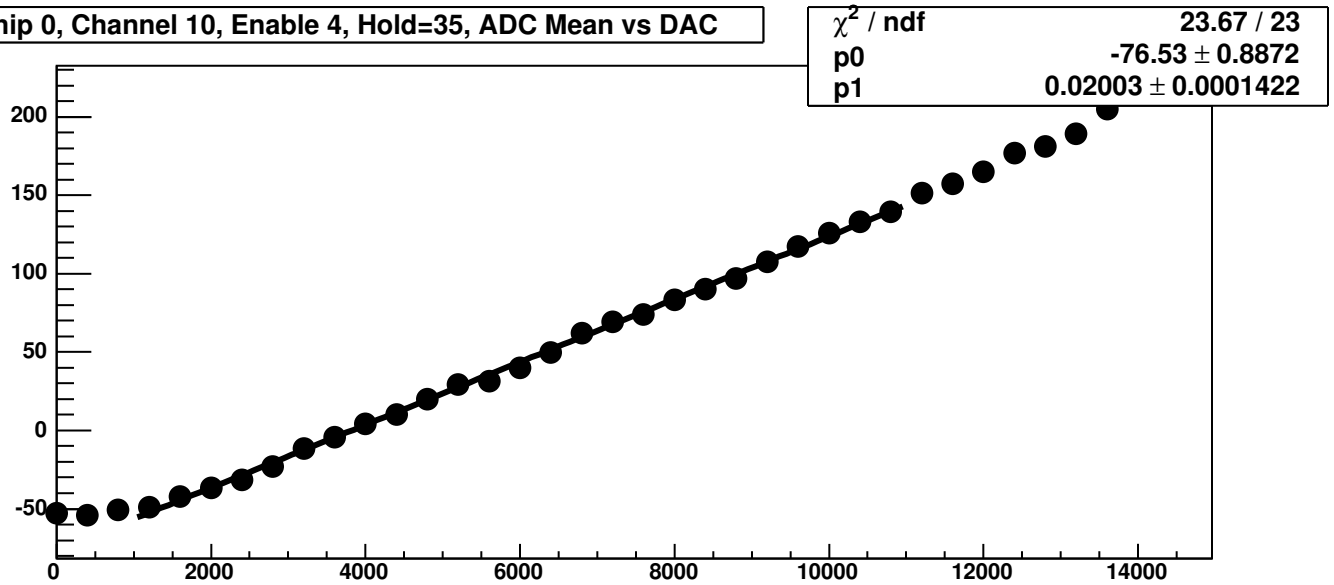

Chip 0, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 533.5 / 23
p0 -19.77 ± 2.17
p1 2.183 ± 0.0009249
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

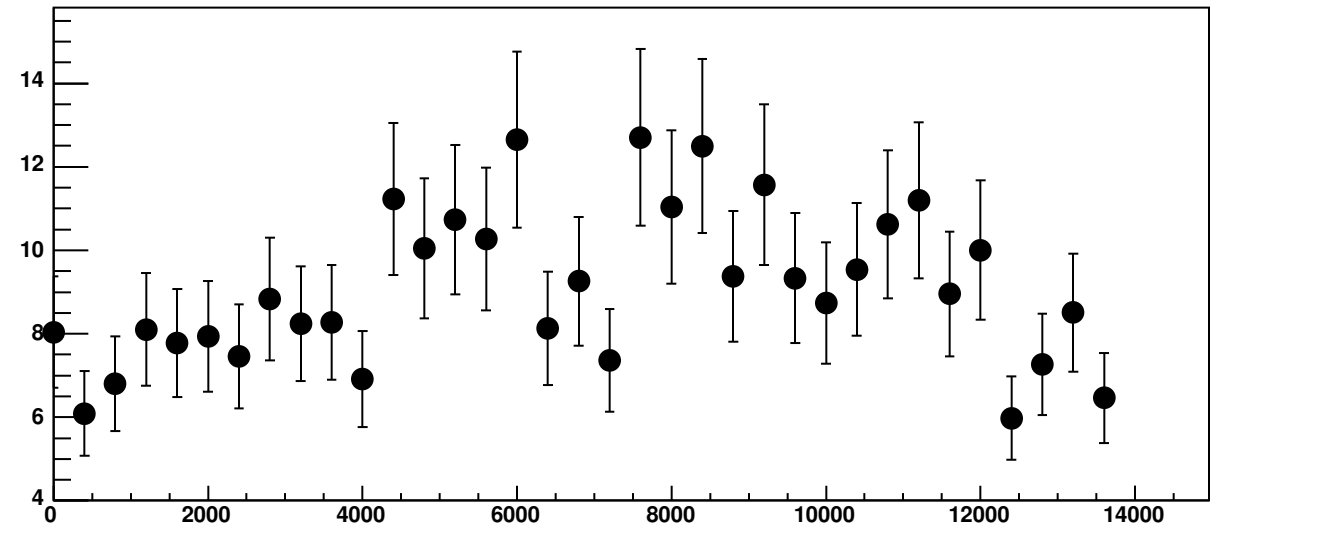

Chip 0, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

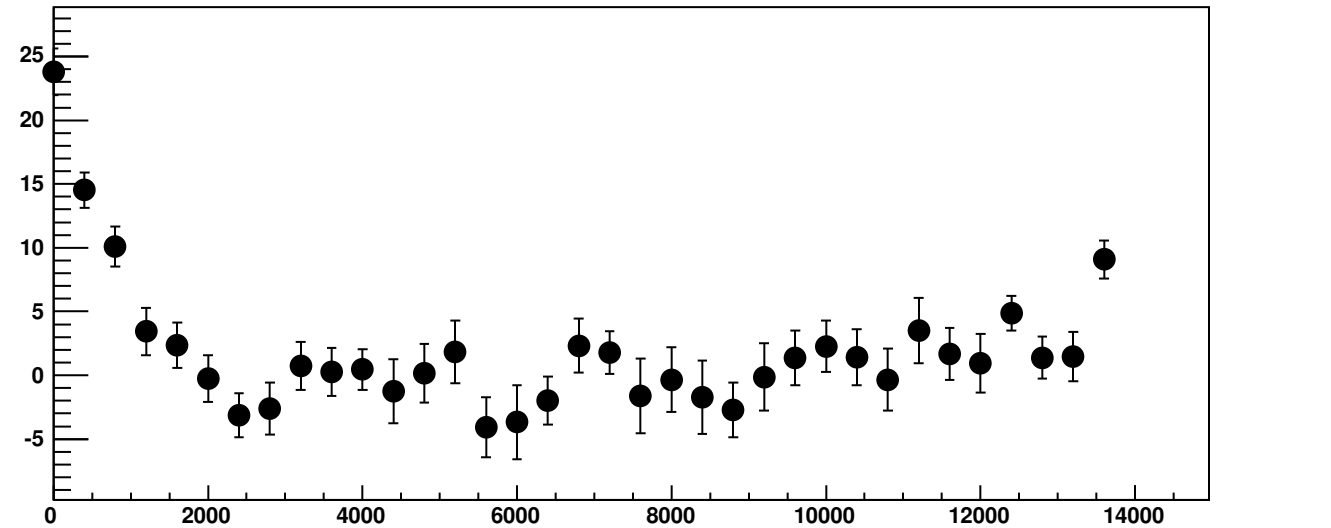
Chip 0, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

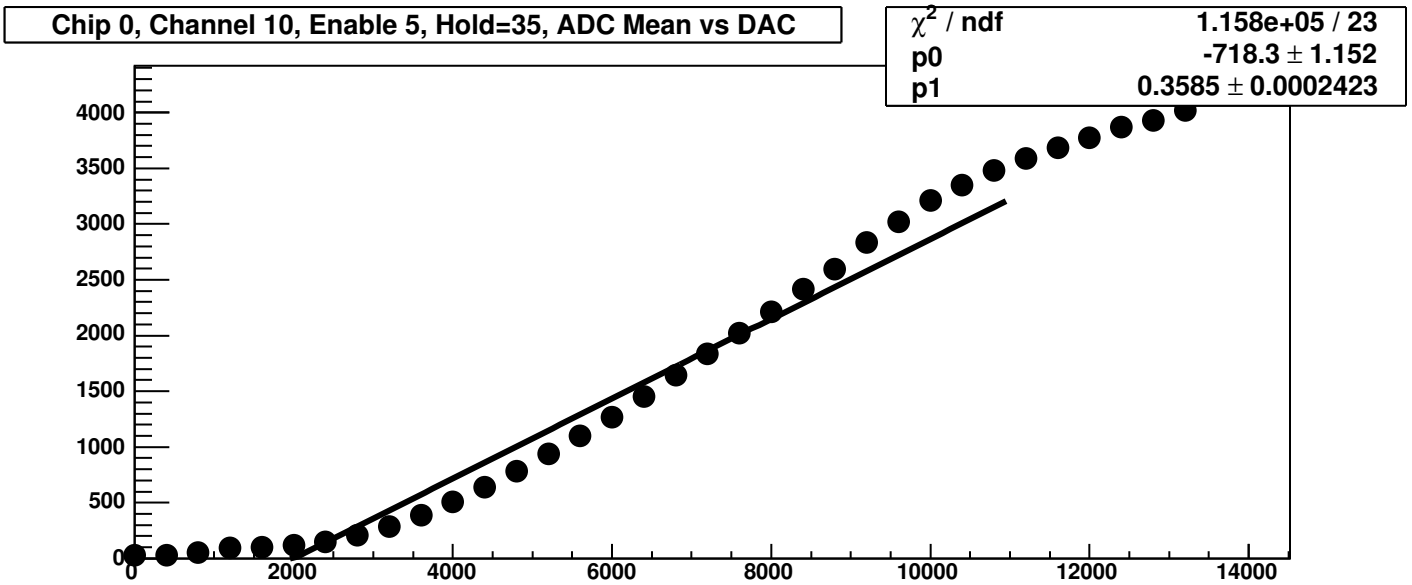


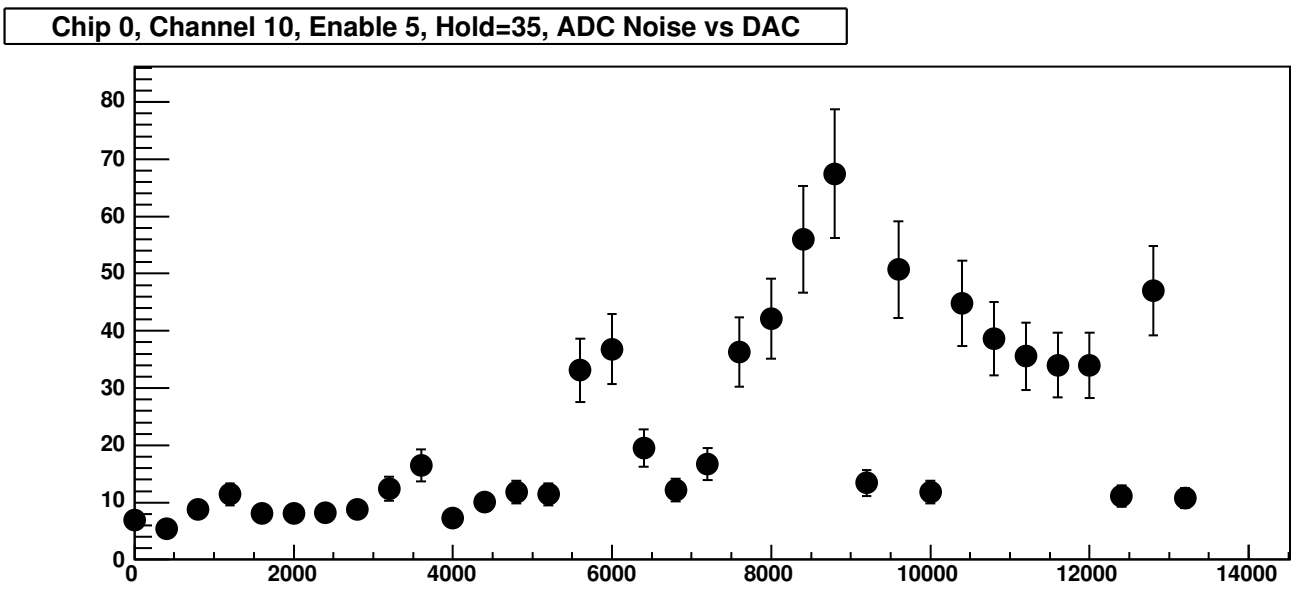
Chip 0, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

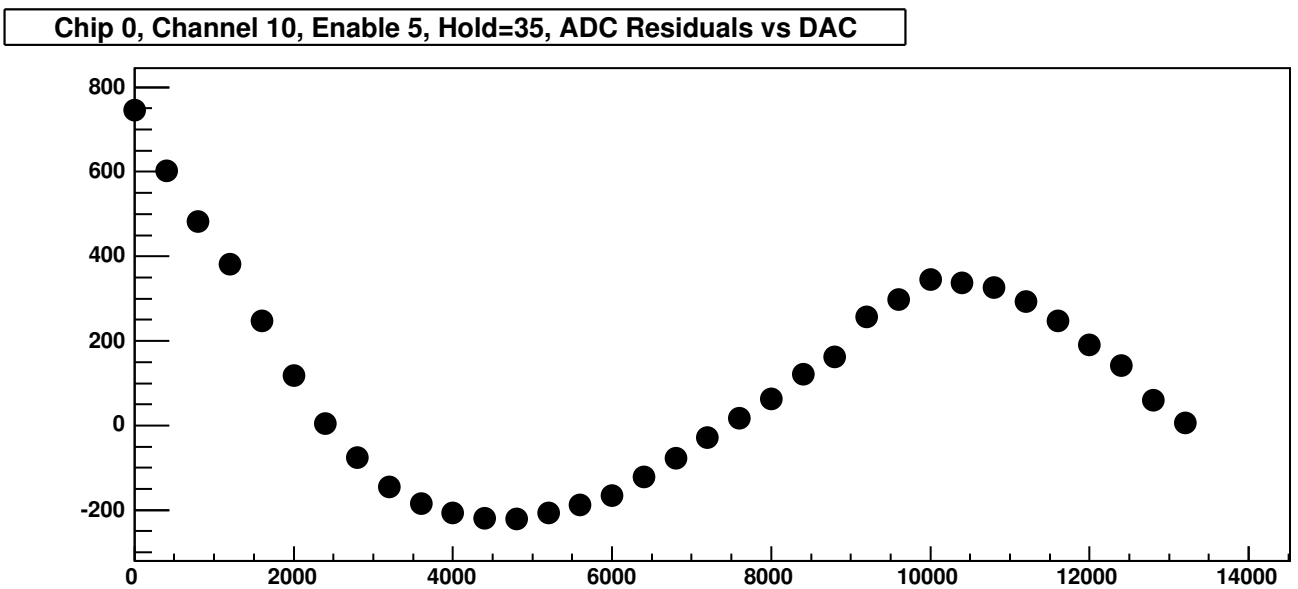


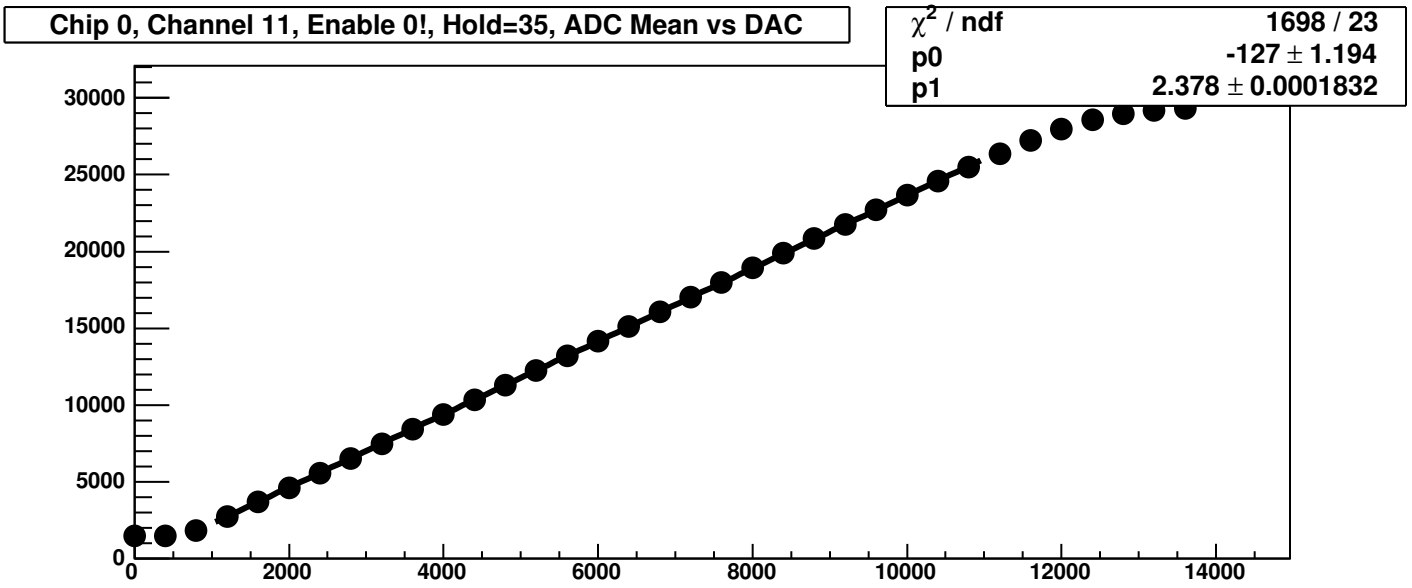
Chip 0, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

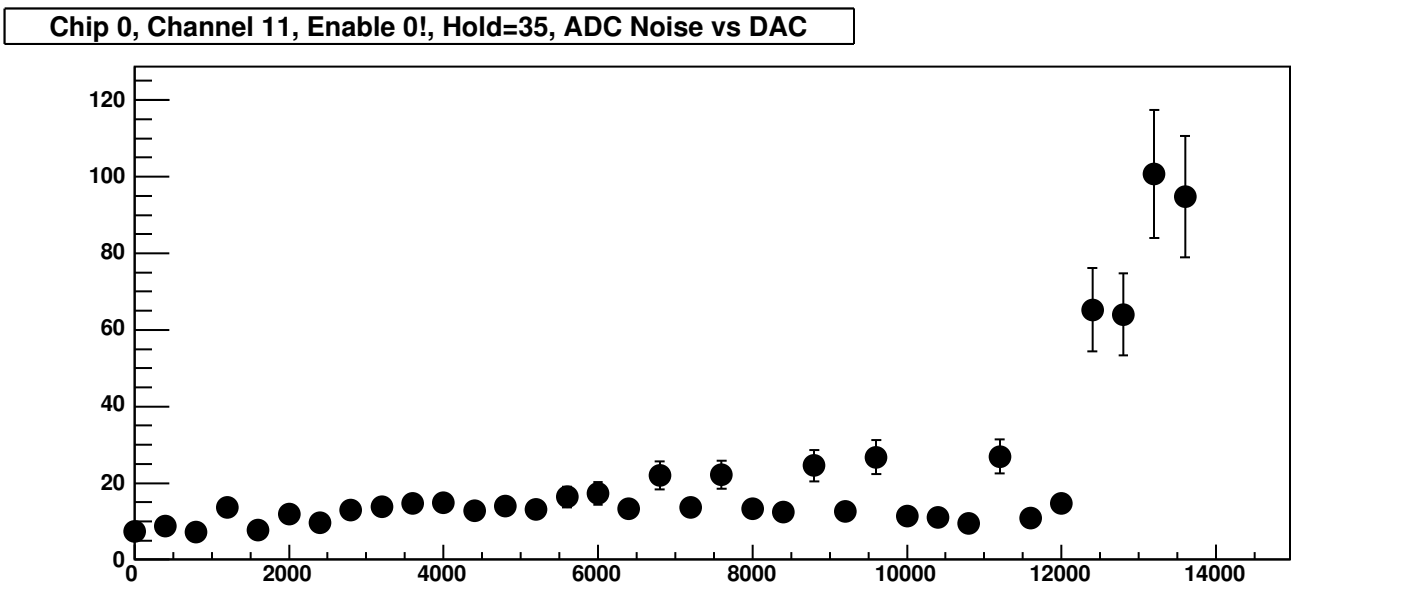


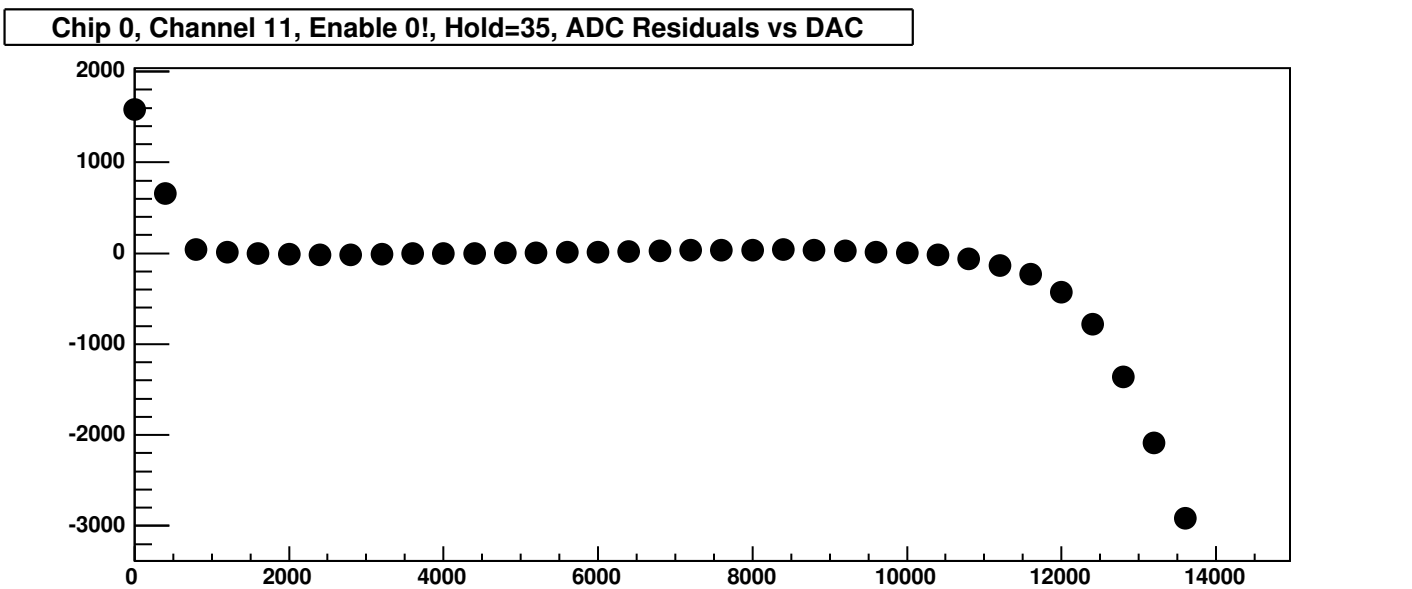

Chip 0, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.158e+05 / 23
p0 -718.3 ± 1.152
p1 0.3585 ± 0.0002423
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

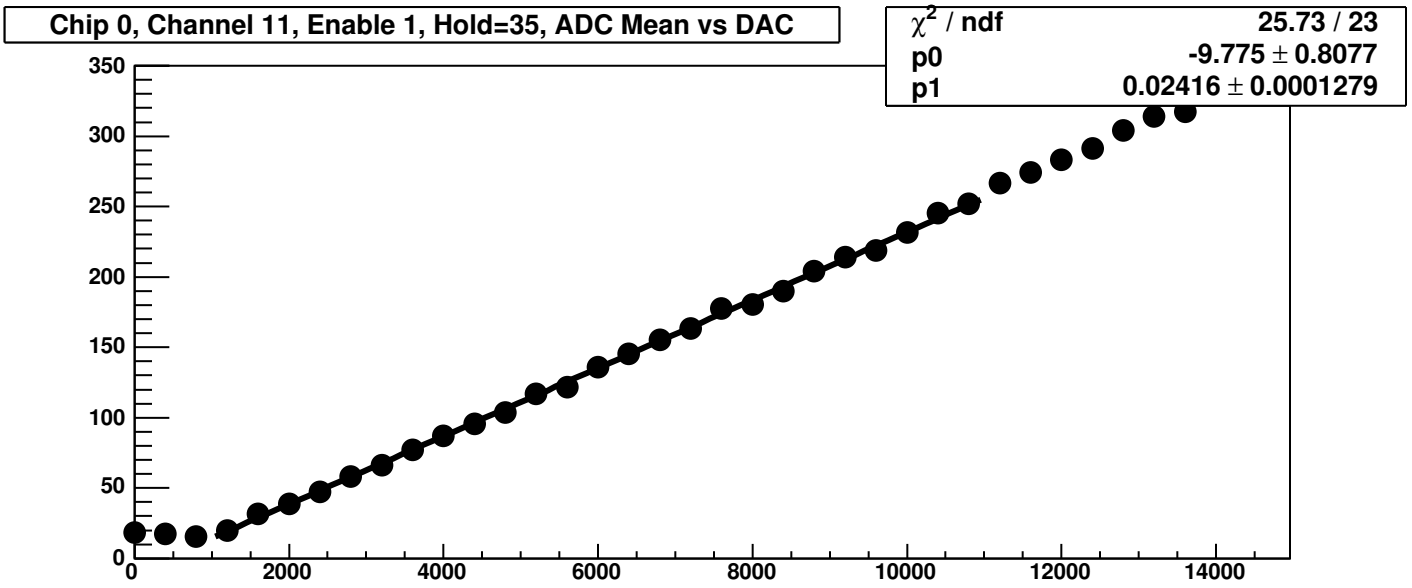

Chip 0, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

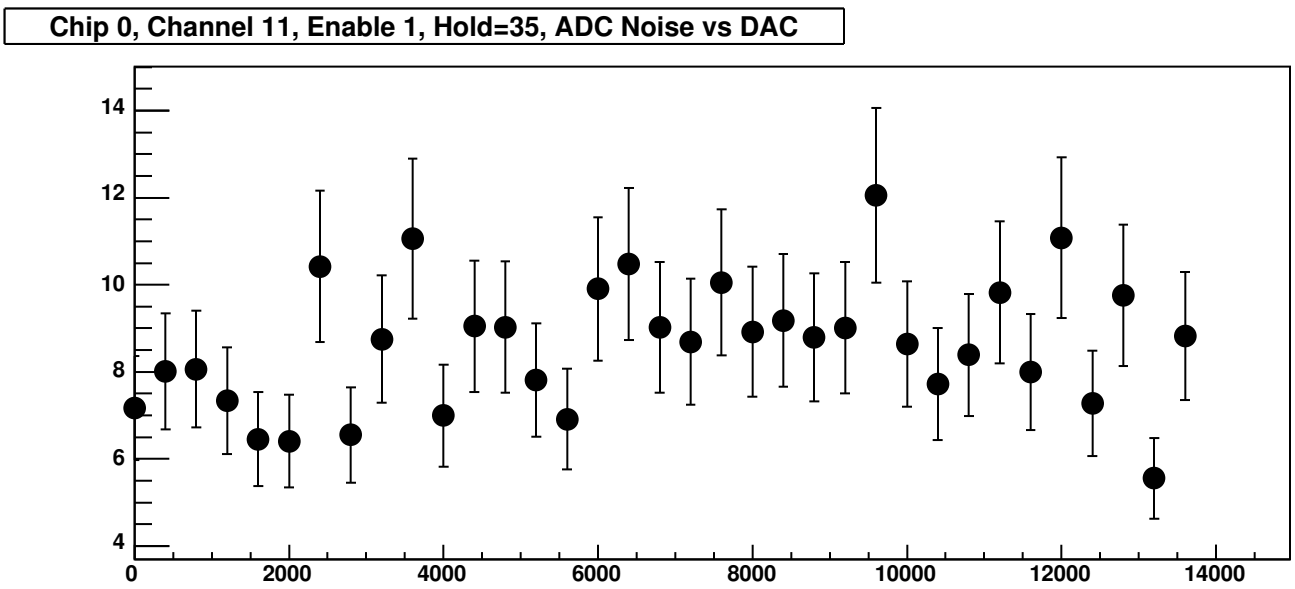

Chip 0, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

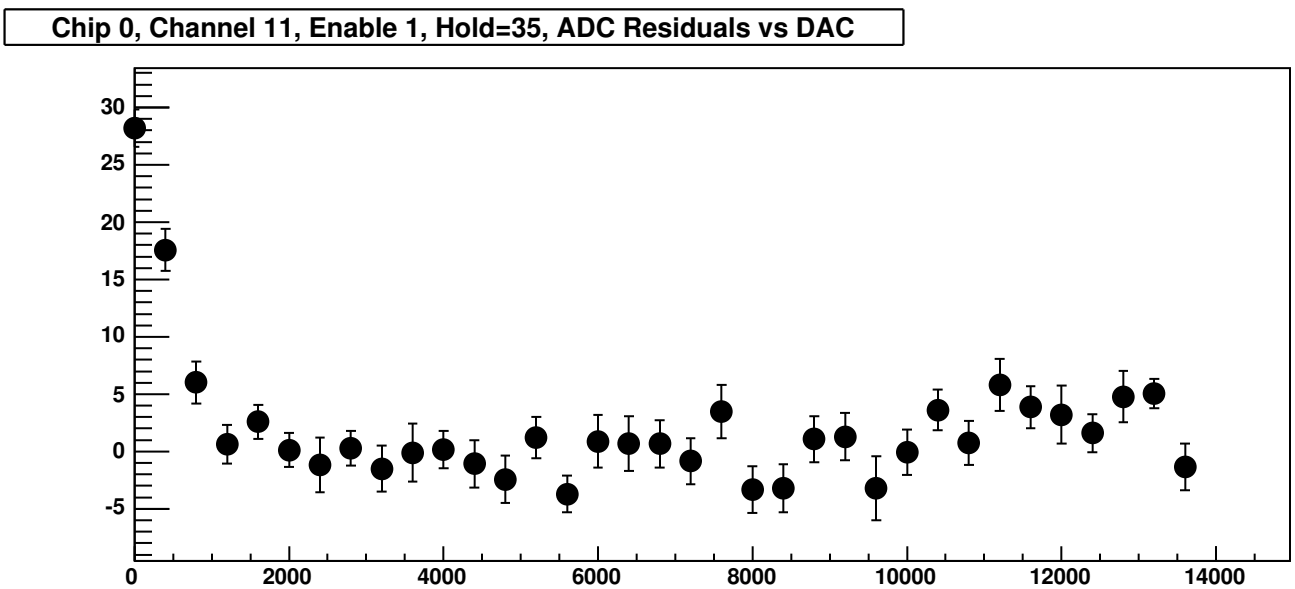

Chip 0, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1698 / 23
p0 -127 ± 1.194
p1 2.378 ± 0.0001832
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

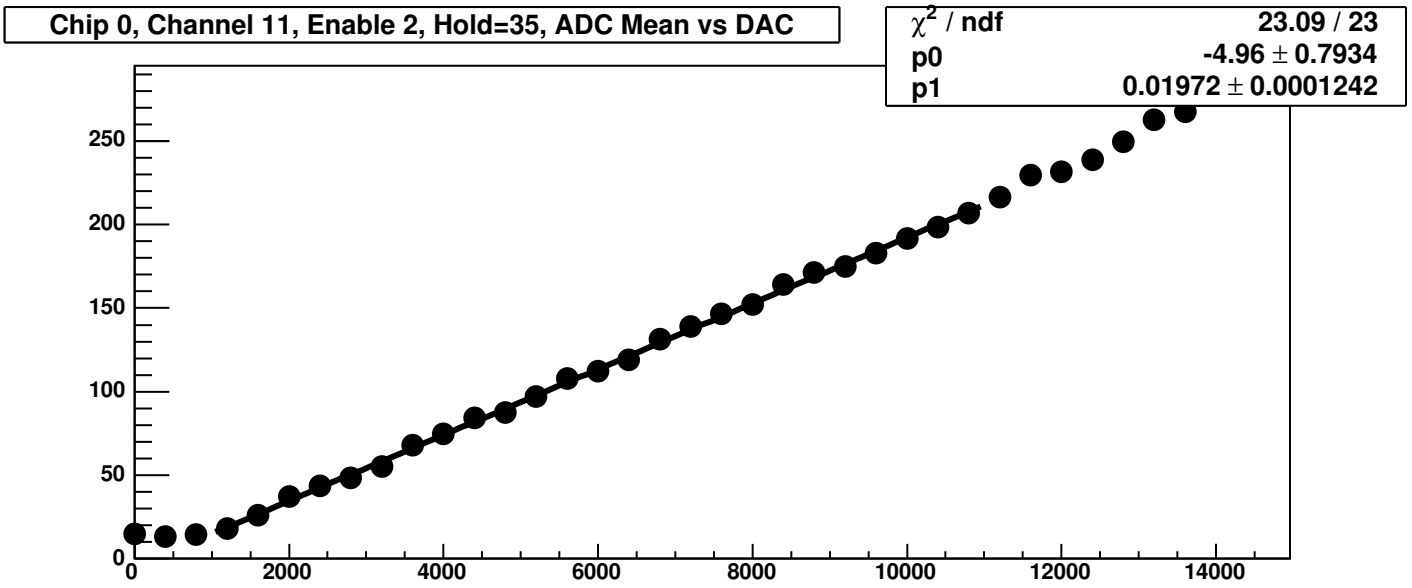

Chip 0, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

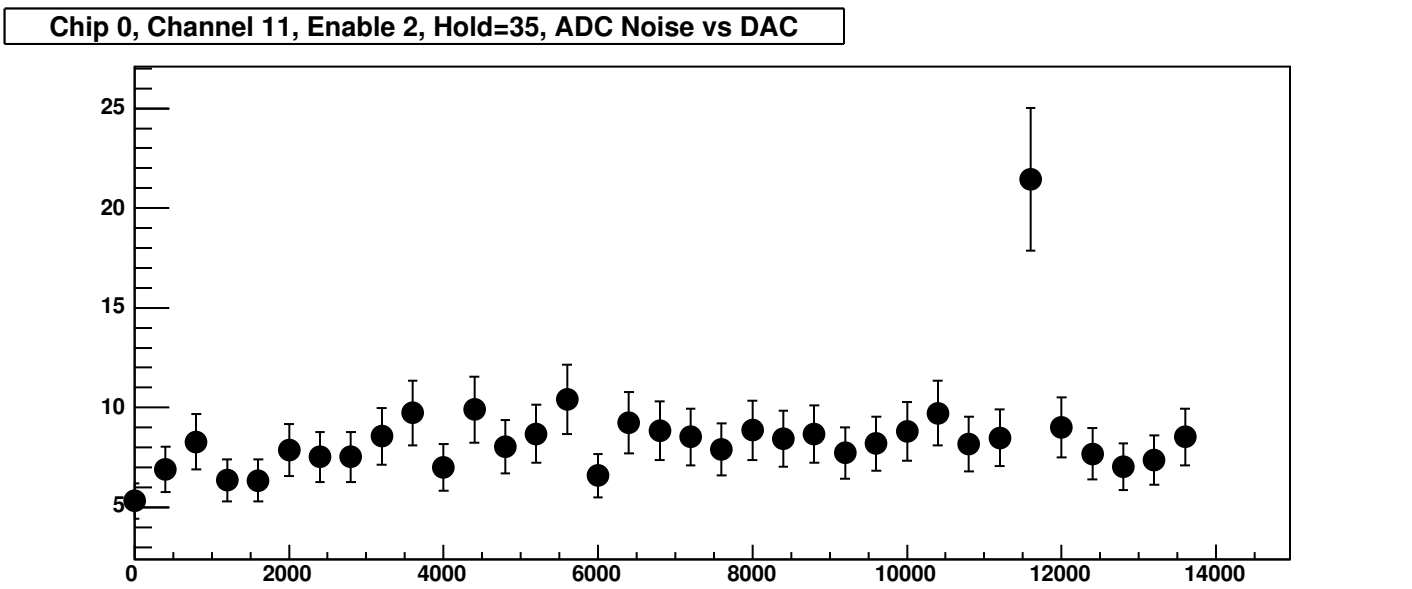

Chip 0, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

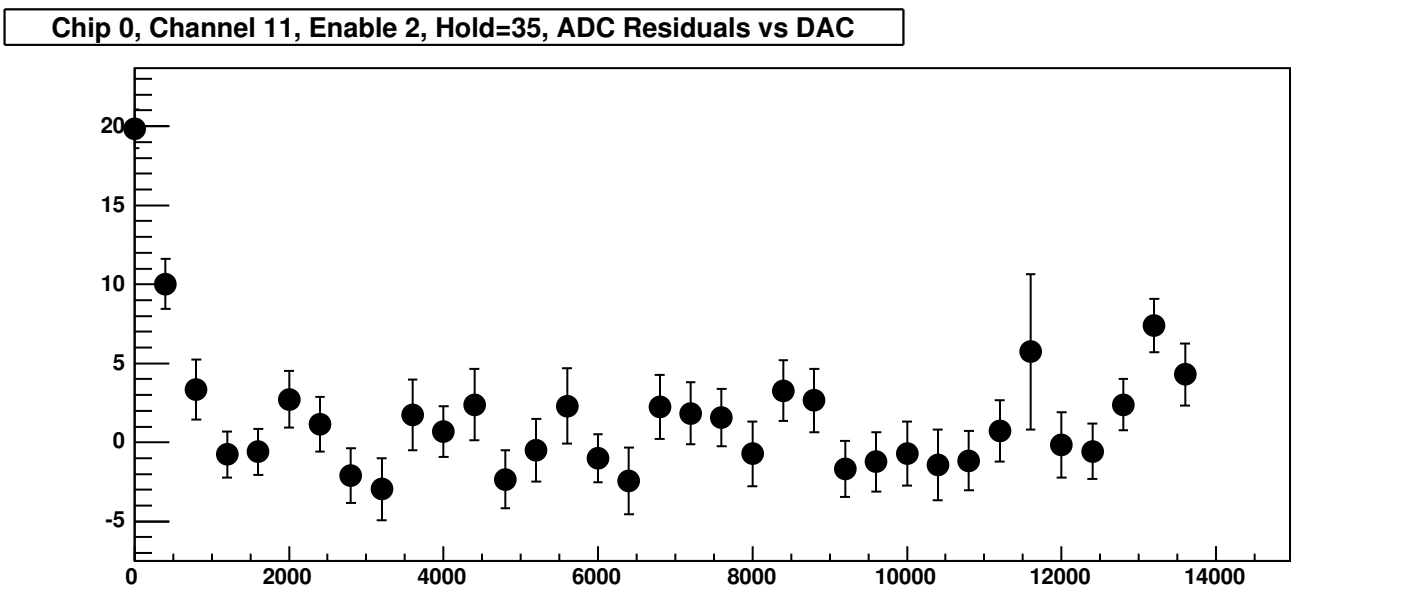

Chip 0, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 25.73 / 23
p0 -9.775 ± 0.8077
p1 0.02416 ± 0.0001279
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

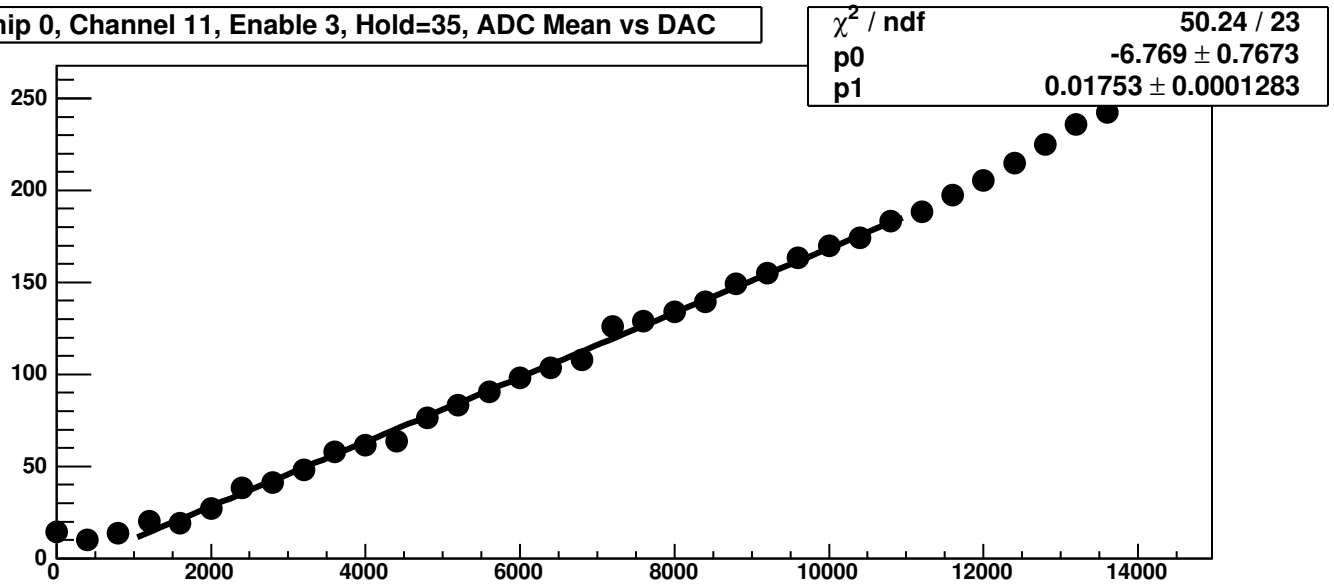

Chip 0, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 23.09 / 23
p0 -4.96 ± 0.7934
p1 0.01972 ± 0.0001242
0
50
100
150
200
250
0
2000
4000
6000
8000
10000
12000
14000

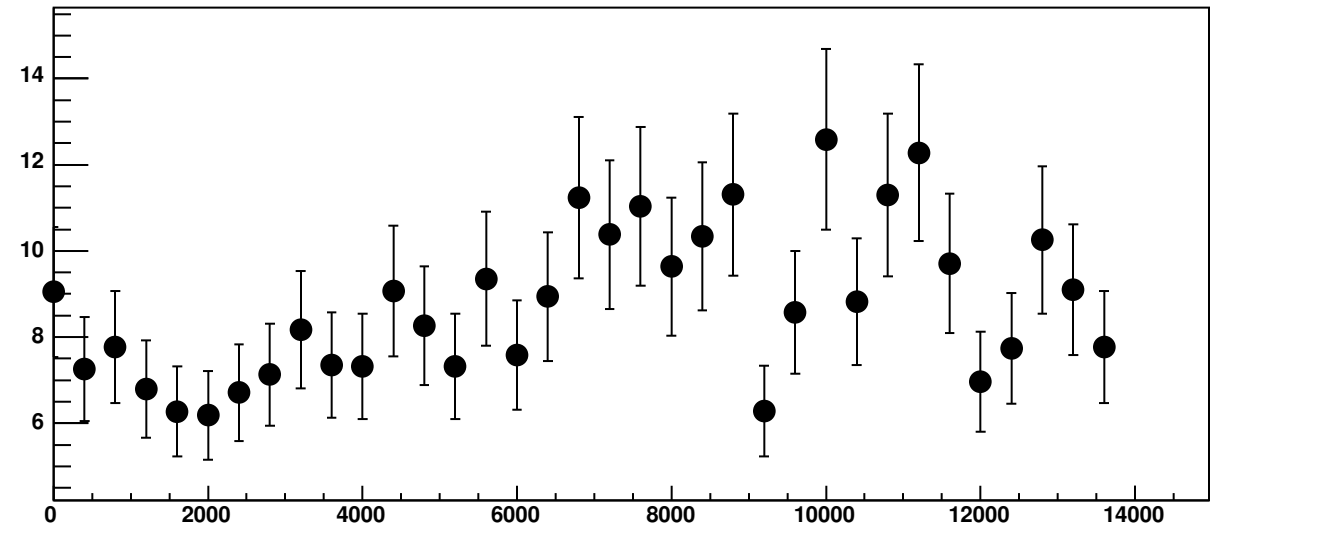

Chip 0, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
5
10
15
20
25
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
-5
0
5
10
15
20
0
2000
4000
6000
8000
10000
12000
14000

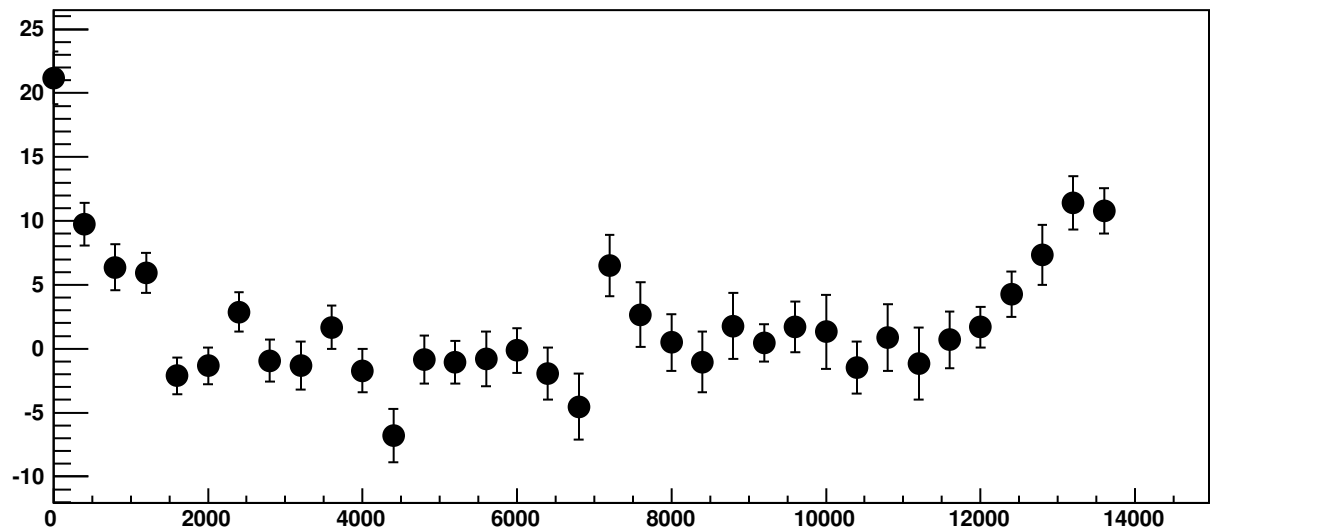
Chip 0, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC

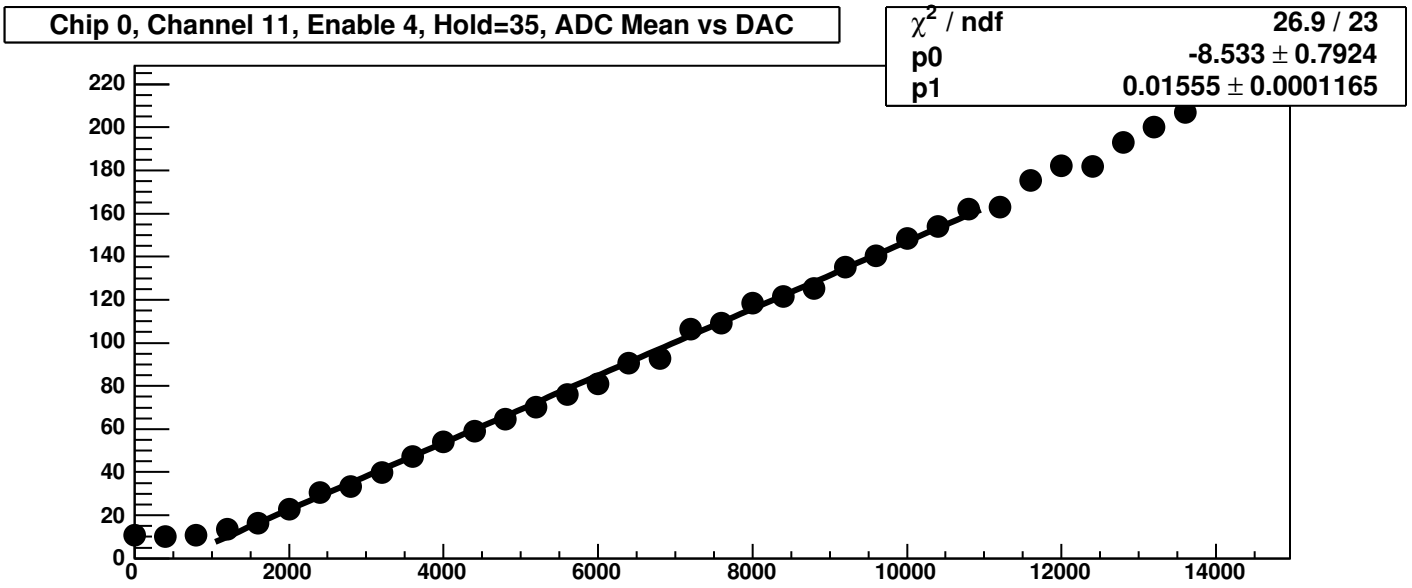


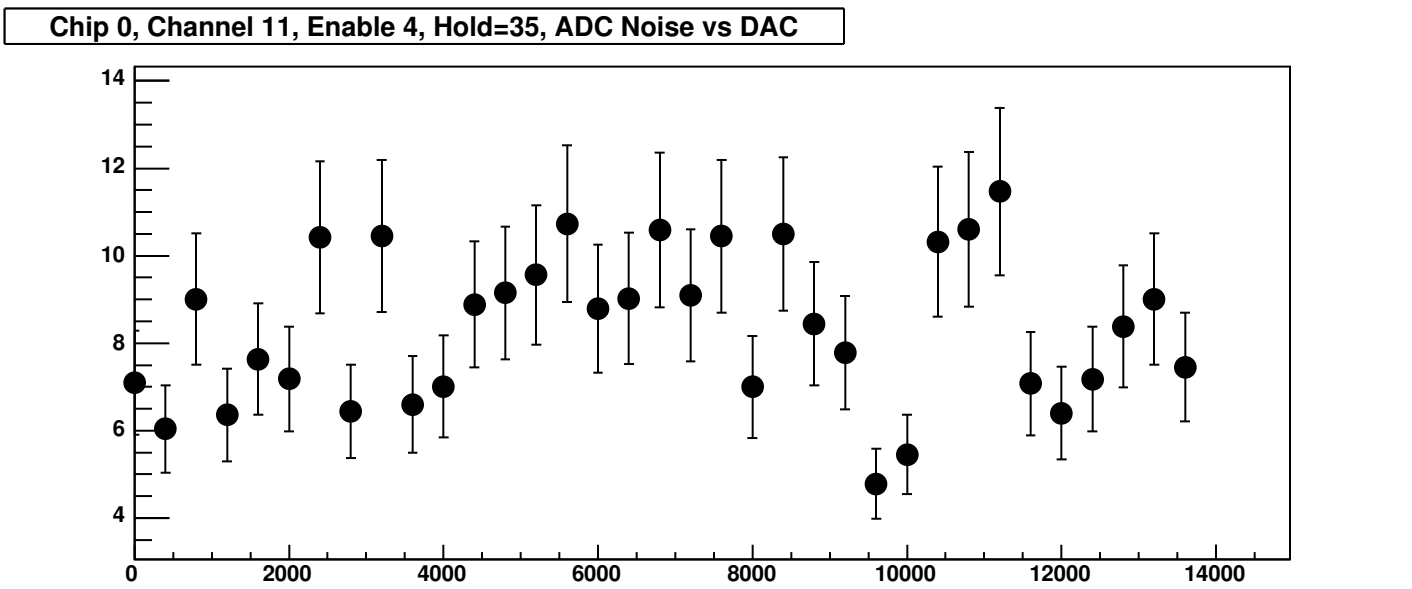
Chip 0, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC

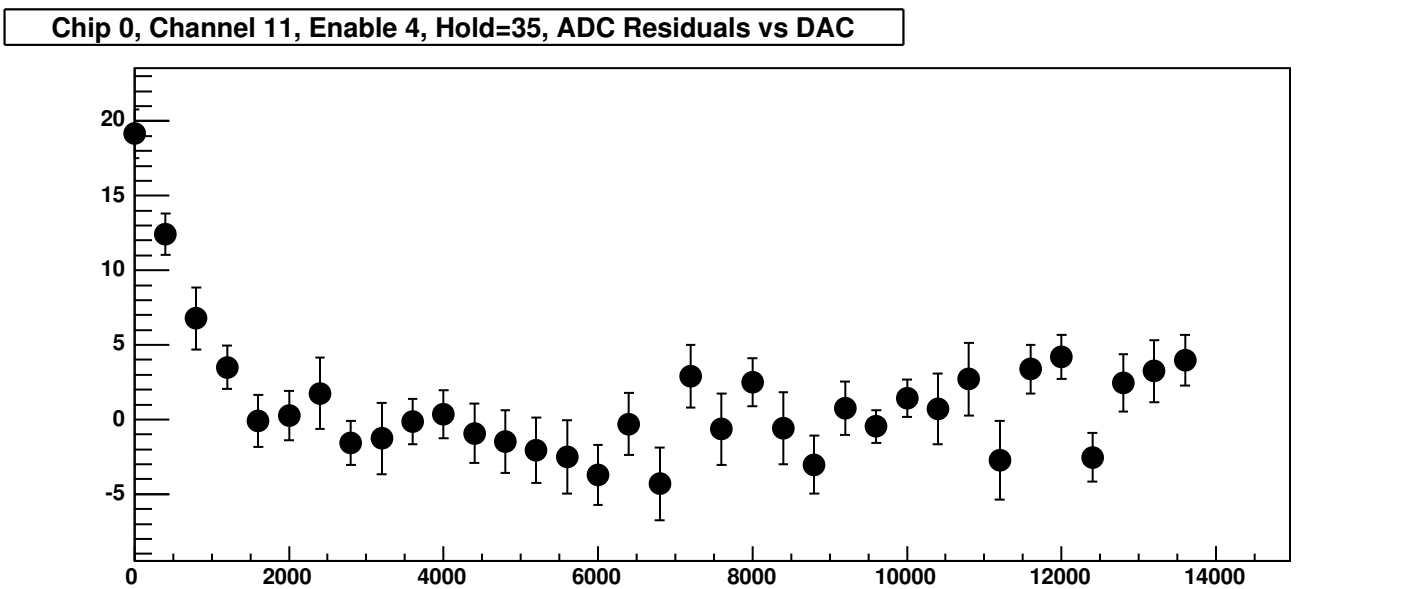


Chip 0, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC

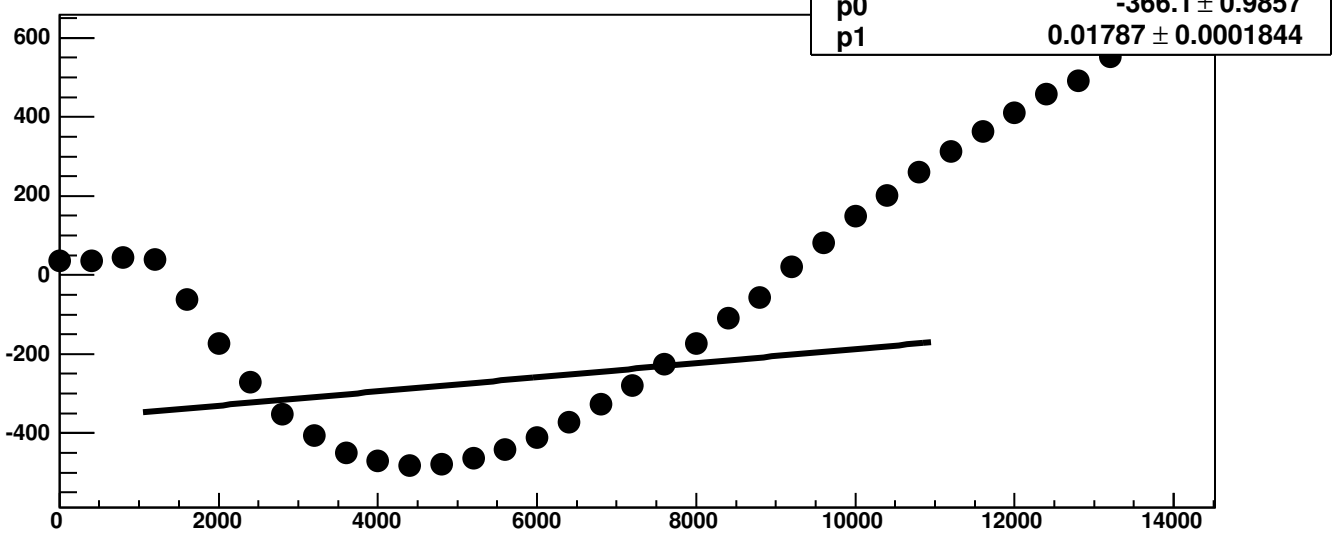



Chip 0, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.9 / 23
p0 -8.533 ± 0.7924
p1 0.01555 ± 0.0001165
0 20 40 60 80 100 120 140 160 180 200 220
0 2000 4000 6000 8000 10000 12000 14000

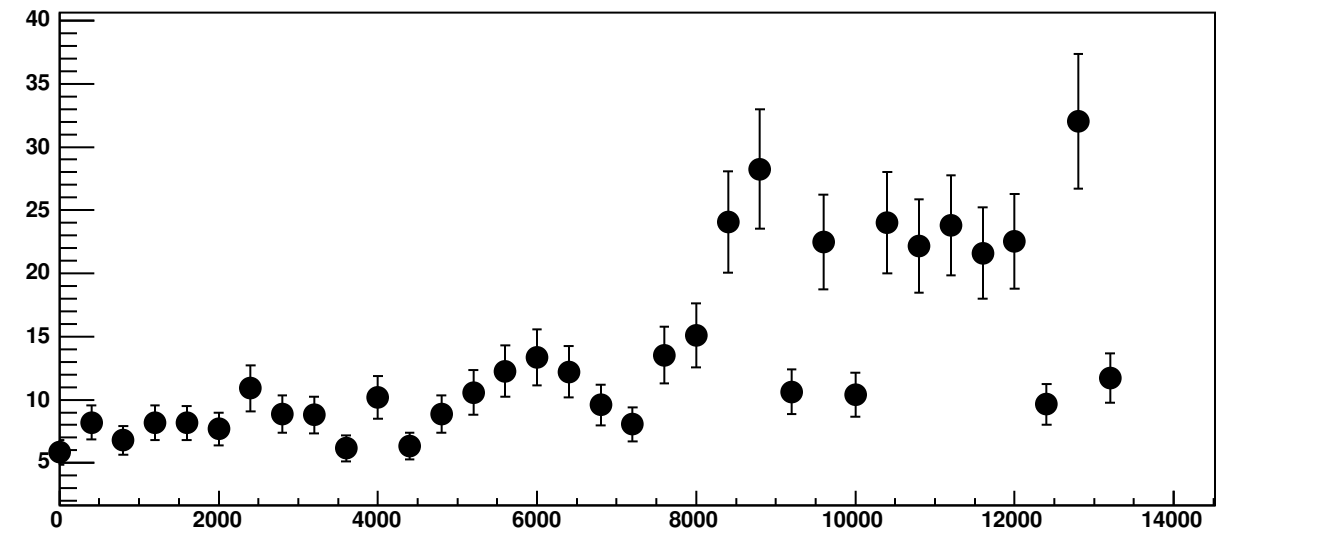

Chip 0, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
4 6 8 10 12 14
0 2000 4000 6000 8000 10000 12000 14000


Chip 0, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
-5 0 5 10 15 20
0 2000 4000 6000 8000 10000 12000 14000

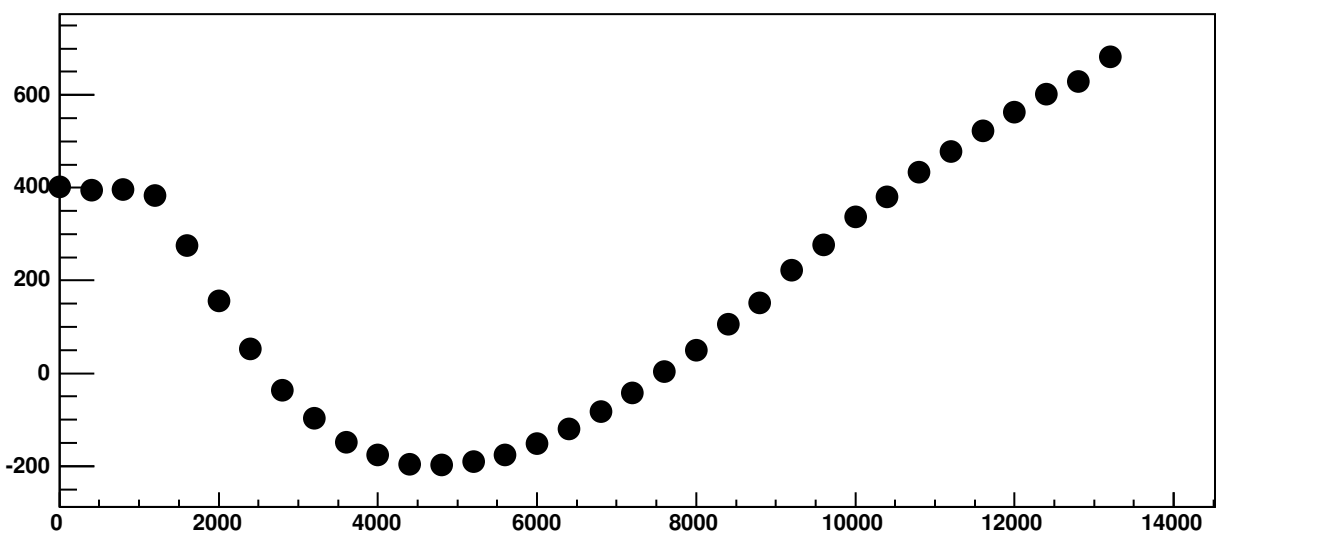
Chip 0, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

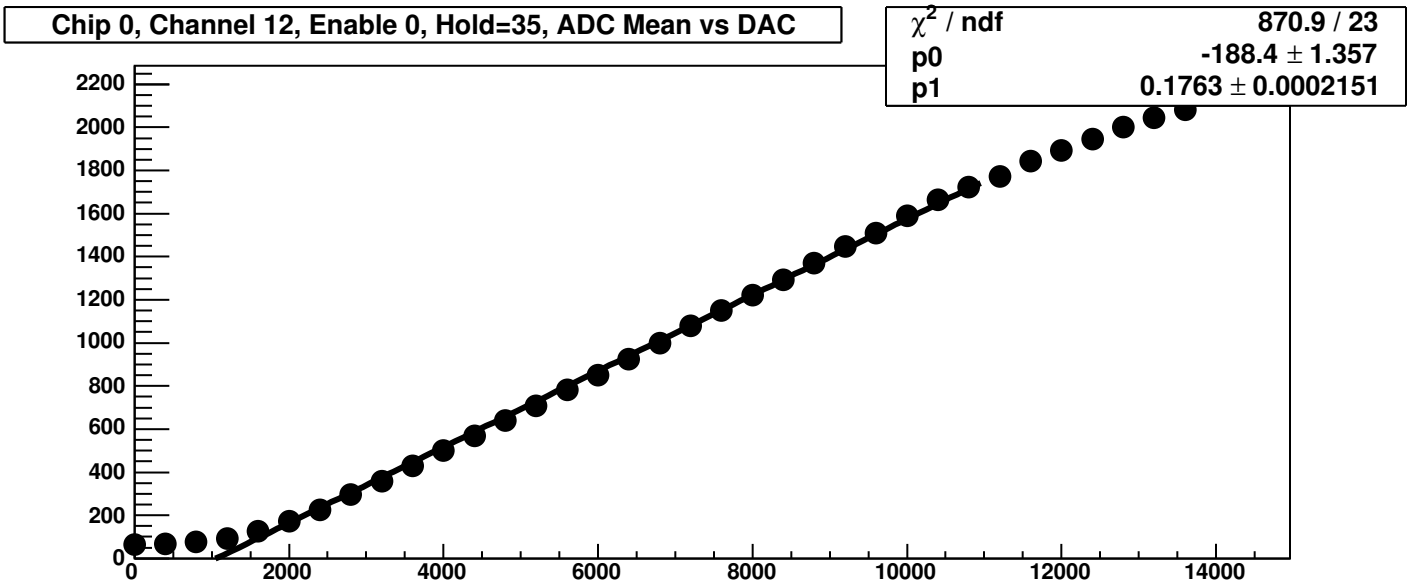


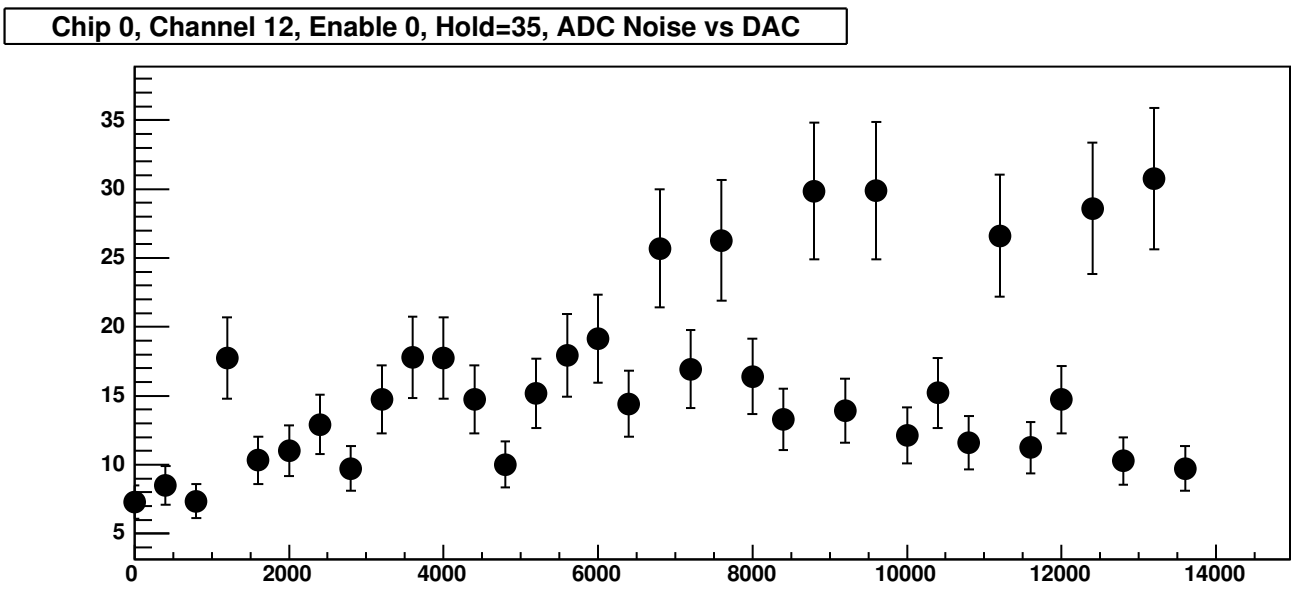
Chip 0, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

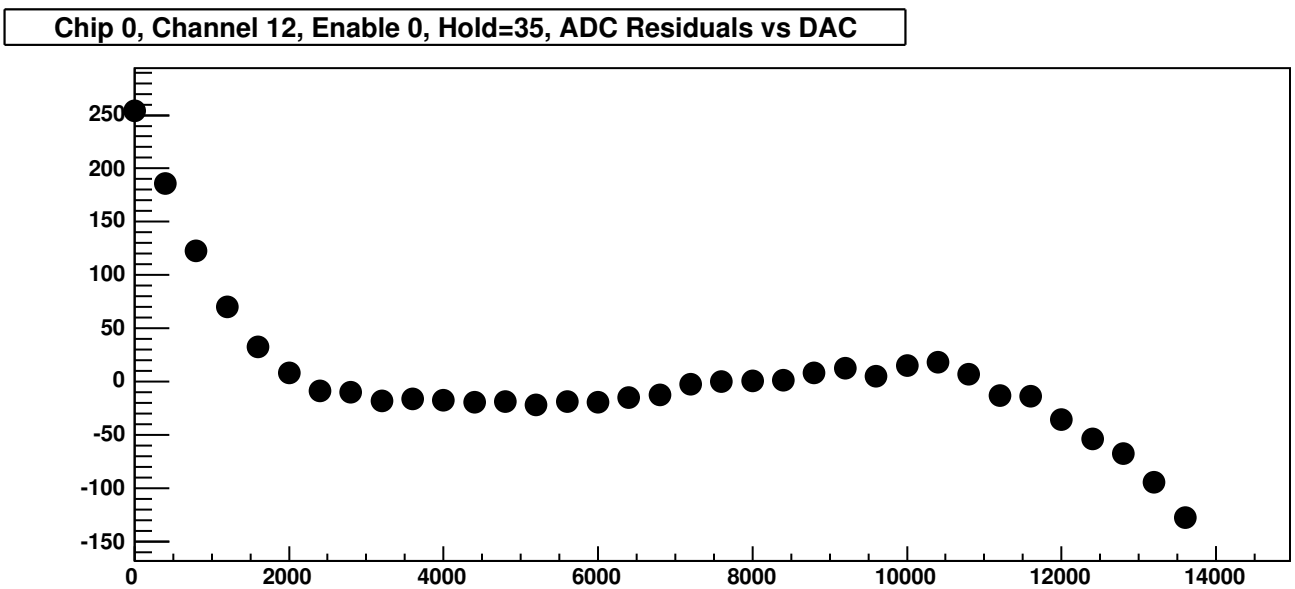


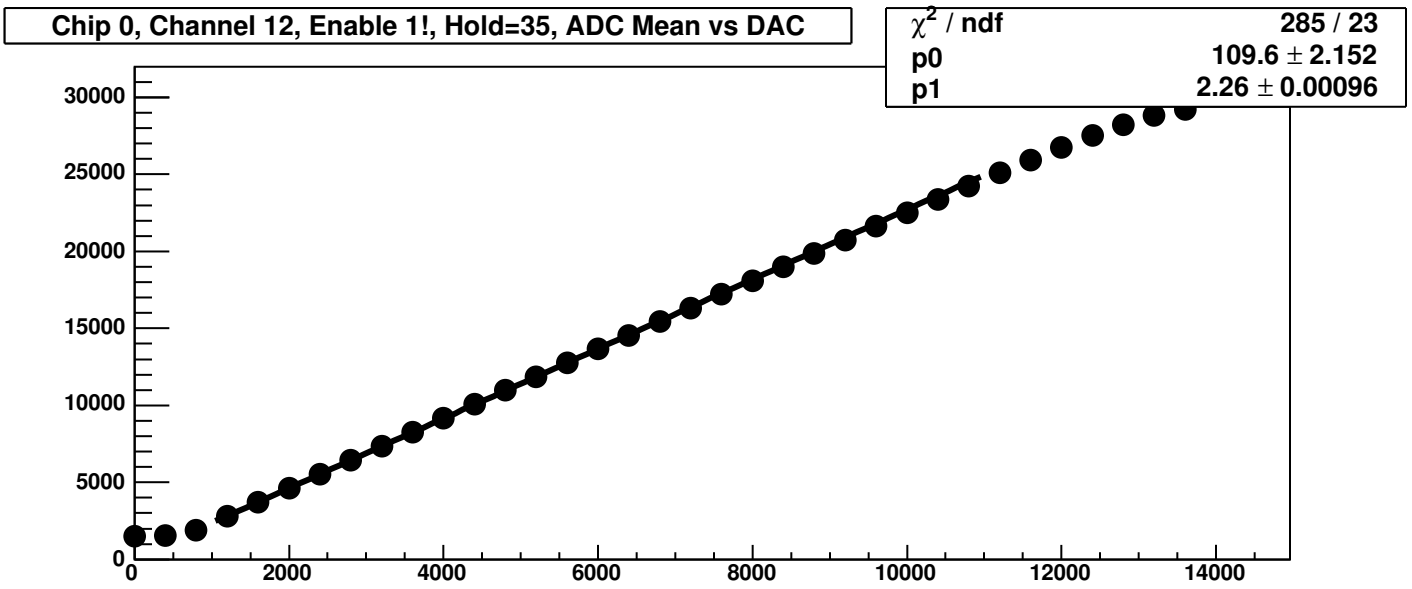
Chip 0, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

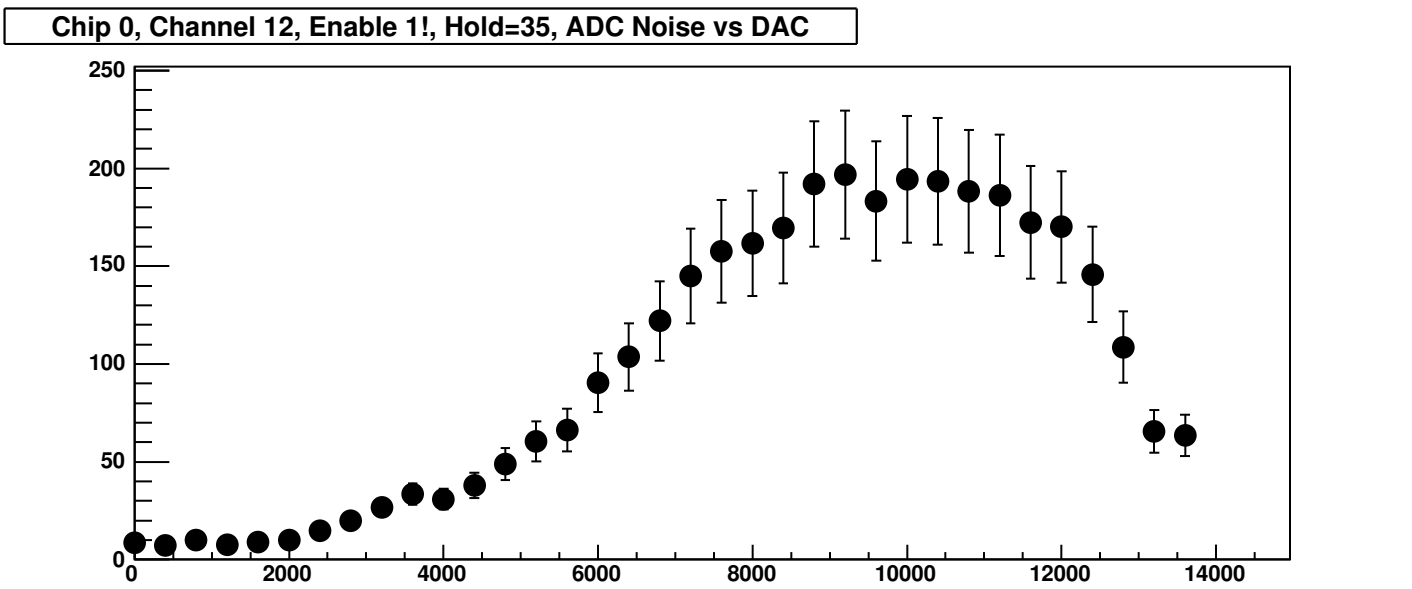


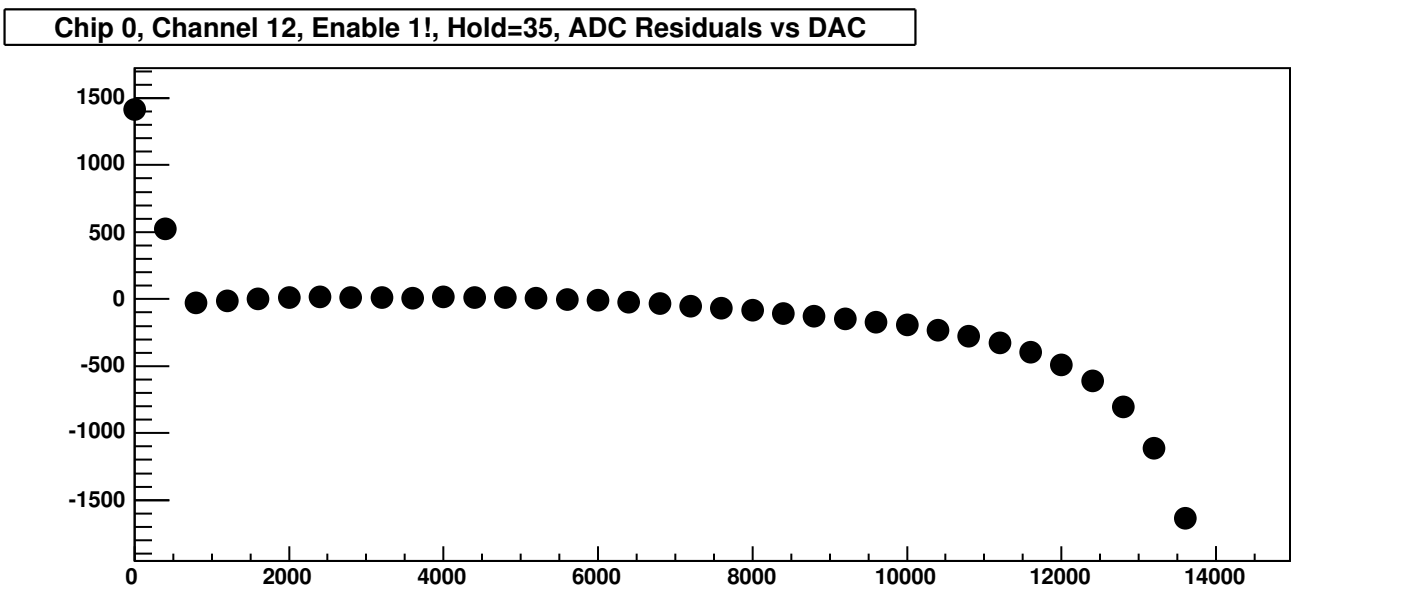

Chip 0, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 870.9 / 23
p0 -188.4 ± 1.357
p1 0.1763 ± 0.0002151
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

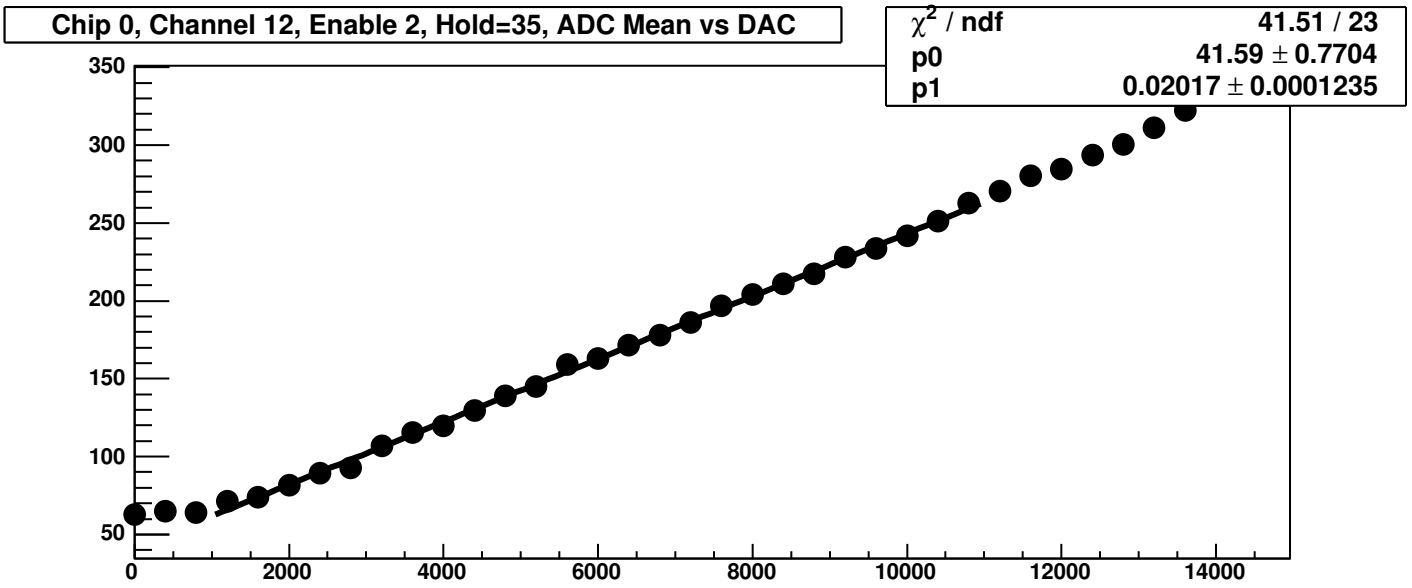

Chip 0, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

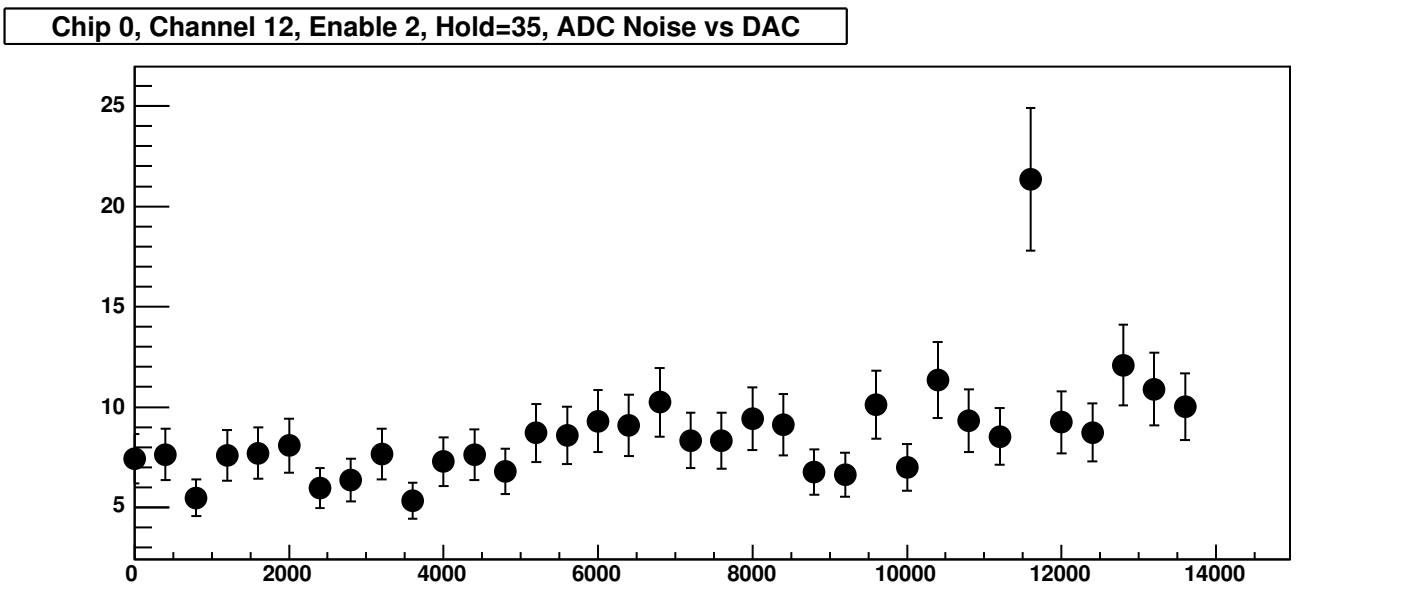

Chip 0, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

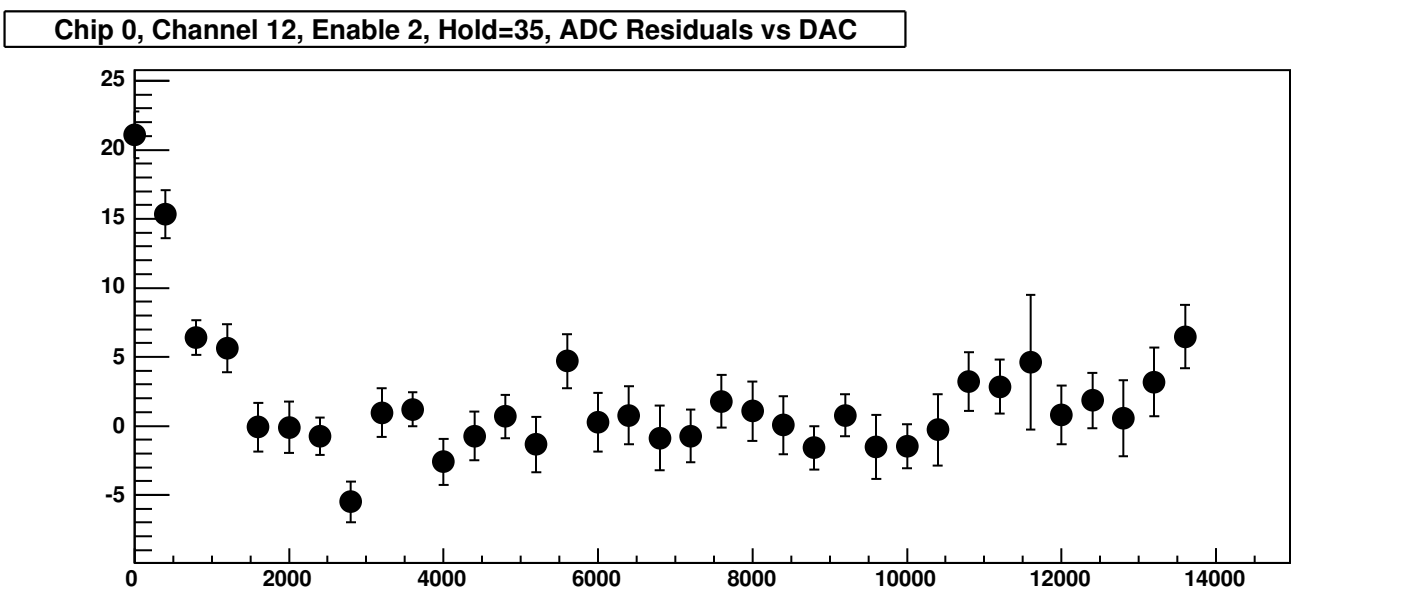

Chip 0, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 285 / 23
p0 109.6 ± 2.152
p1 2.26 ± 0.00096
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

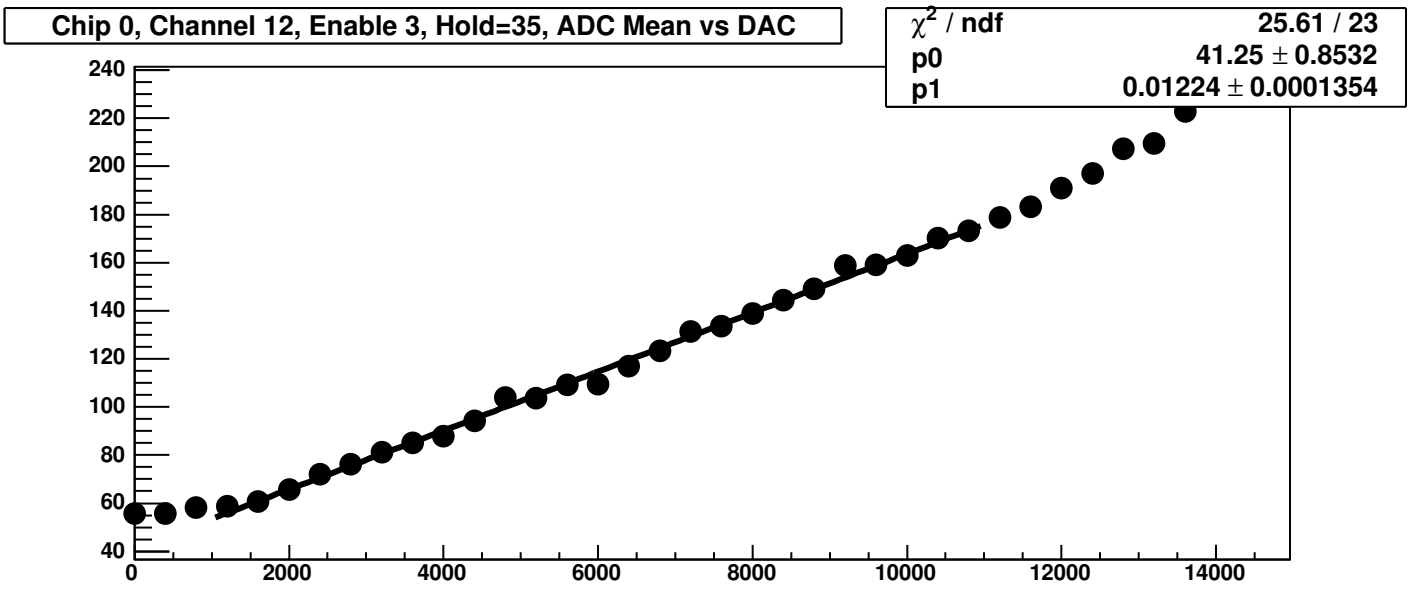

Chip 0, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

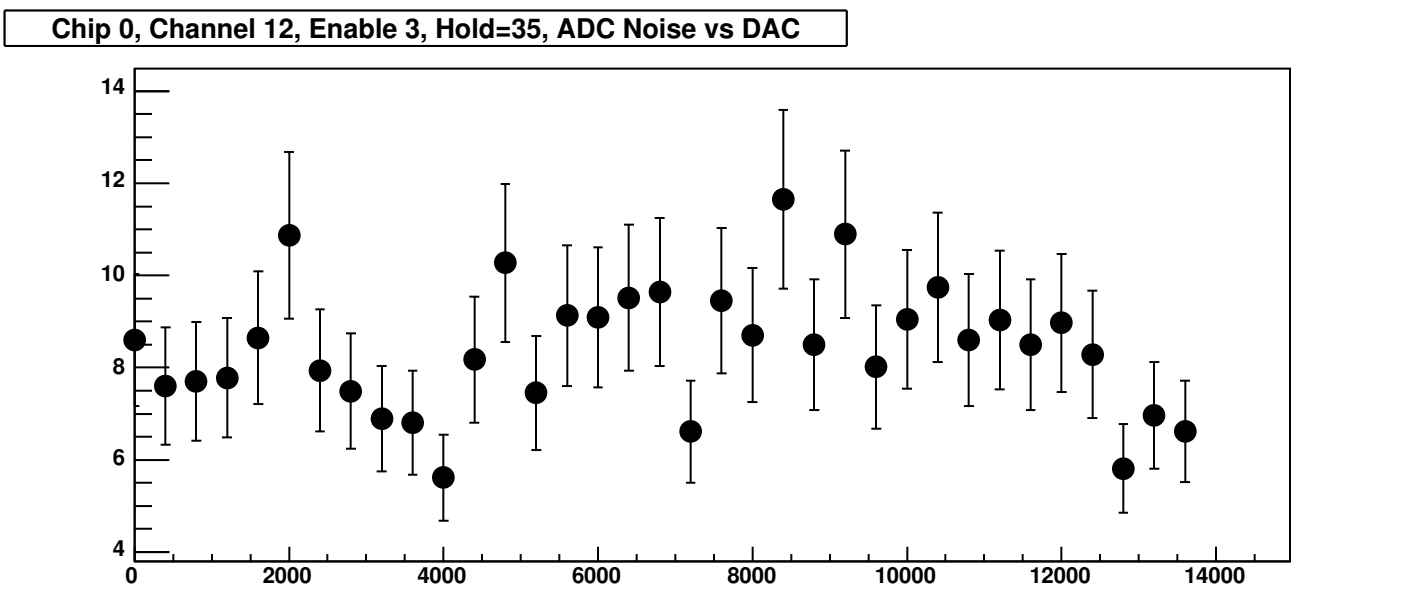

Chip 0, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

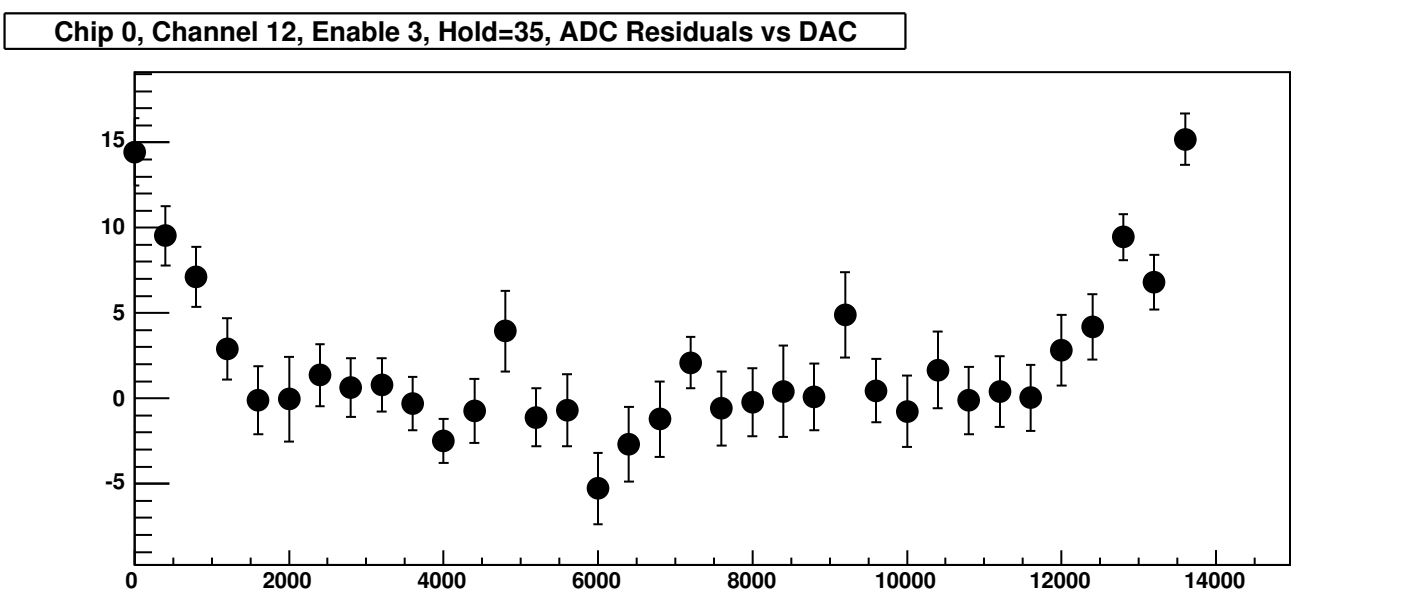

Chip 0, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 41.51 / 23
p0 41.59 ± 0.7704
p1 0.02017 ± 0.0001235
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

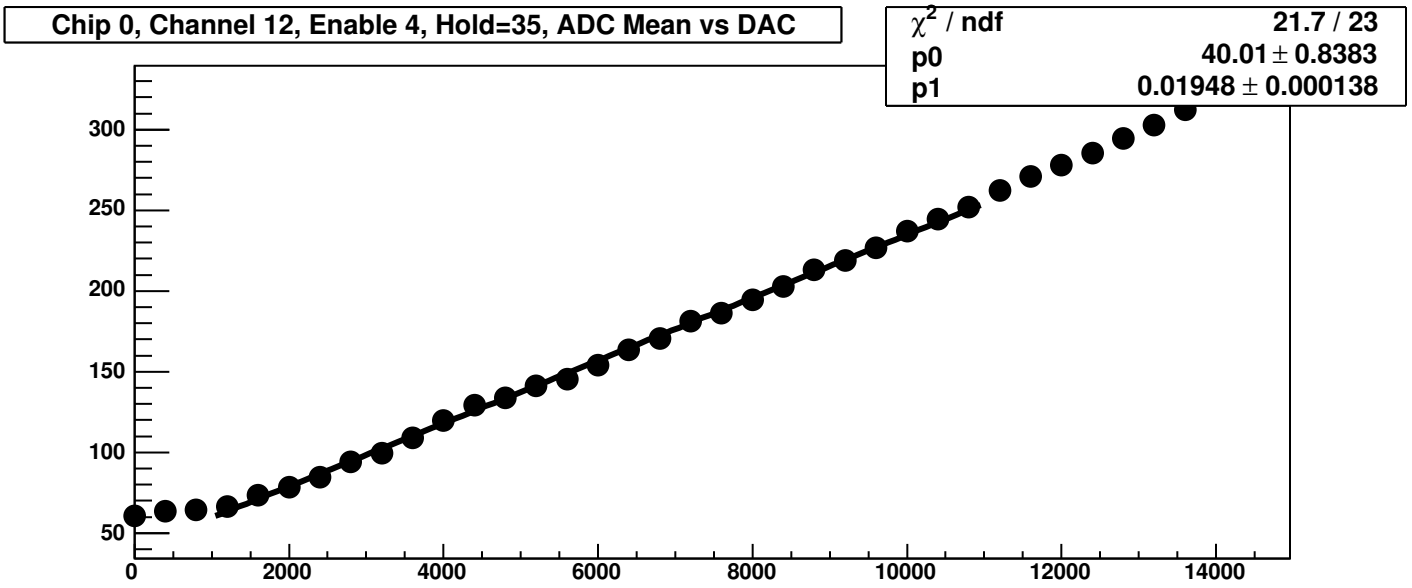

Chip 0, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

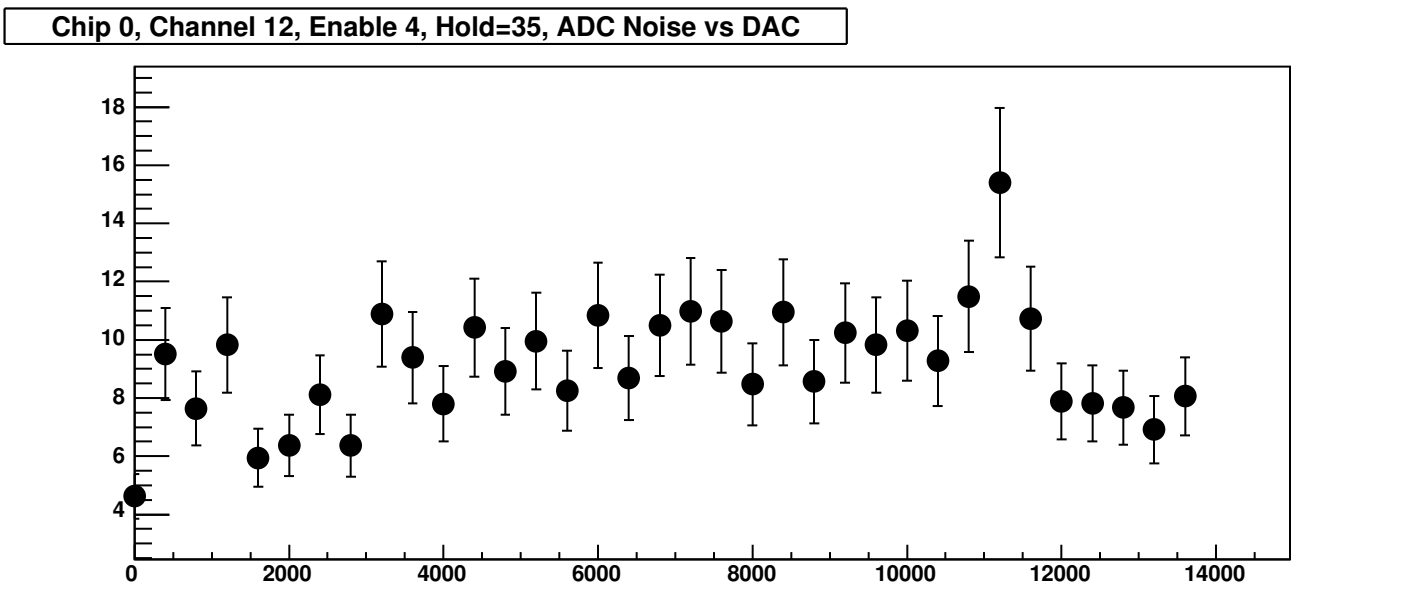

Chip 0, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

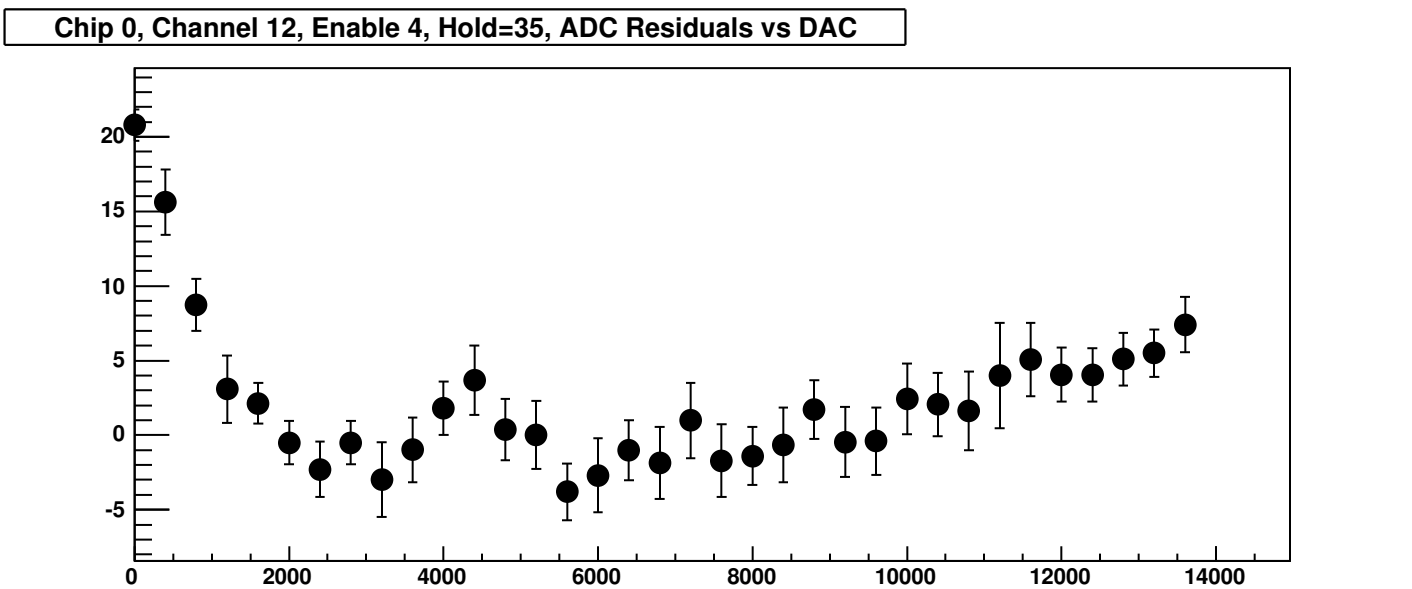

Chip 0, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.61 / 23
p0 41.25 ± 0.8532
p1 0.01224 ± 0.0001354
240
220
200
180
160
140
120
100
80
60
40
0
2000
4000
6000
8000
10000
12000
14000

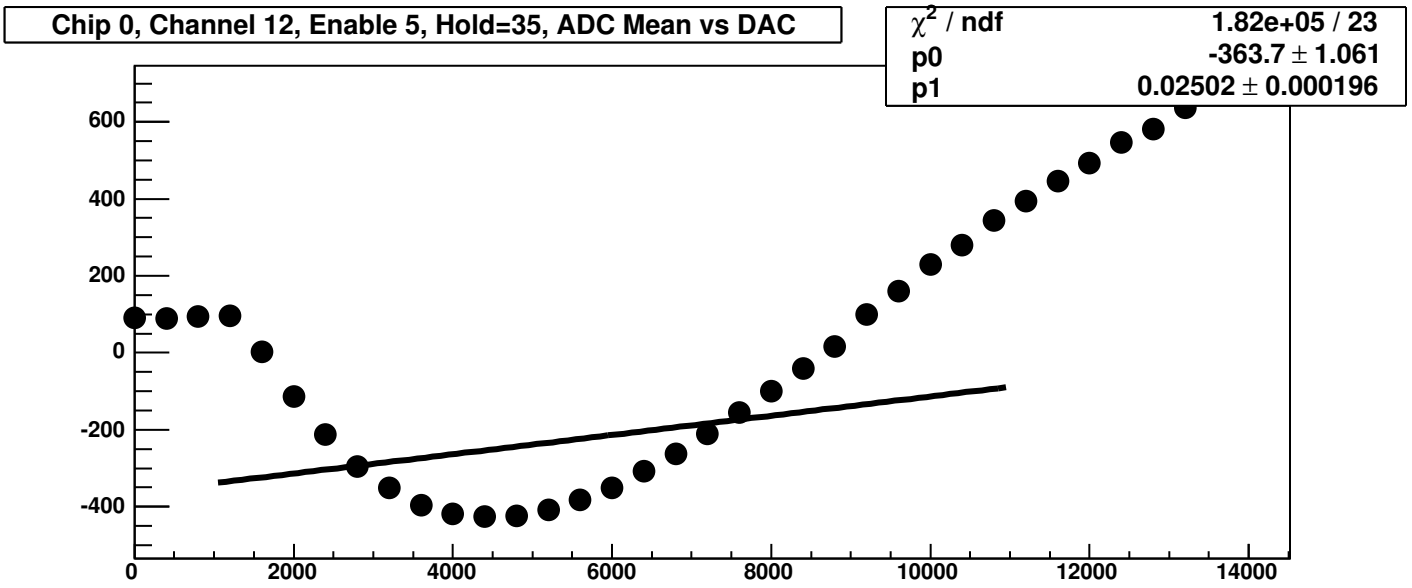

Chip 0, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

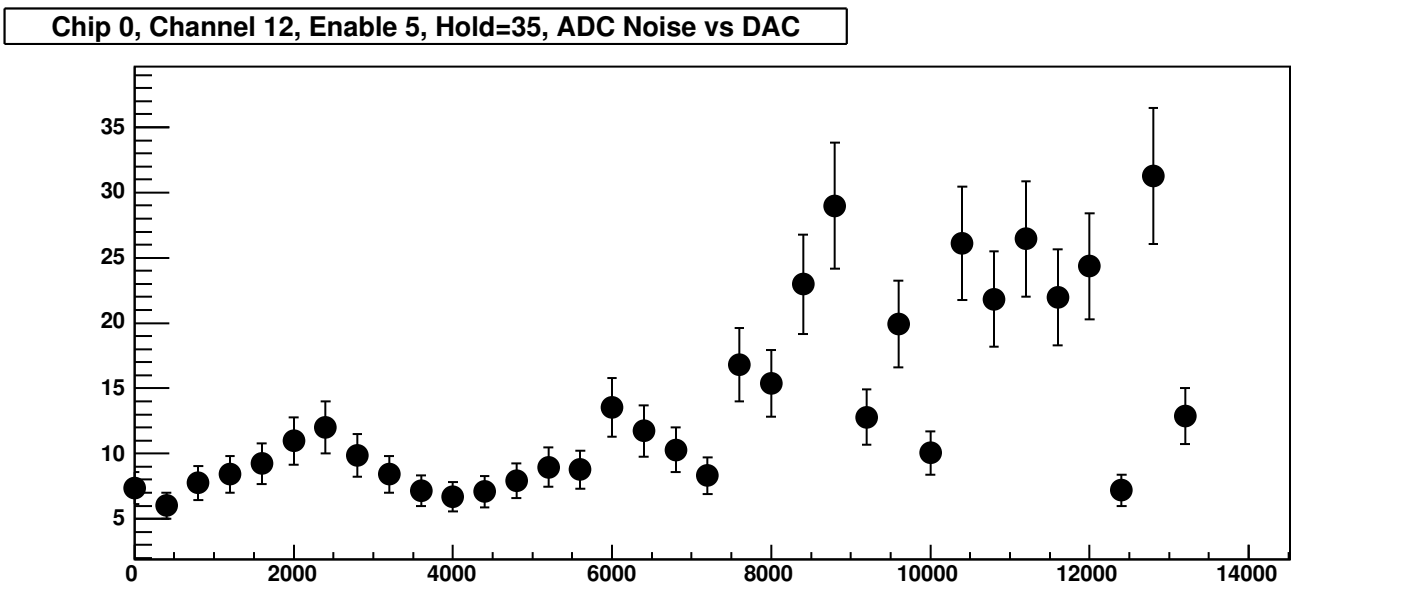

Chip 0, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

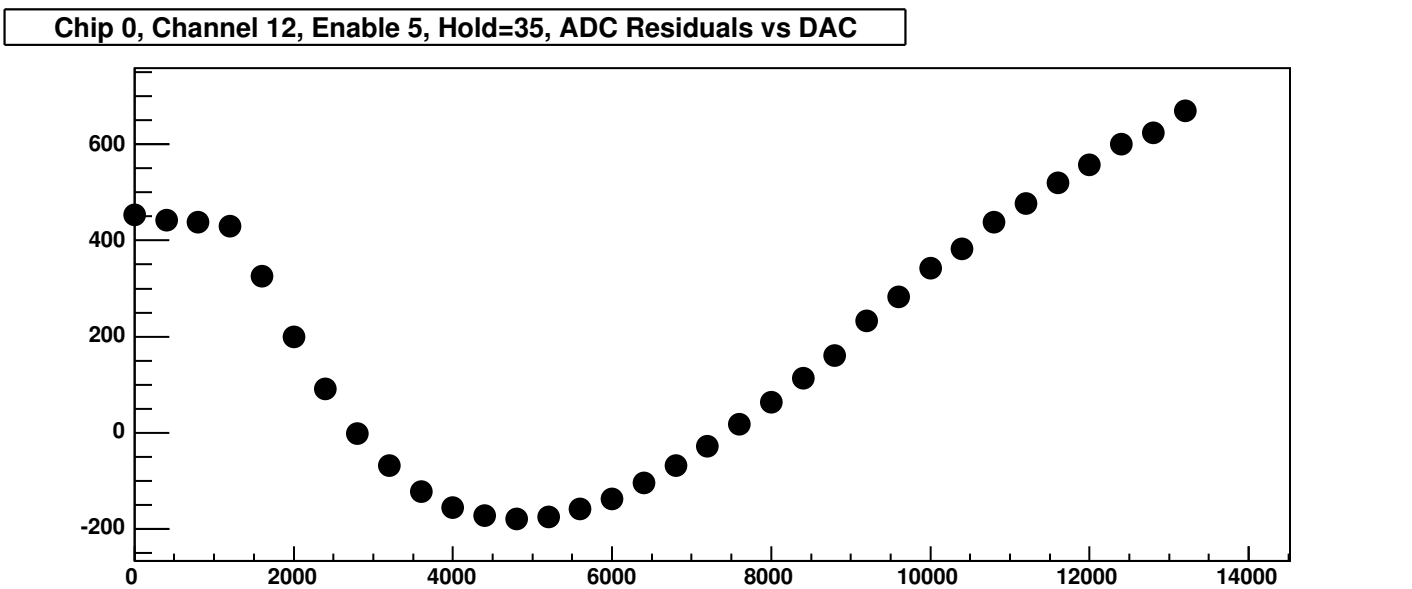

Chip 0, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 21.7 / 23
p0 40.01 ± 0.8383
p1 0.01948 ± 0.000138
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

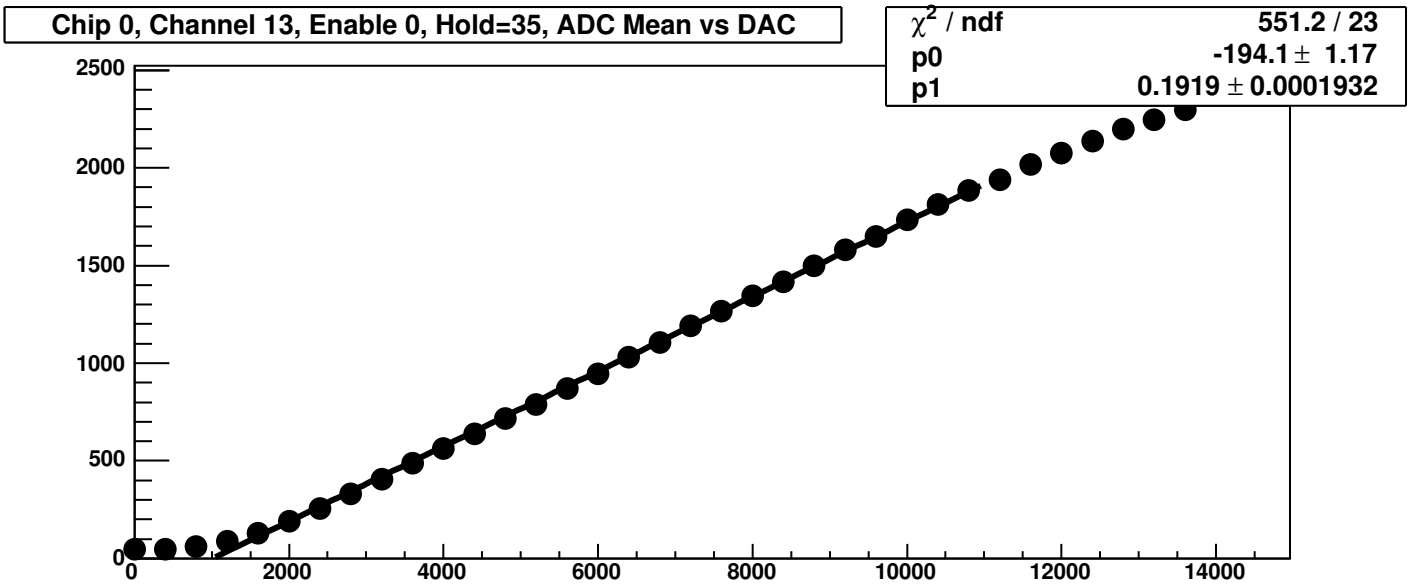

Chip 0, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

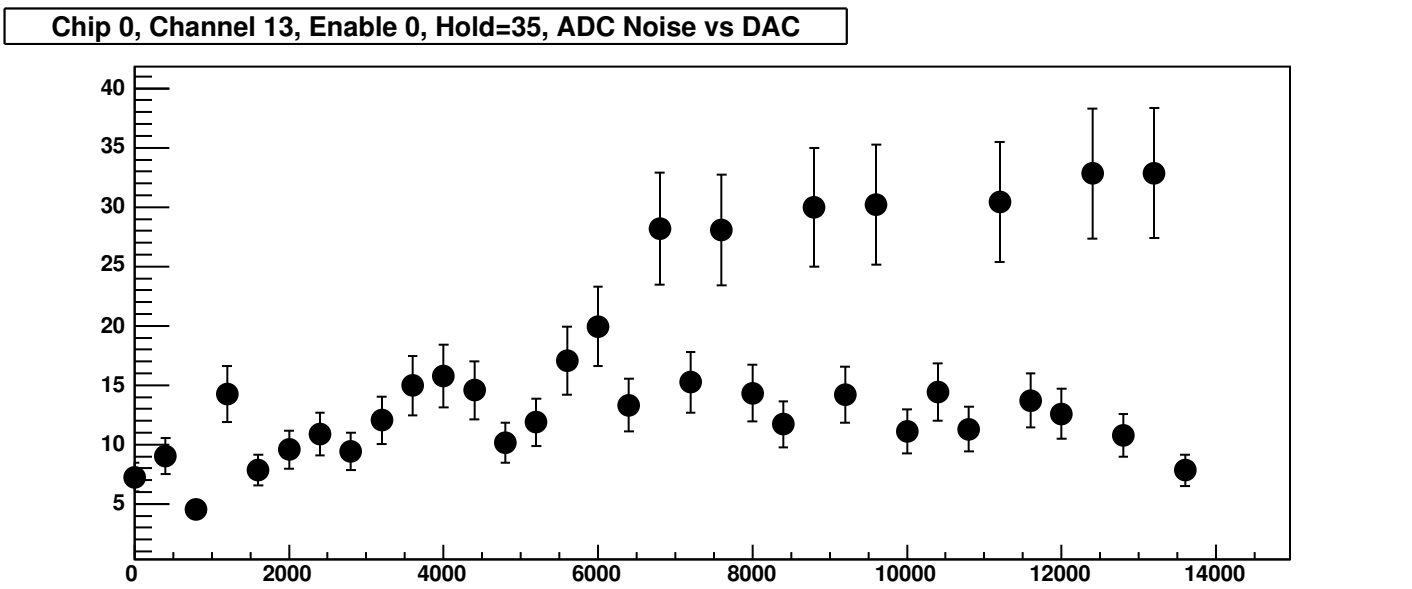

Chip 0, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

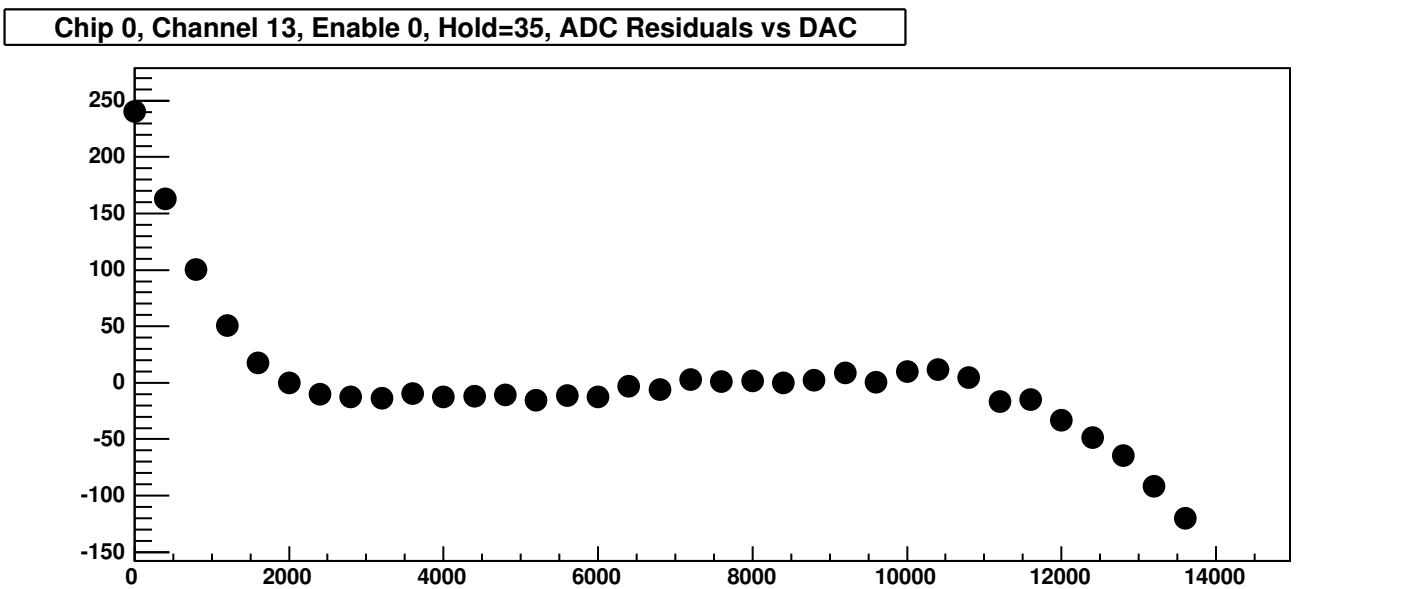

Chip 0, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.82e+05 / 23
p0 -363.7 ± 1.061
p1 0.02502 ± 0.000196
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

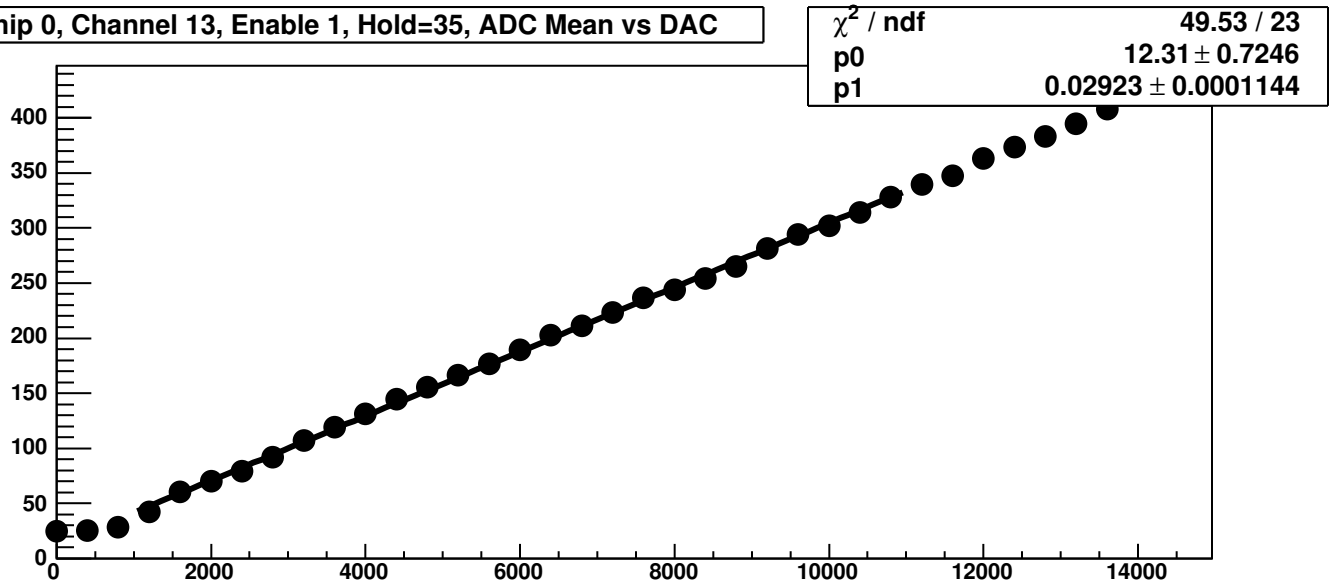

Chip 0, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 551.2 / 23
p0 -194.1 ± 1.17
p1 0.1919 ± 0.0001932
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

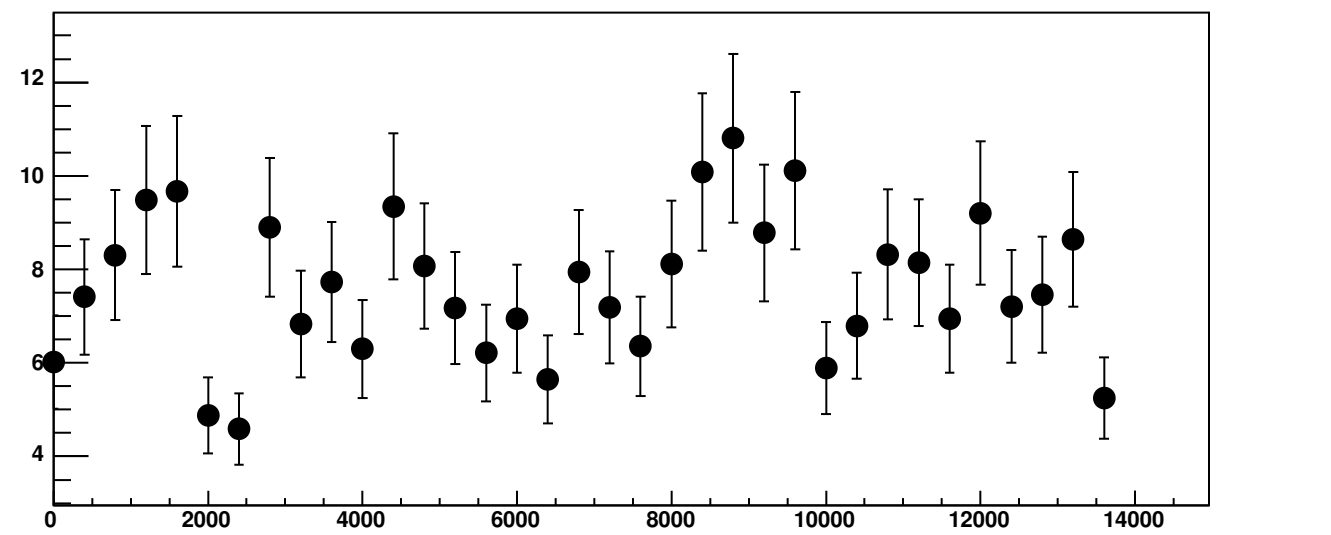

Chip 0, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

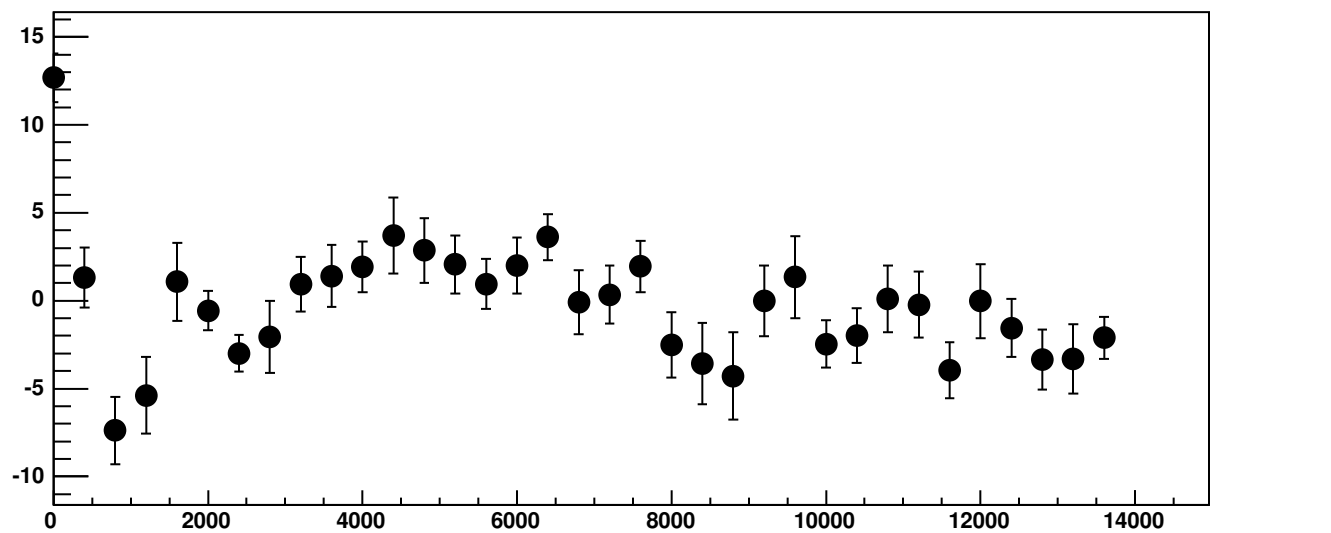
Chip 0, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC

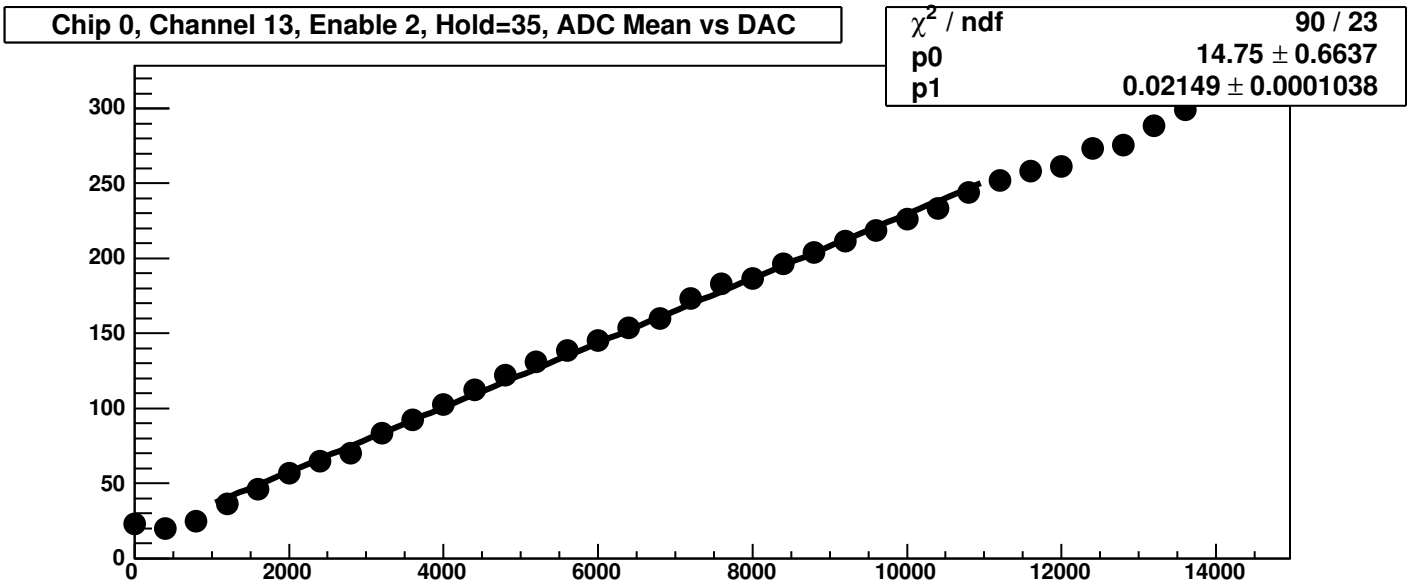


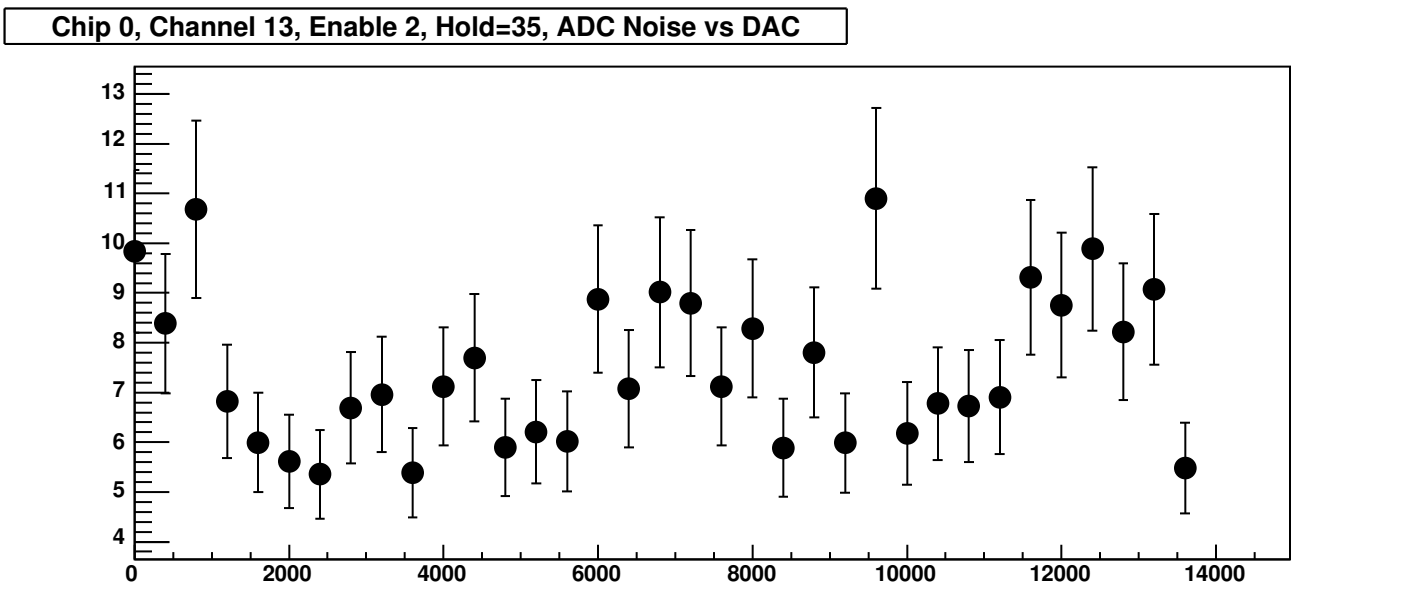
Chip 0, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC

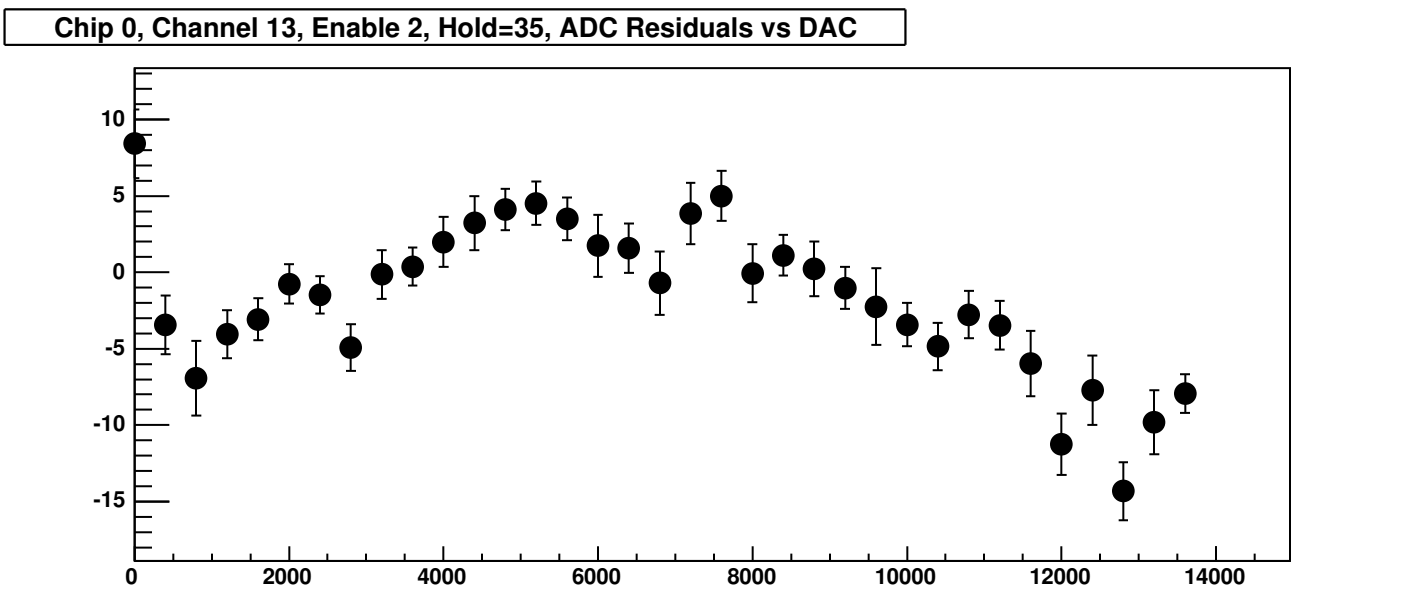


Chip 0, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC

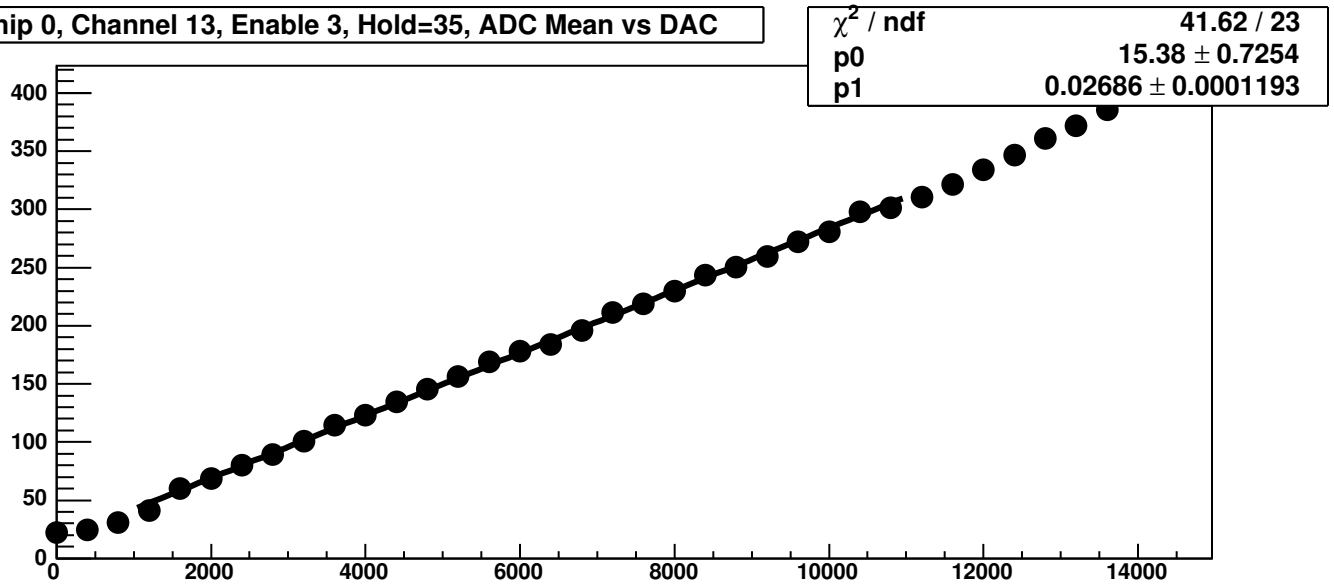



Chip 0, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 90 / 23
p0 14.75 ± 0.6637
p1 0.02149 ± 0.0001038
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

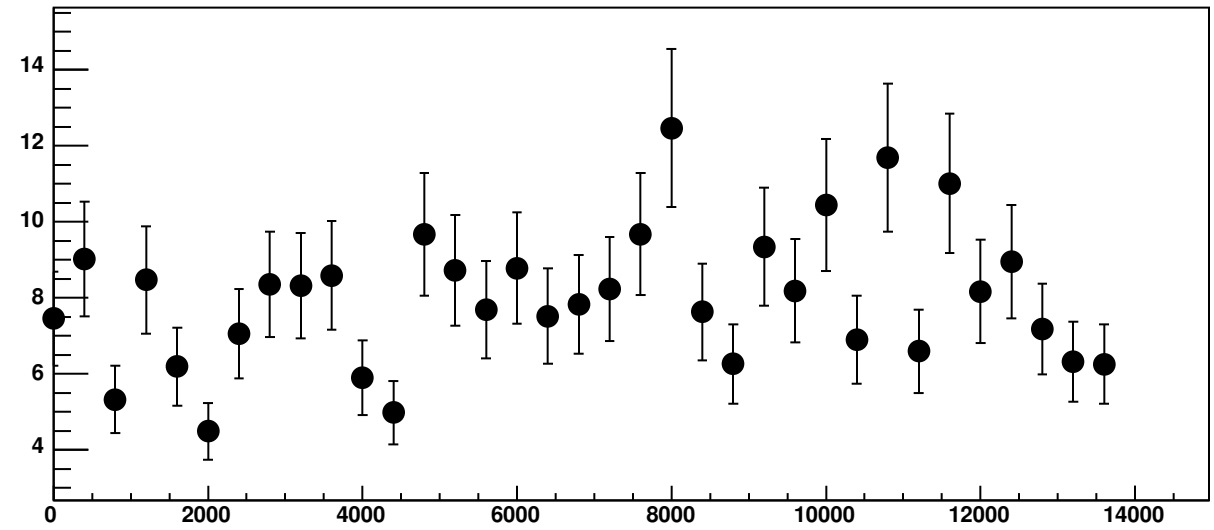

Chip 0, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

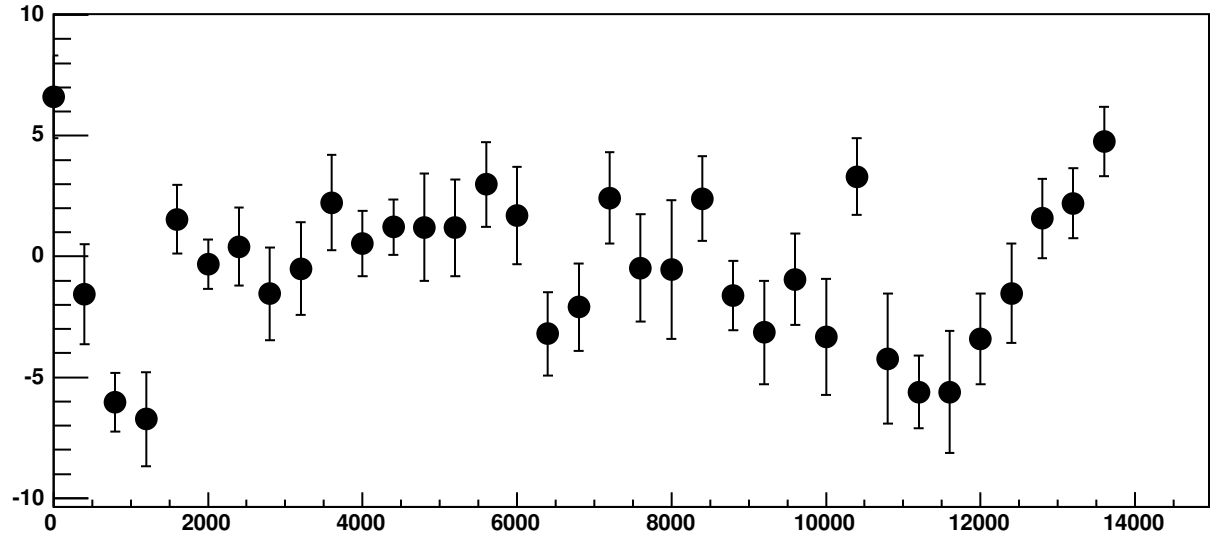
Chip 0, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

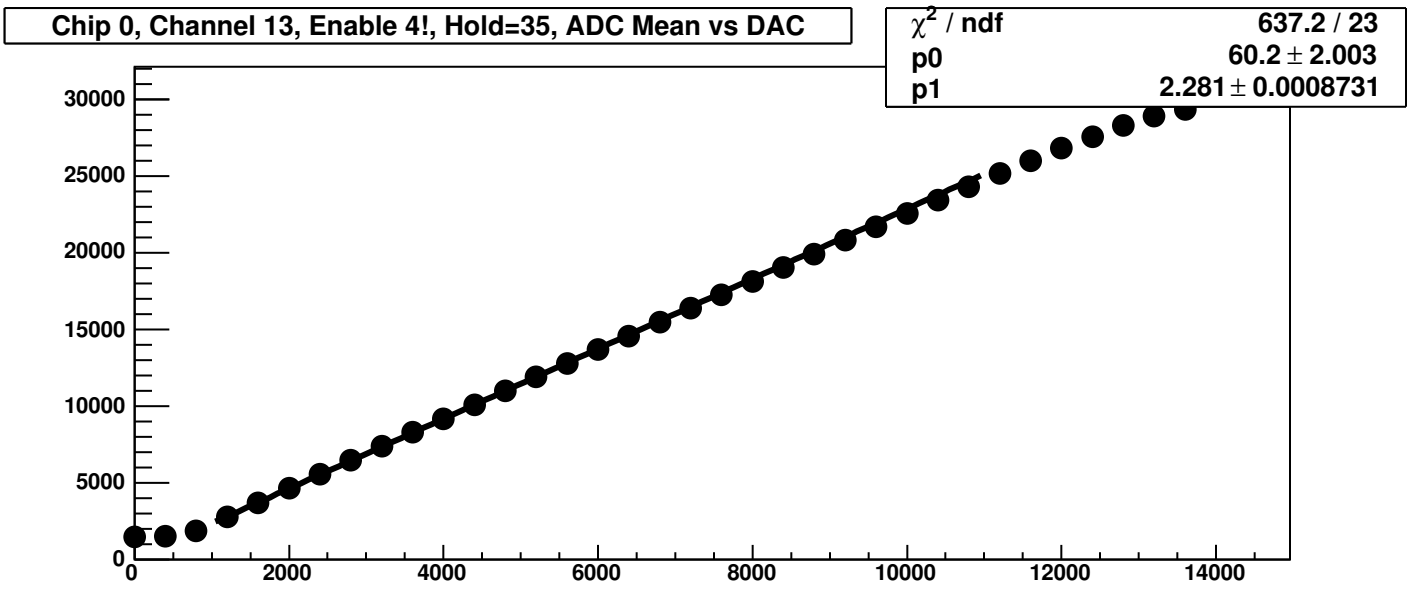


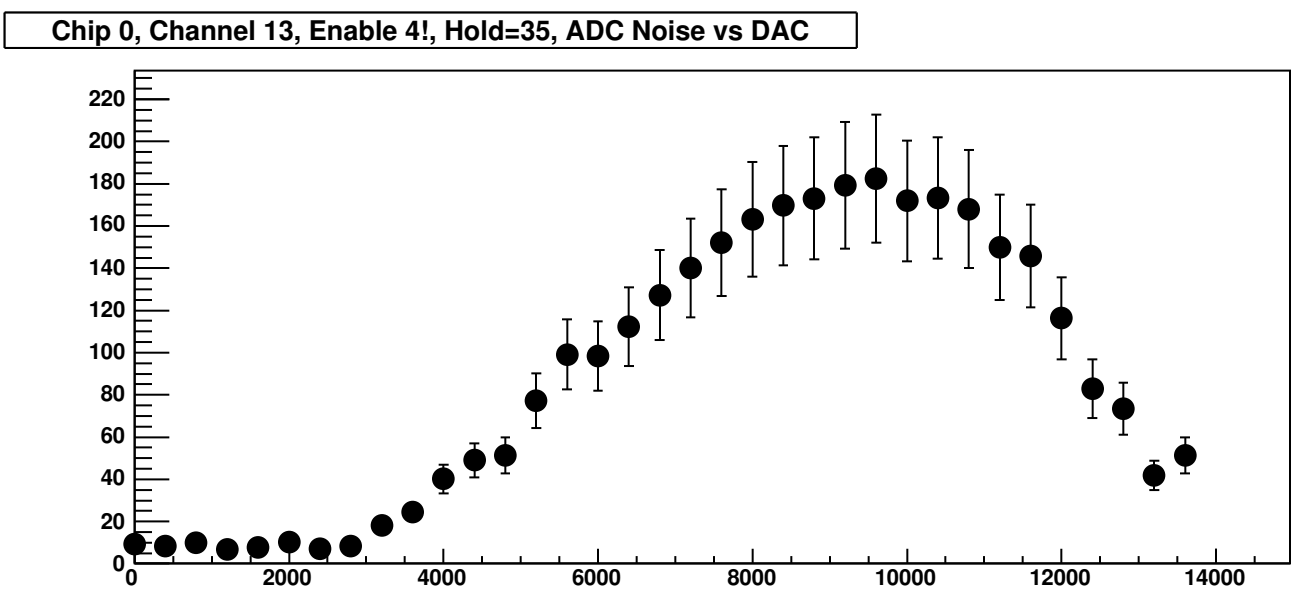
Chip 0, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

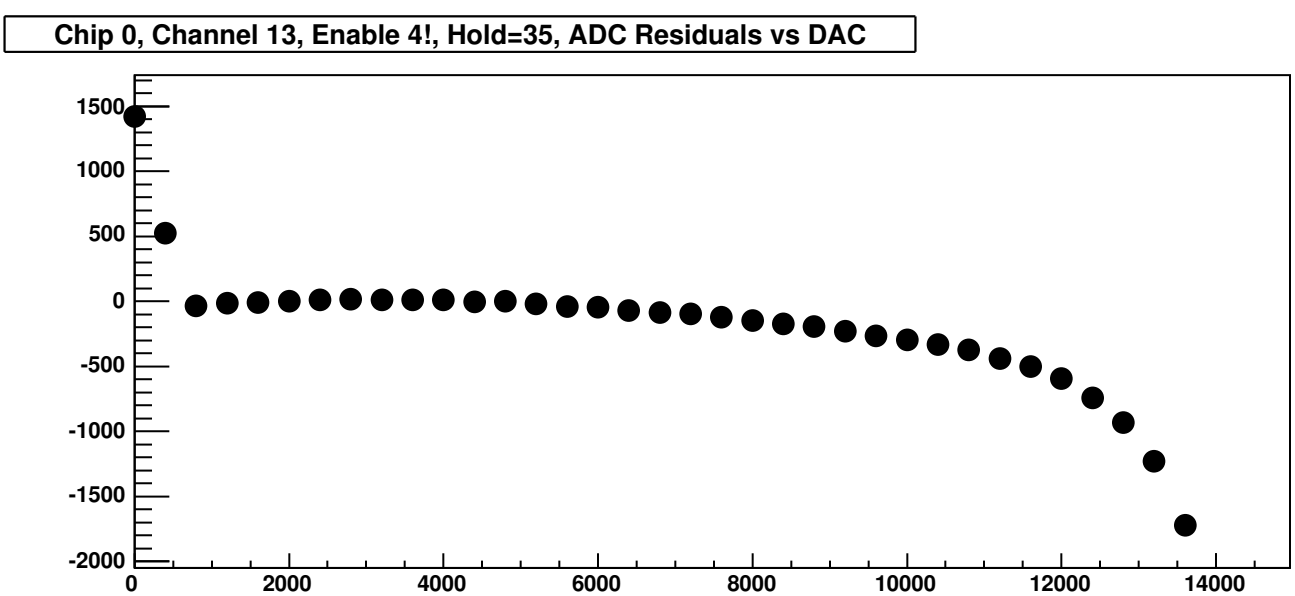


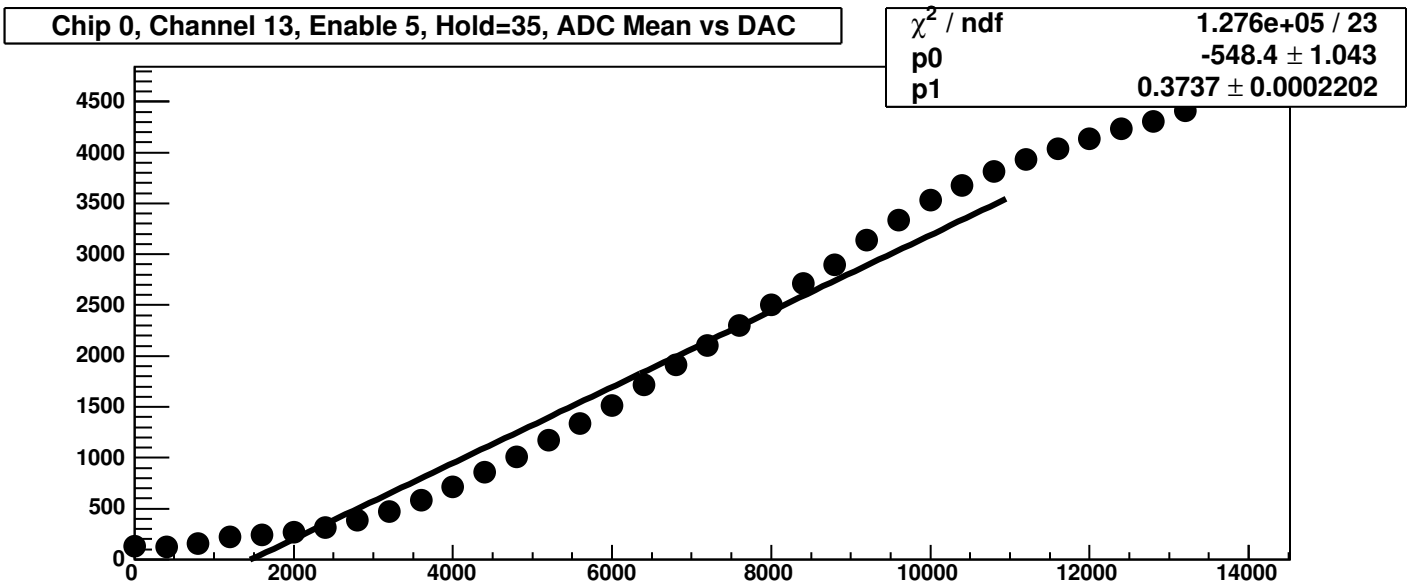
Chip 0, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

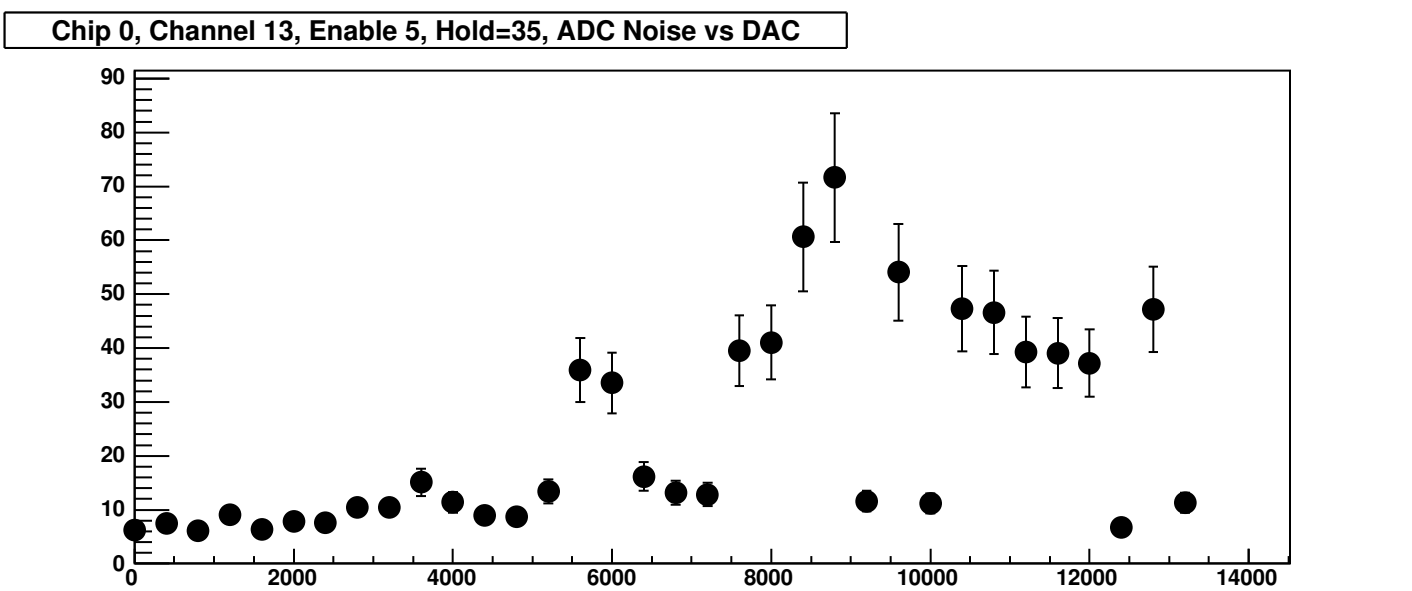


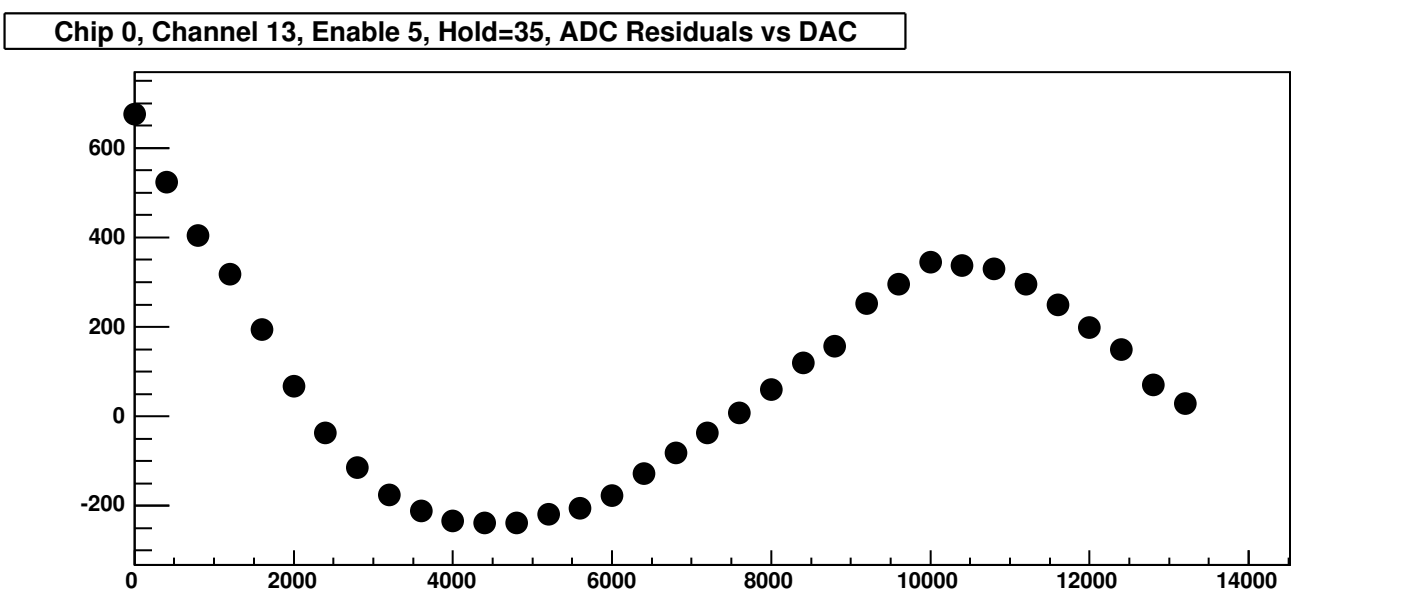

Chip 0, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 637.2 / 23
p0 60.2 ± 2.003
p1 2.281 ± 0.0008731
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

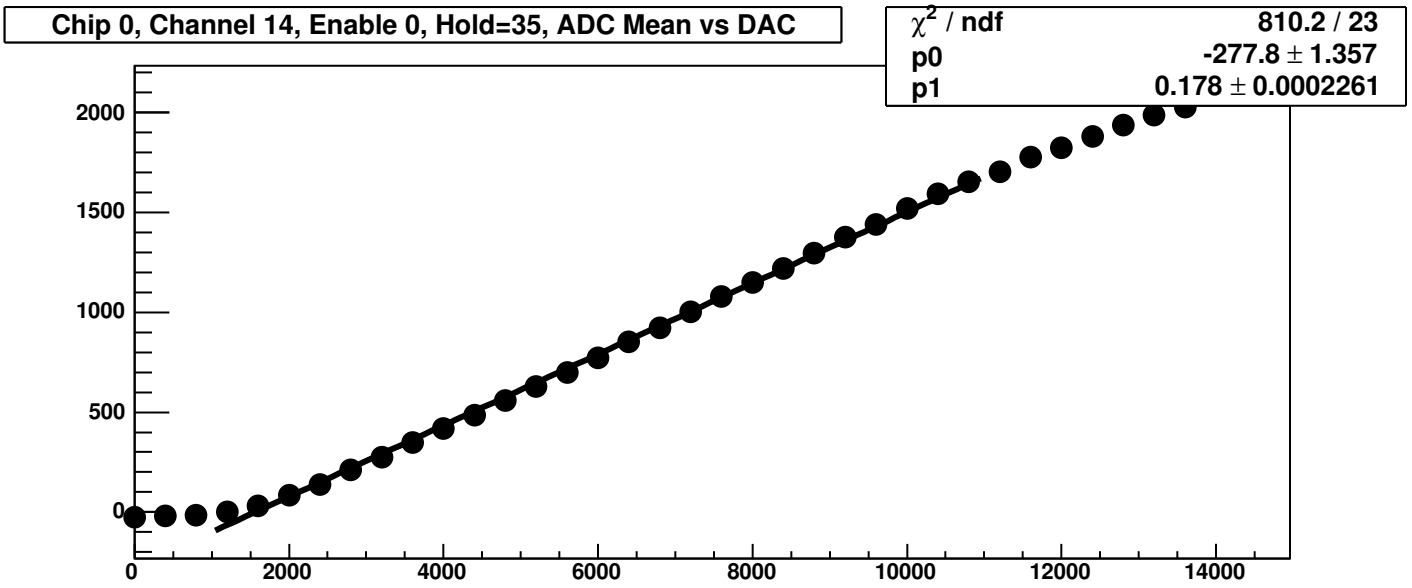

Chip 0, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

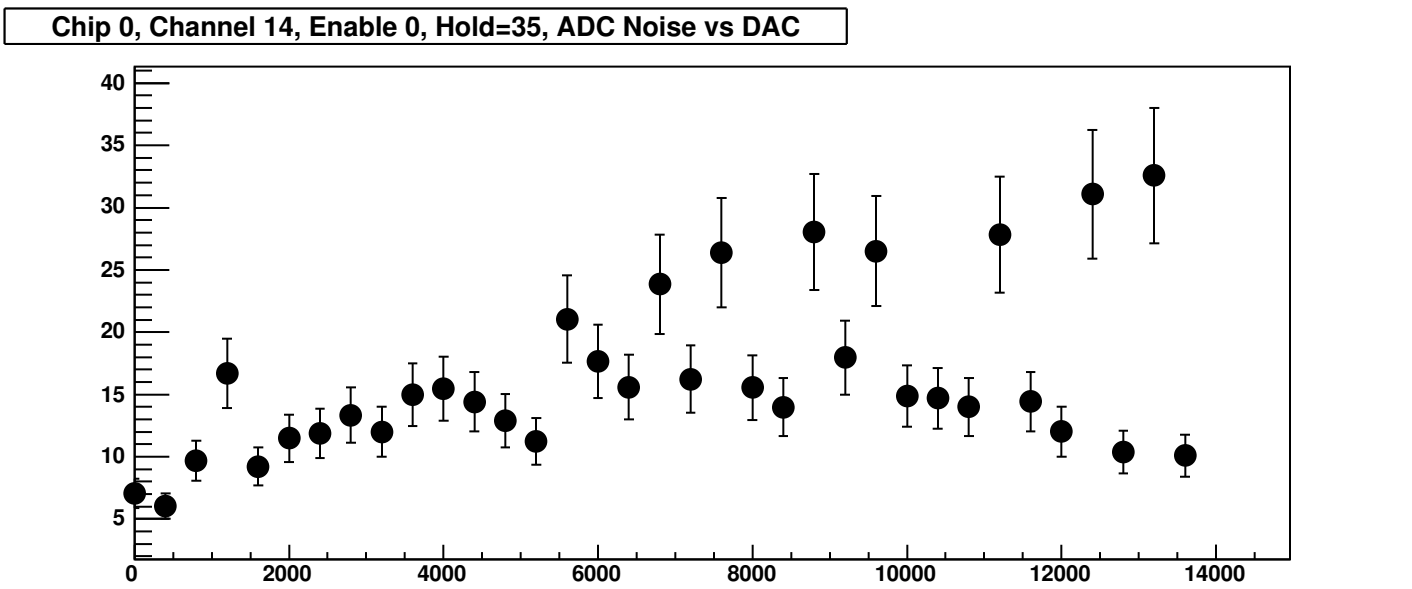

Chip 0, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

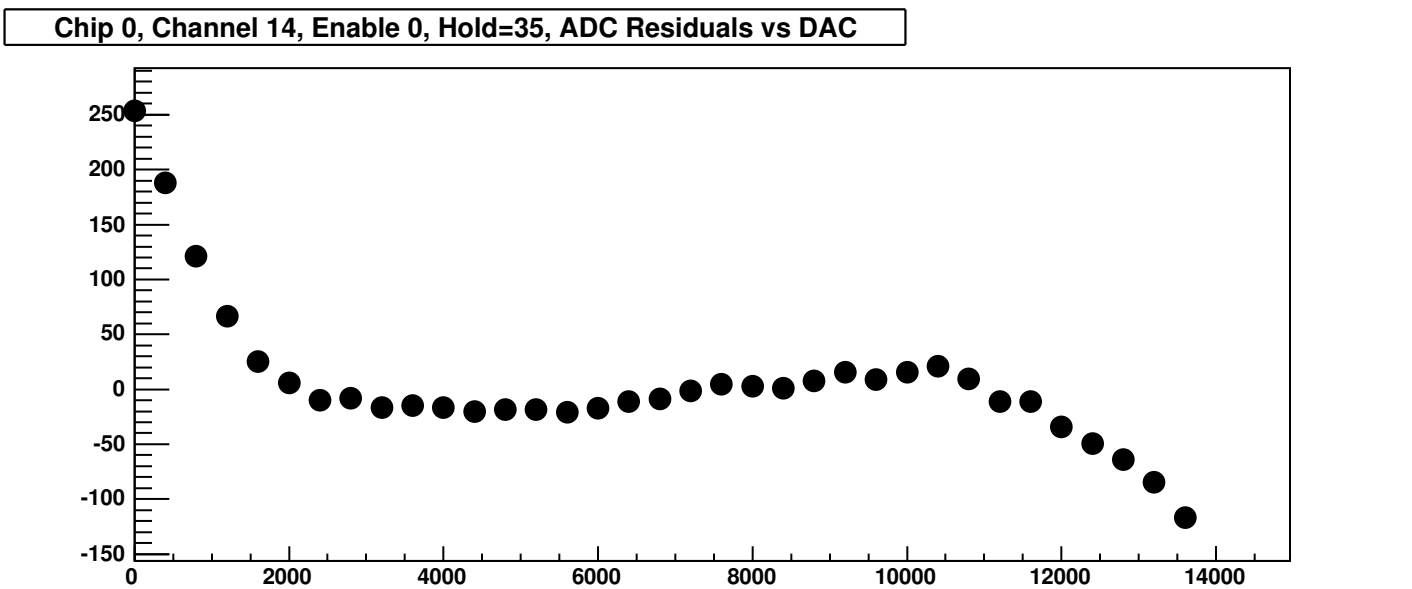

Chip 0, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.276e+05 / 23
p0 -548.4 ± 1.043
p1 0.3737 ± 0.0002202
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

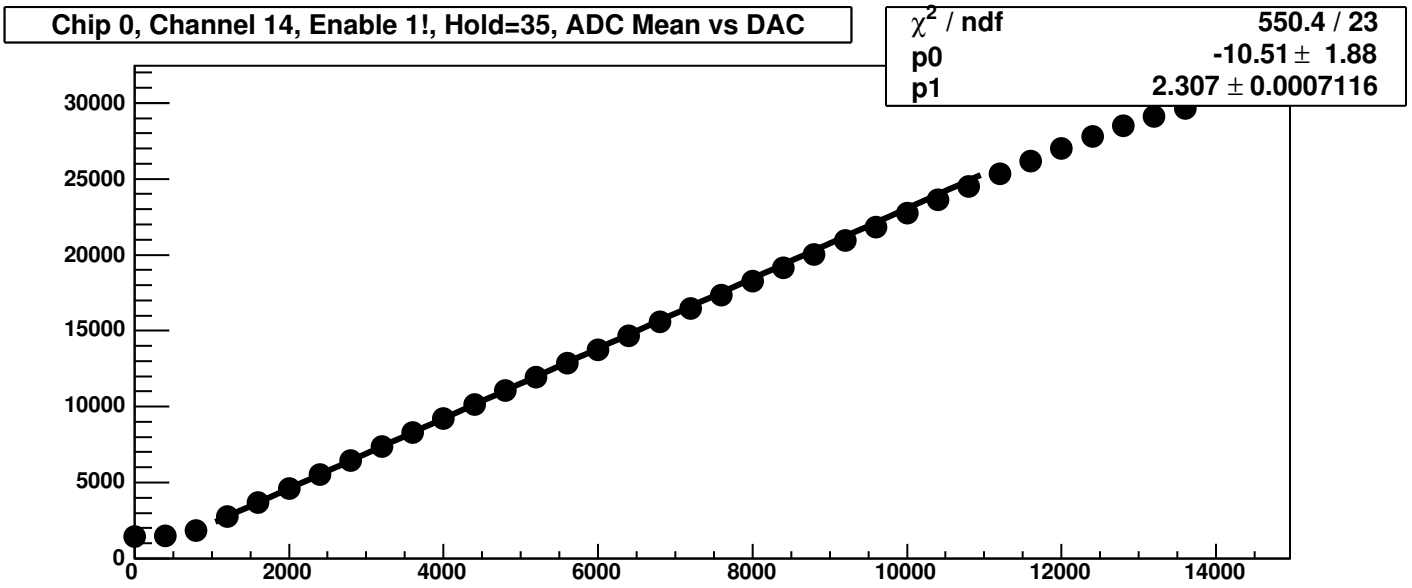

Chip 0, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

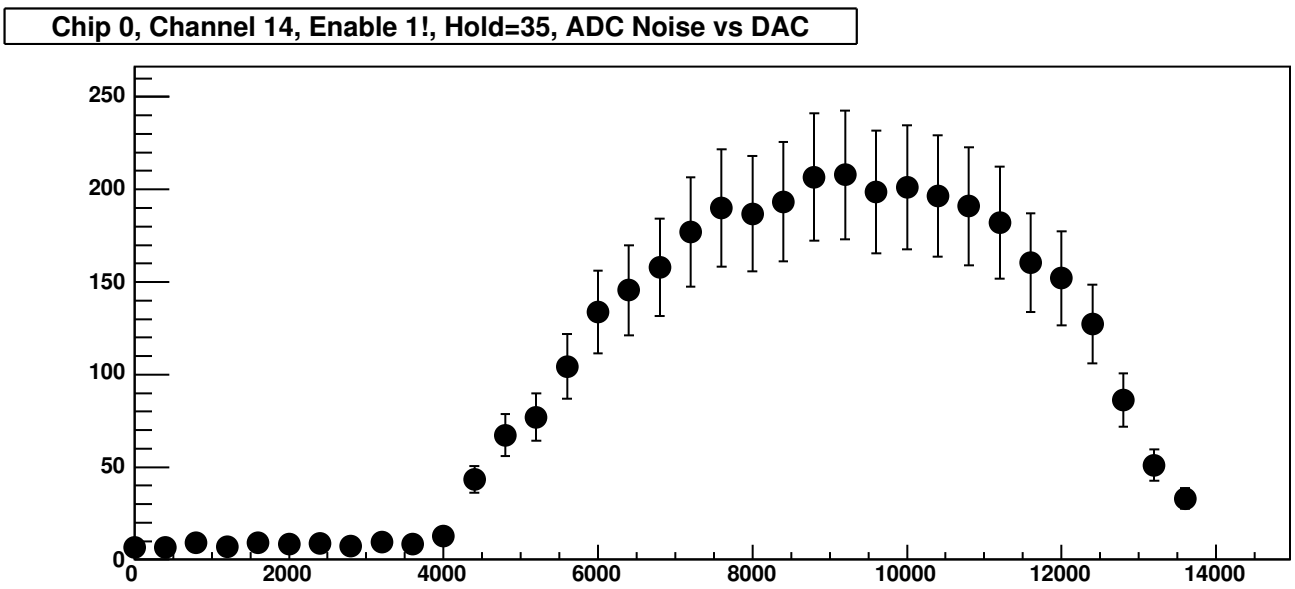

Chip 0, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

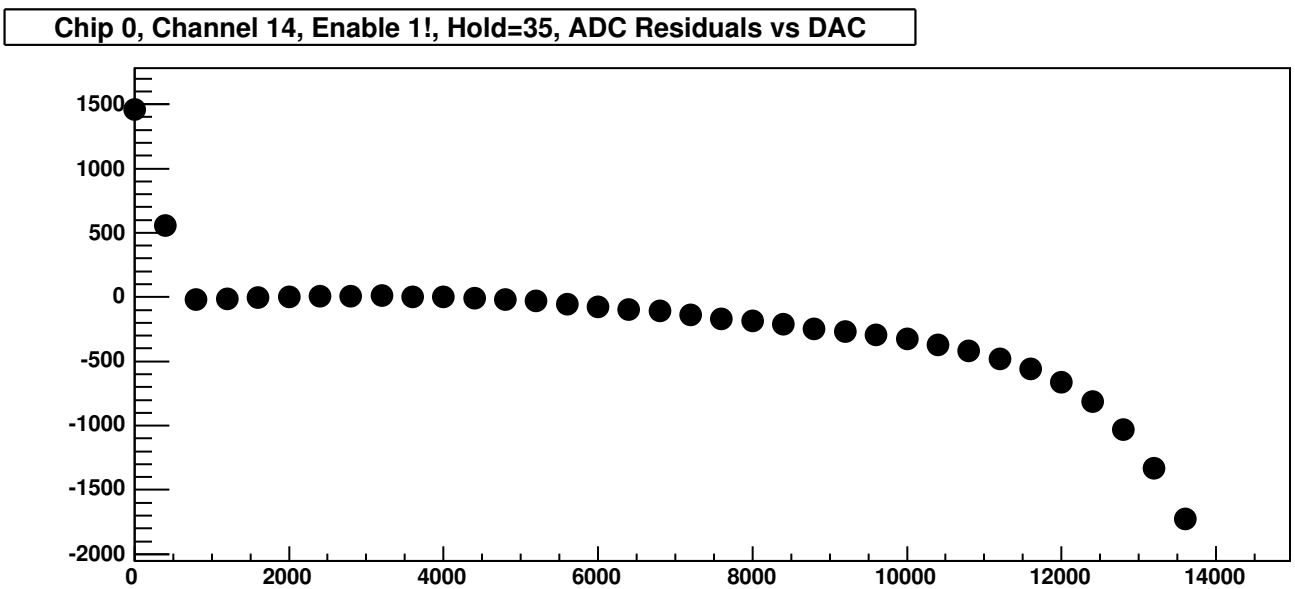

Chip 0, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 810.2 / 23
p0 -277.8 ± 1.357
p1 0.178 ± 0.0002261
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

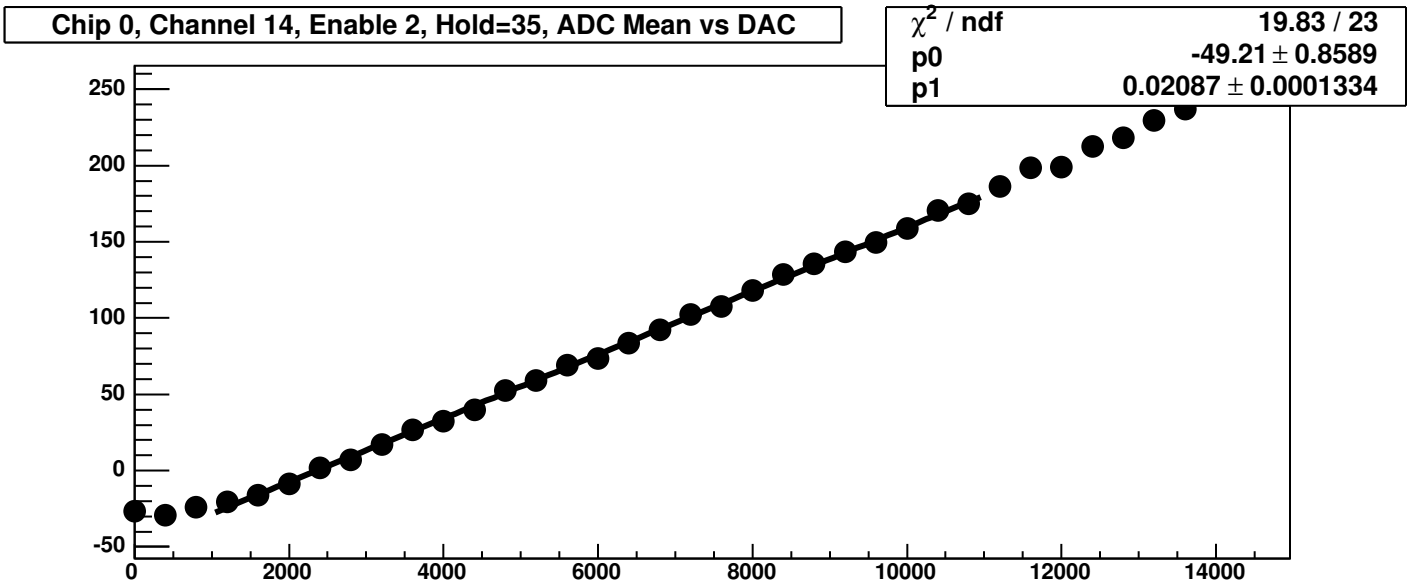

Chip 0, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

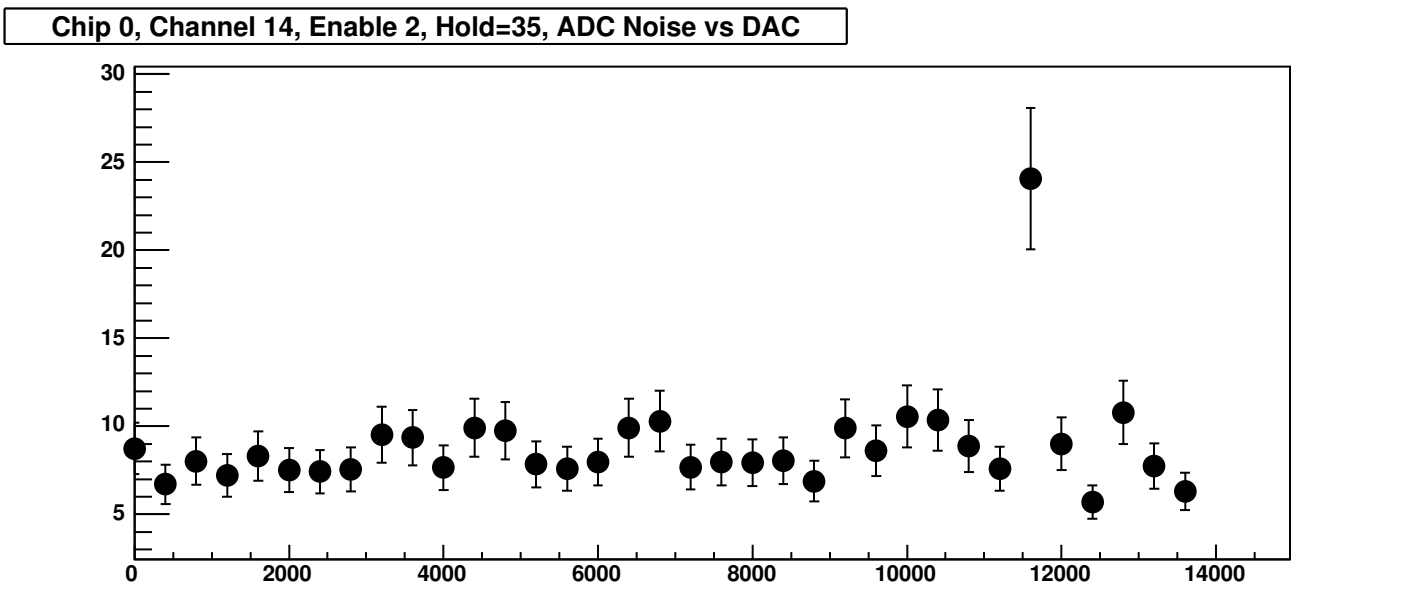

Chip 0, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

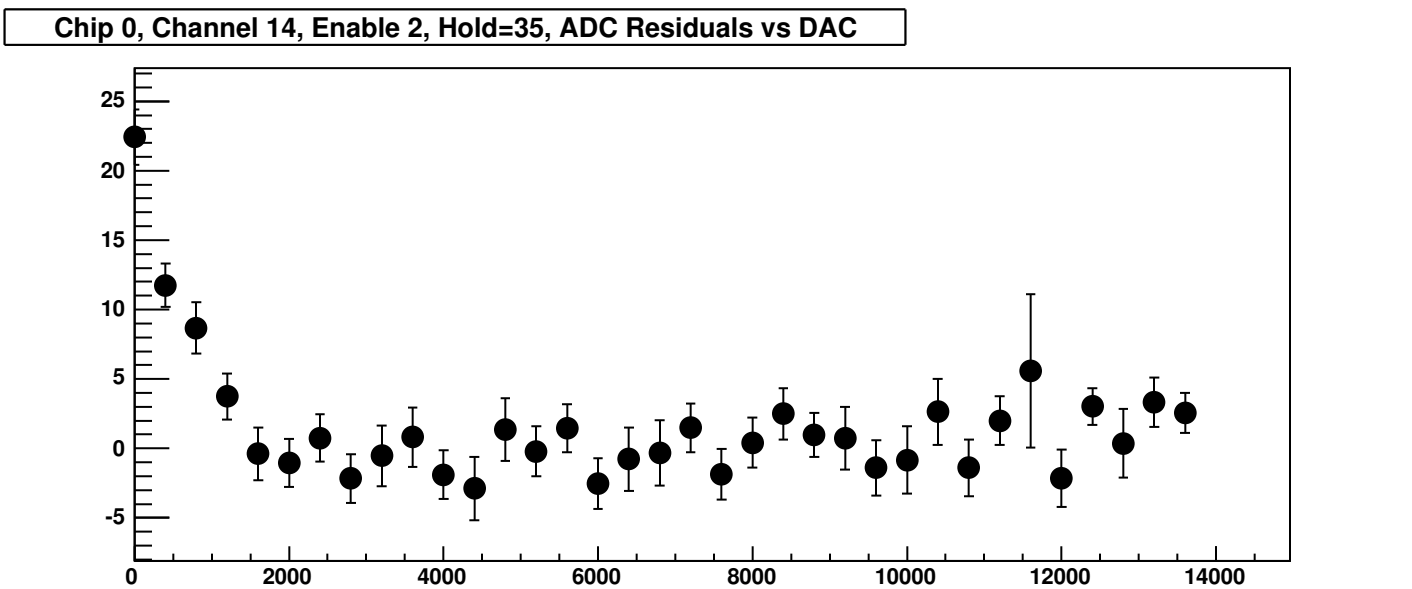

Chip 0, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 550.4 / 23
p0 -10.51 ± 1.88
p1 2.307 ± 0.0007116
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

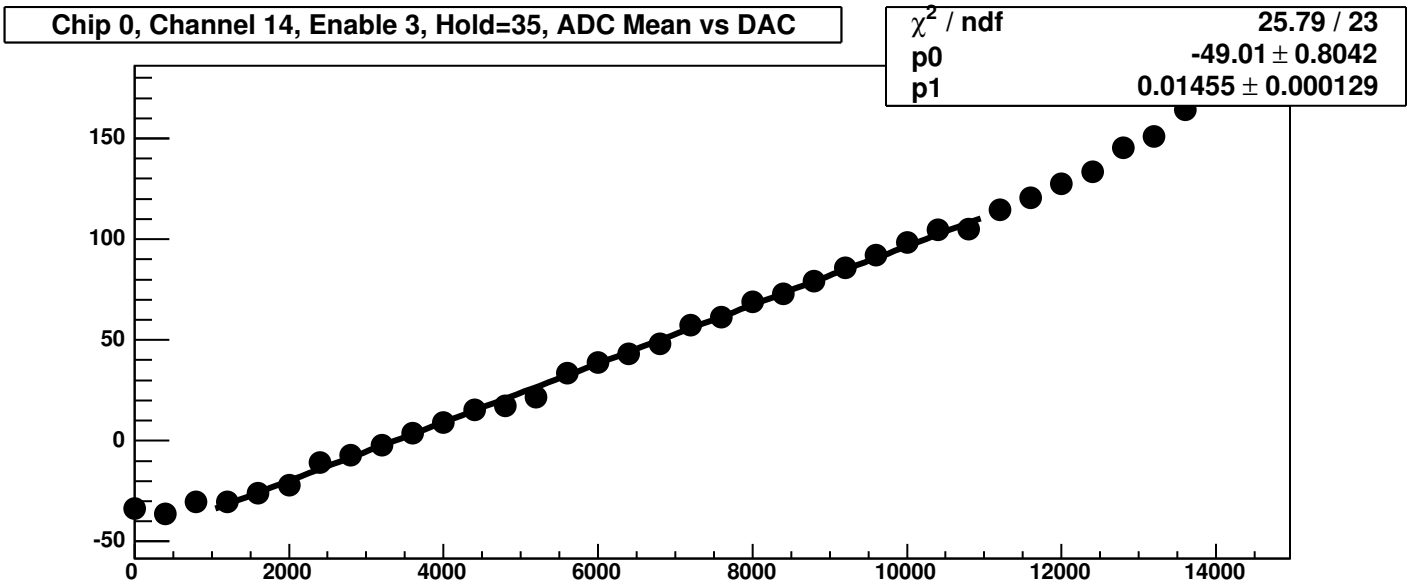

Chip 0, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

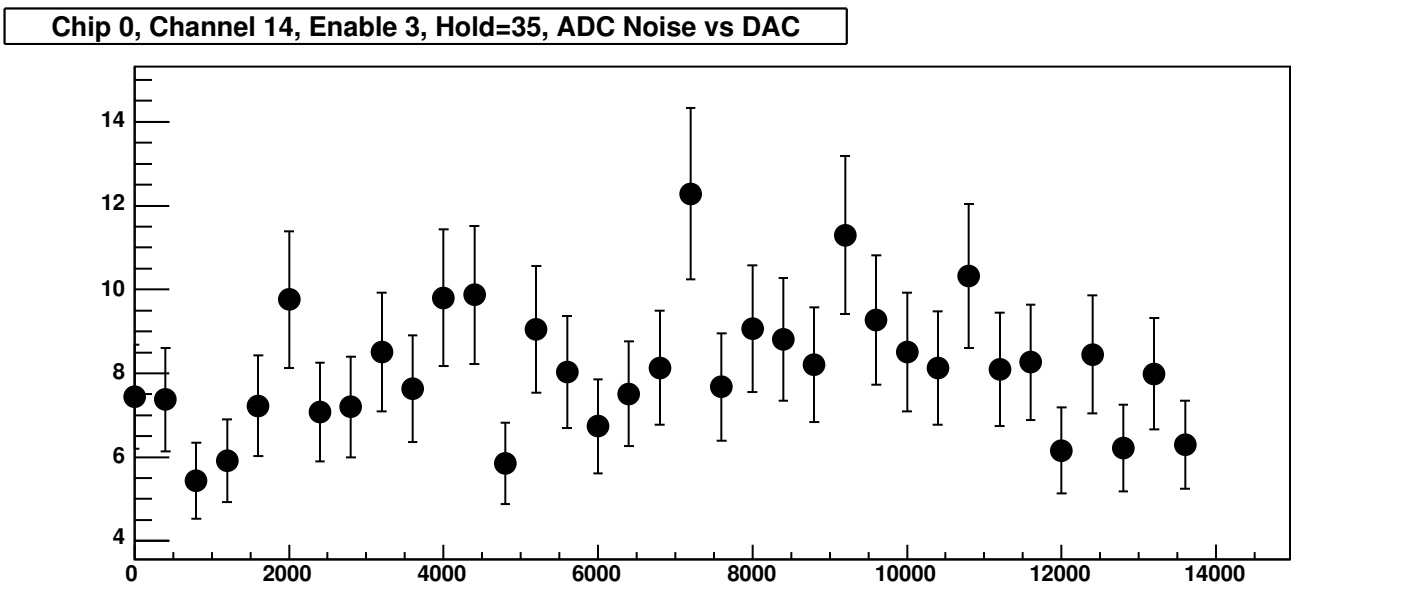

Chip 0, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

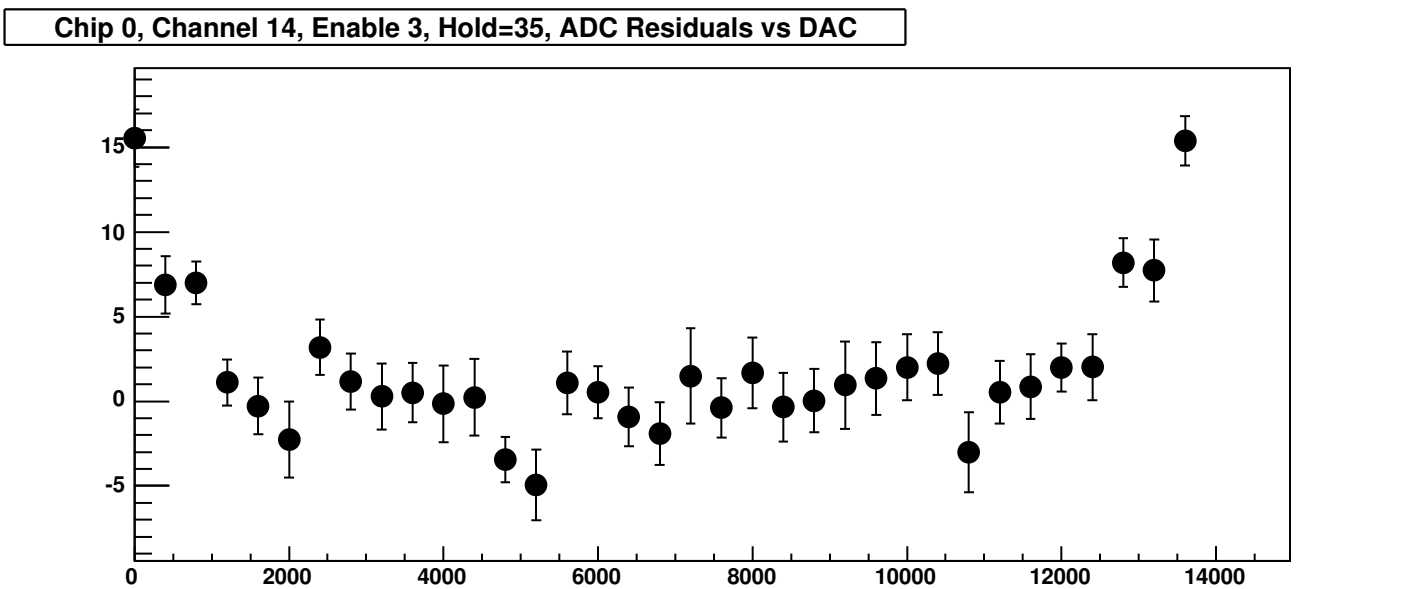

Chip 0, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 19.83 / 23
p0 -49.21 ± 0.8589
p1 0.02087 ± 0.0001334
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

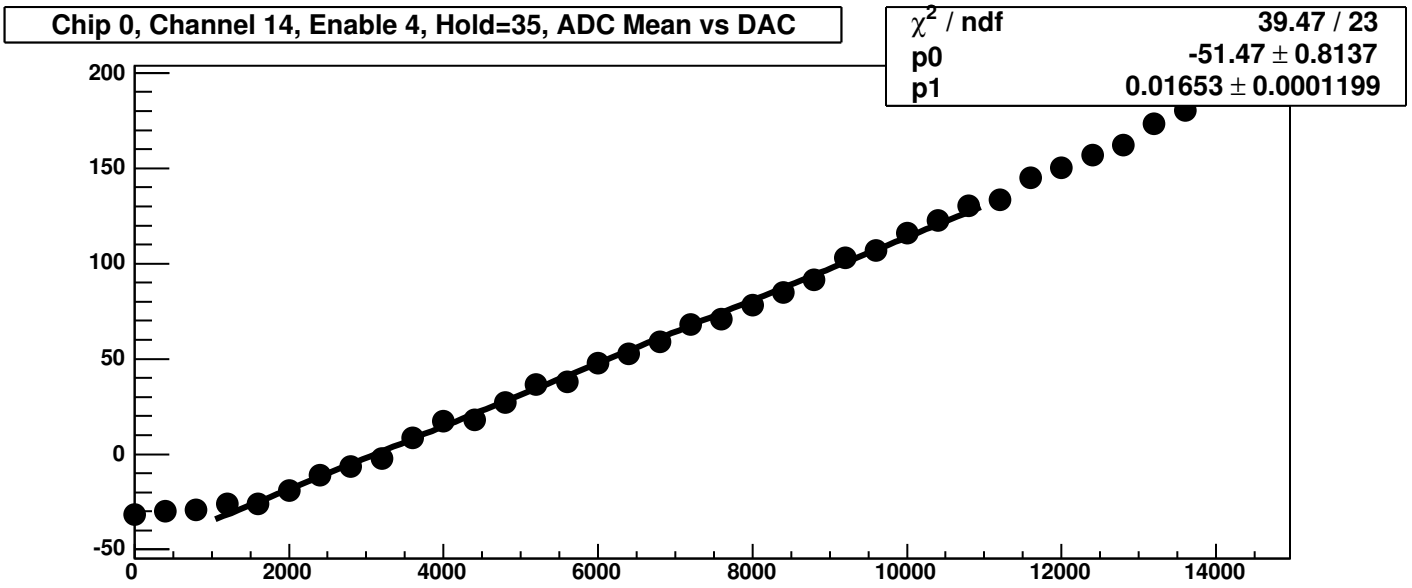

Chip 0, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

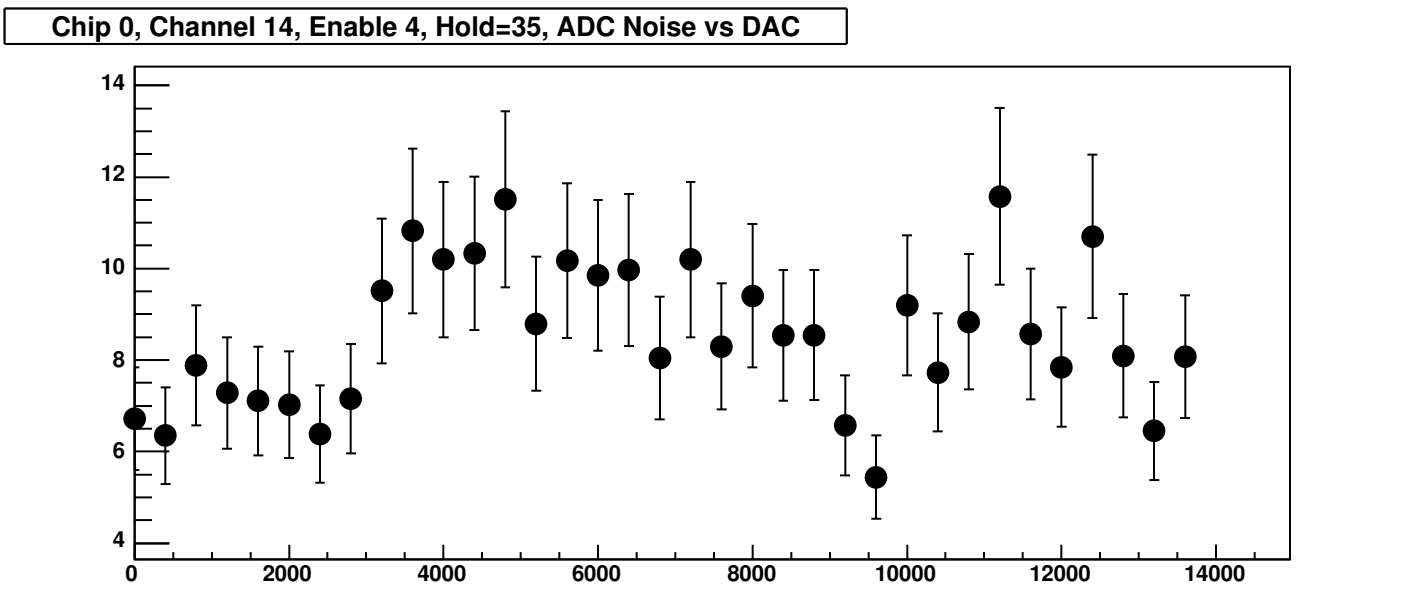

Chip 0, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

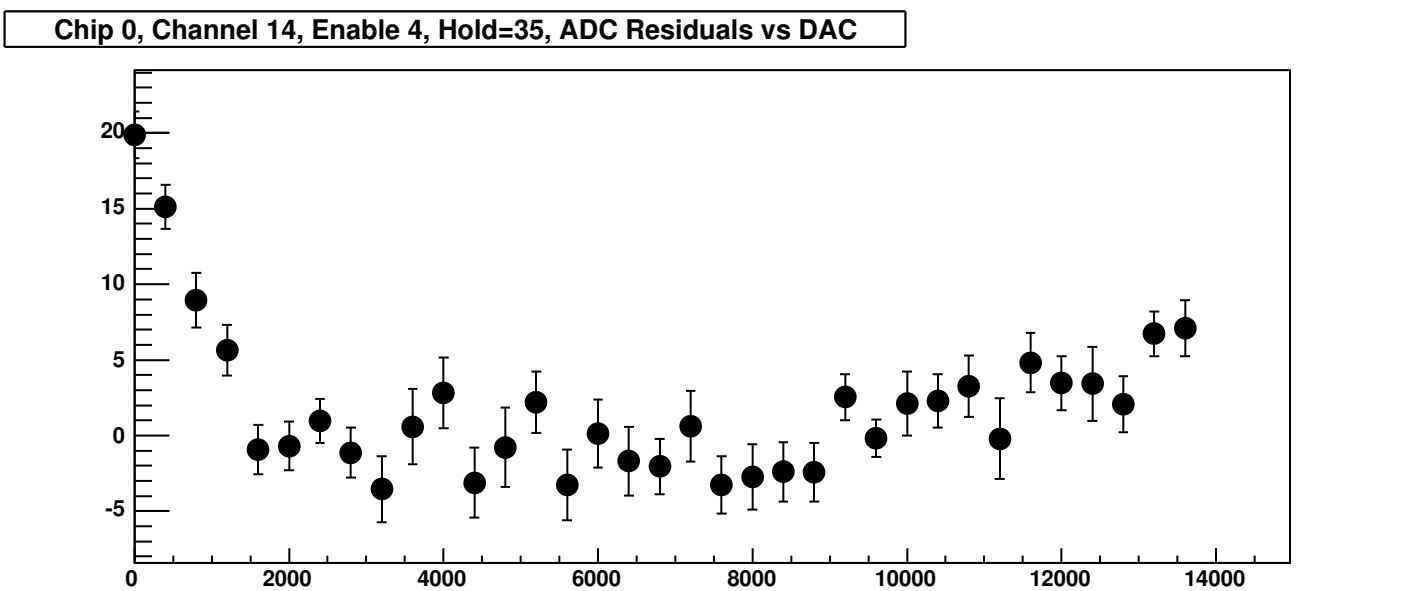

Chip 0, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.79 / 23
p0 -49.01 ± 0.8042
p1 0.01455 ± 0.000129
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

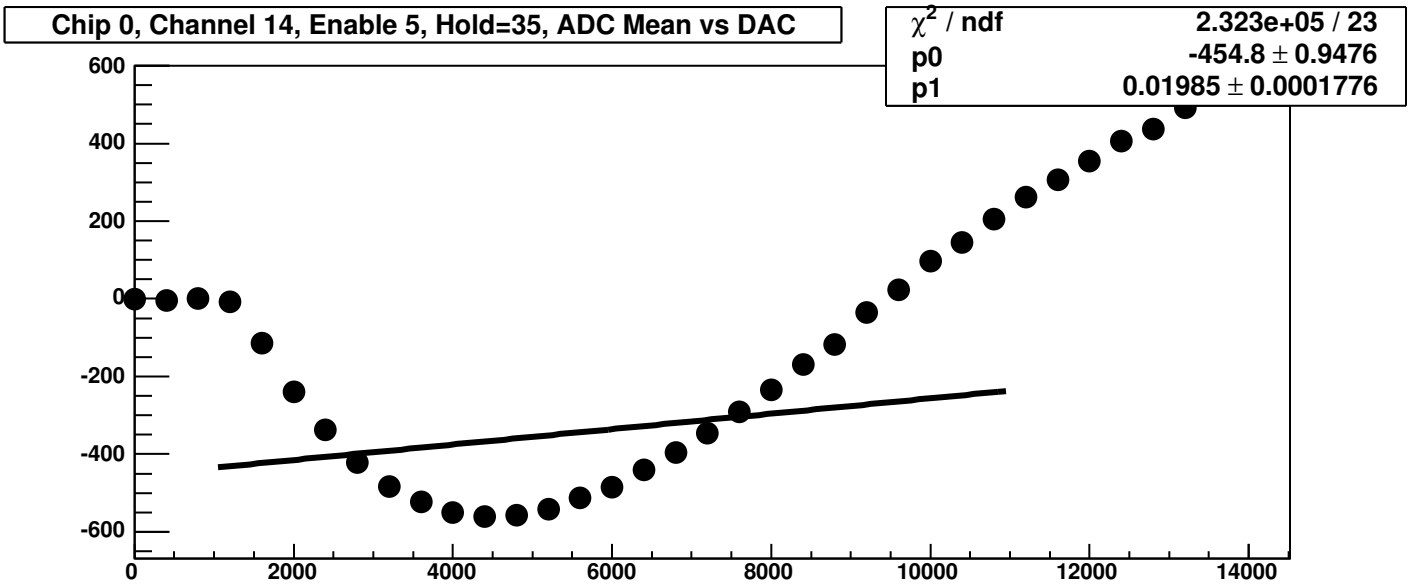

Chip 0, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

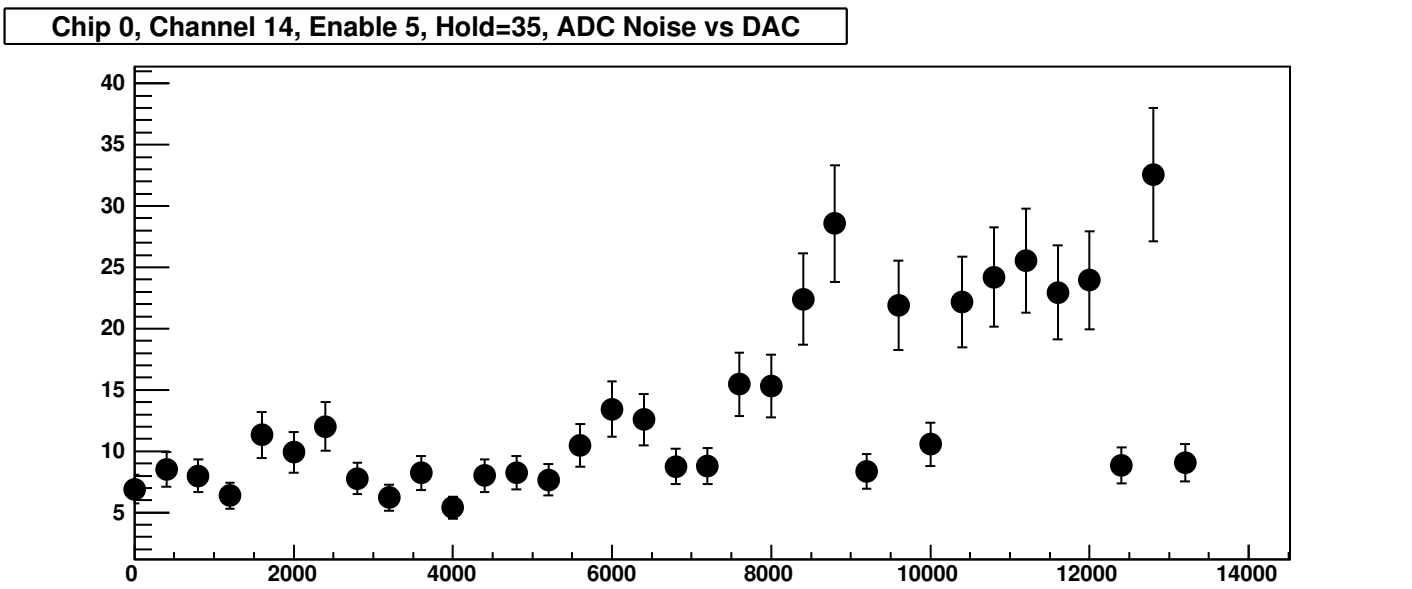

Chip 0, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

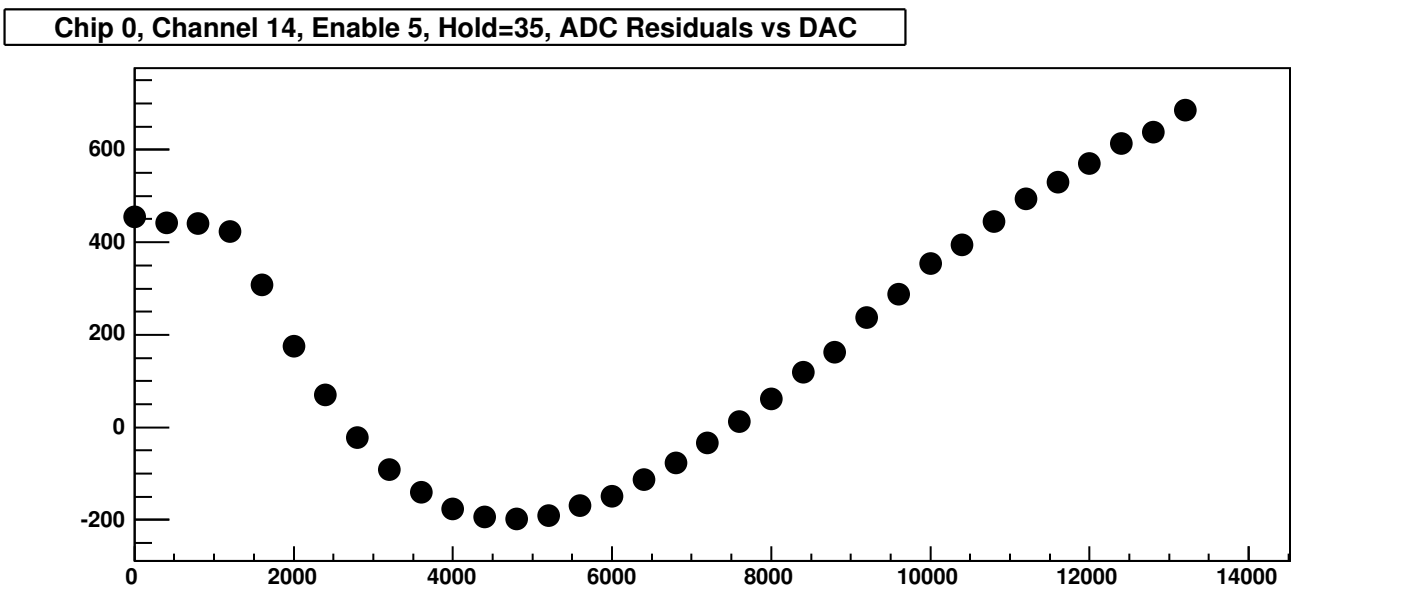

Chip 0, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 39.47 / 23
p0 -51.47 ± 0.8137
p1 0.01653 ± 0.0001199
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

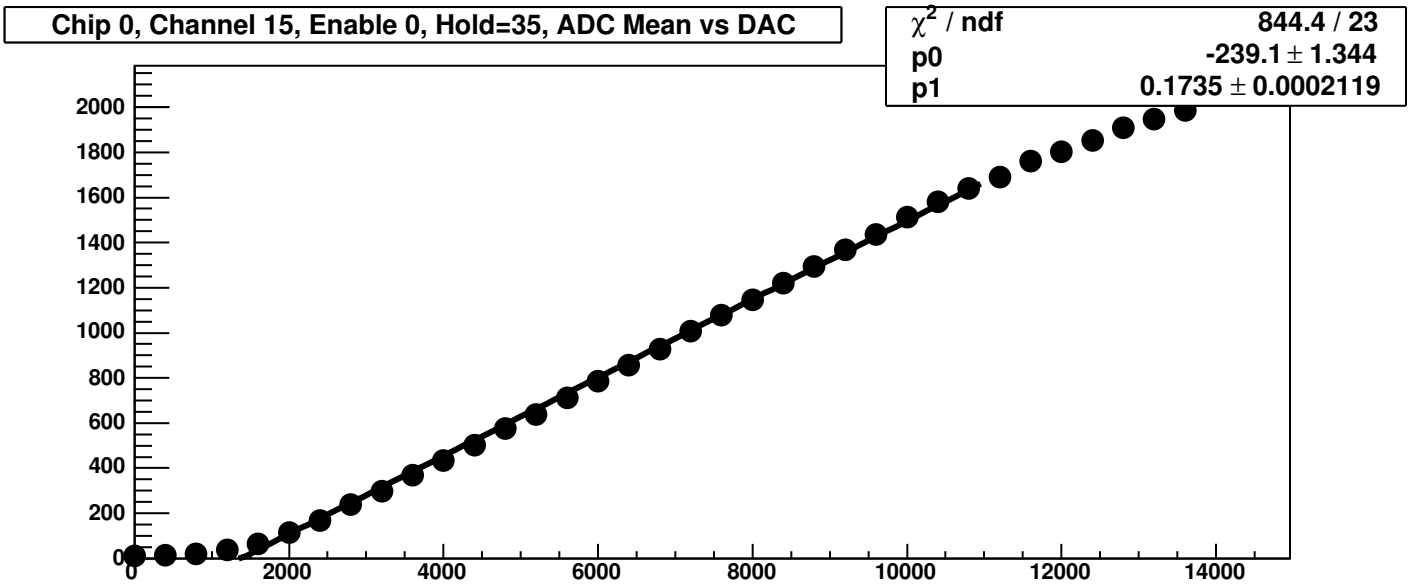

Chip 0, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

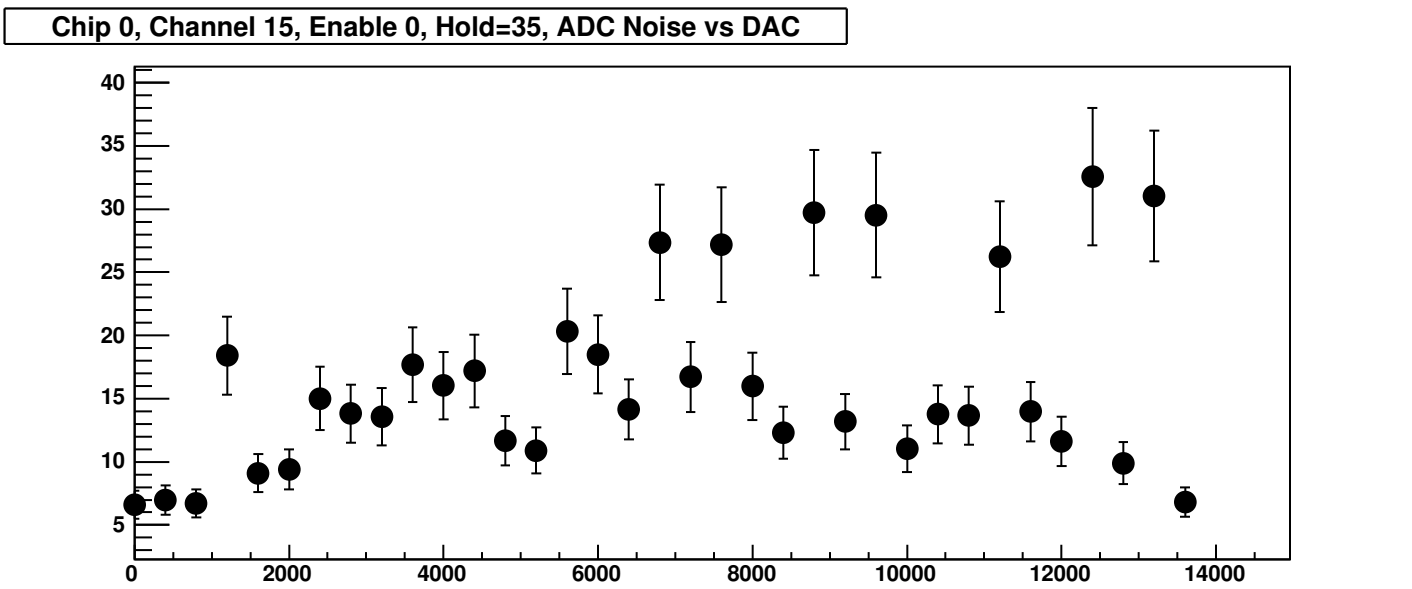

Chip 0, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

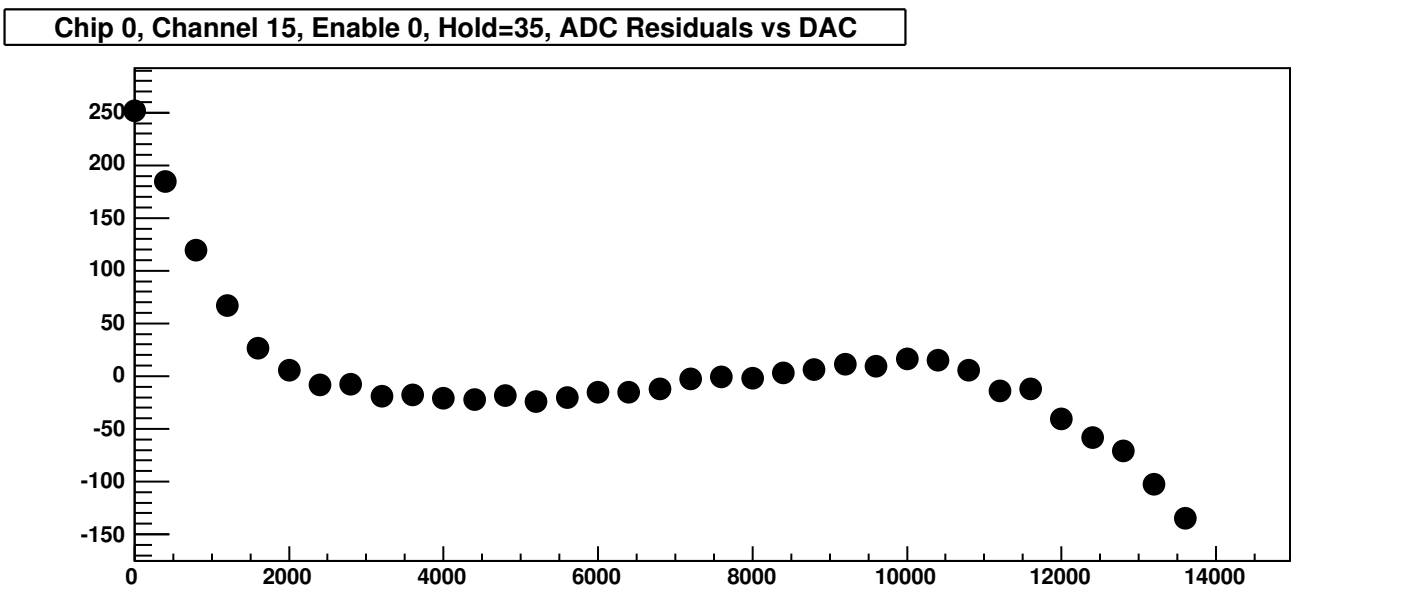

Chip 0, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2.323e+05 / 23
p0 -454.8 ± 0.9476
p1 0.01985 ± 0.0001776
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

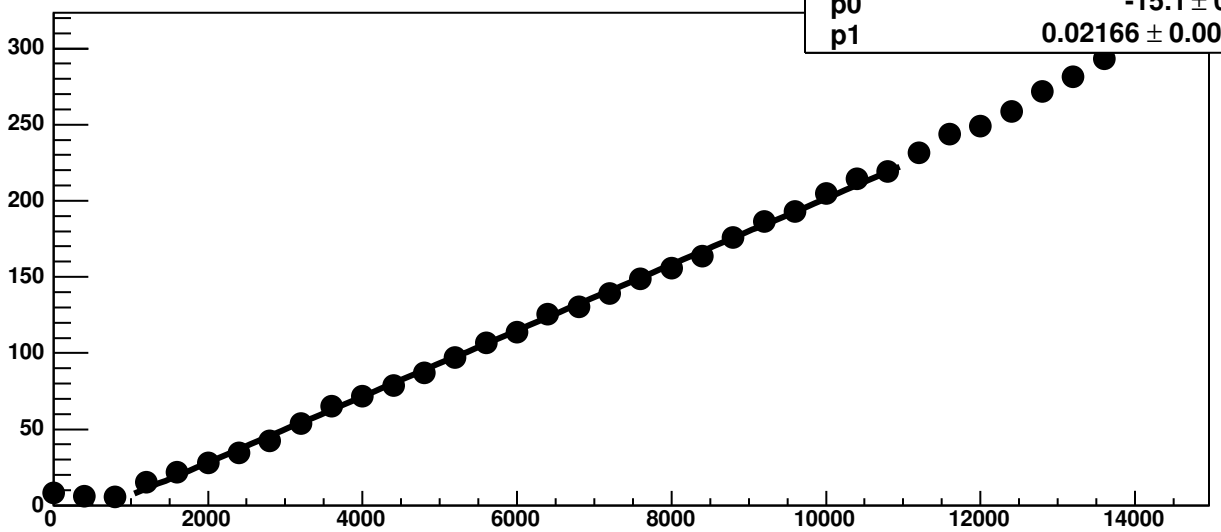

Chip 0, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 844.4 / 23
p0 -239.1 ± 1.344
p1 0.1735 ± 0.0002119
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

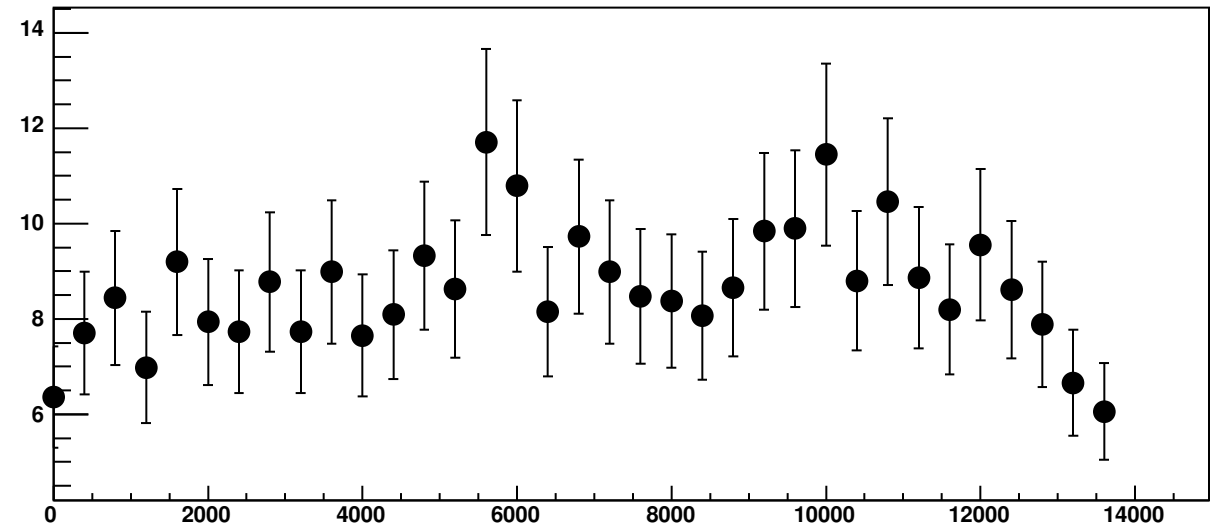

Chip 0, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

Chip 0, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

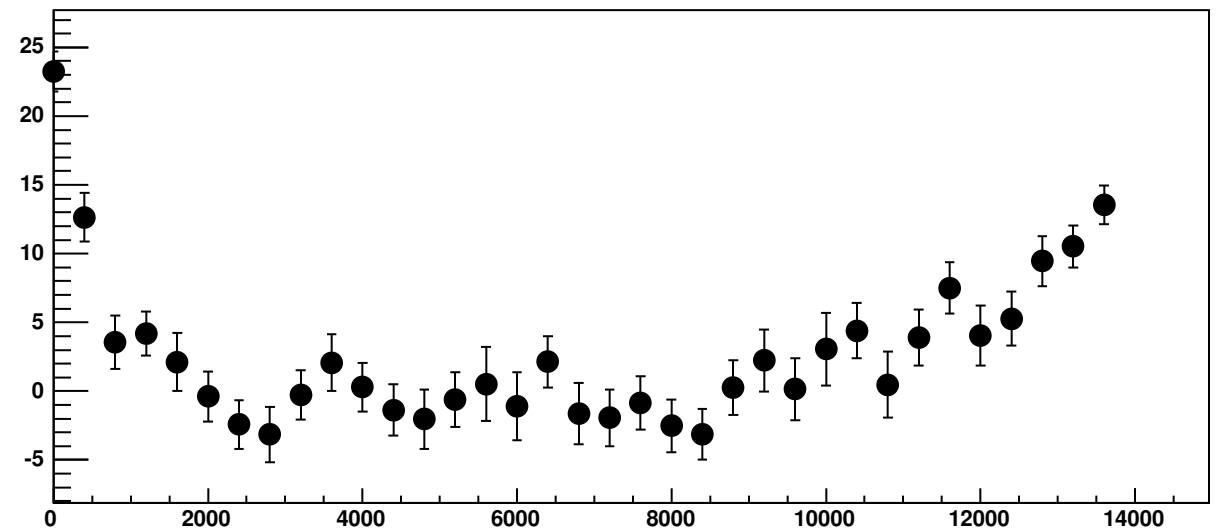
χ^2 / ndf	29.65 / 23
p0	-15.1 ± 0.8785
p1	0.02166 ± 0.0001389

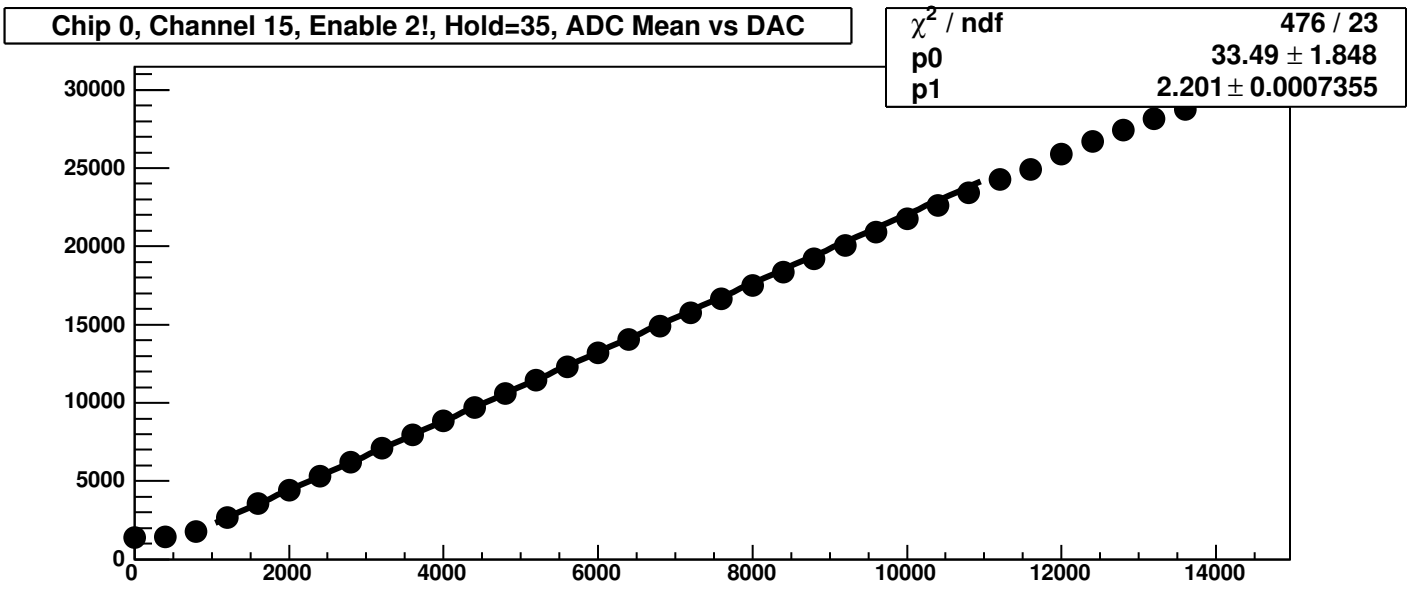


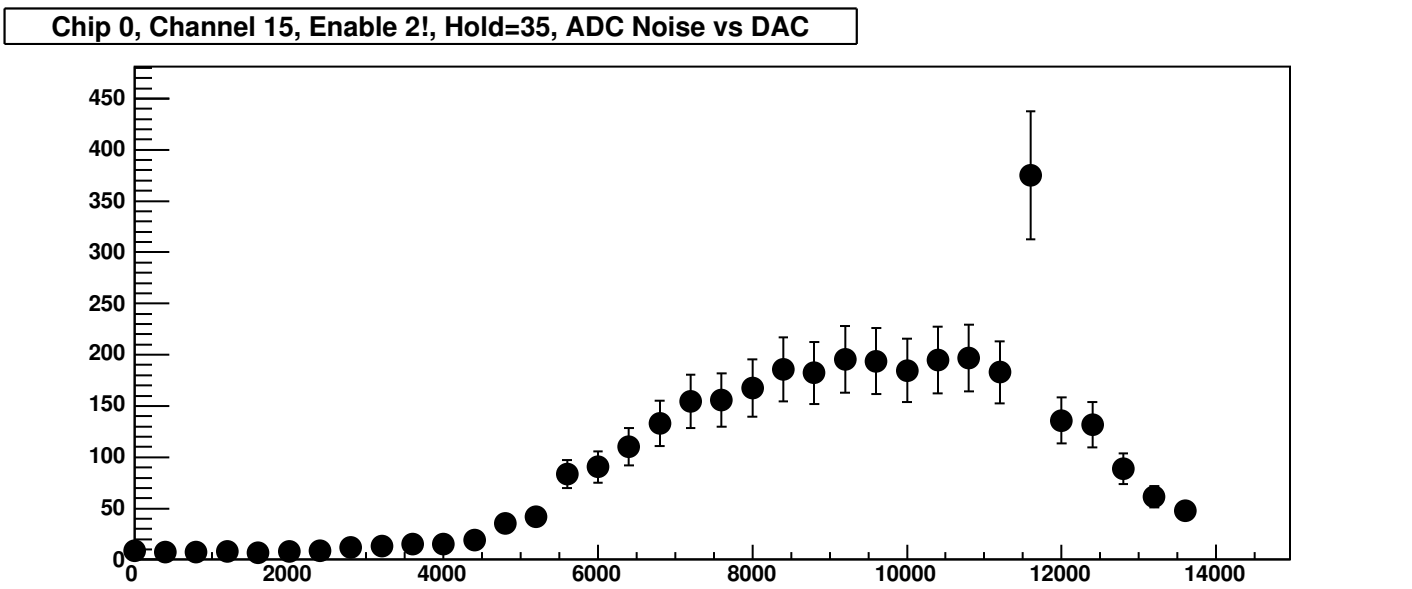
Chip 0, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

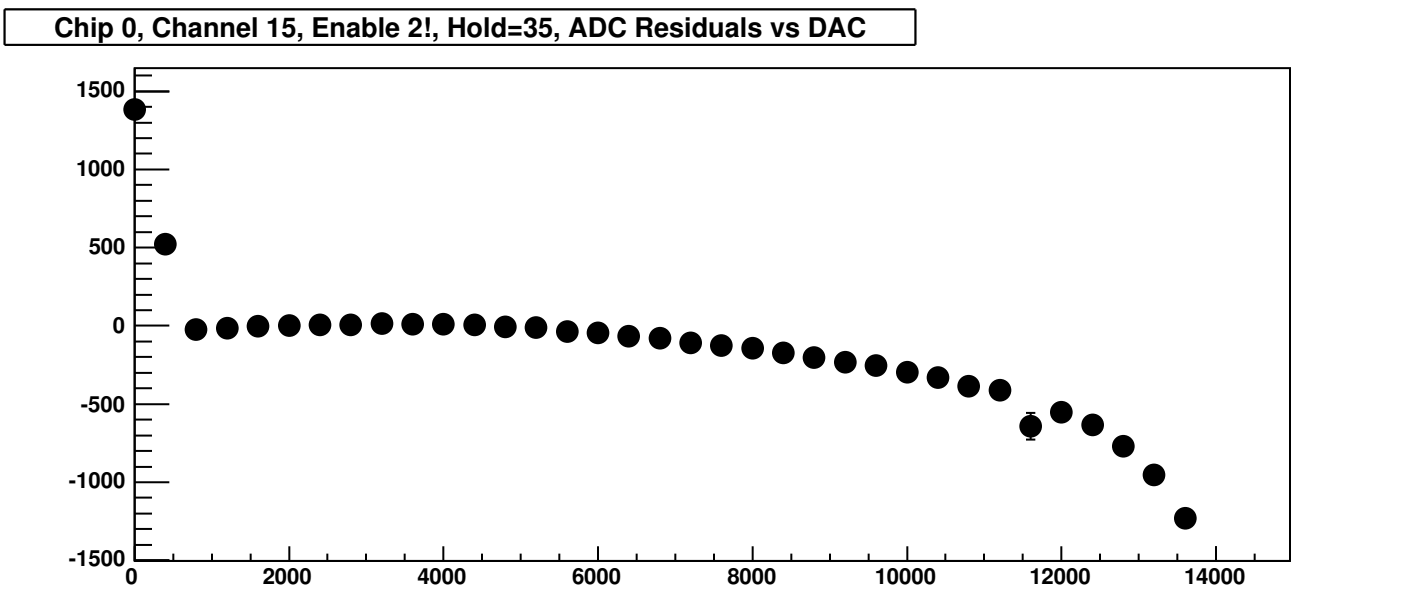


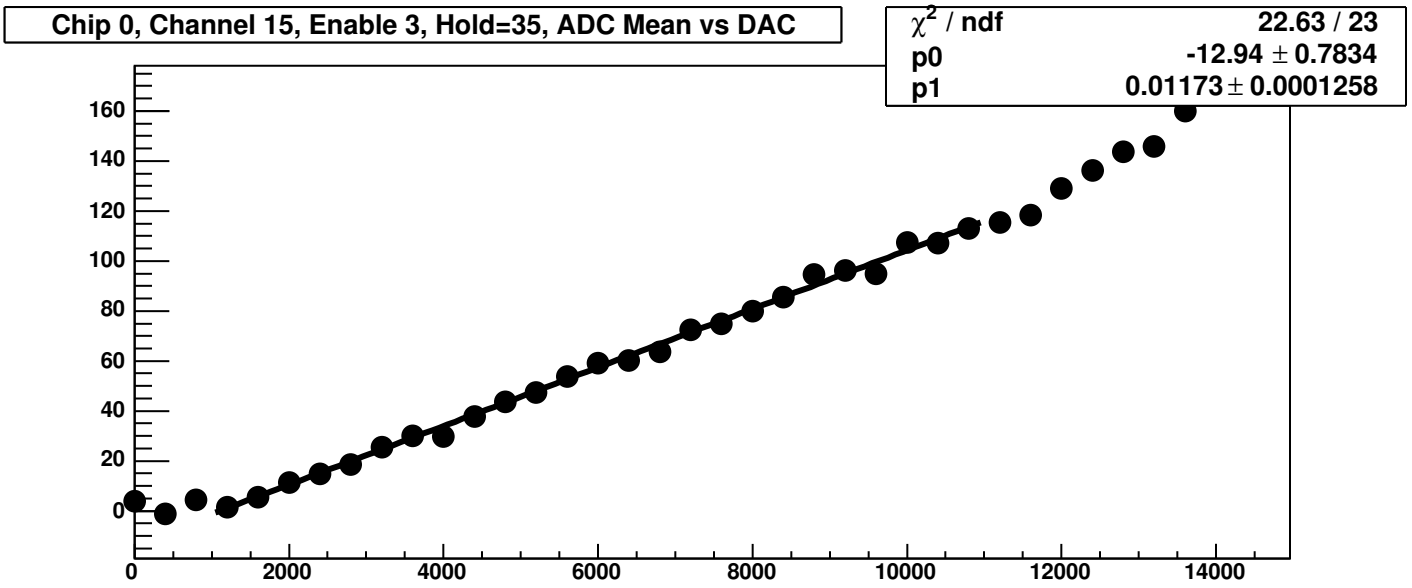
Chip 0, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

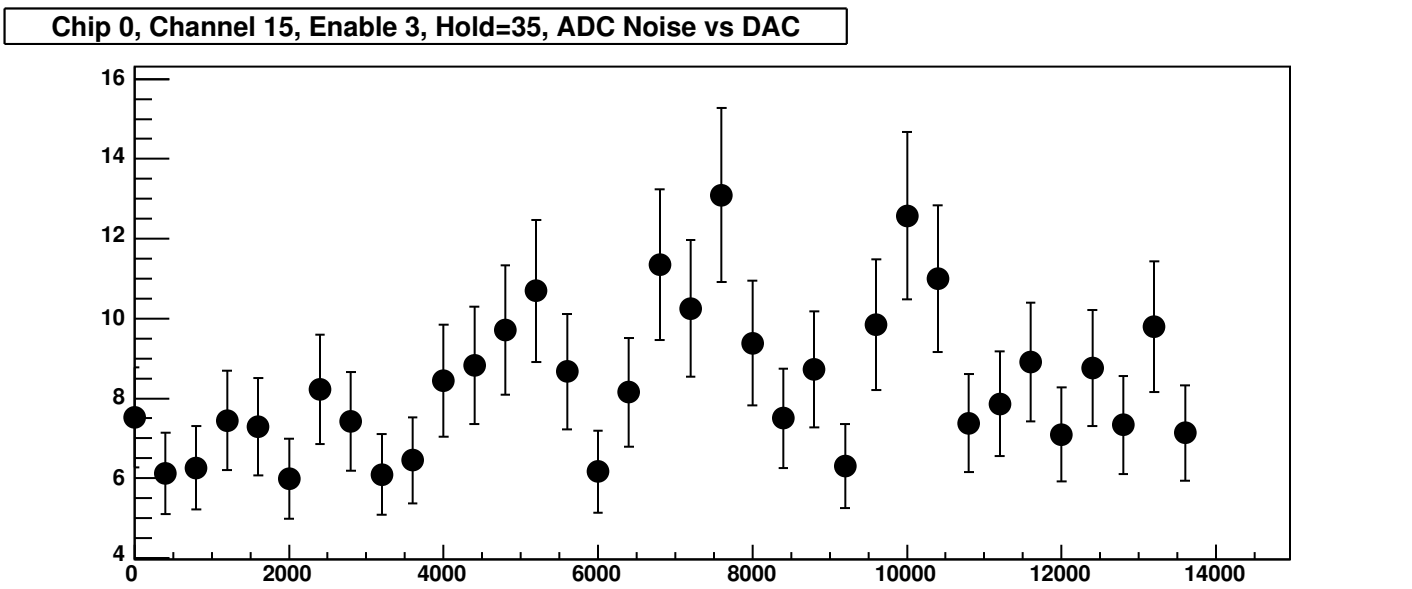


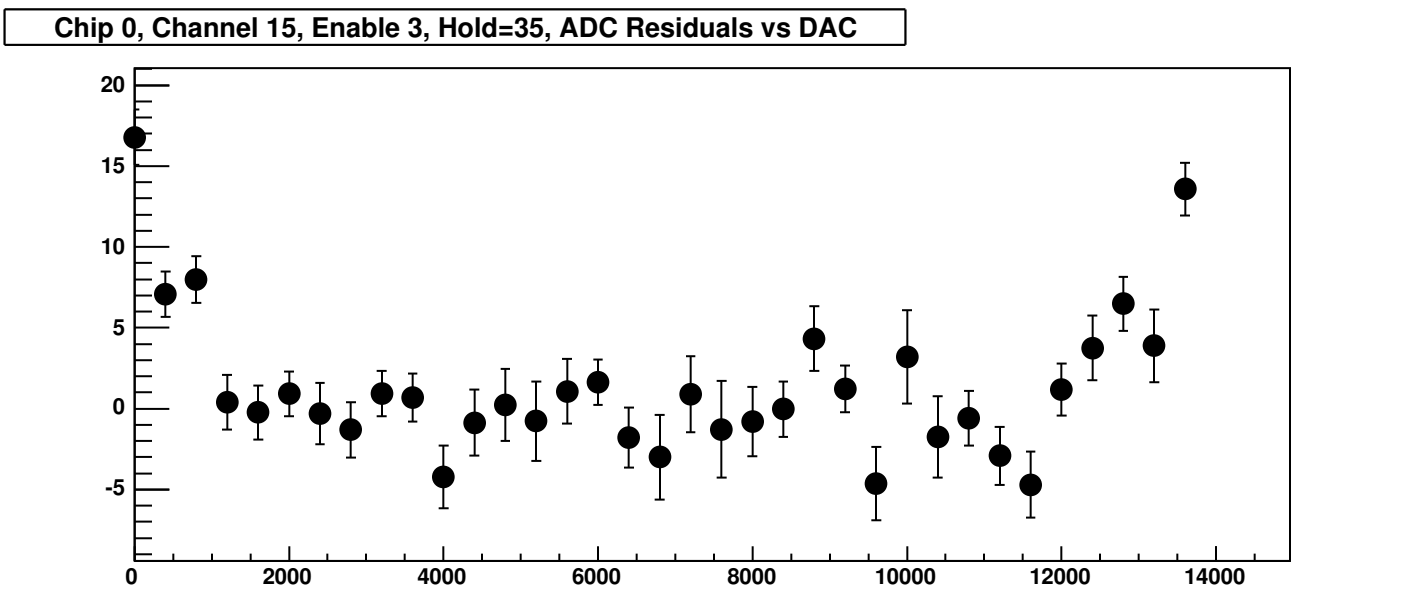

Chip 0, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 476 / 23
p0 33.49 ± 1.848
p1 2.201 ± 0.0007355
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

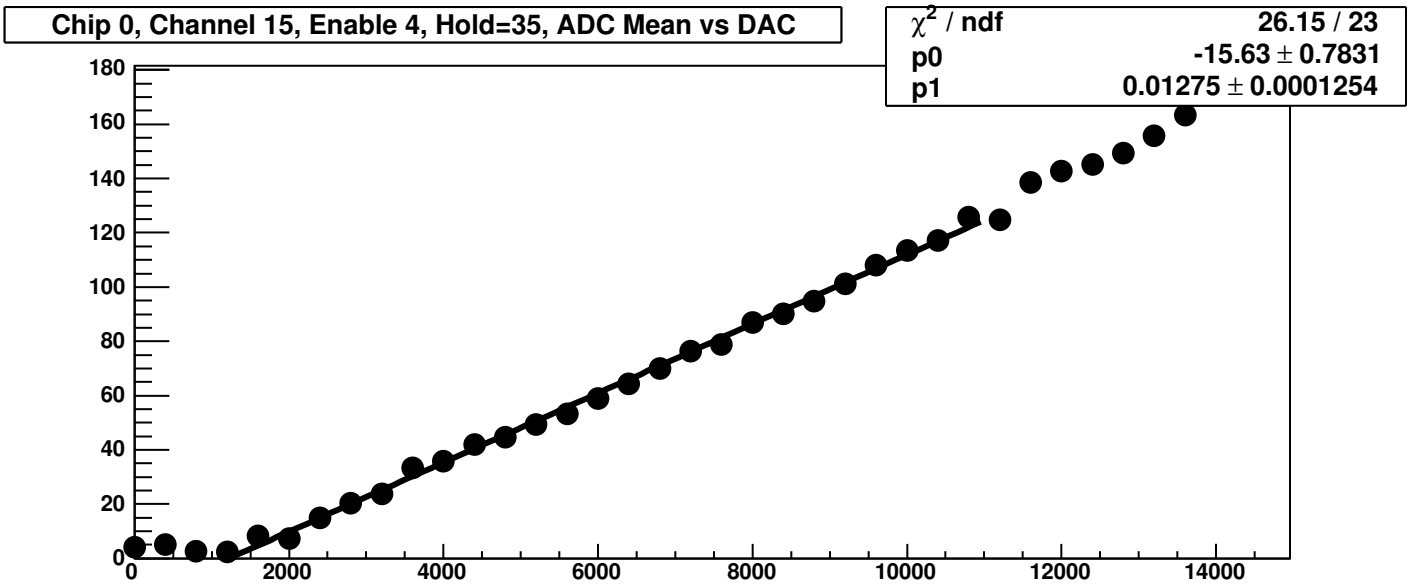

Chip 0, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

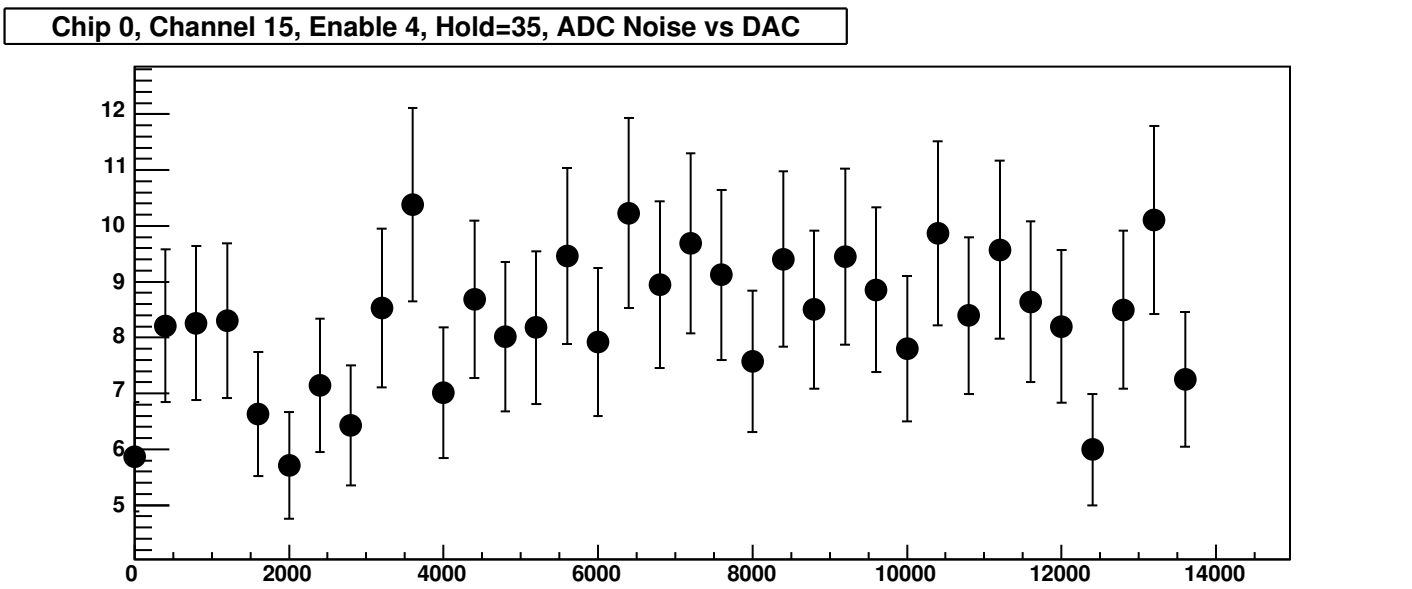

Chip 0, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

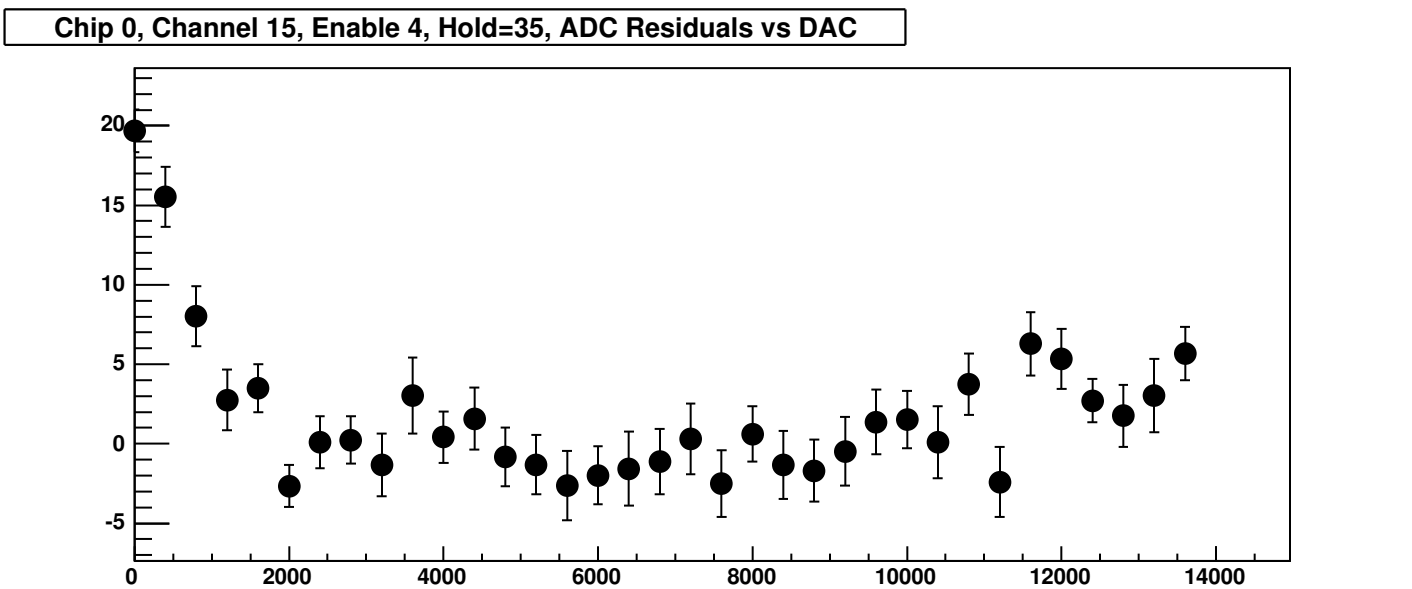

Chip 0, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 22.63 / 23
p0 -12.94 ± 0.7834
p1 0.01173 ± 0.0001258
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

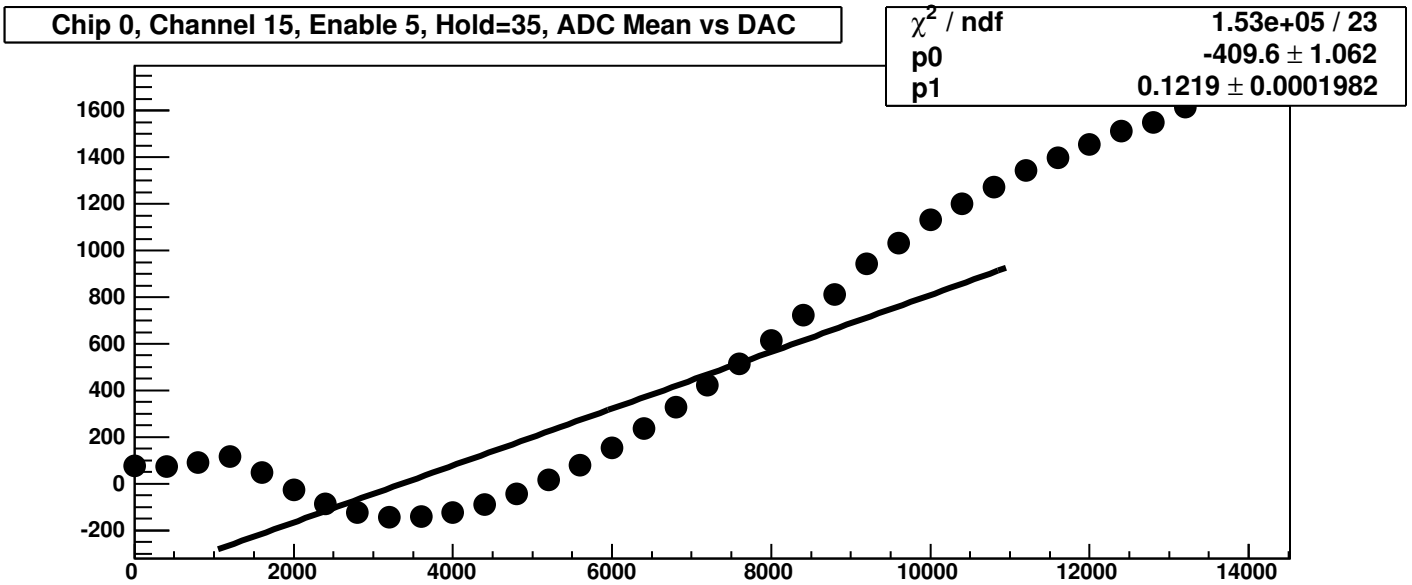

Chip 0, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

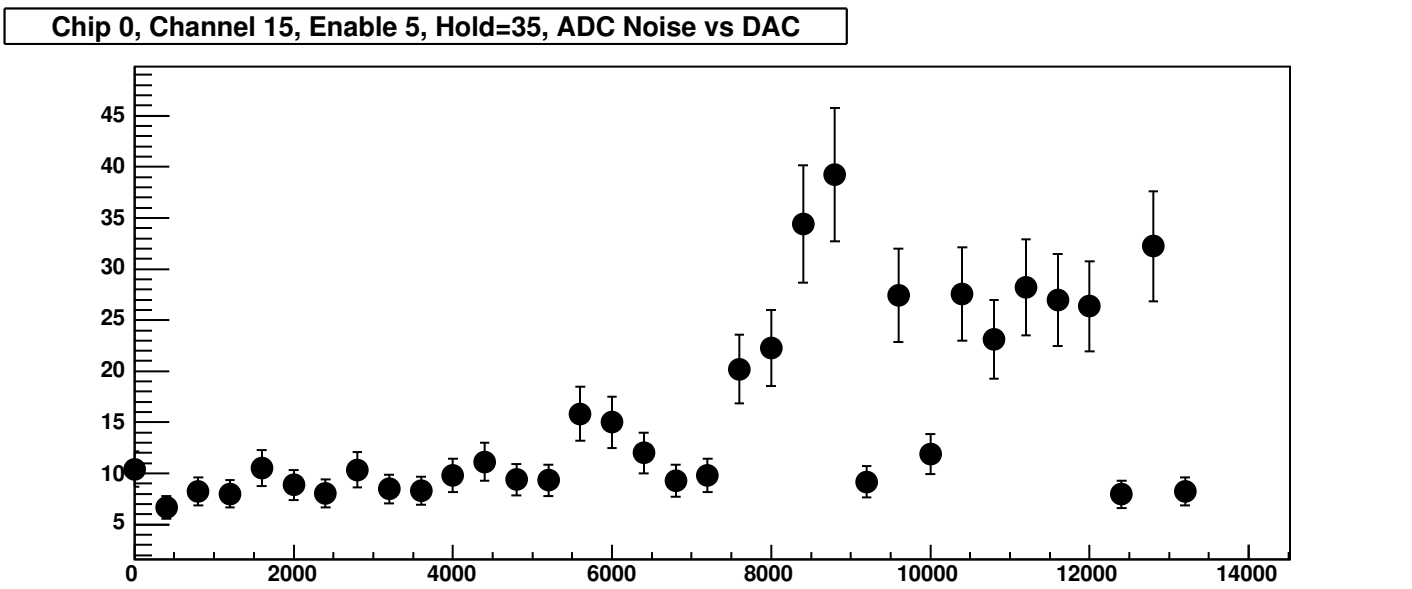

Chip 0, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

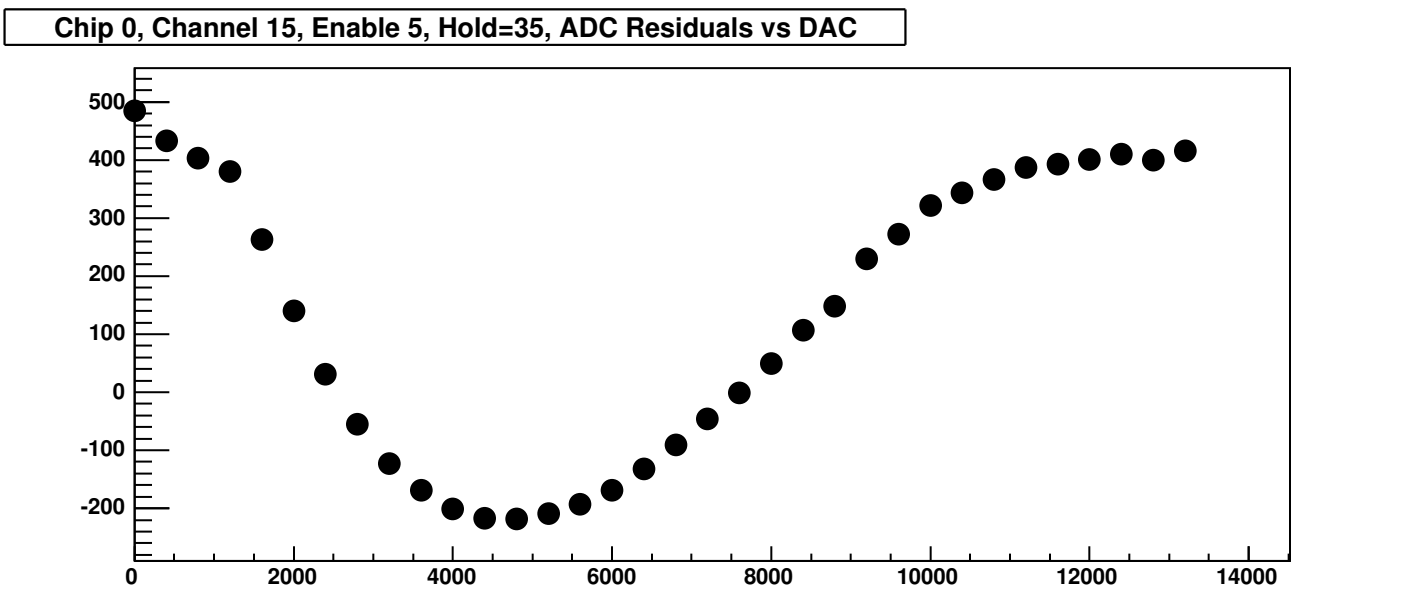

Chip 0, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.15 / 23
p0 -15.63 ± 0.7831
p1 0.01275 ± 0.0001254
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

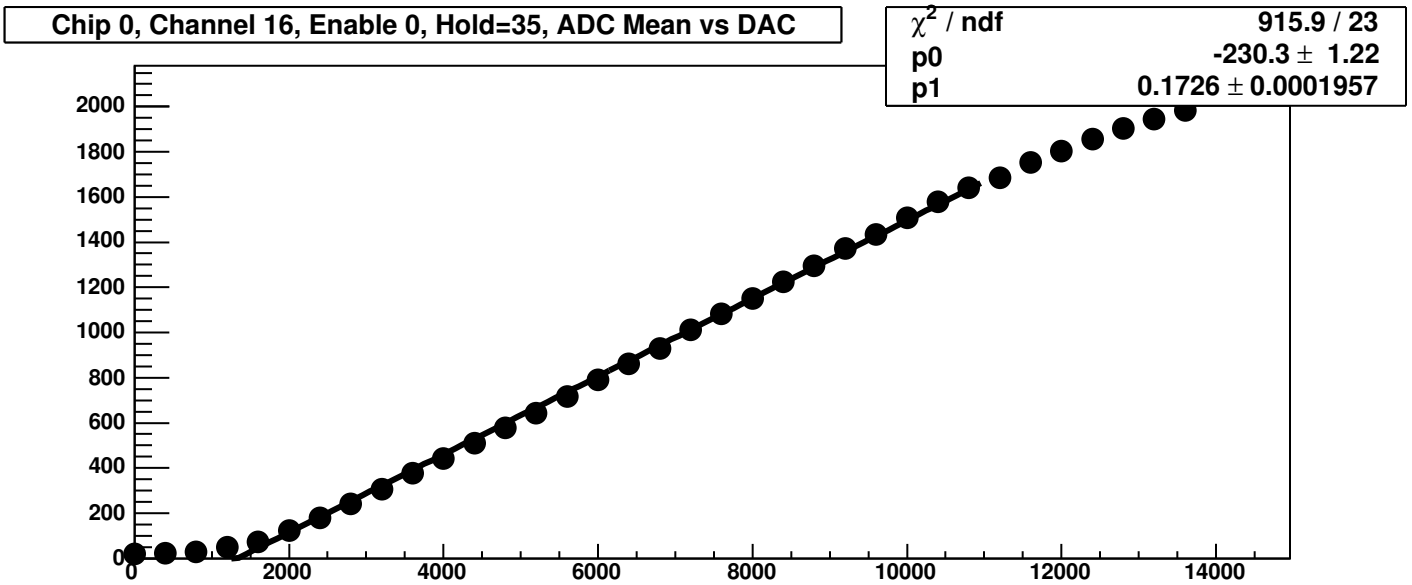

Chip 0, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

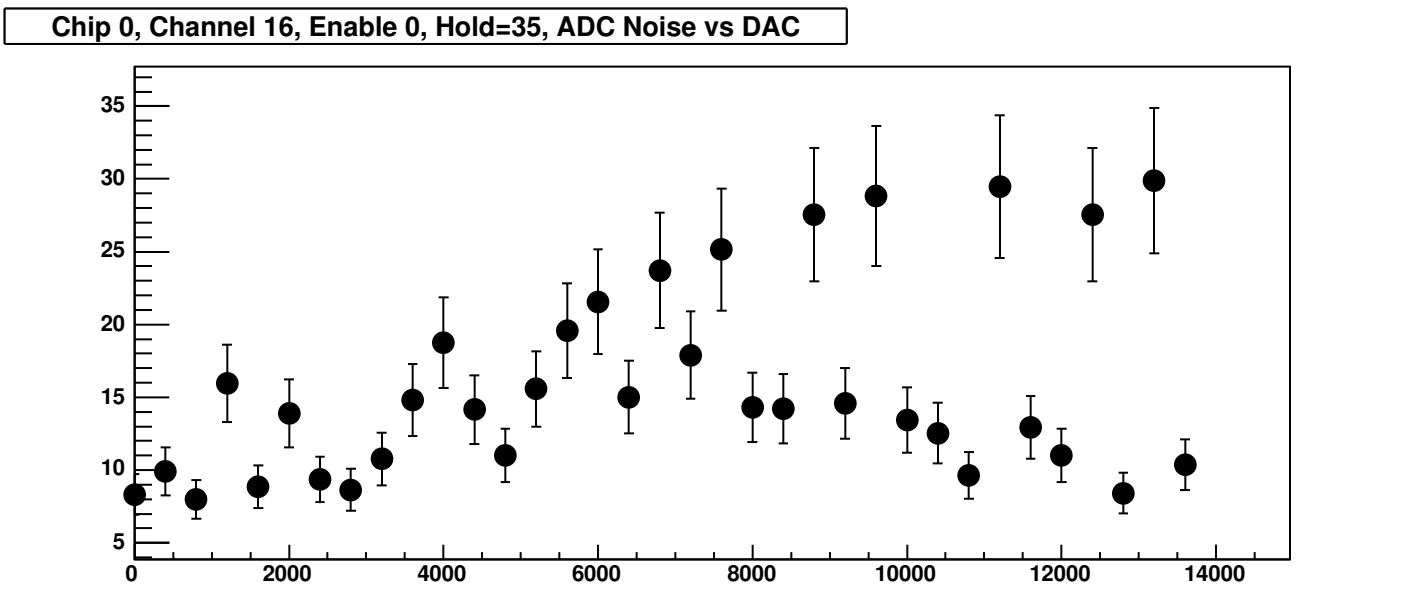

Chip 0, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

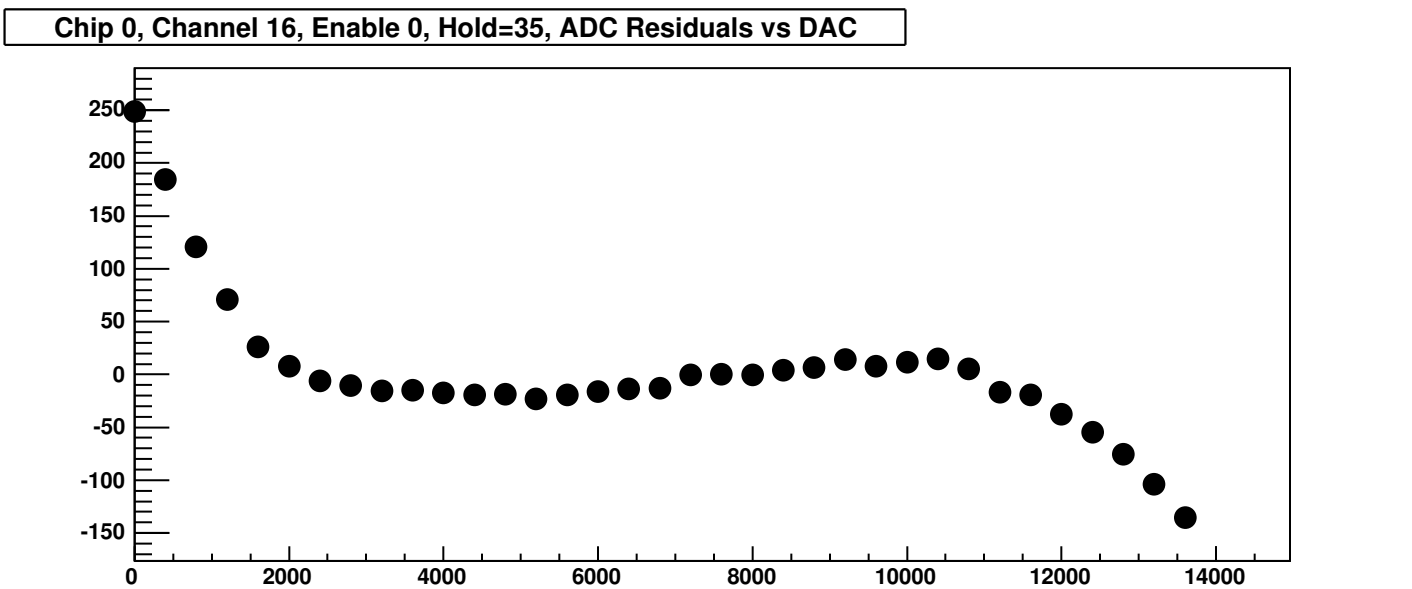

Chip 0, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.53e+05 / 23
p0 -409.6 ± 1.062
p1 0.1219 ± 0.0001982
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

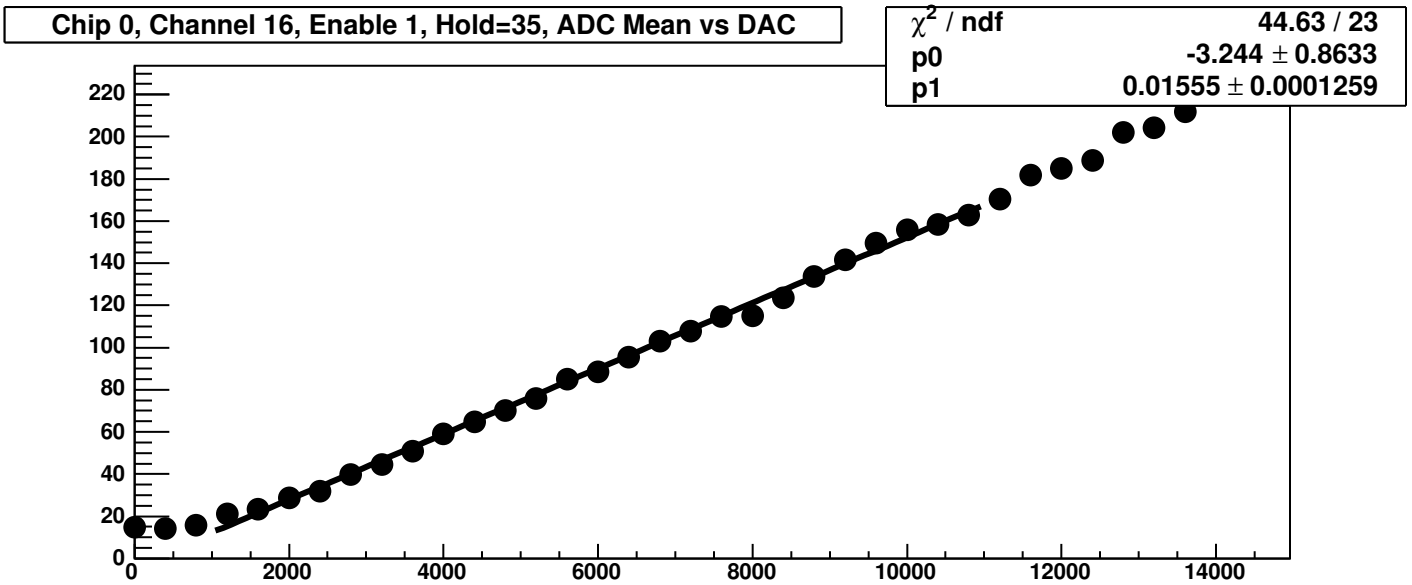

Chip 0, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

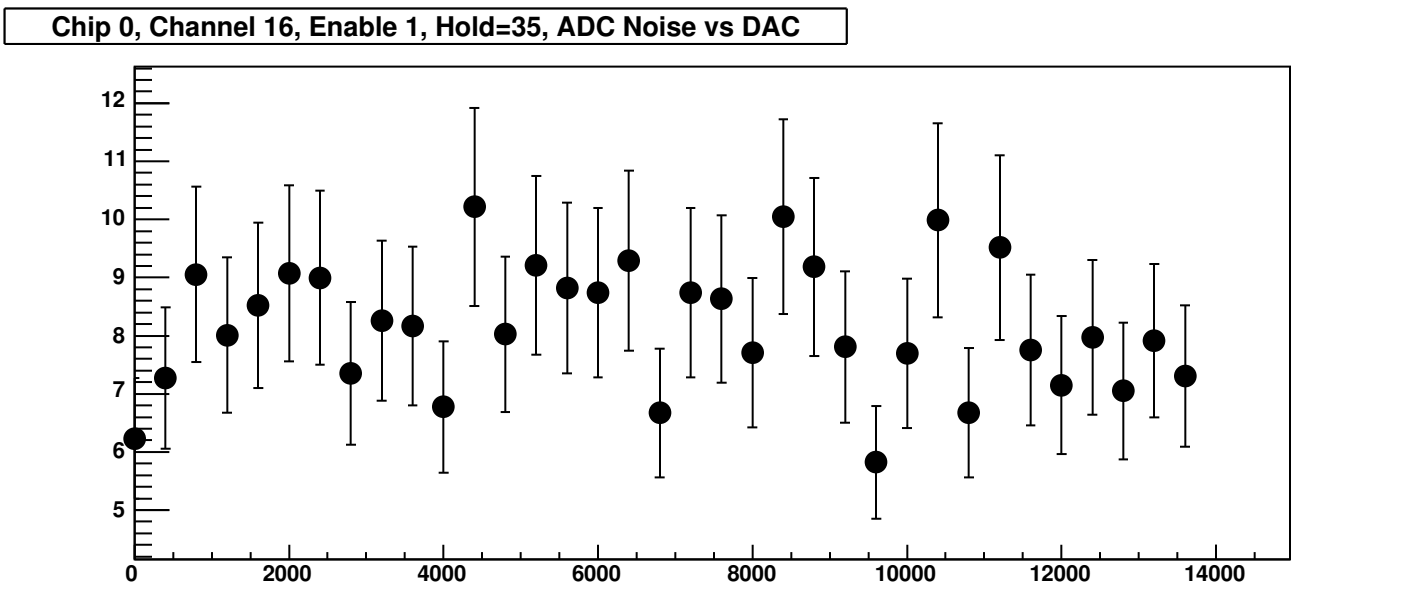

Chip 0, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

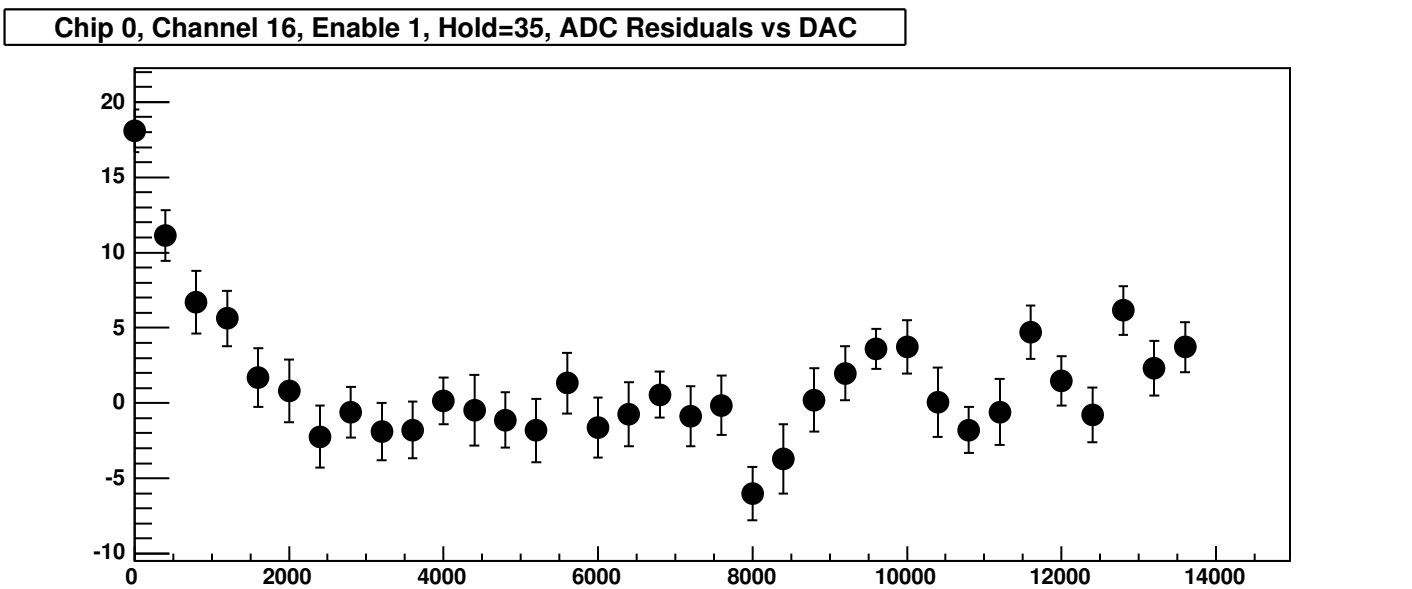

Chip 0, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 915.9 / 23
p0 -230.3 ± 1.22
p1 0.1726 ± 0.0001957
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

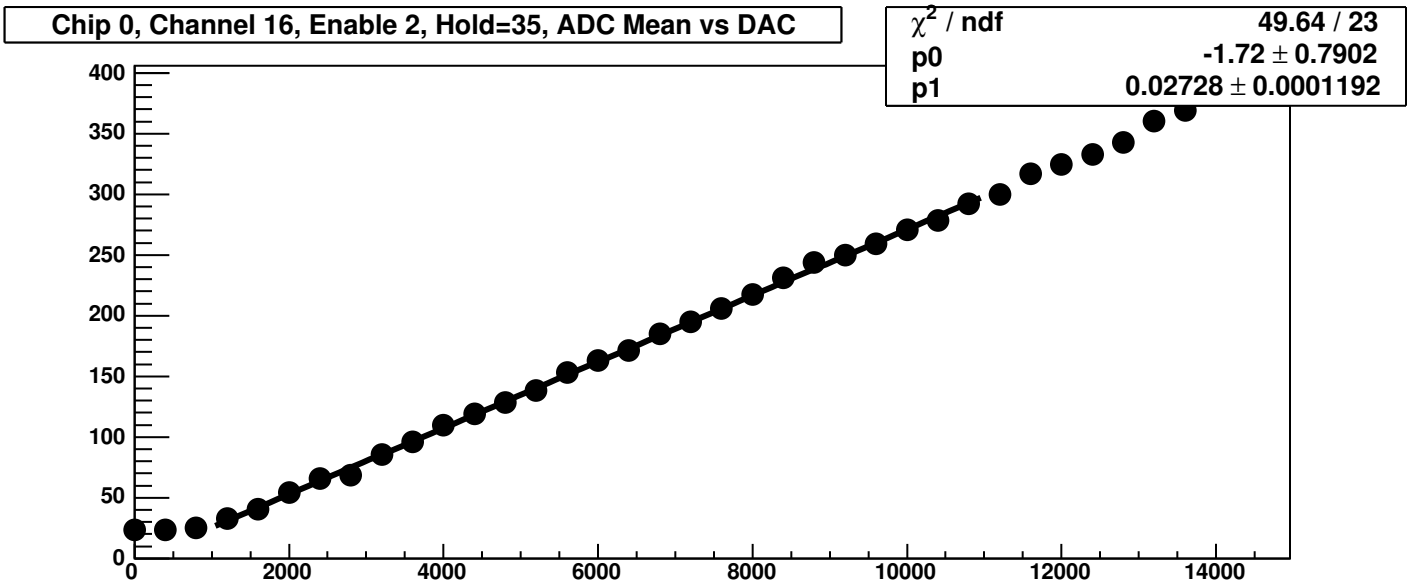

Chip 0, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

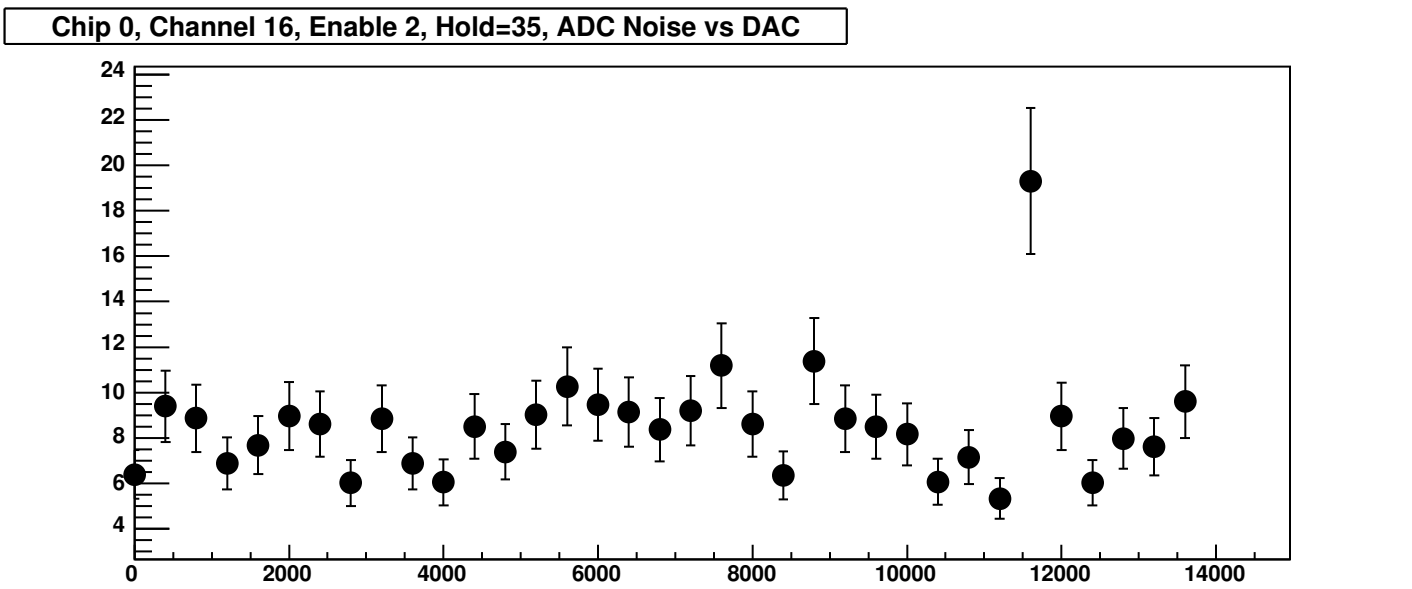

Chip 0, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

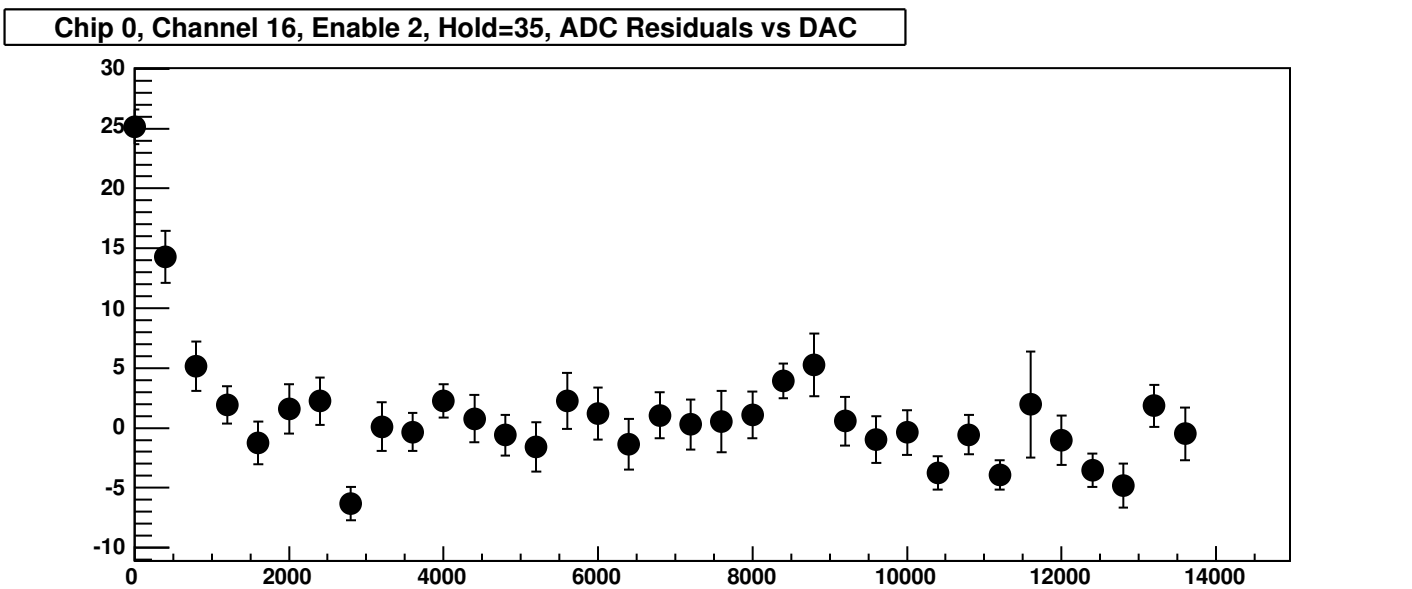

Chip 0, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 44.63 / 23
p0 -3.244 ± 0.8633
p1 0.01555 ± 0.0001259
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

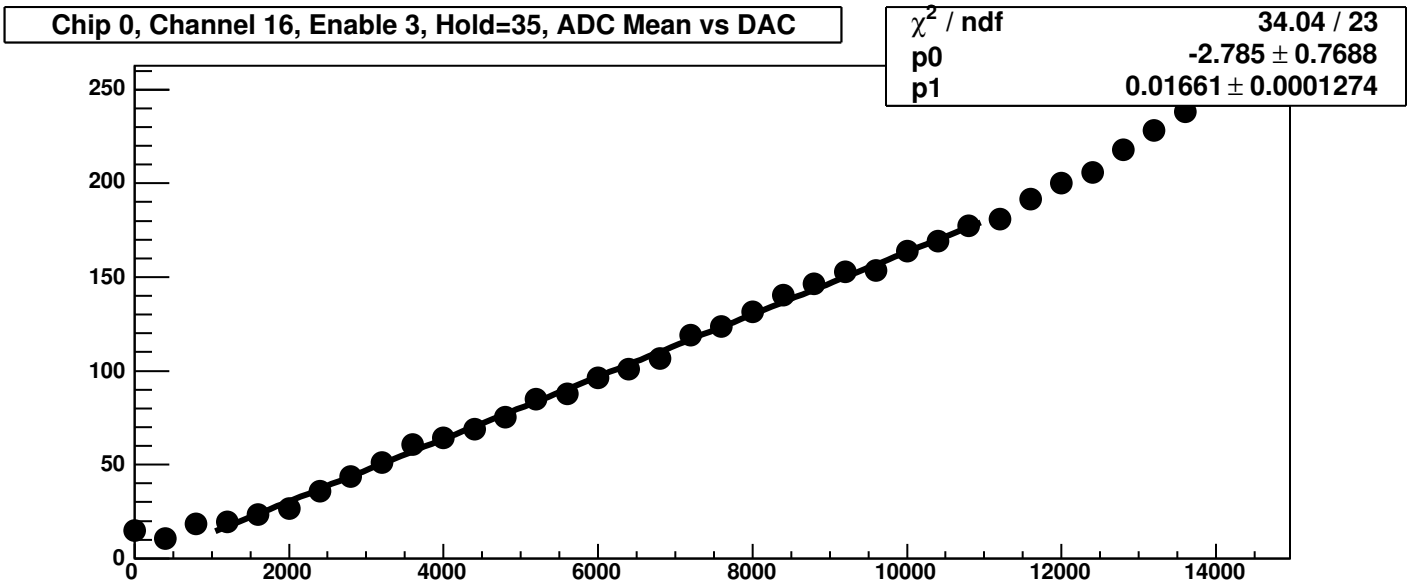

Chip 0, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

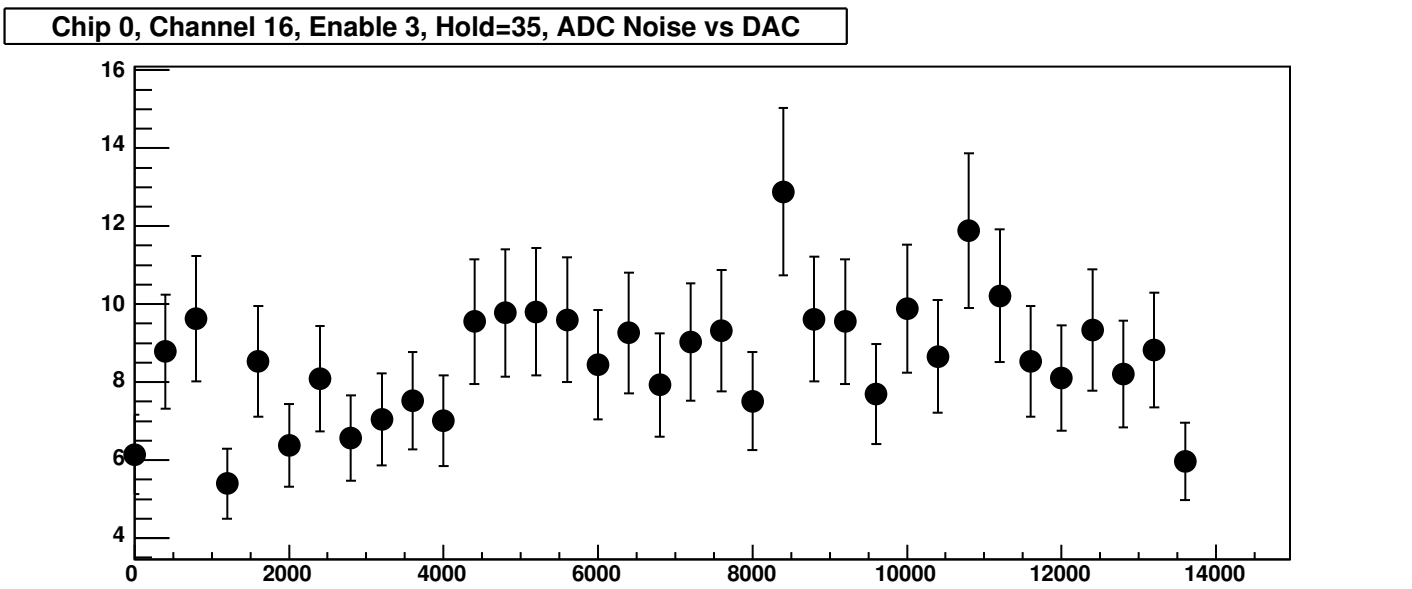

Chip 0, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

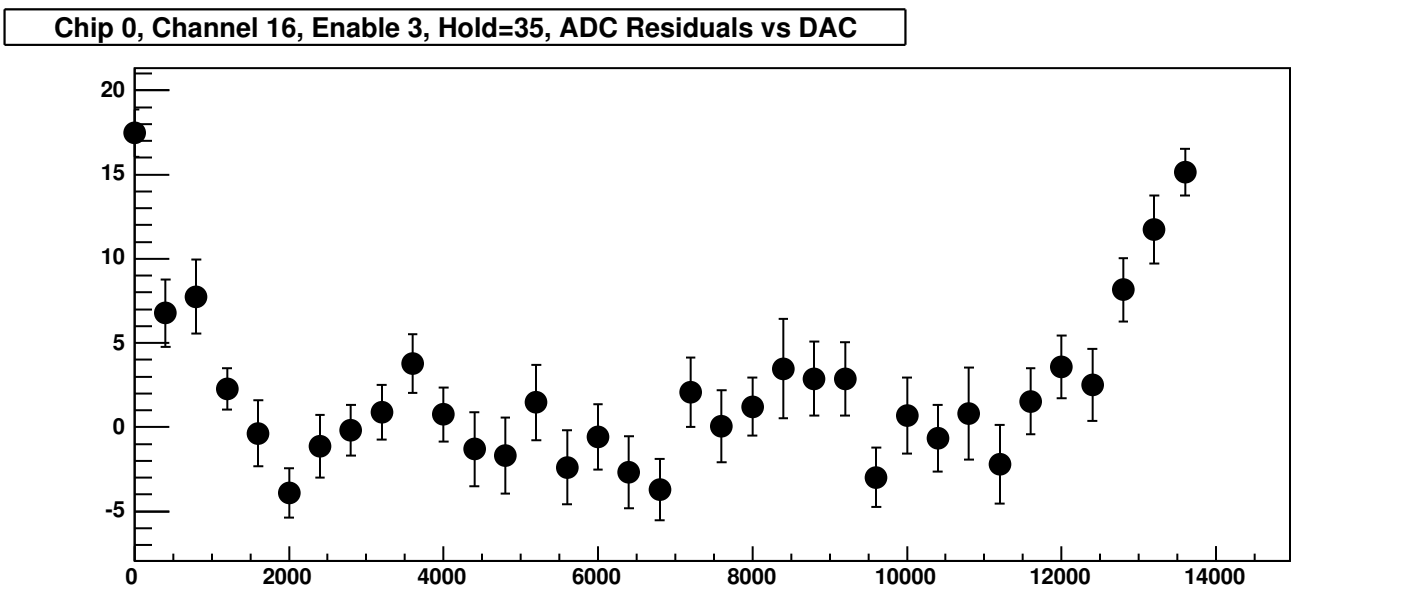

Chip 0, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 49.64 / 23
p0 -1.72 ± 0.7902
p1 0.02728 ± 0.0001192
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

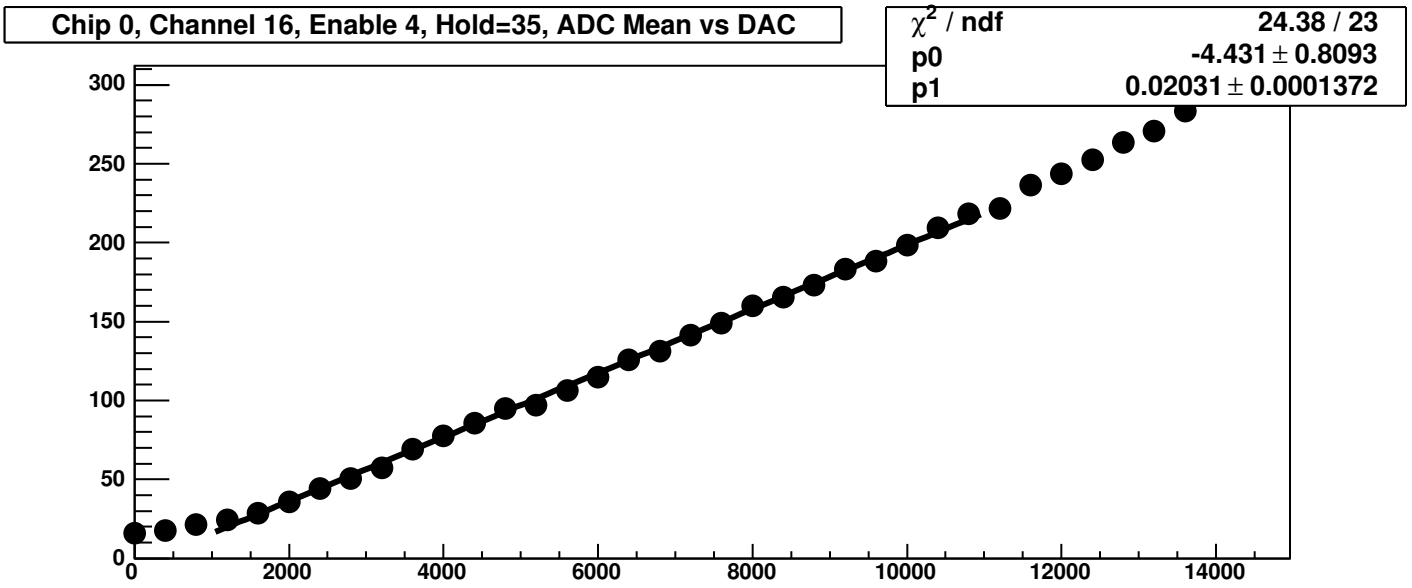

Chip 0, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC
24
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

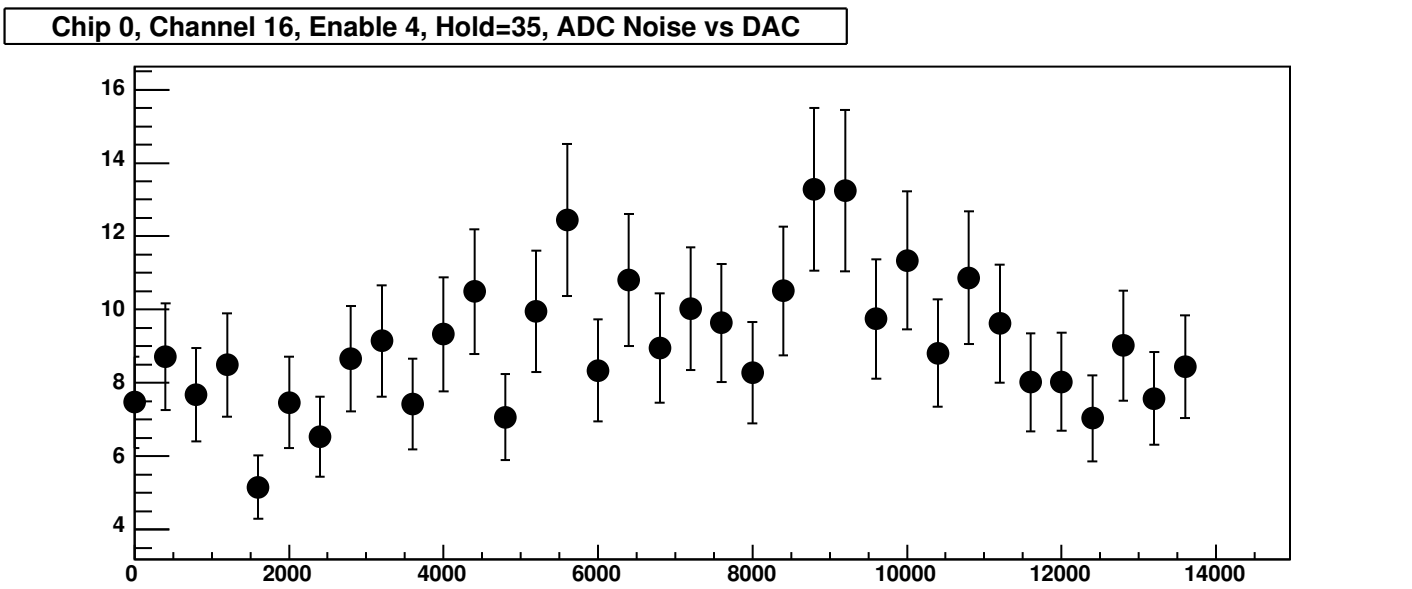

Chip 0, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

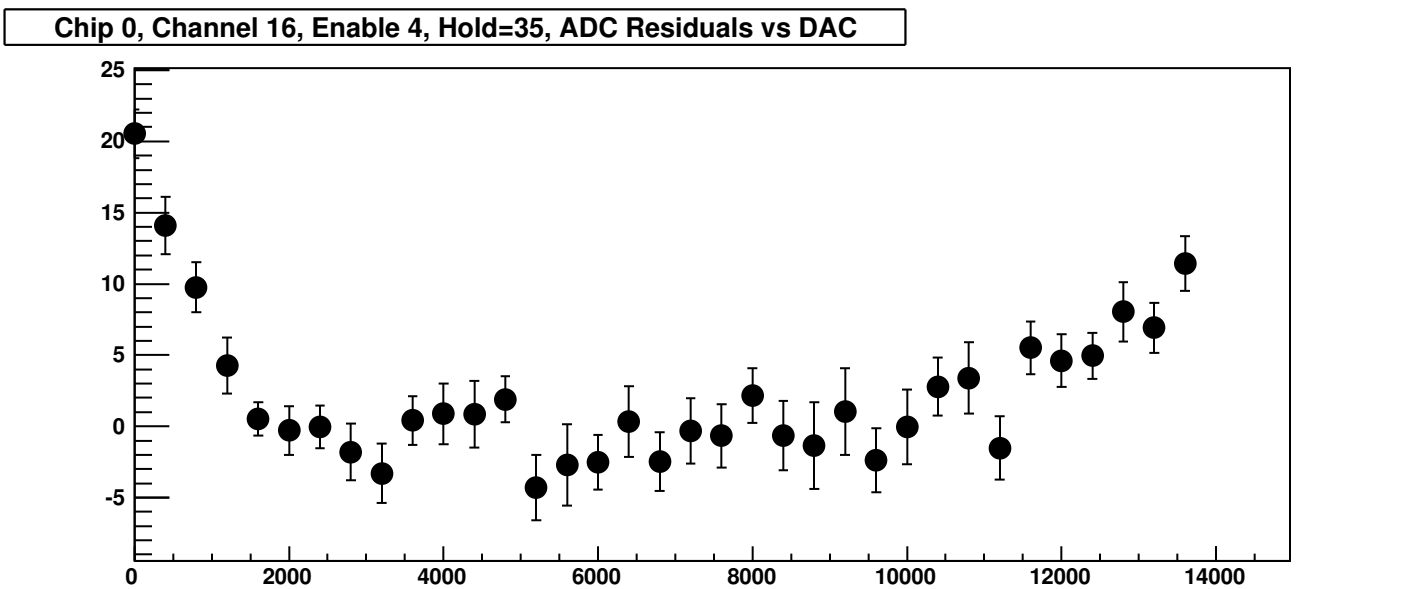

Chip 0, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 34.04 / 23
p0 -2.785 ± 0.7688
p1 0.01661 ± 0.0001274
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

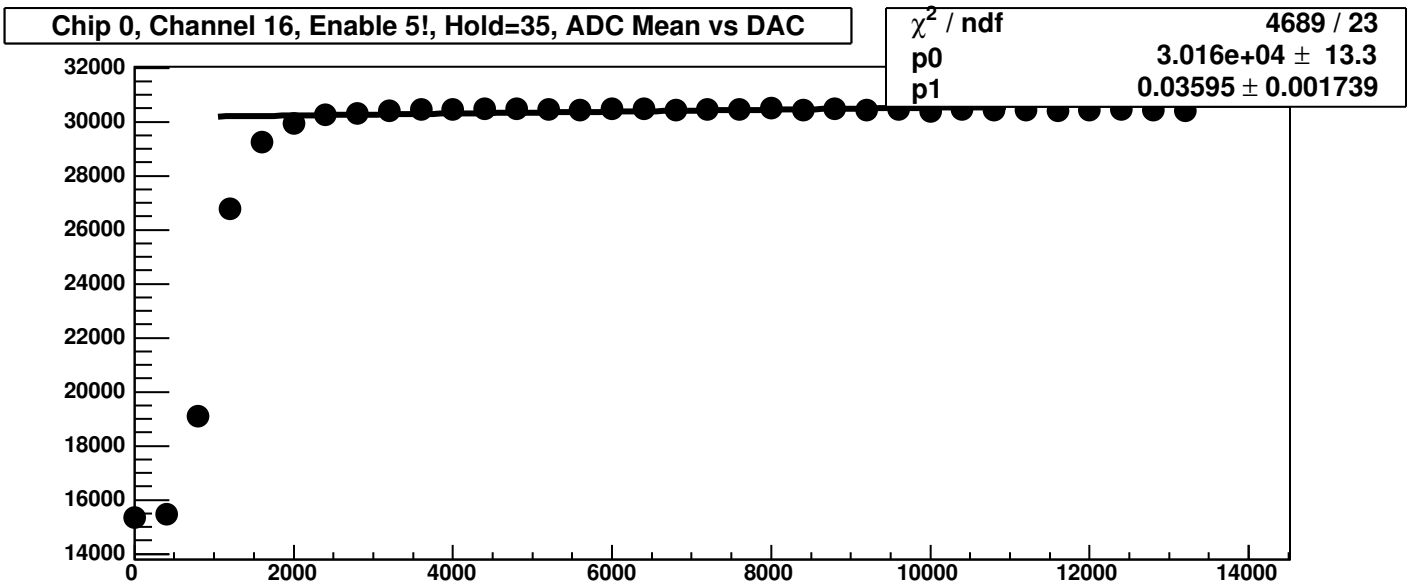

Chip 0, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

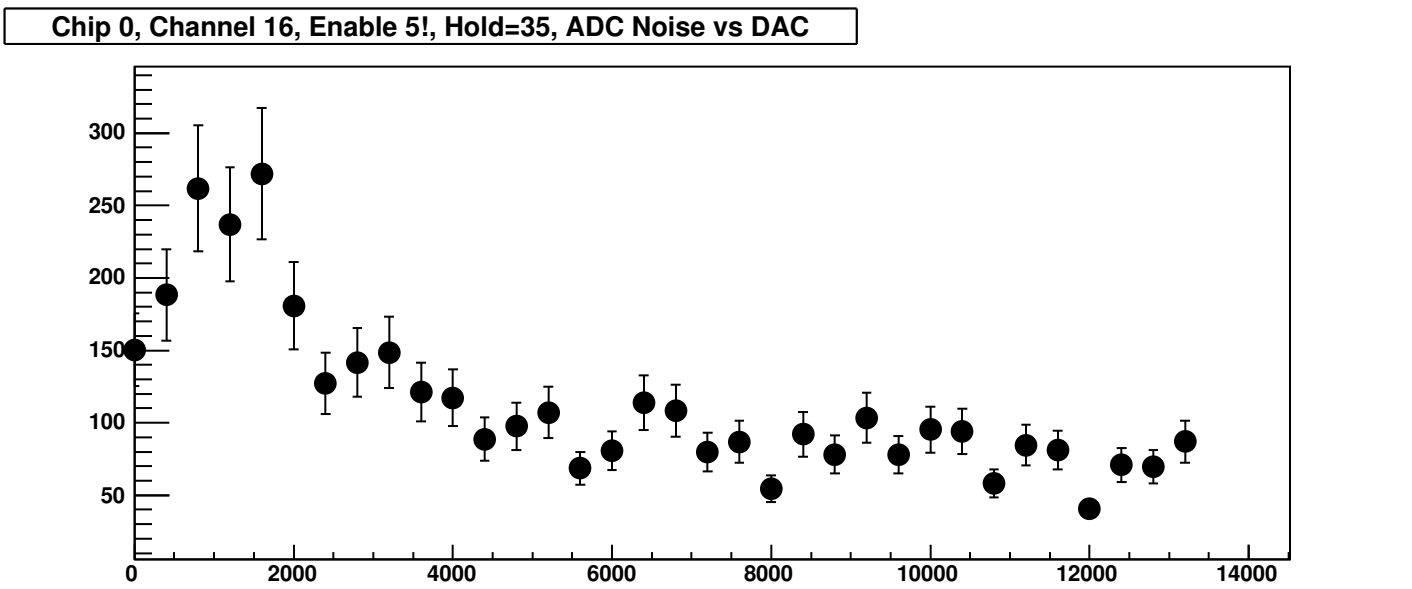

Chip 0, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

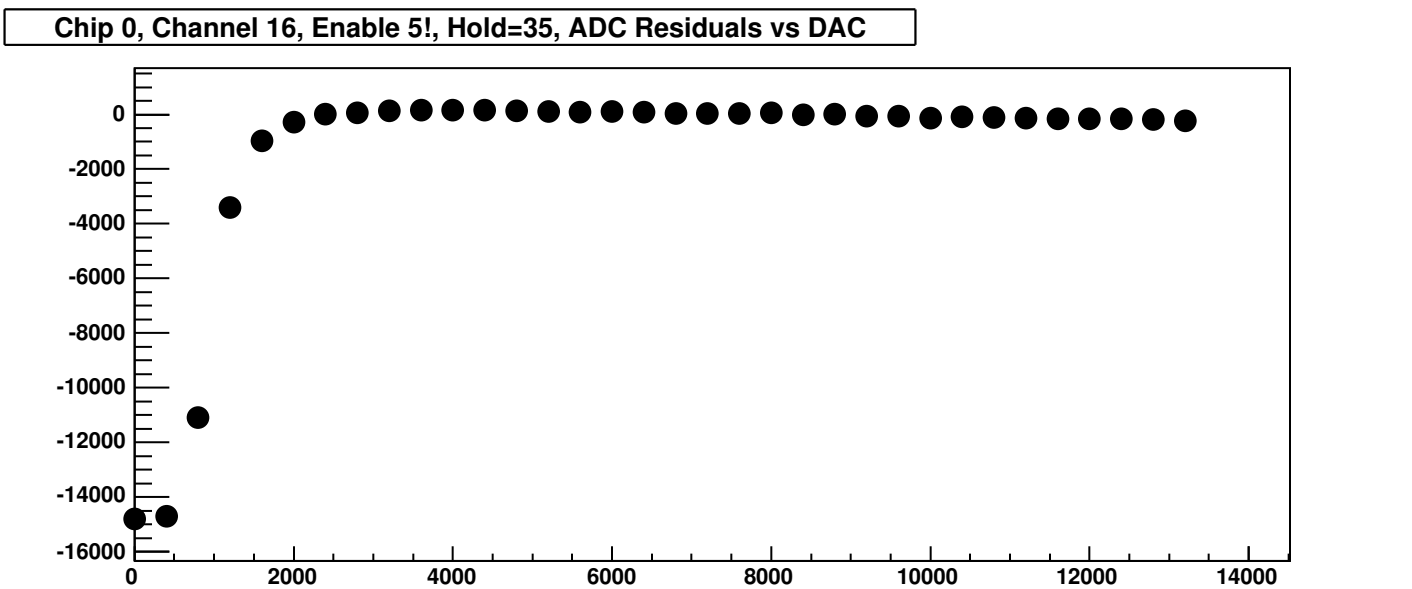

Chip 0, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.38 / 23
p0 -4.431 ± 0.8093
p1 0.02031 ± 0.0001372
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

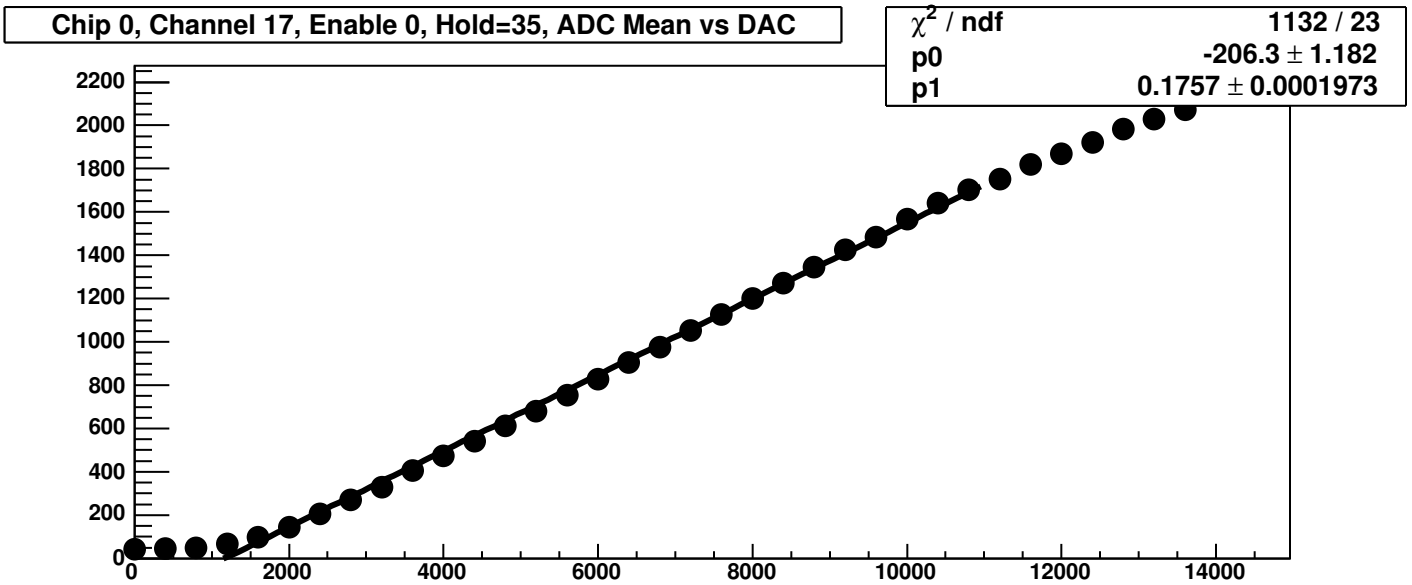

Chip 0, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

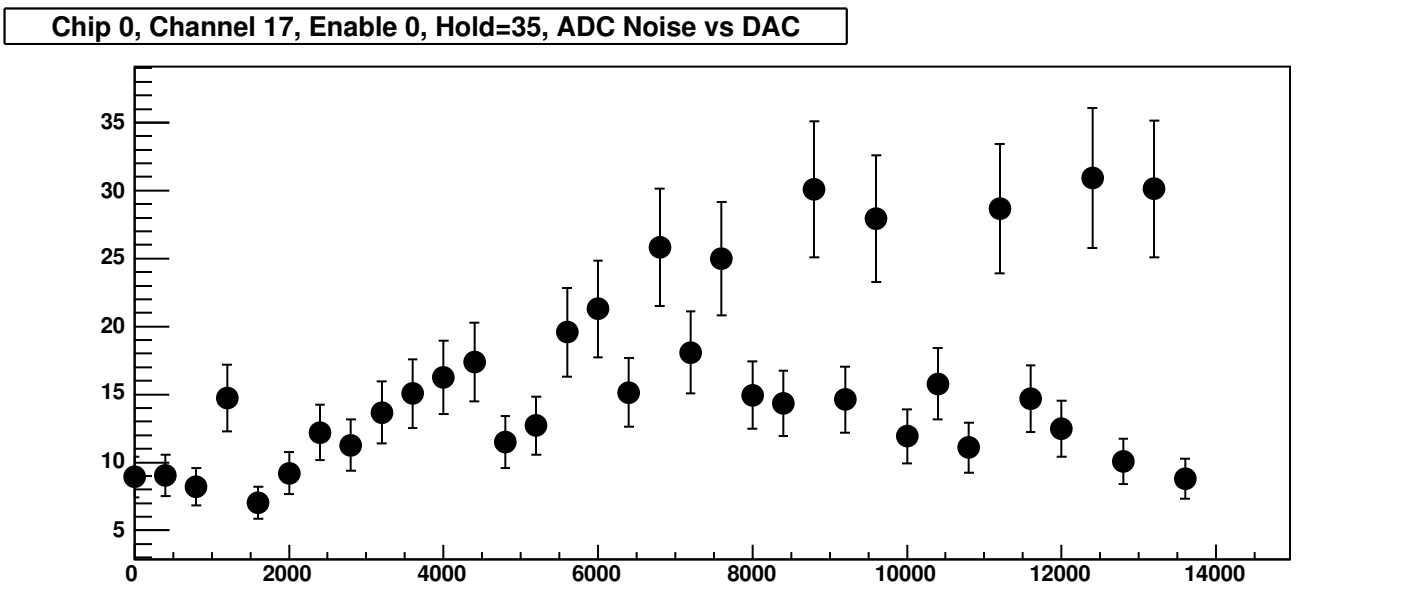

Chip 0, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

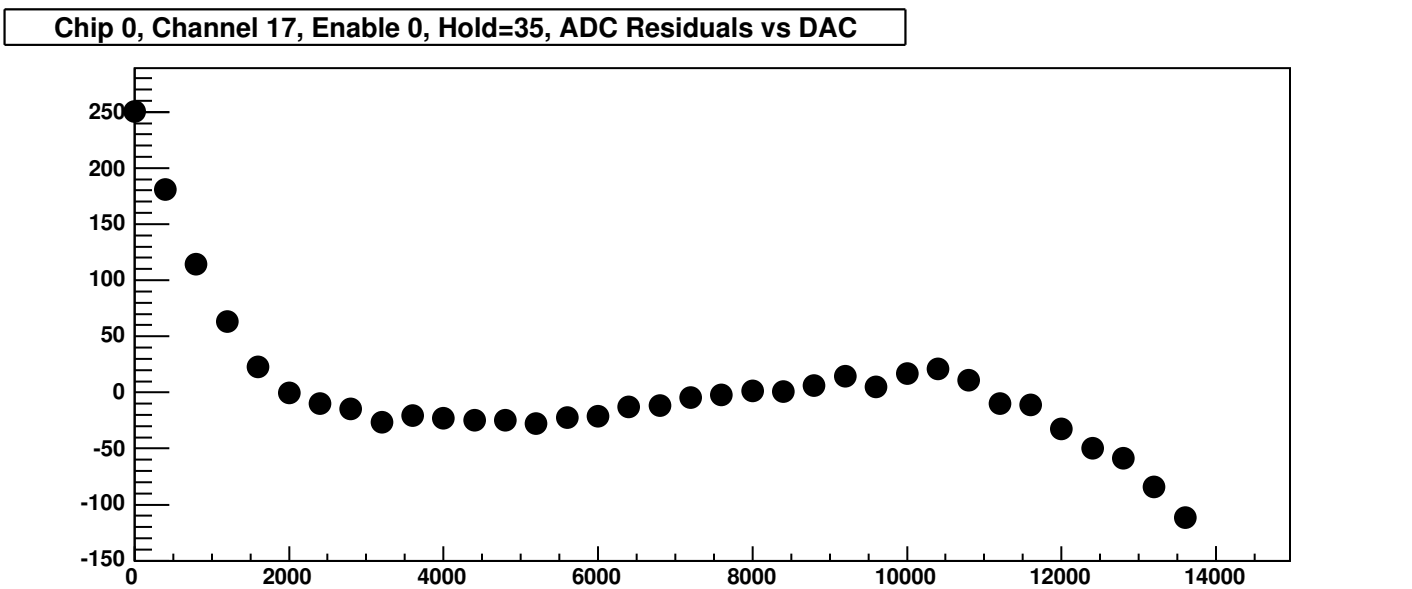

Chip 0, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4689 / 23
p0 3.016e+04 ± 13.3
p1 0.03595 ± 0.001739
32000
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

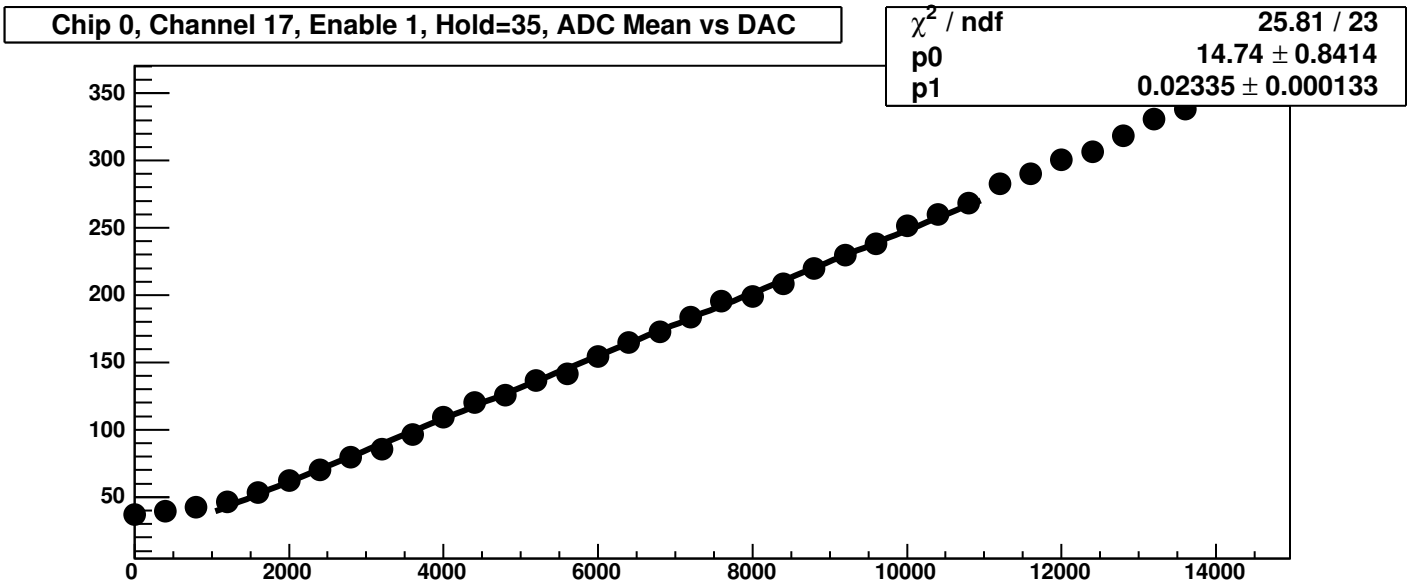

Chip 0, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

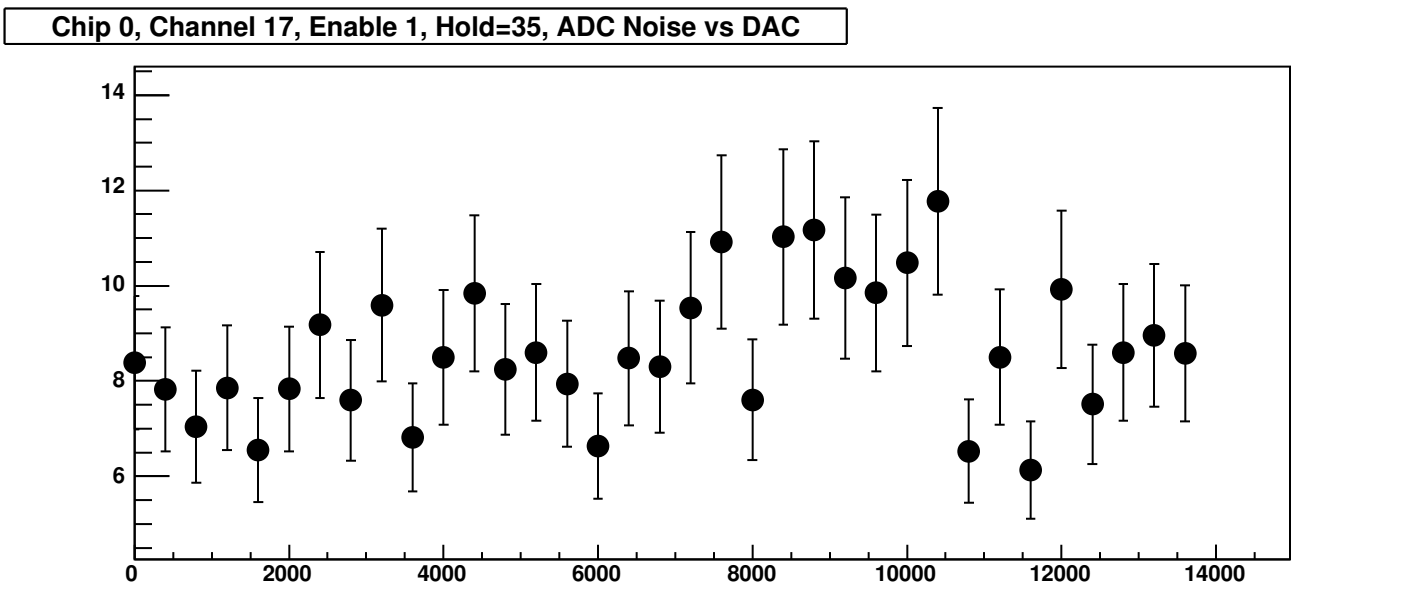

Chip 0, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

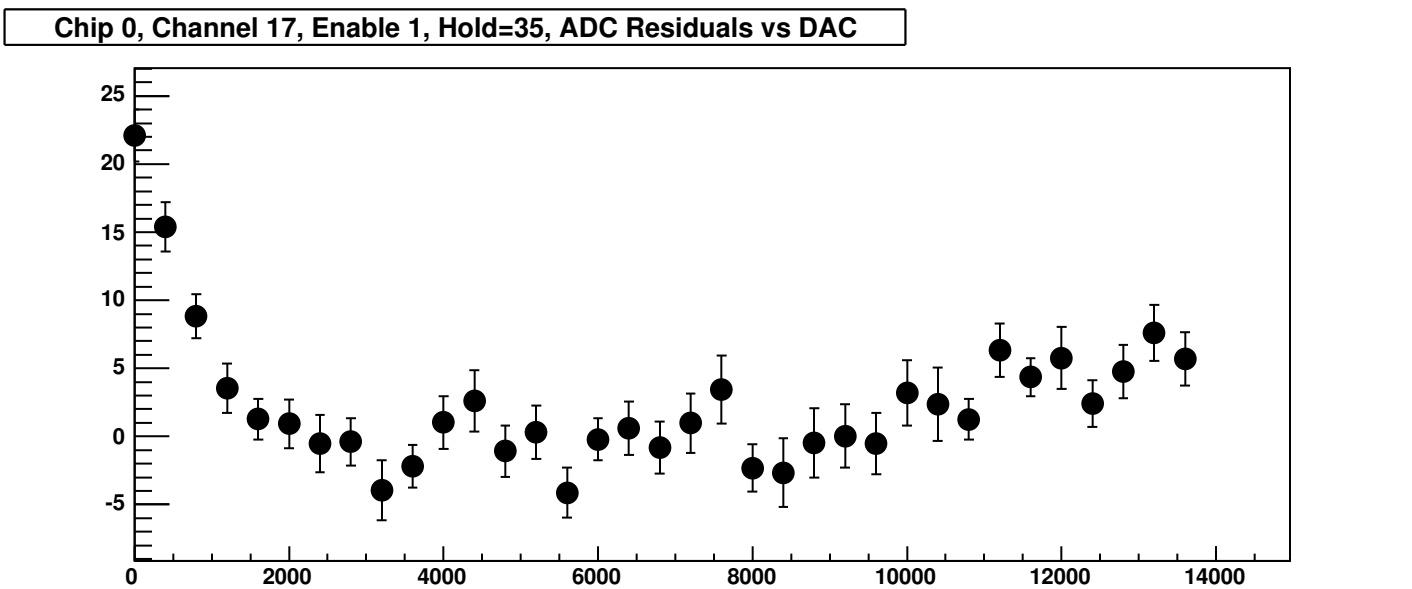

Chip 0, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1132 / 23
p0 -206.3 ± 1.182
p1 0.1757 ± 0.0001973
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

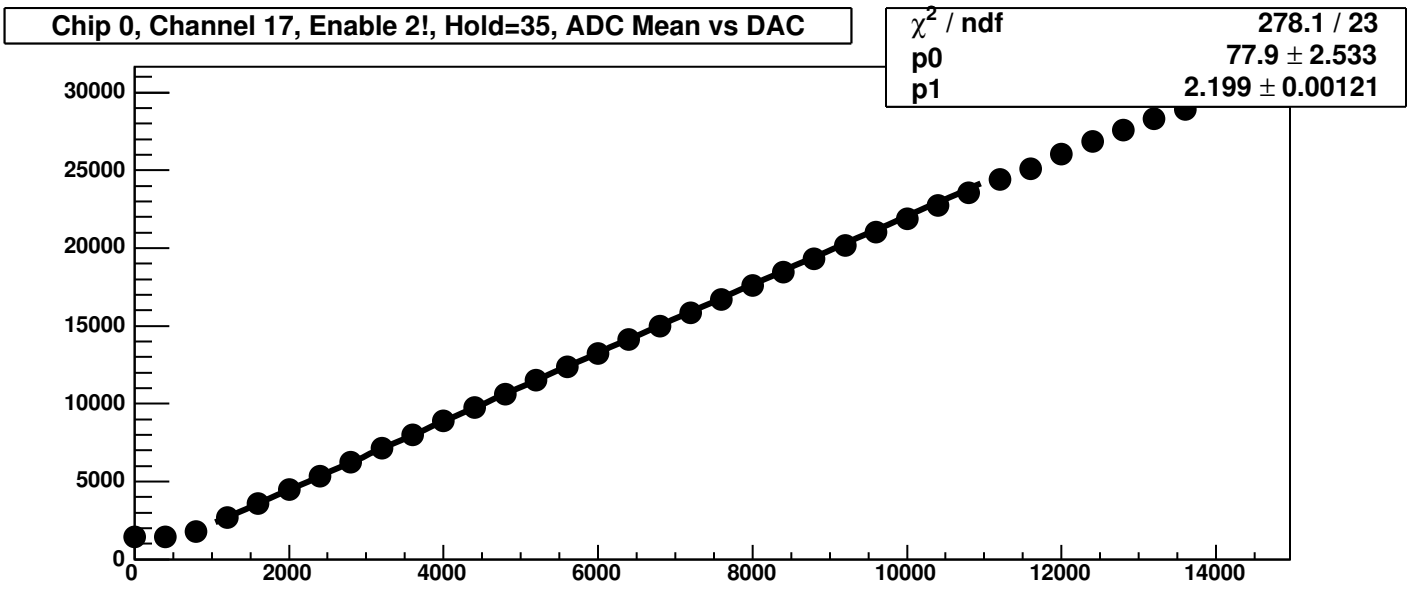

Chip 0, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

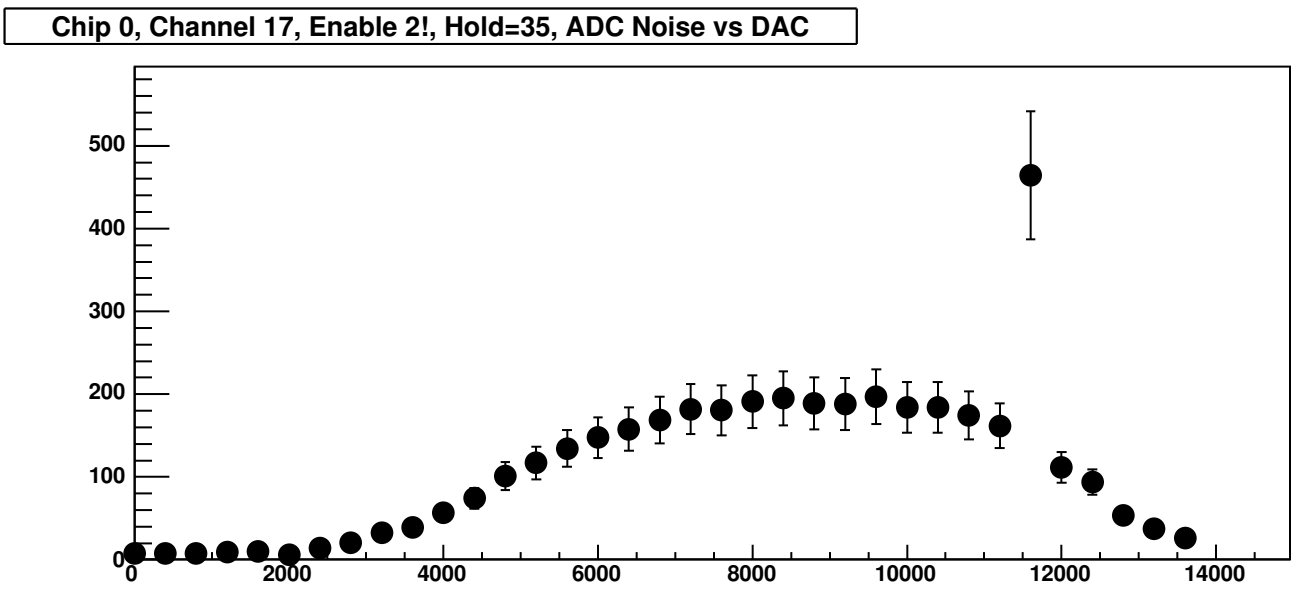

Chip 0, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

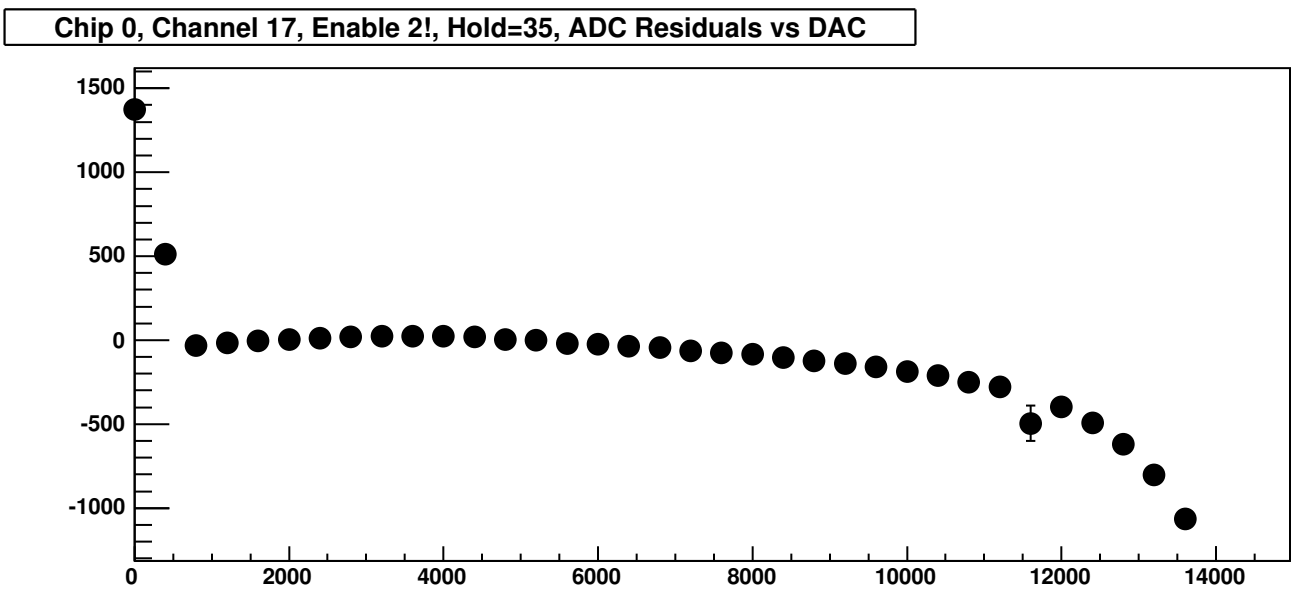

Chip 0, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 25.81 / 23
p0 14.74 ± 0.8414
p1 0.02335 ± 0.000133
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

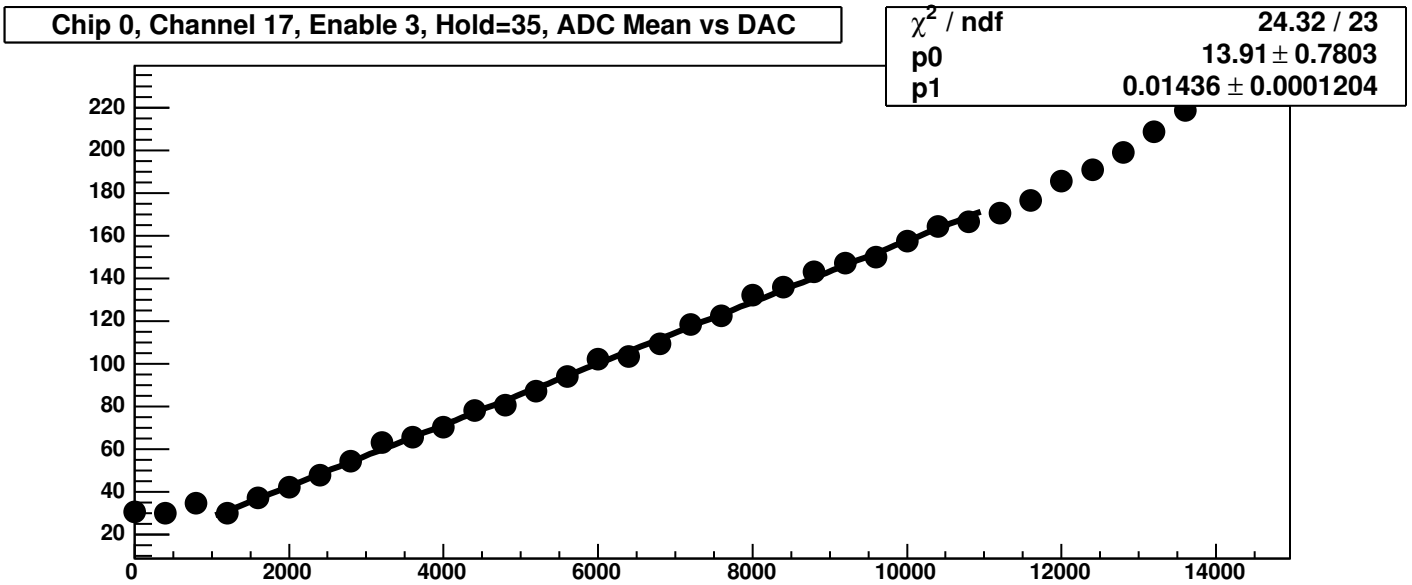

Chip 0, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

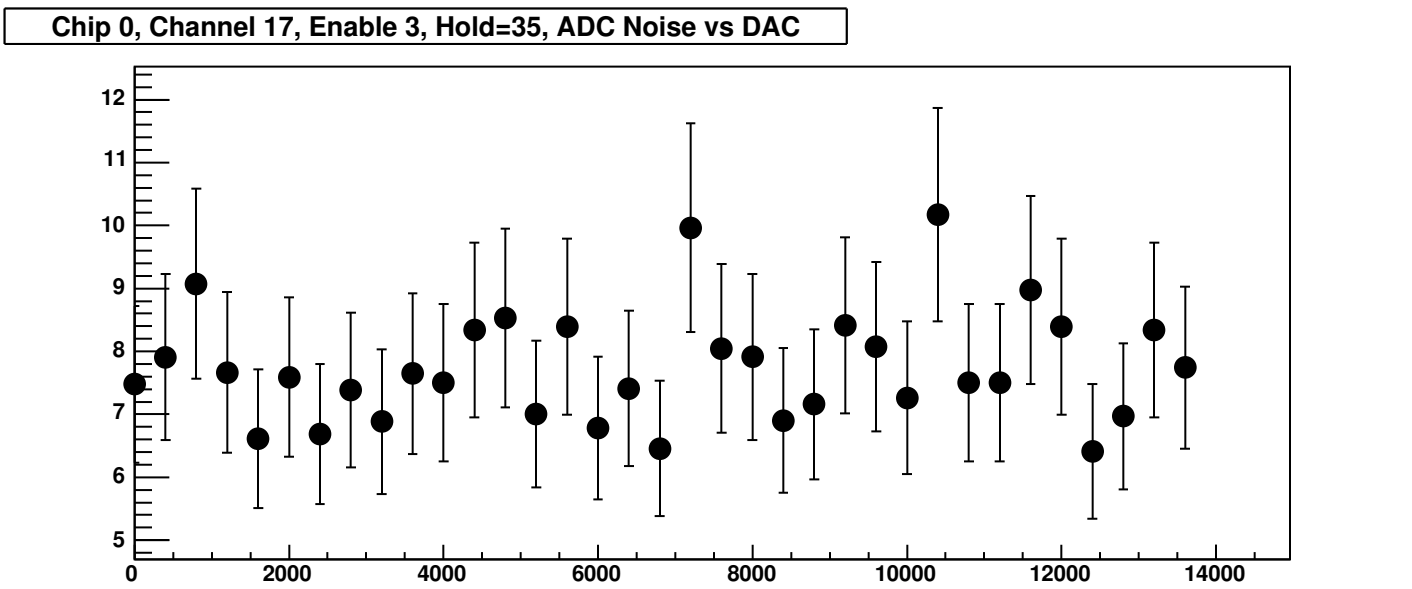

Chip 0, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

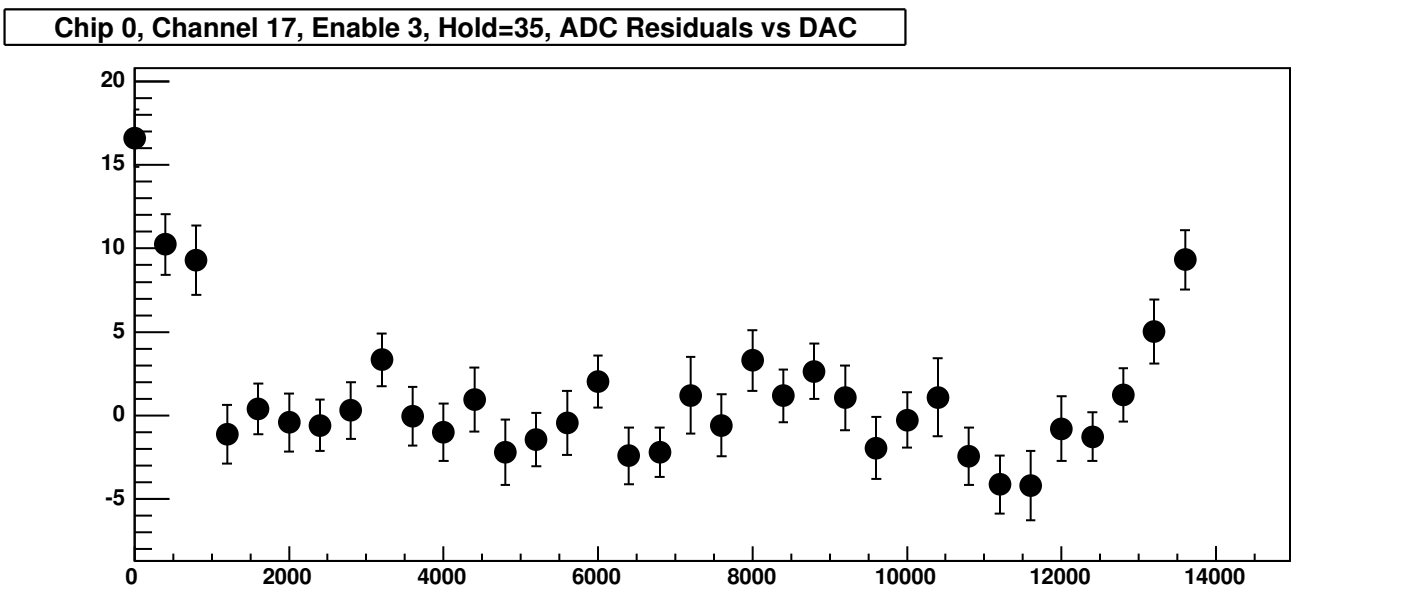

Chip 0, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 278.1 / 23
p0 77.9 ± 2.533
p1 2.199 ± 0.00121
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

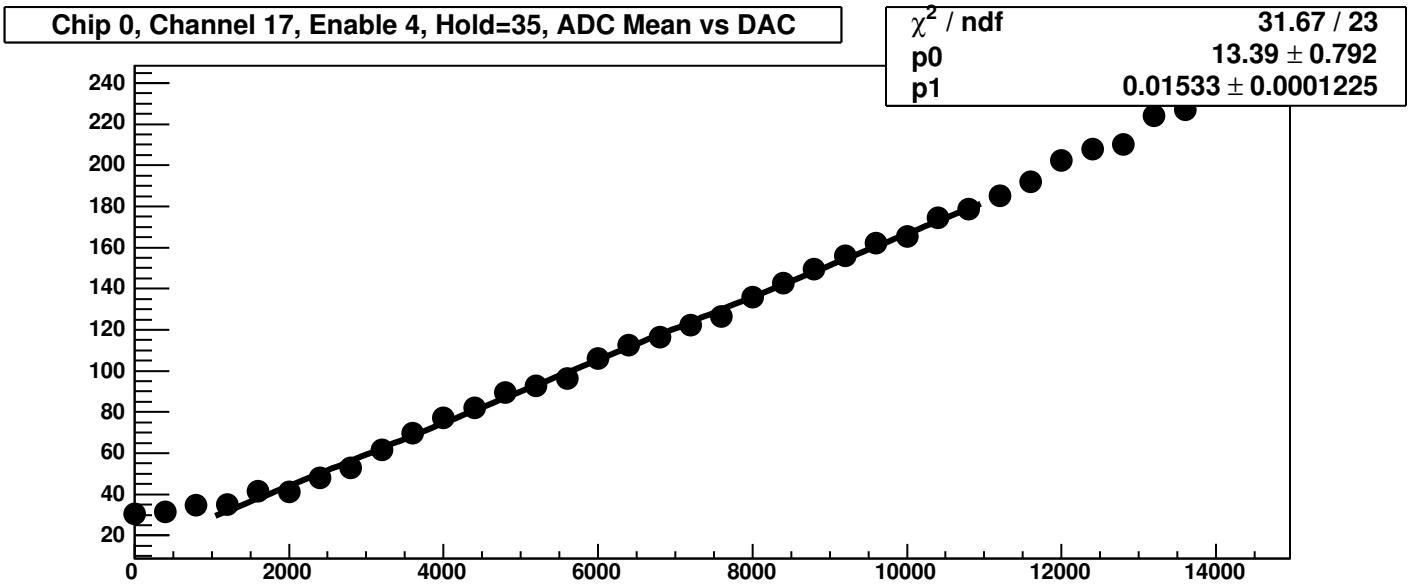

Chip 0, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

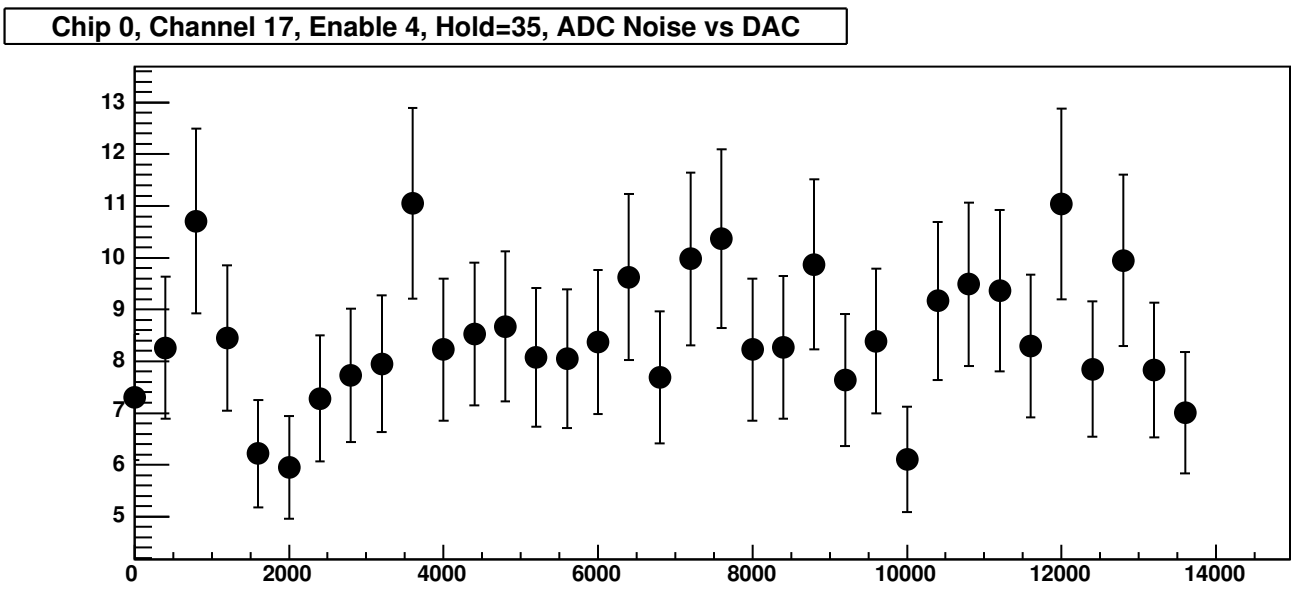

Chip 0, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

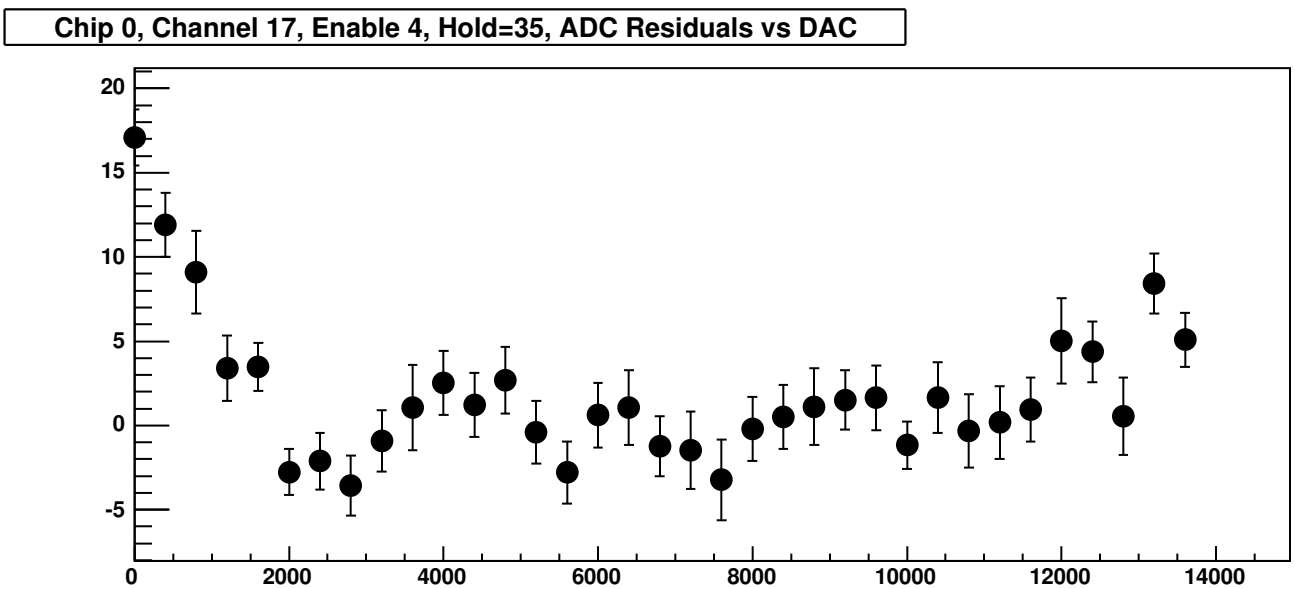

Chip 0, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.32 / 23
p0 13.91 ± 0.7803
p1 0.01436 ± 0.0001204
220
200
180
160
140
120
100
80
60
40
20
0
2000
4000
6000
8000
10000
12000
14000

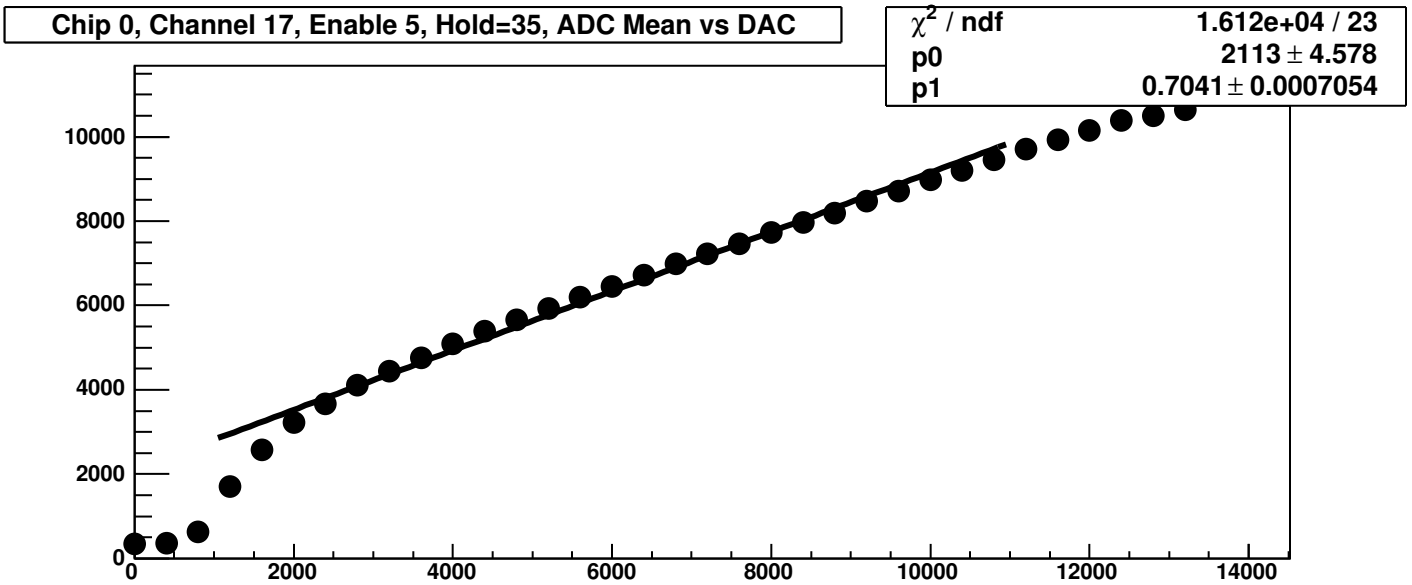

Chip 0, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

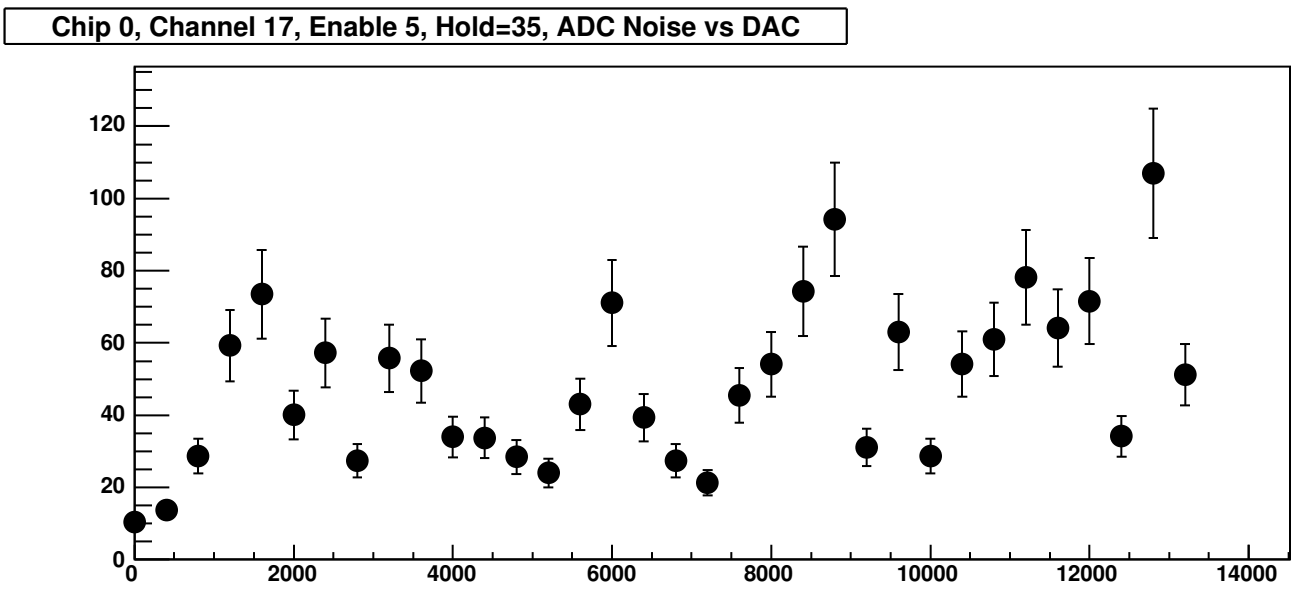

Chip 0, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

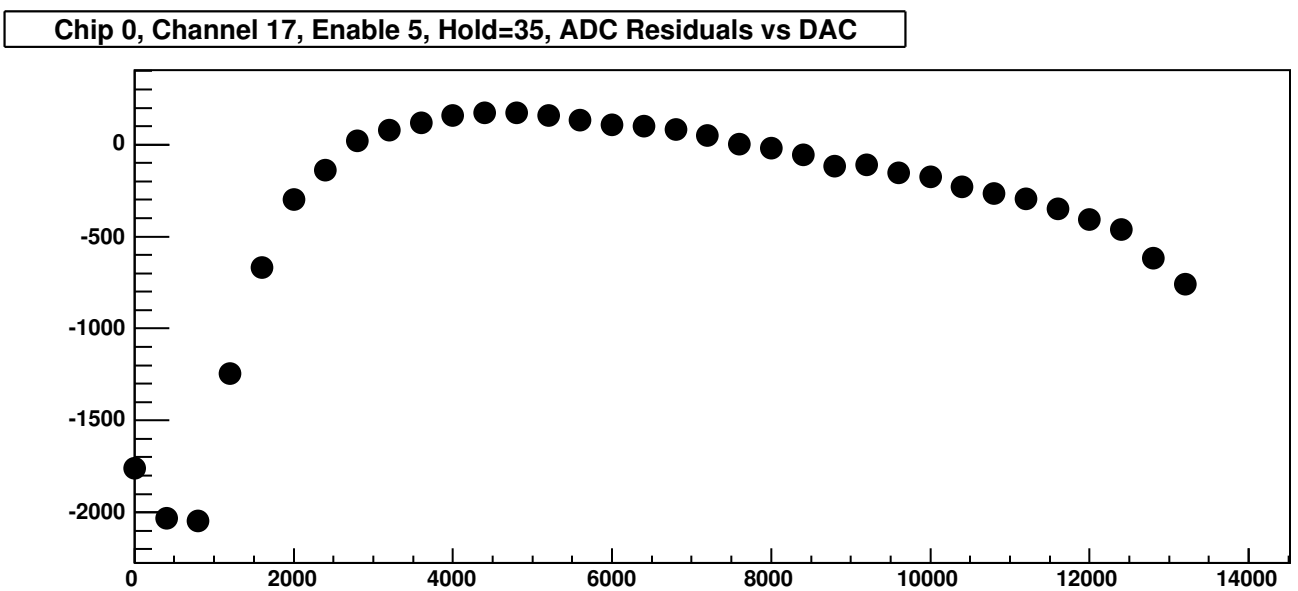

Chip 0, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 31.67 / 23
p0 13.39 ± 0.792
p1 0.01533 ± 0.0001225
240
220
200
180
160
140
120
100
80
60
40
20
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

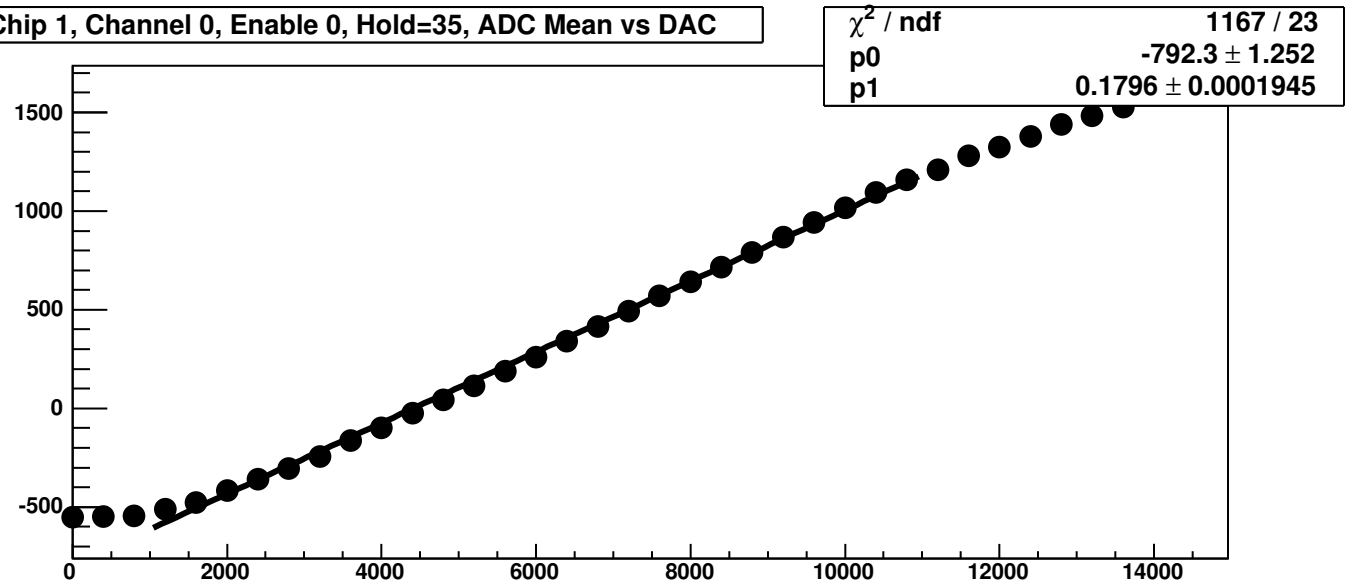

Chip 0, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.612e+04 / 23
p0 2113 ± 4.578
p1 0.7041 ± 0.0007054
0
2000
4000
6000
8000
10000
12000
14000

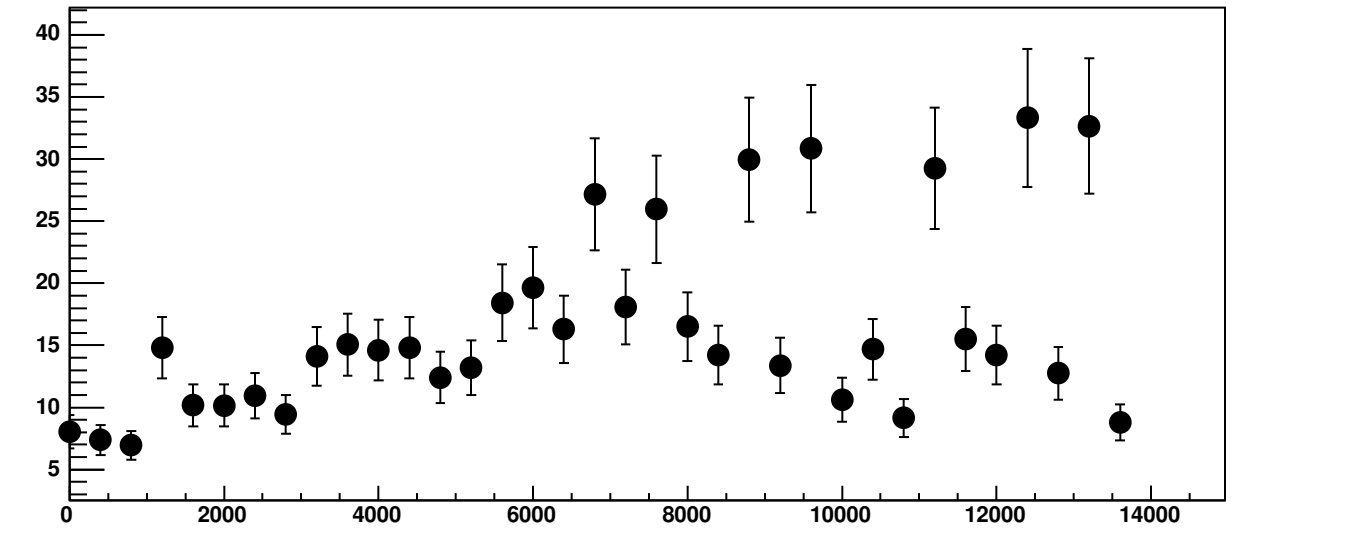

Chip 0, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
0
20
40
60
80
100
120
2000
4000
6000
8000
10000
12000
14000


Chip 0, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
2000
4000
6000
8000
10000
12000
14000

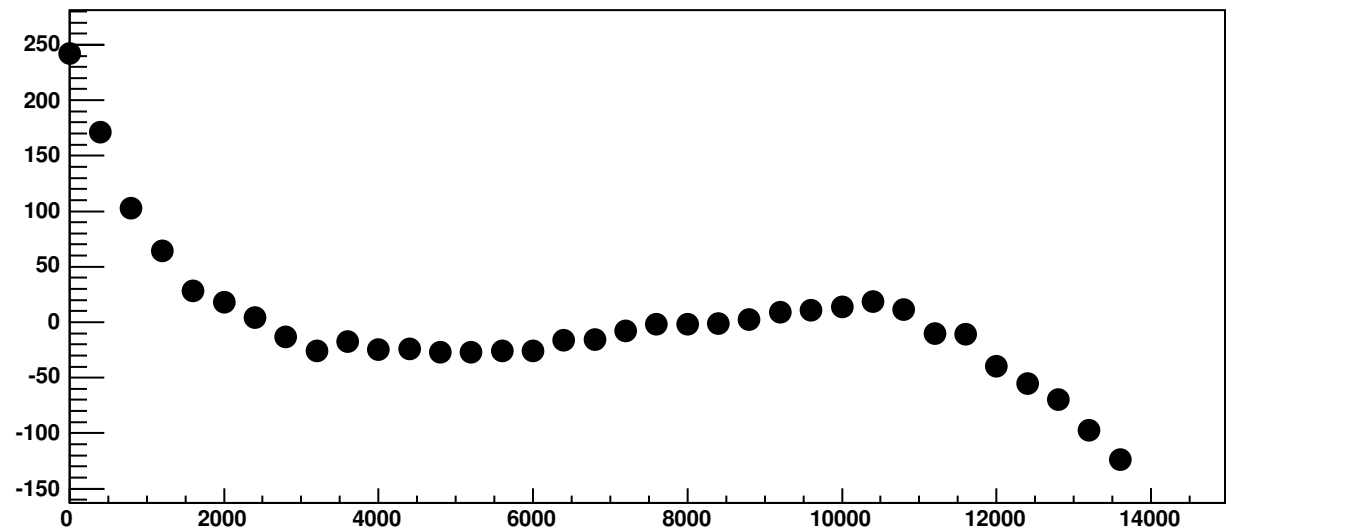
Chip 1, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

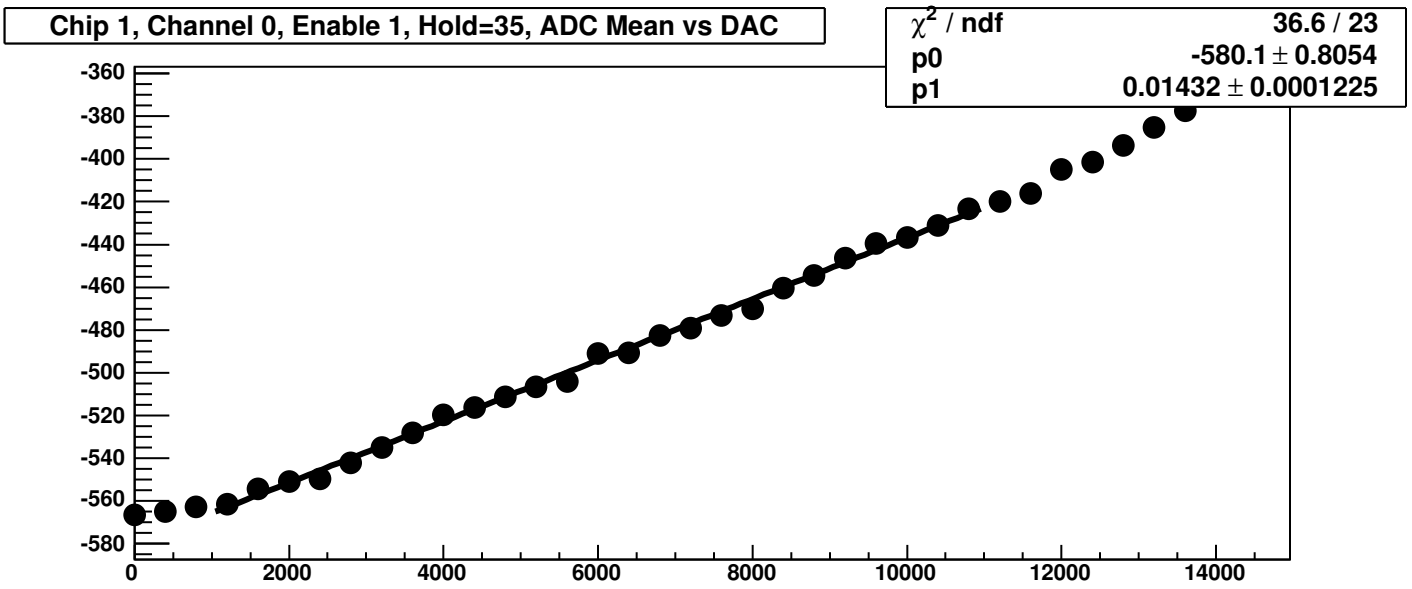


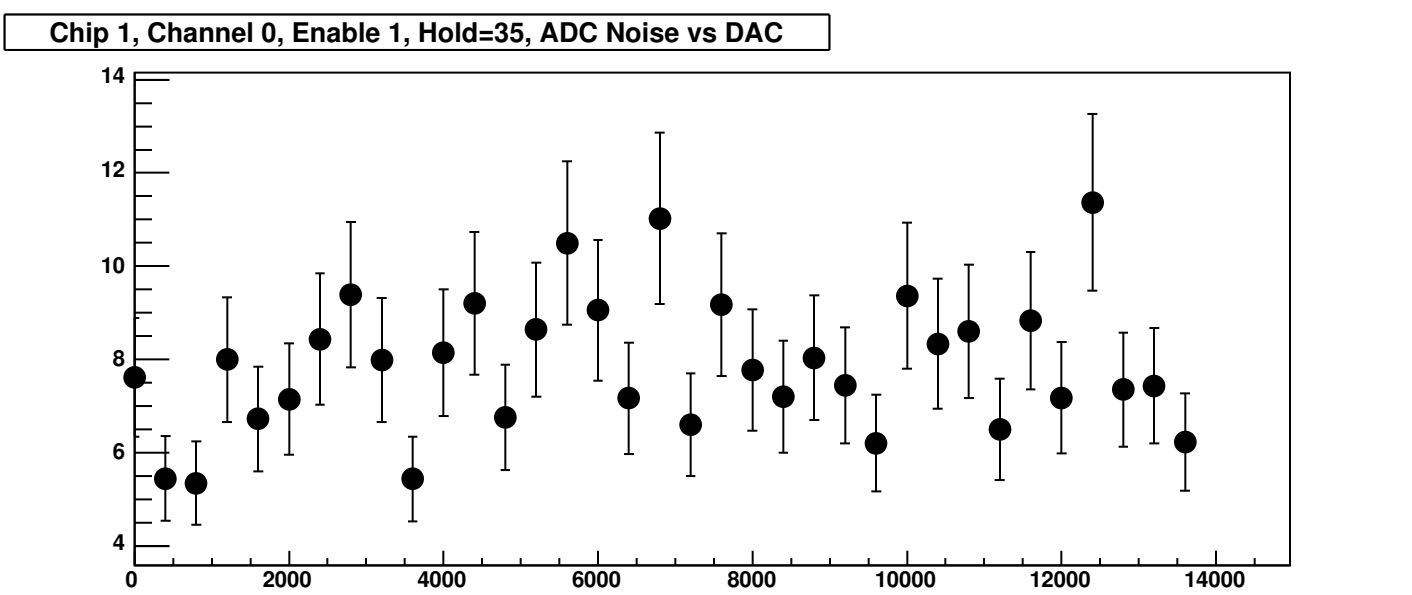
Chip 1, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC

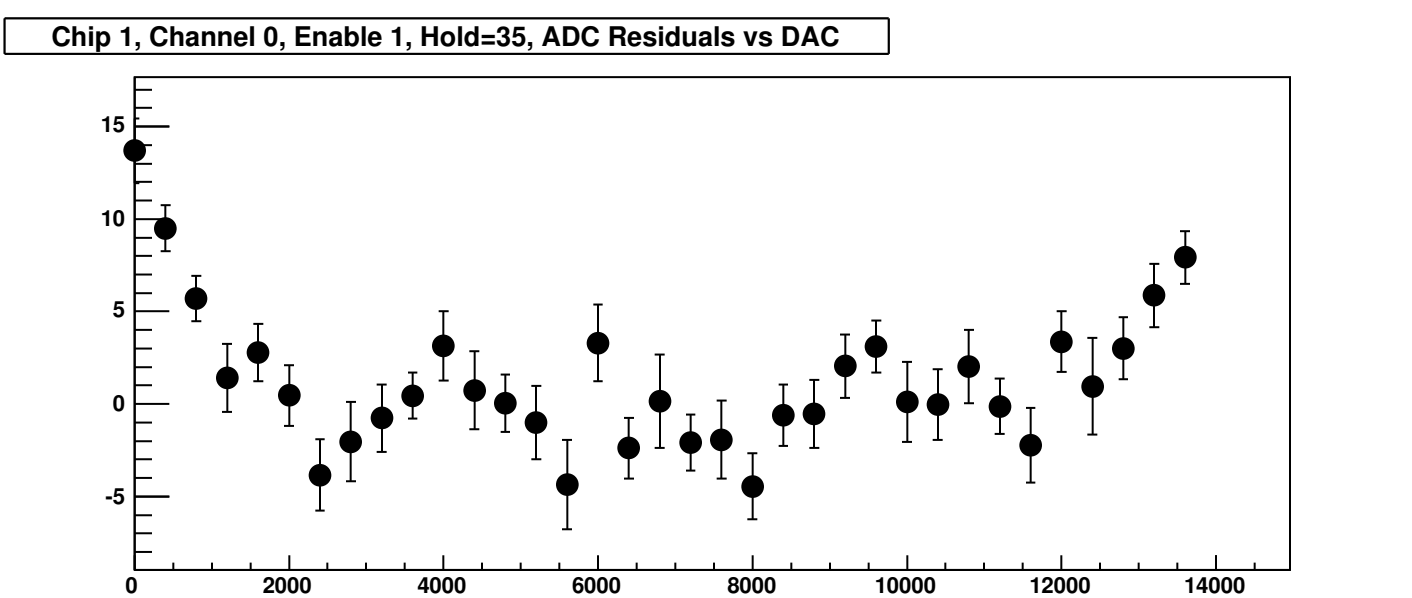


Chip 1, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

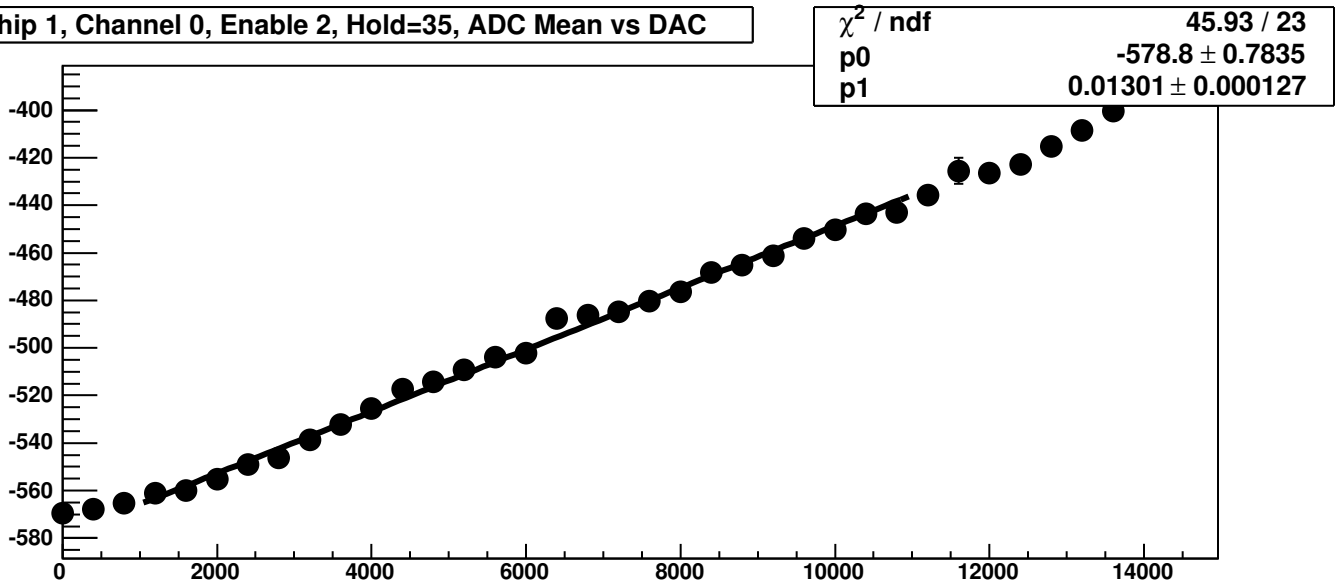



Chip 1, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 36.6 / 23
p0 -580.1 ± 0.8054
p1 0.01432 ± 0.0001225
-360
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000

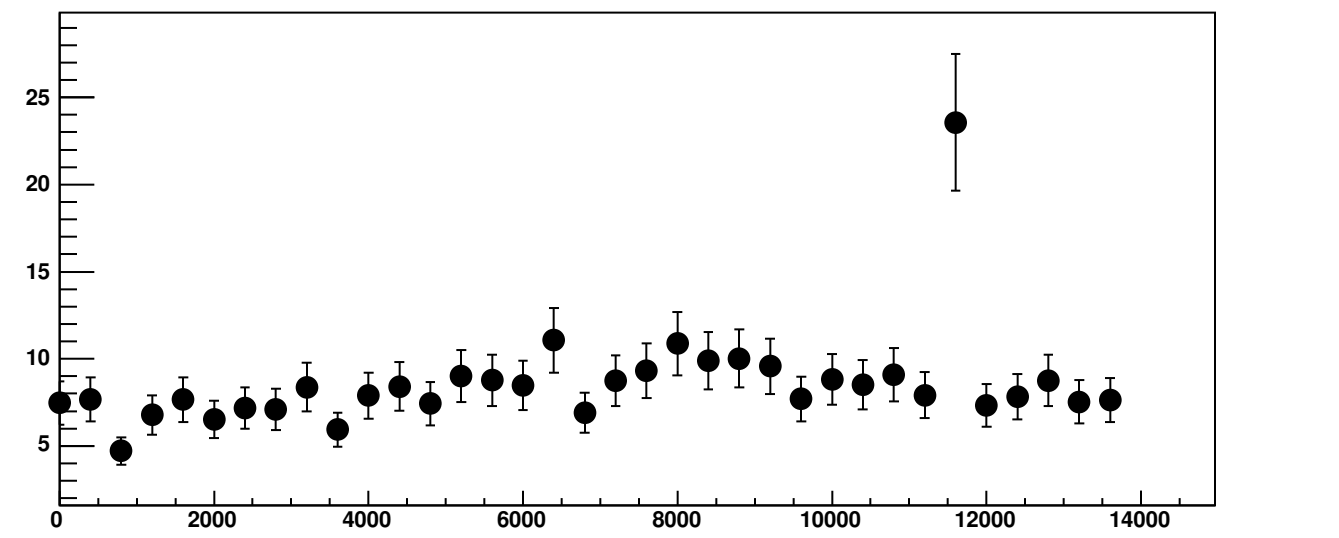

Chip 1, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

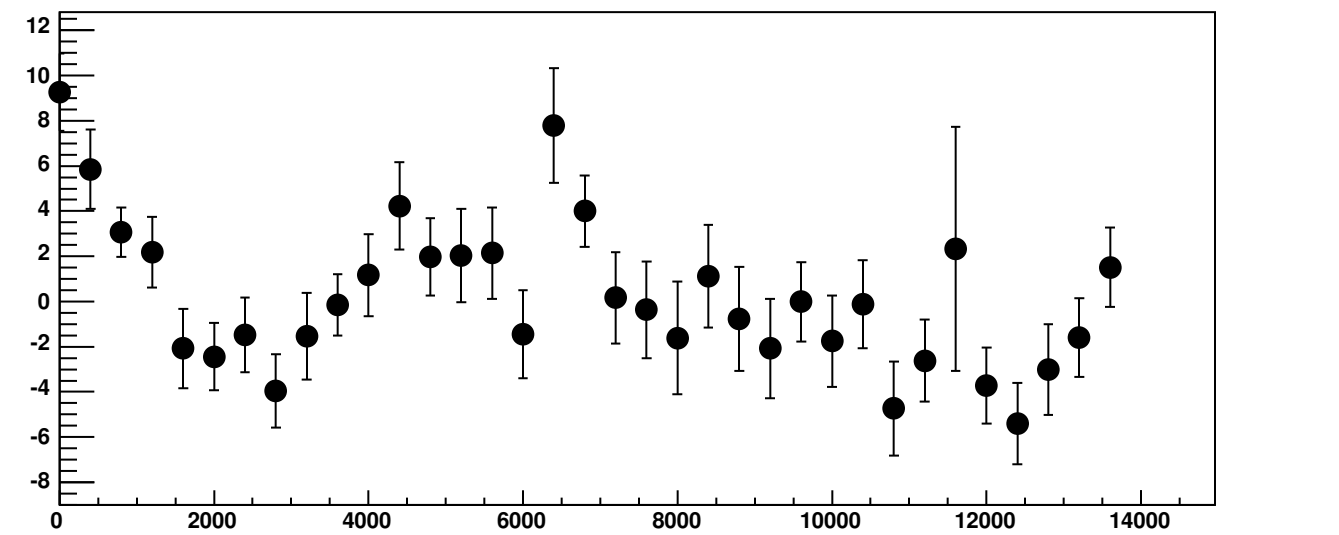
Chip 1, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

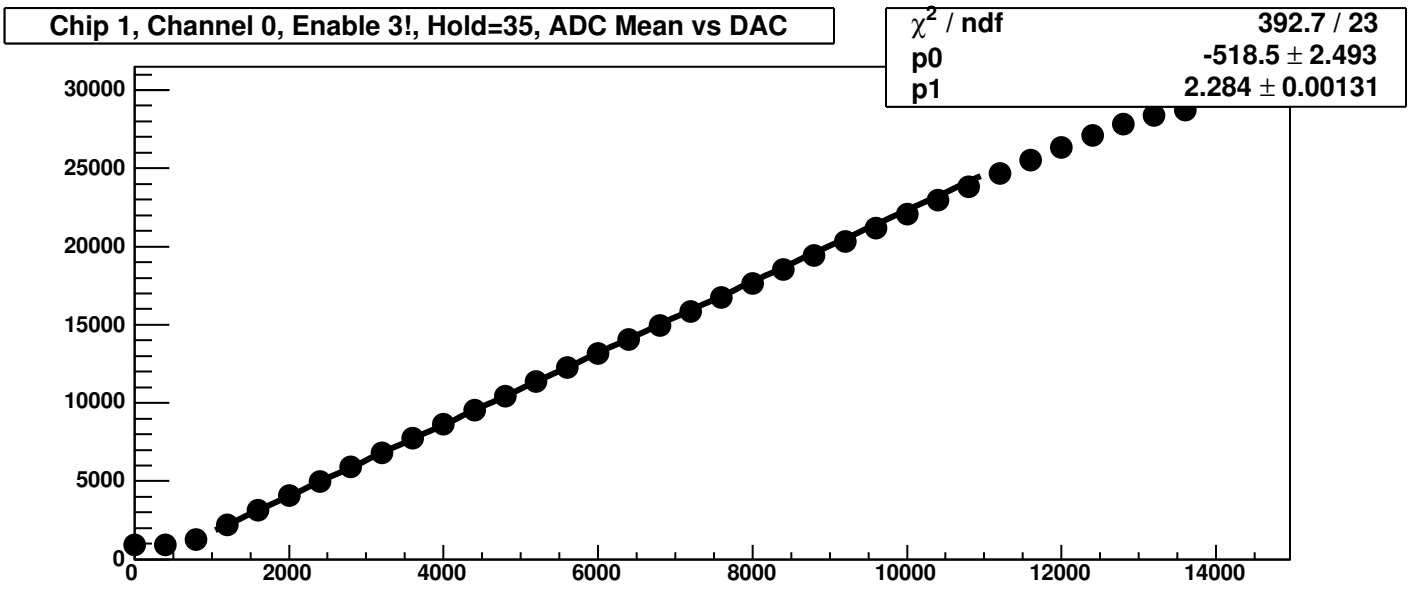


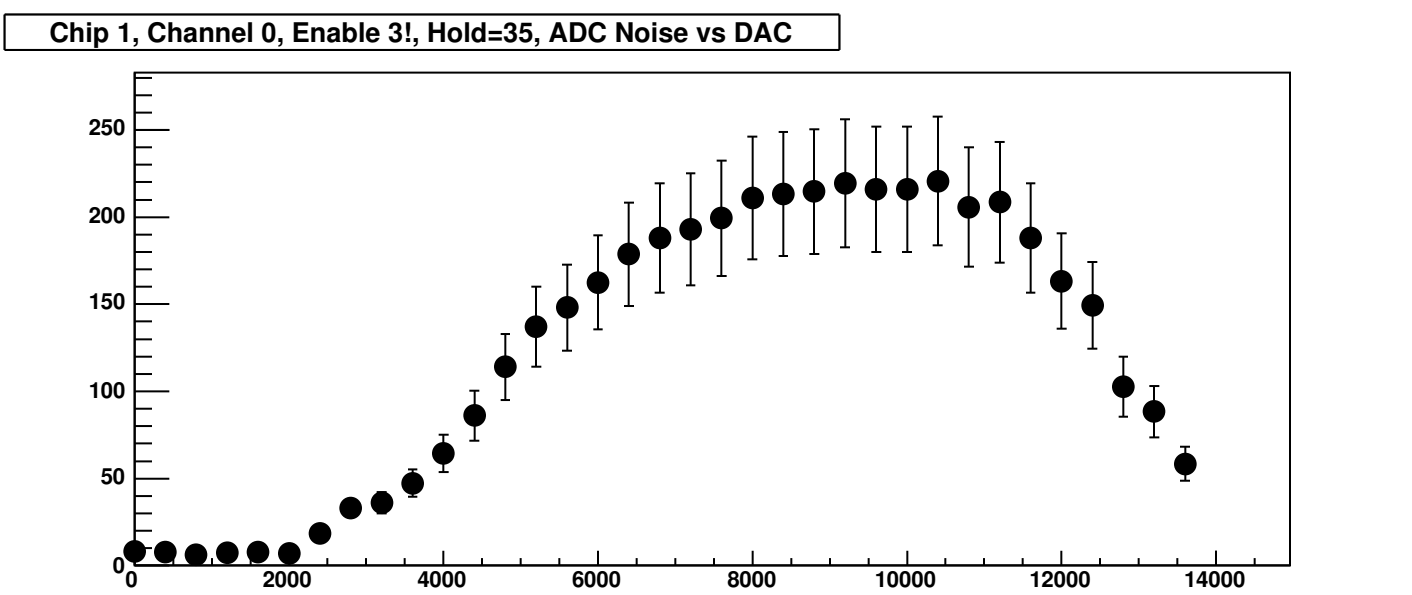
Chip 1, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC

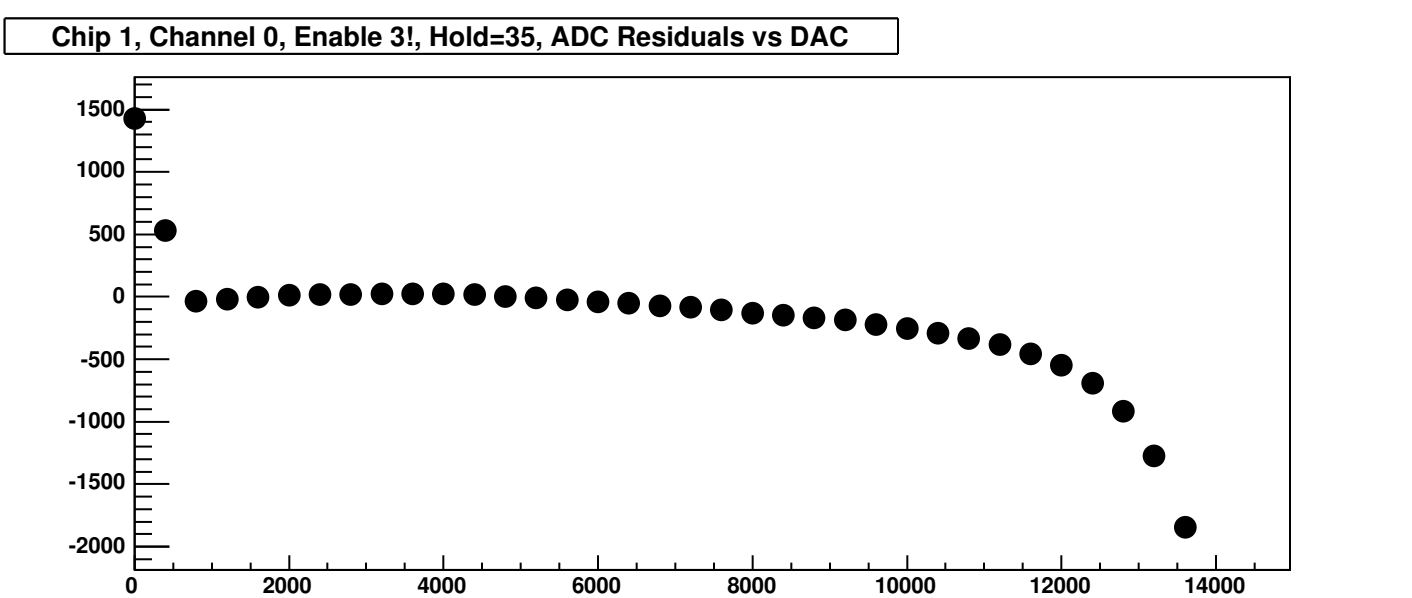


Chip 1, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

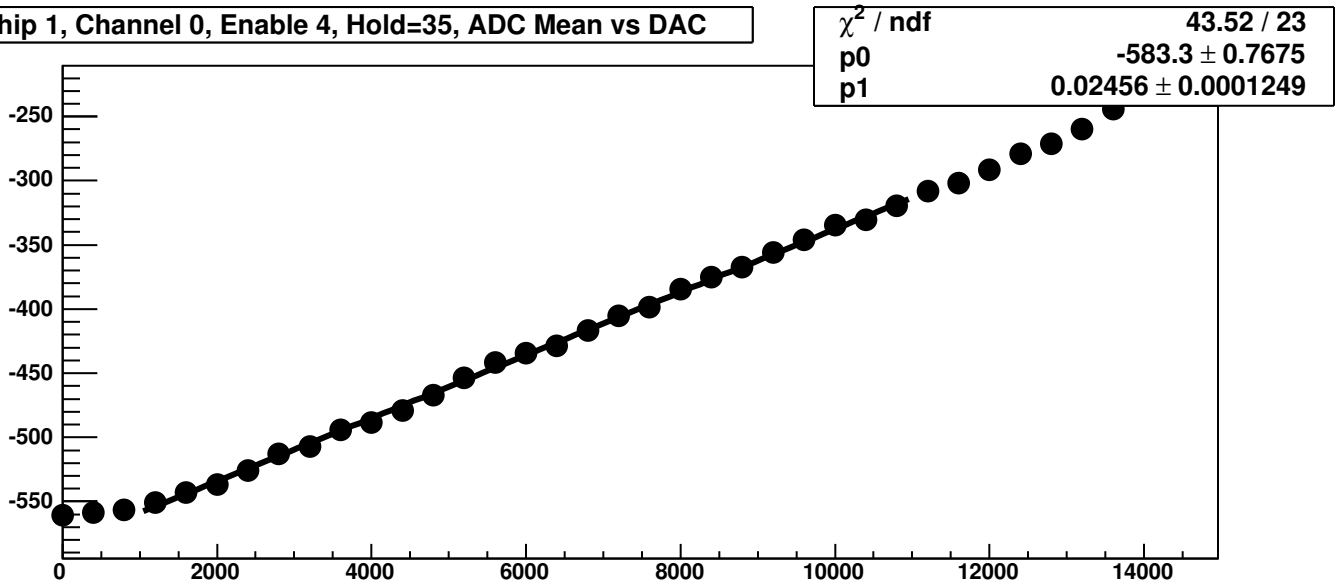



Chip 1, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 392.7 / 23
p0 -518.5 ± 2.493
p1 2.284 ± 0.00131
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

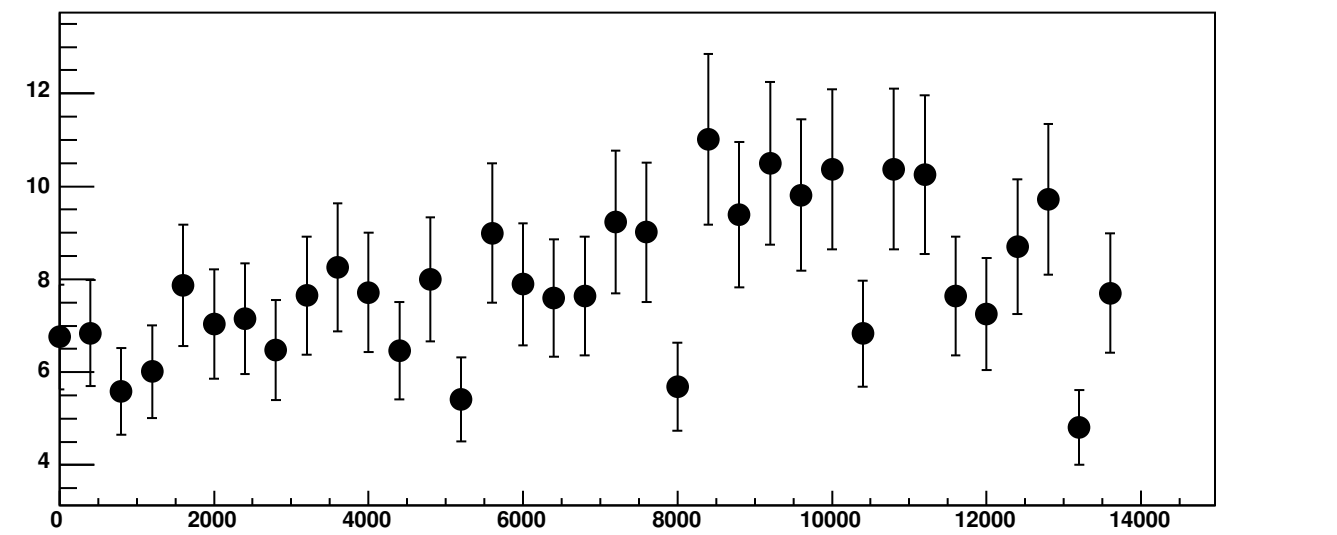

Chip 1, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

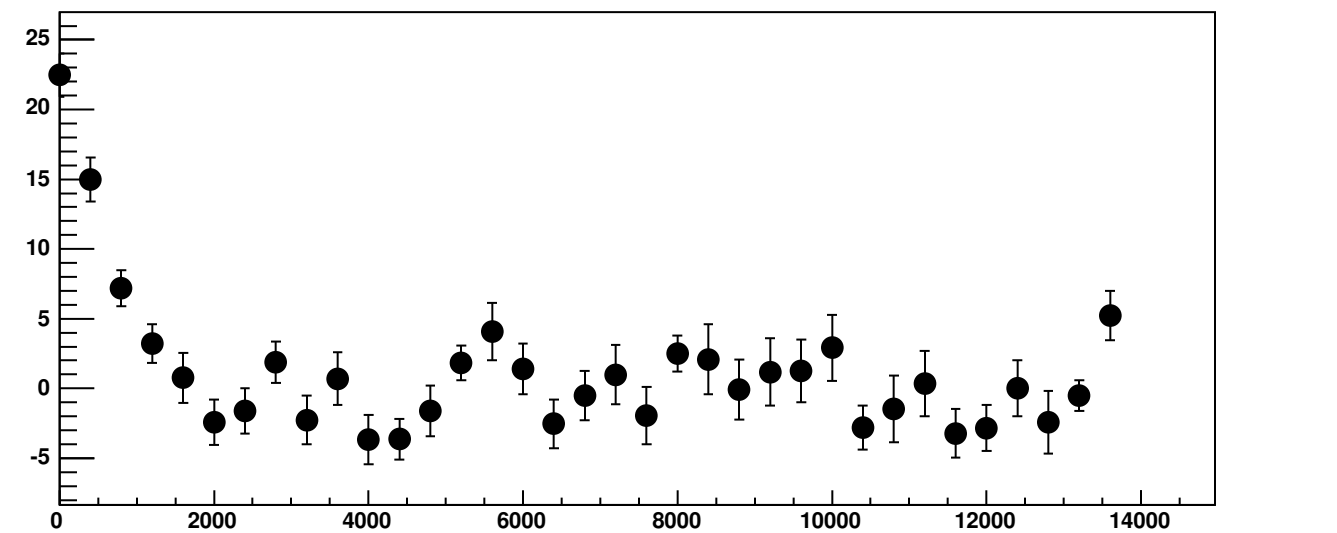
Chip 1, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

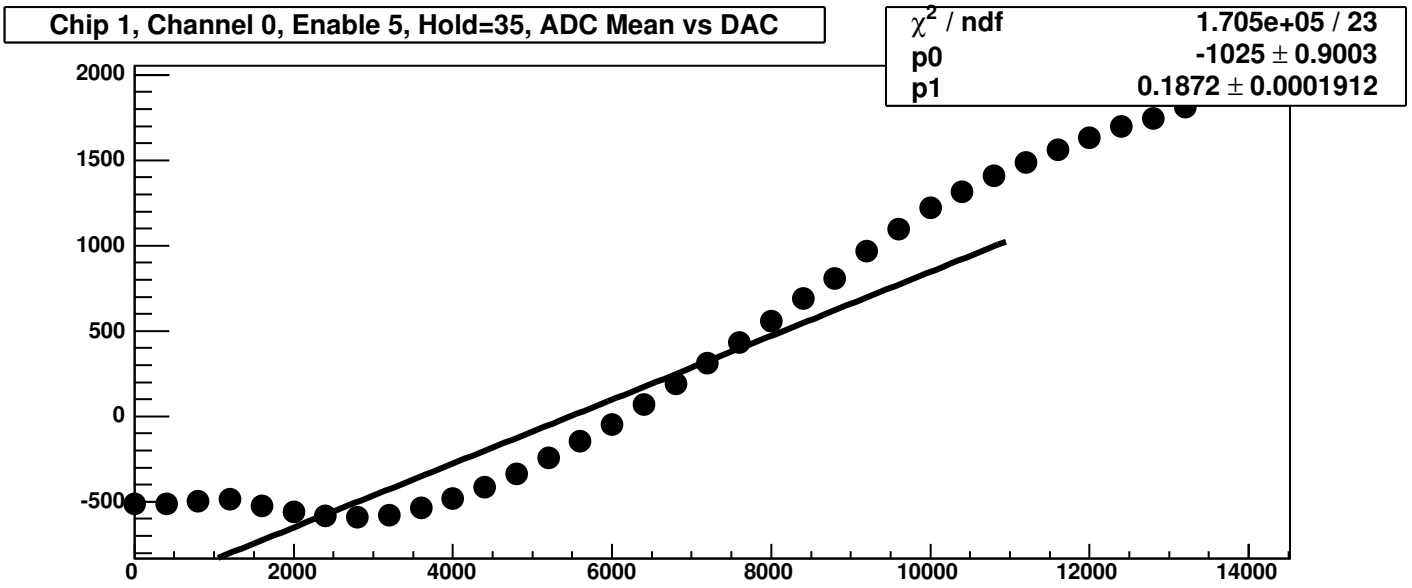


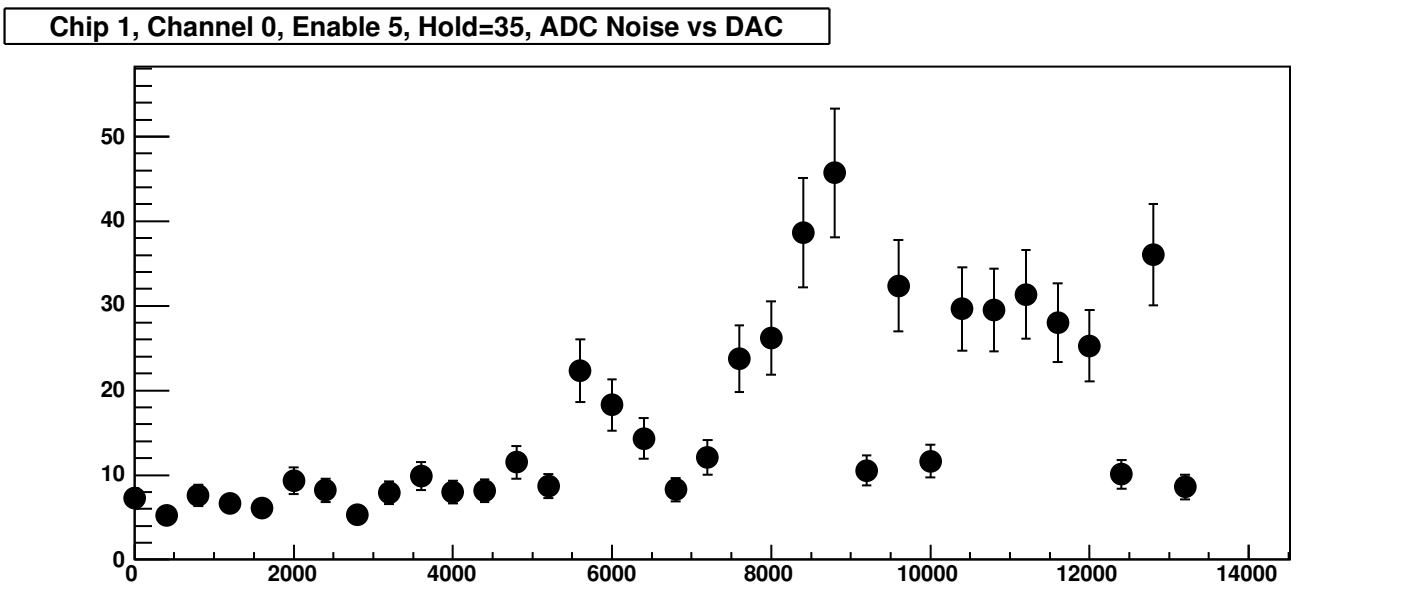
Chip 1, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

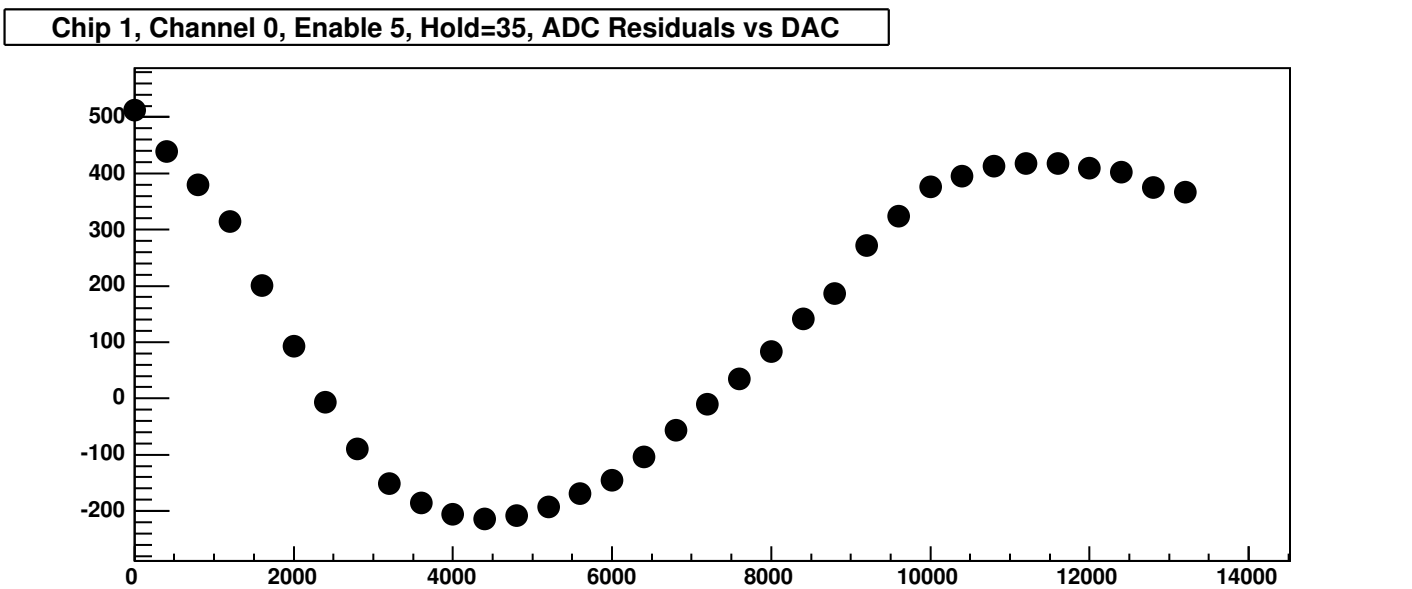


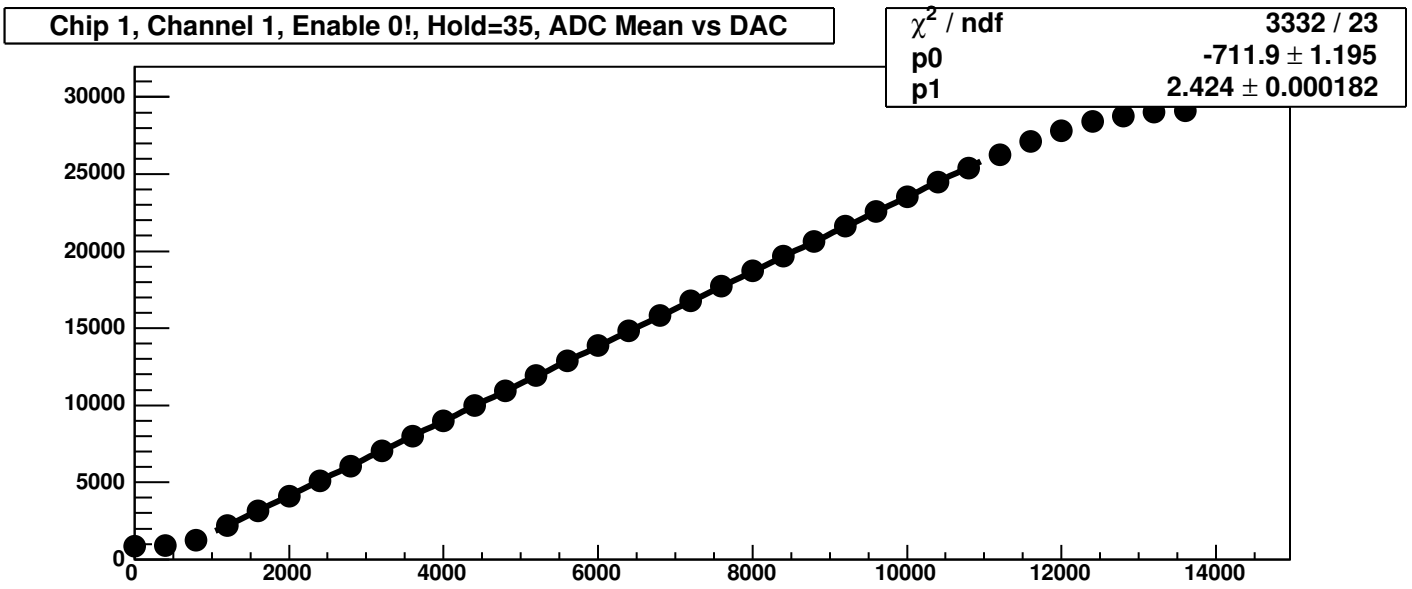
Chip 1, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

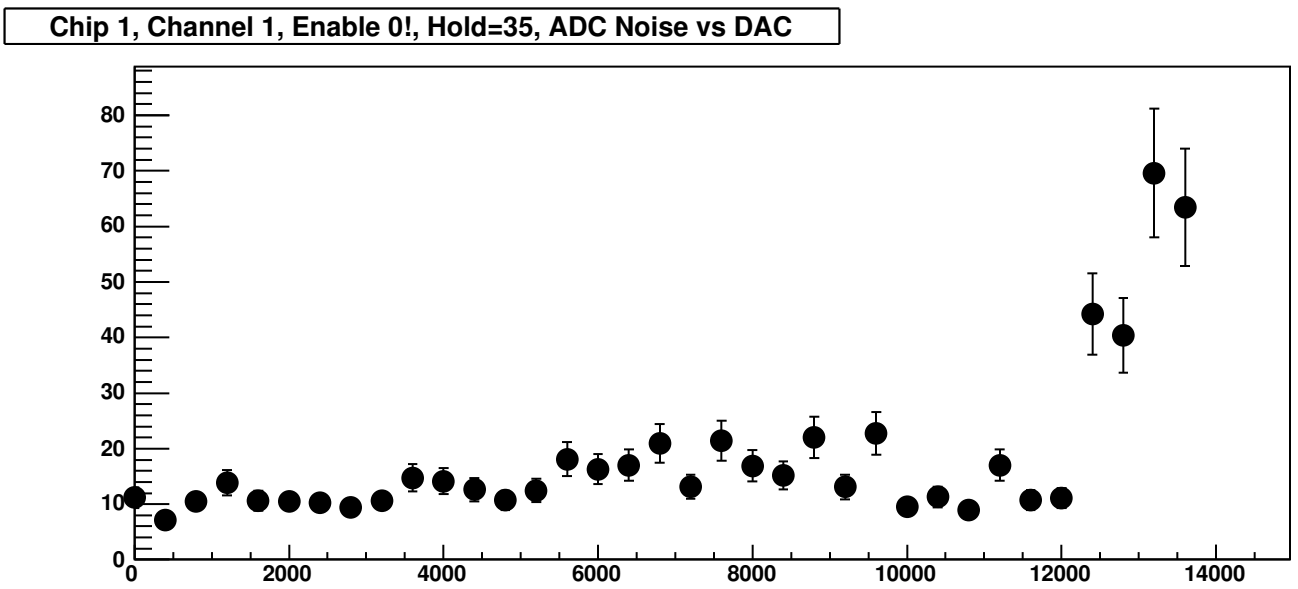


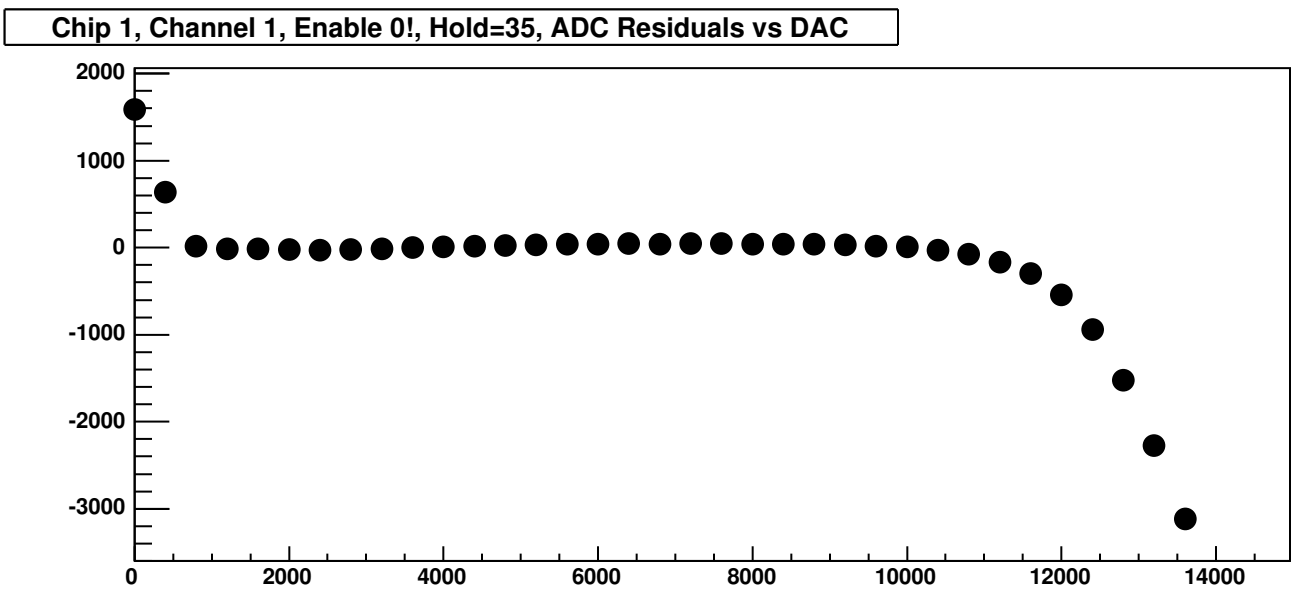

Chip 1, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.705e+05 / 23
p0 -1025 ± 0.9003
p1 0.1872 ± 0.0001912
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

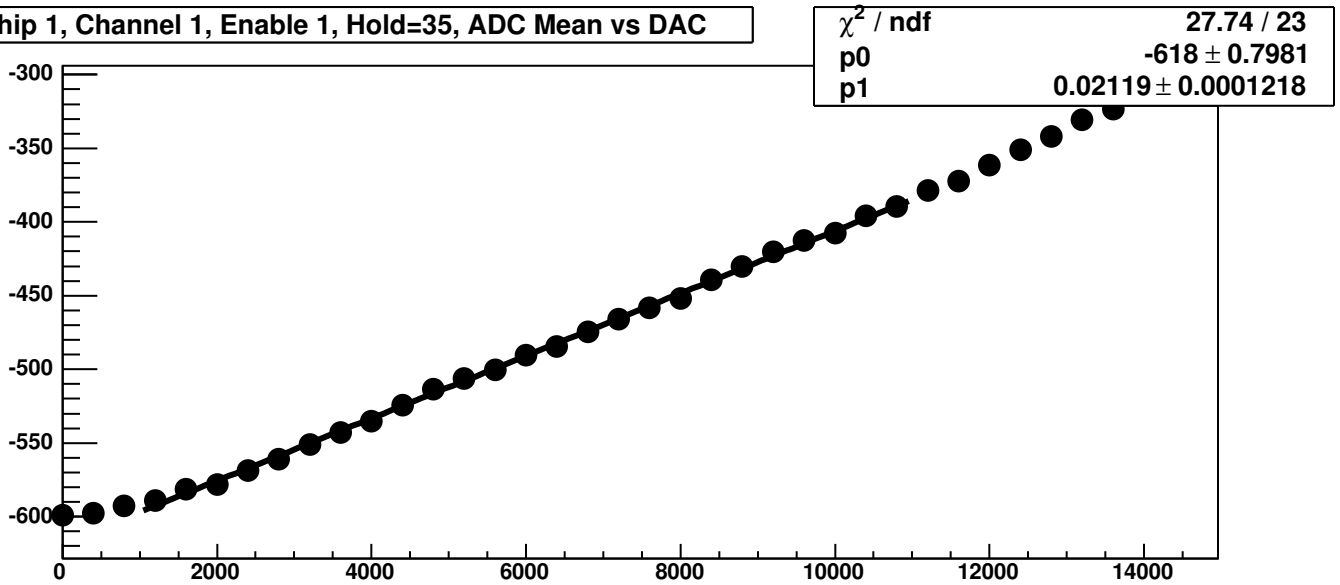

Chip 1, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 3332 / 23
p0 -711.9 ± 1.195
p1 2.424 ± 0.000182
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

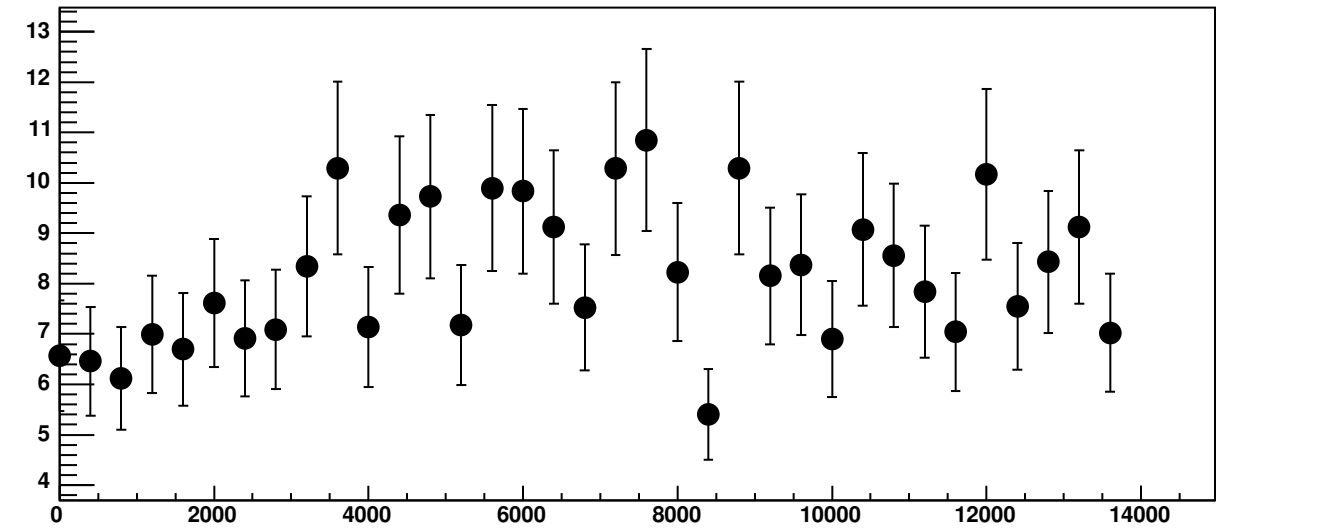

Chip 1, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

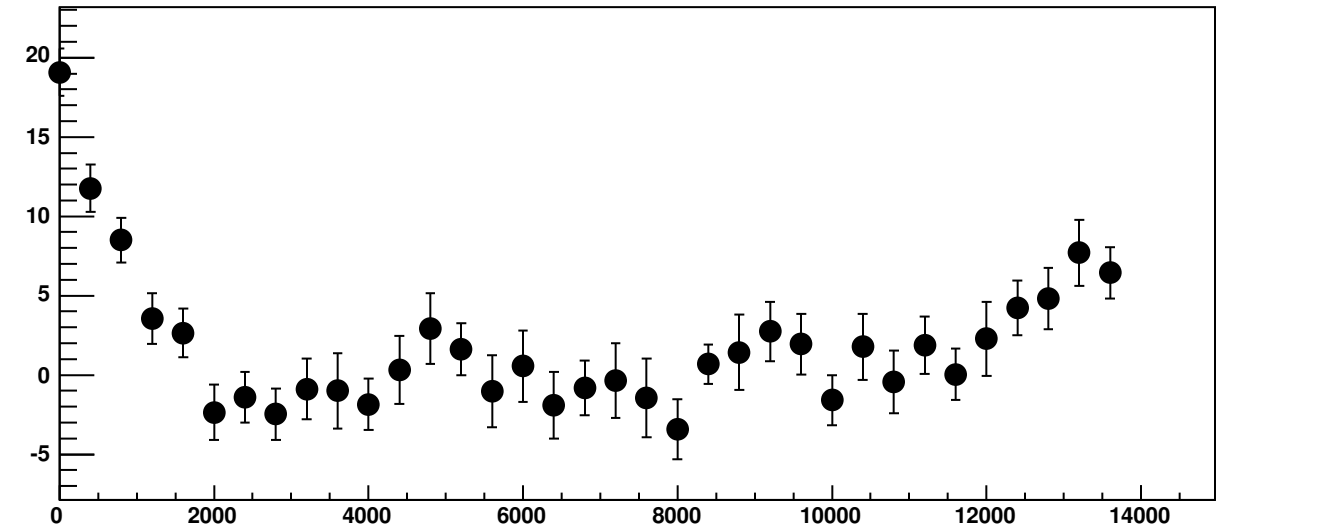
Chip 1, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

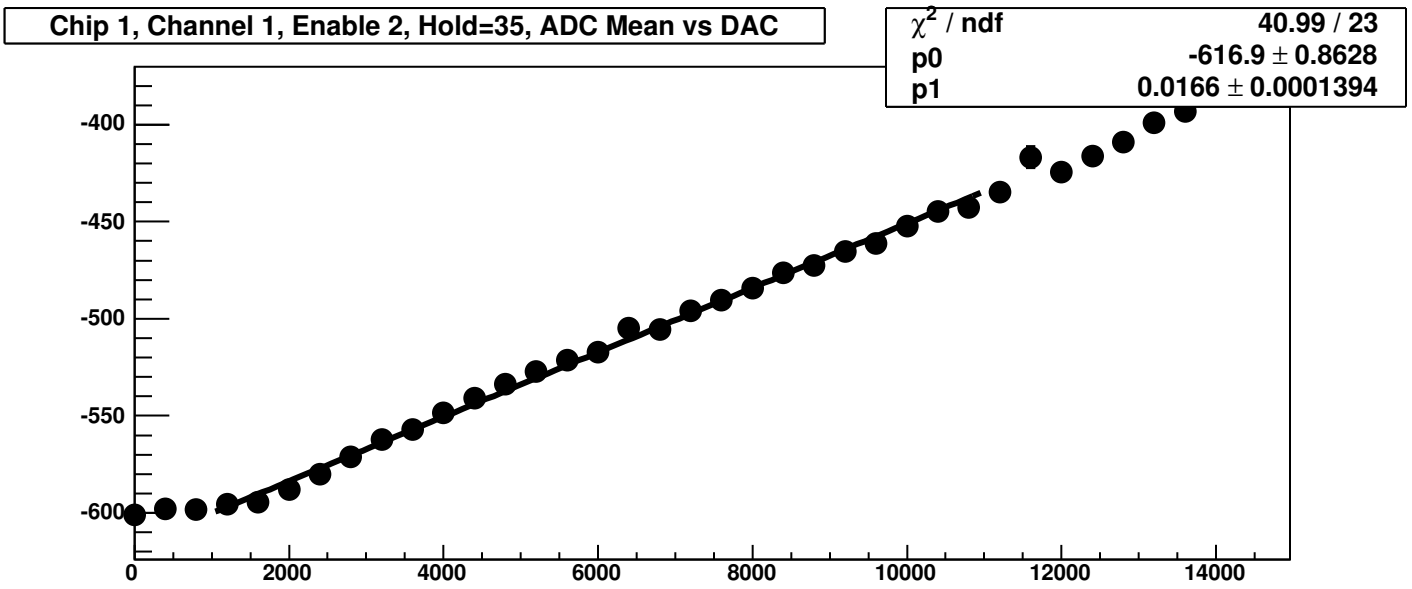


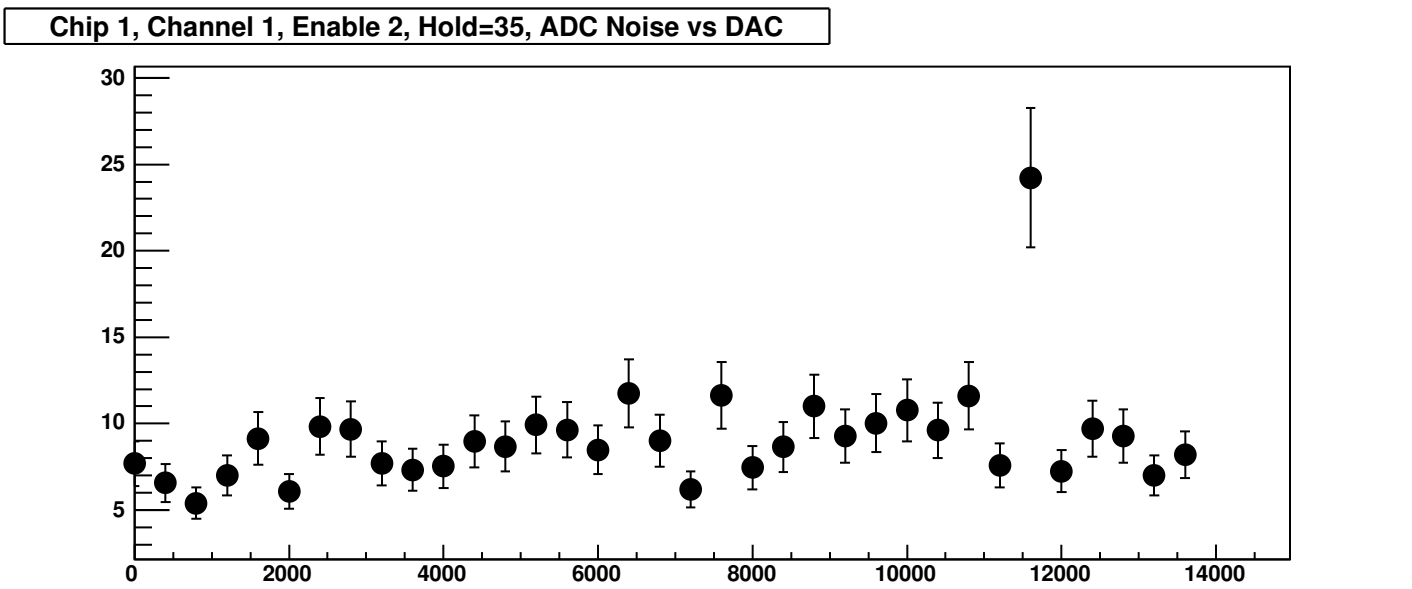
Chip 1, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC

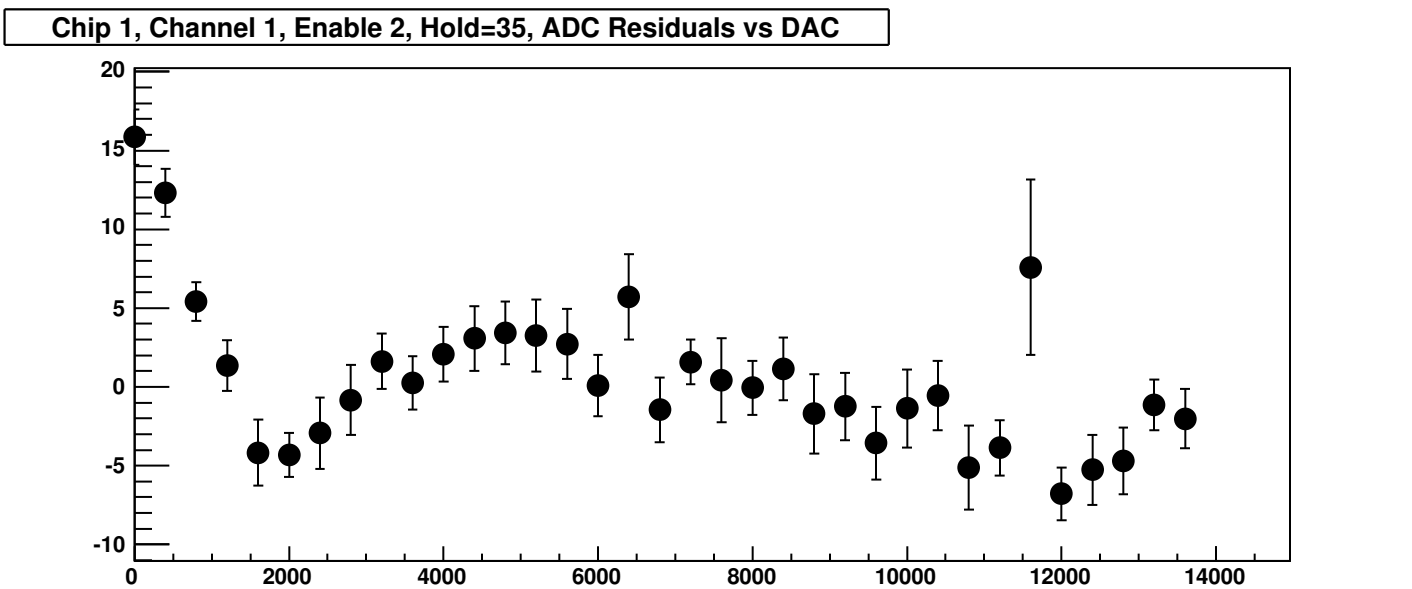


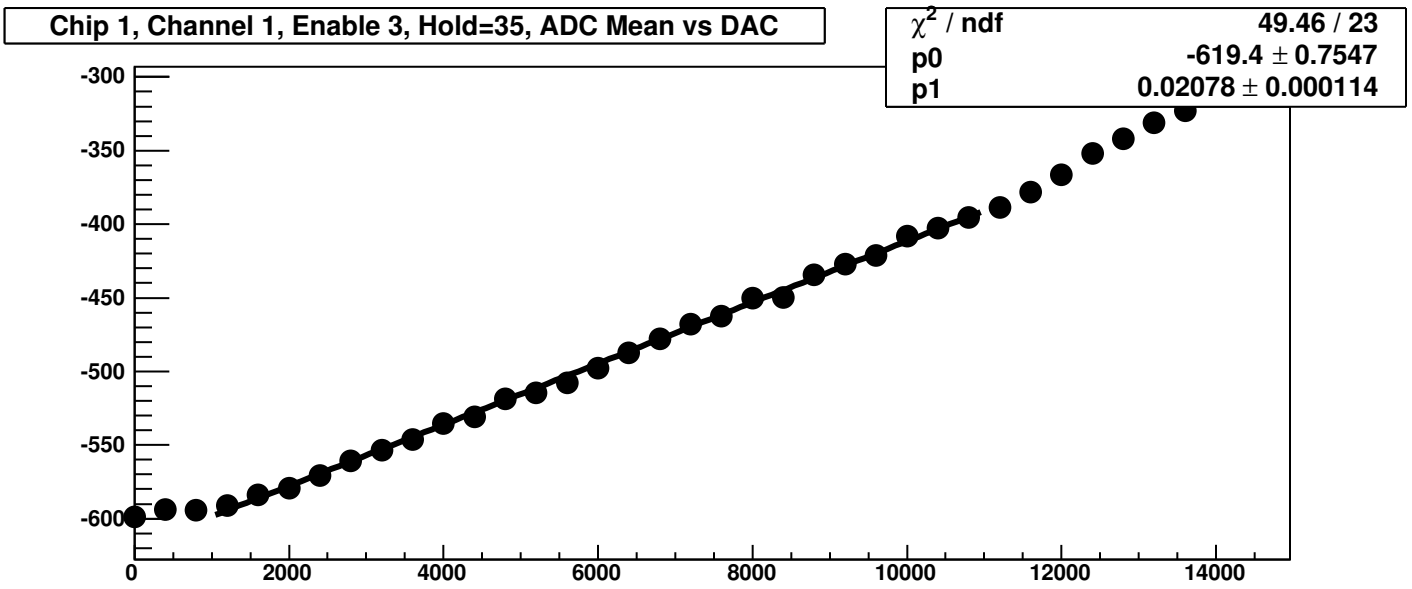
Chip 1, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

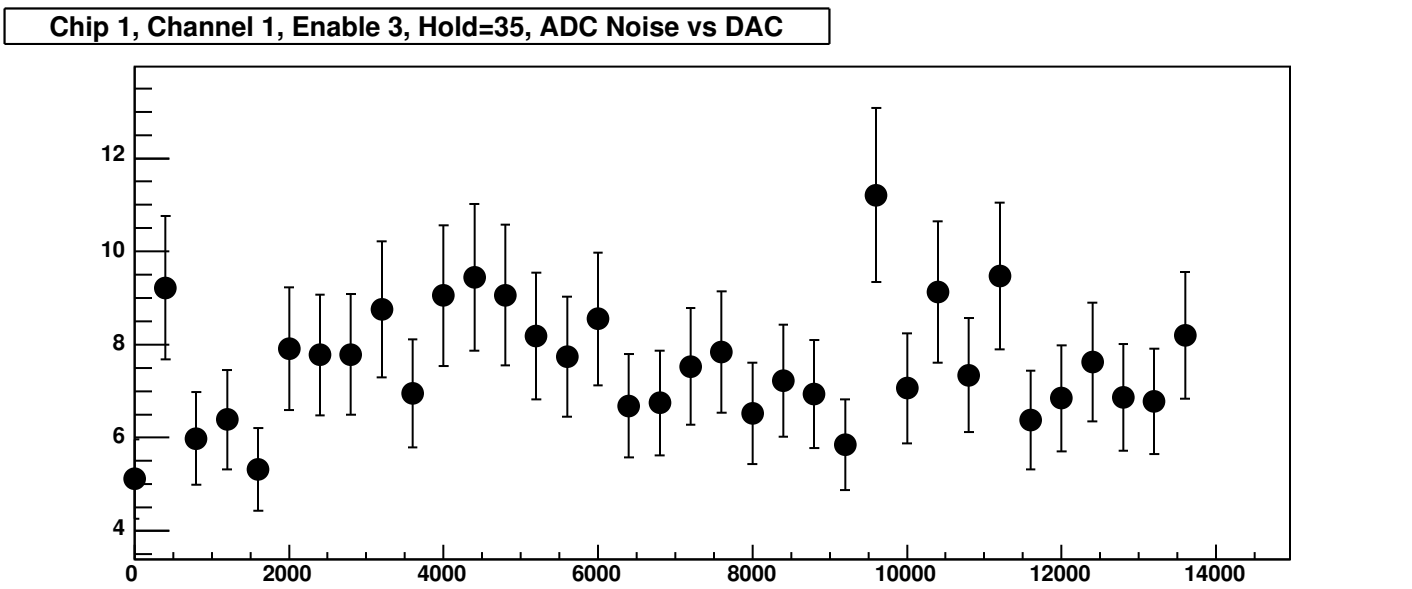


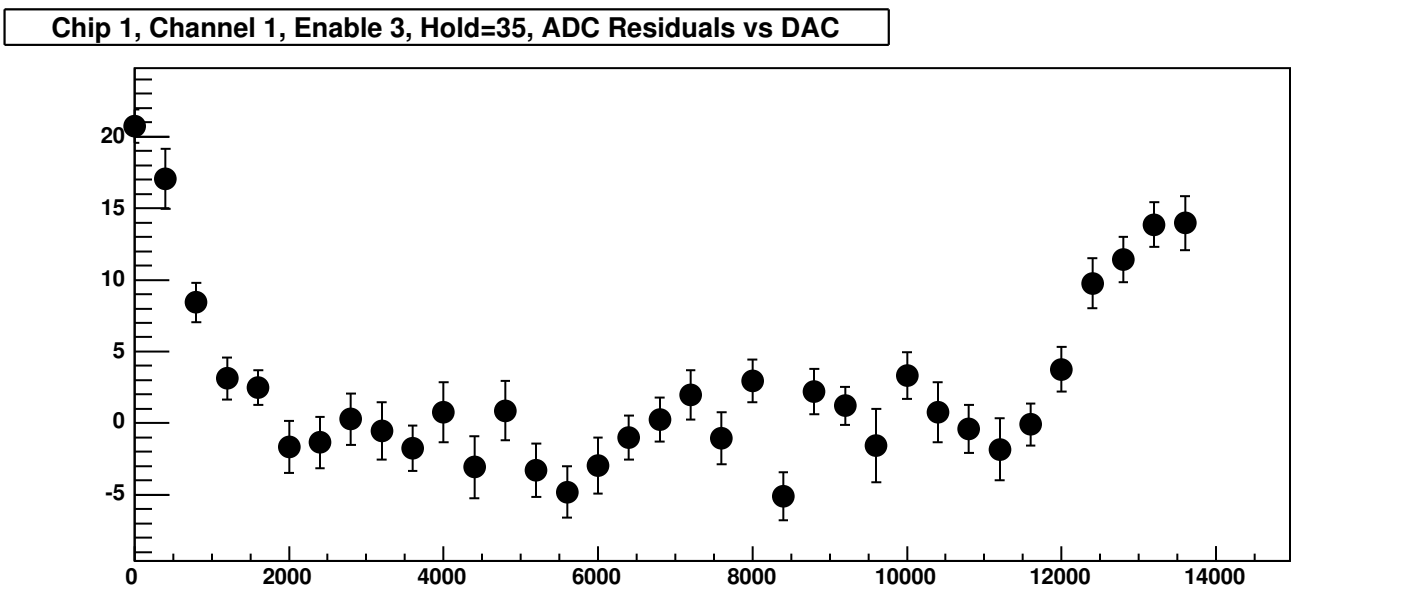

Chip 1, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 40.99 / 23
p0 -616.9 ± 0.8628
p1 0.0166 ± 0.0001394
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

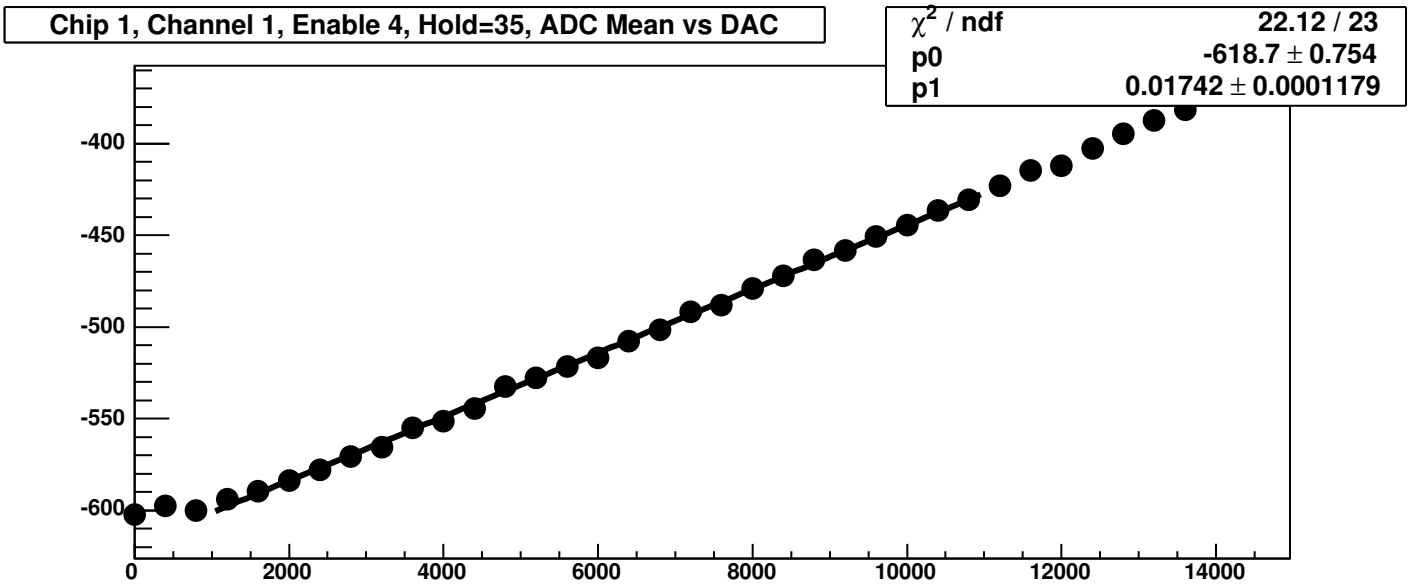

Chip 1, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

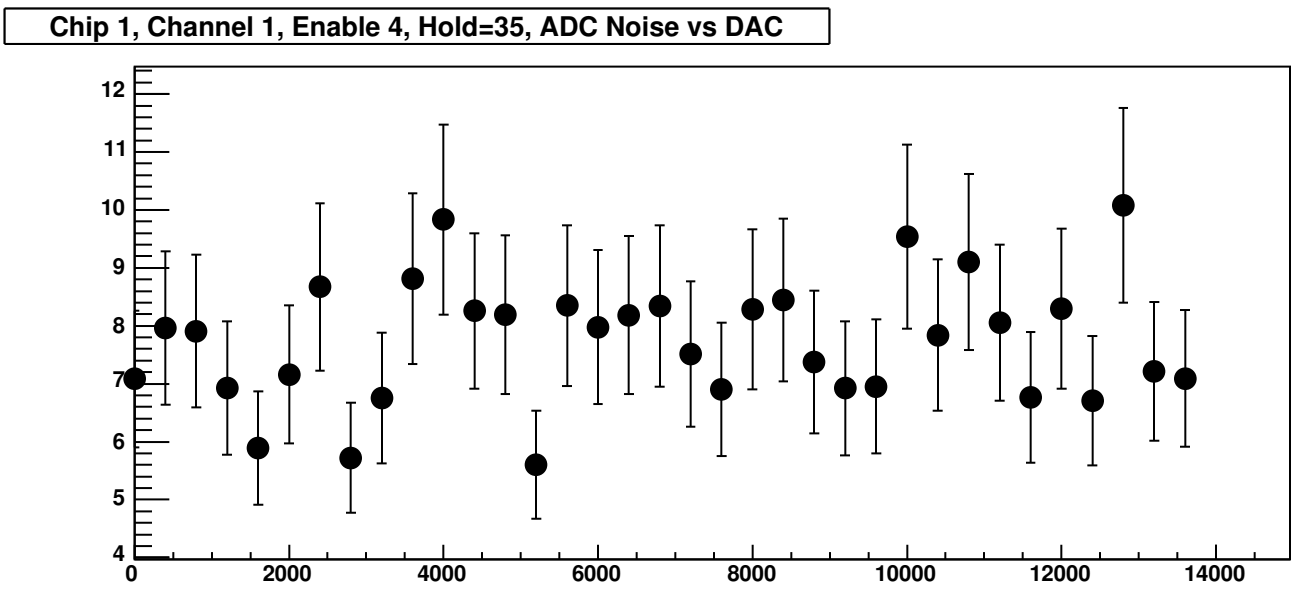

Chip 1, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

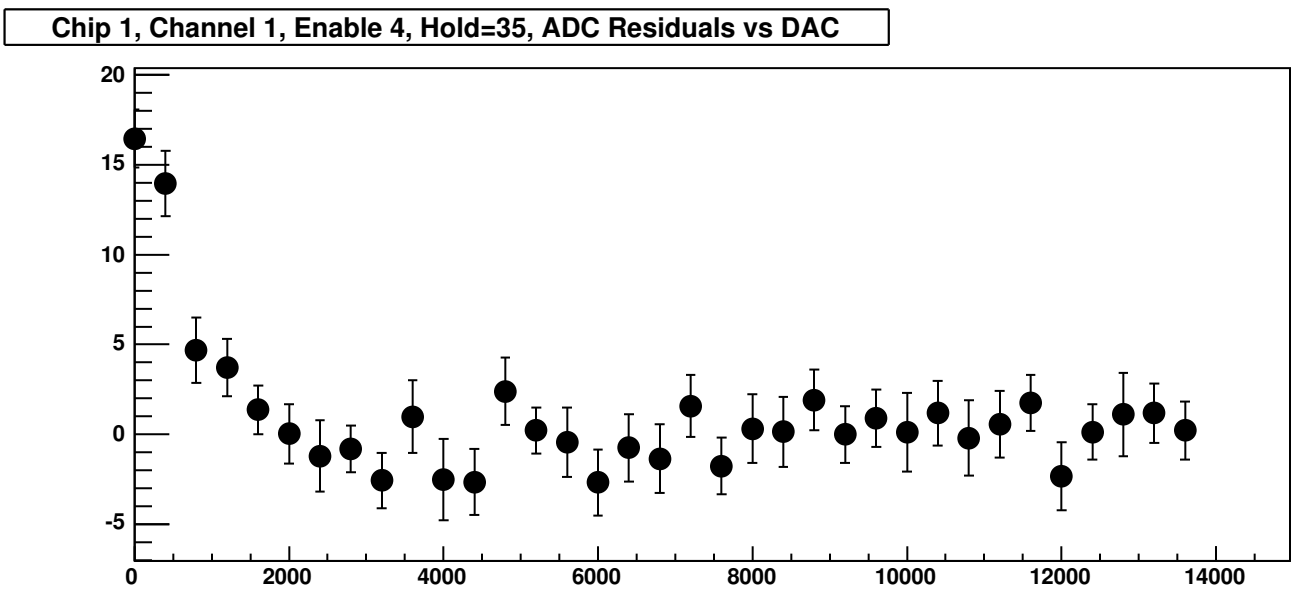

Chip 1, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 49.46 / 23
p0 -619.4 ± 0.7547
p1 0.02078 ± 0.000114
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

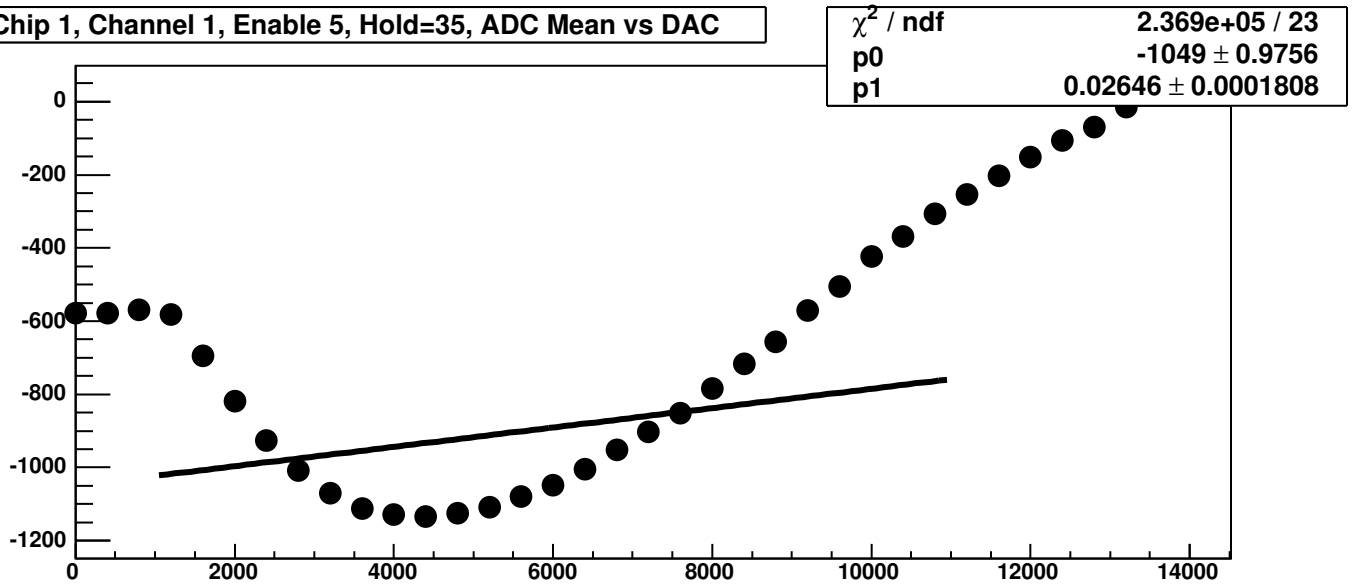

Chip 1, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 22.12 / 23
p0 -618.7 ± 0.754
p1 0.01742 ± 0.0001179
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

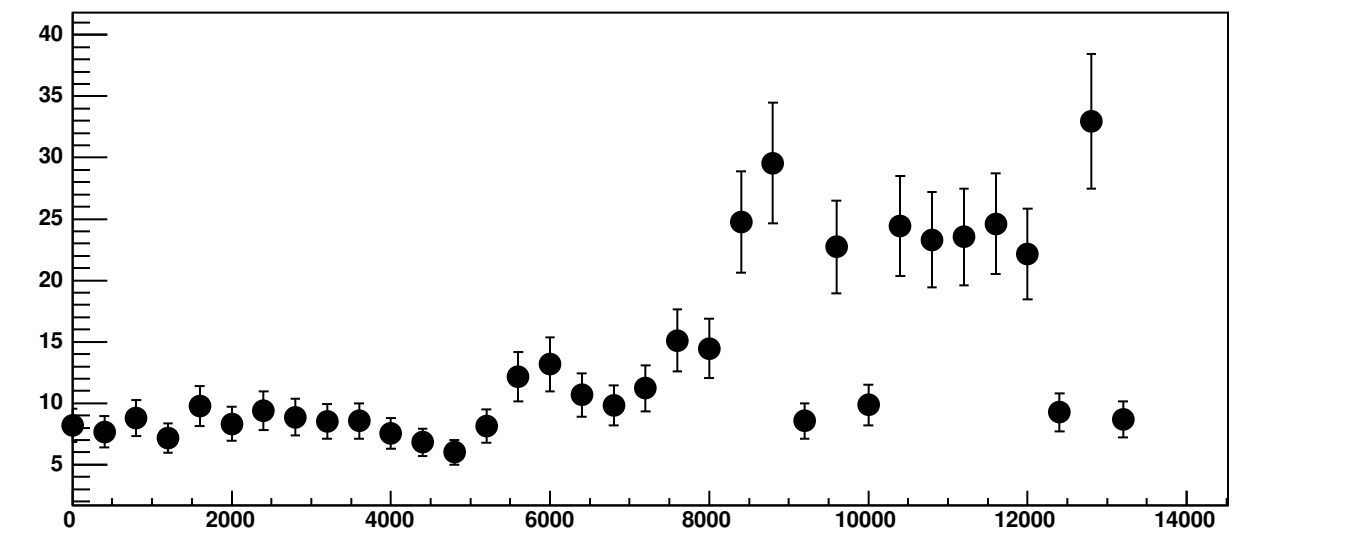

Chip 1, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

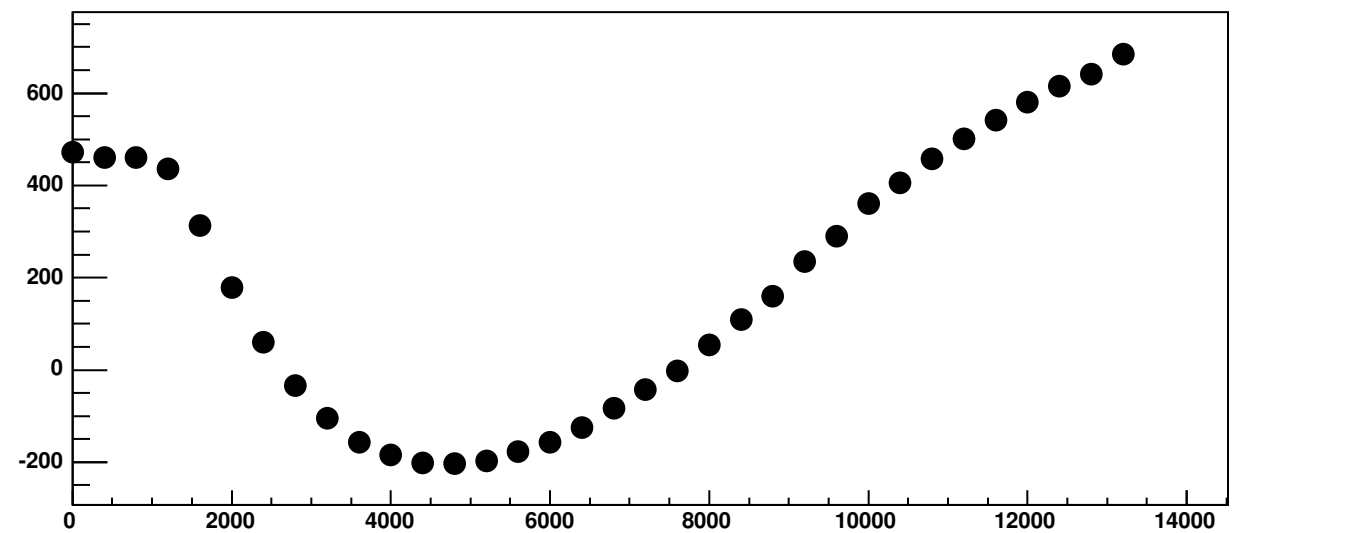
Chip 1, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC



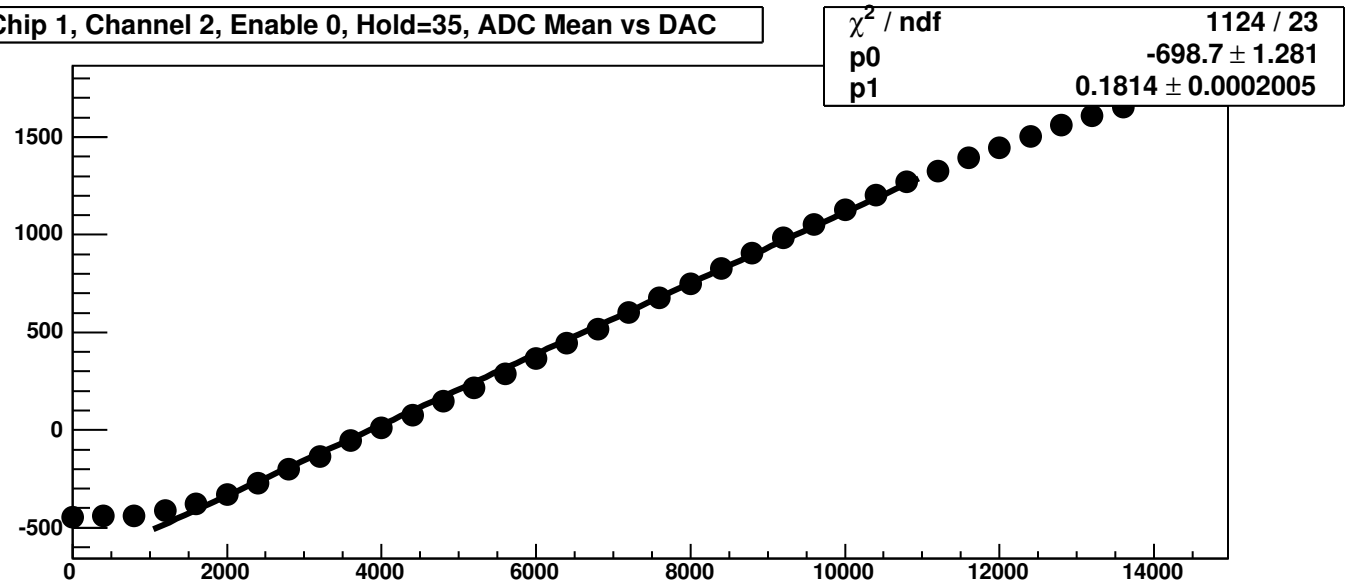
Chip 1, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC



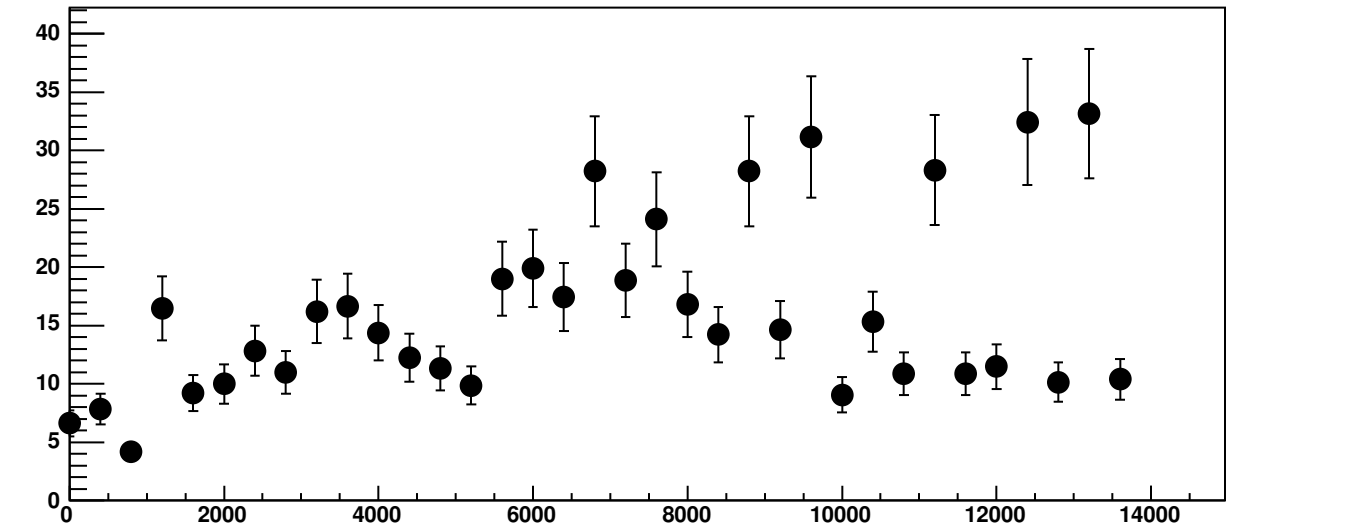
Chip 1, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC



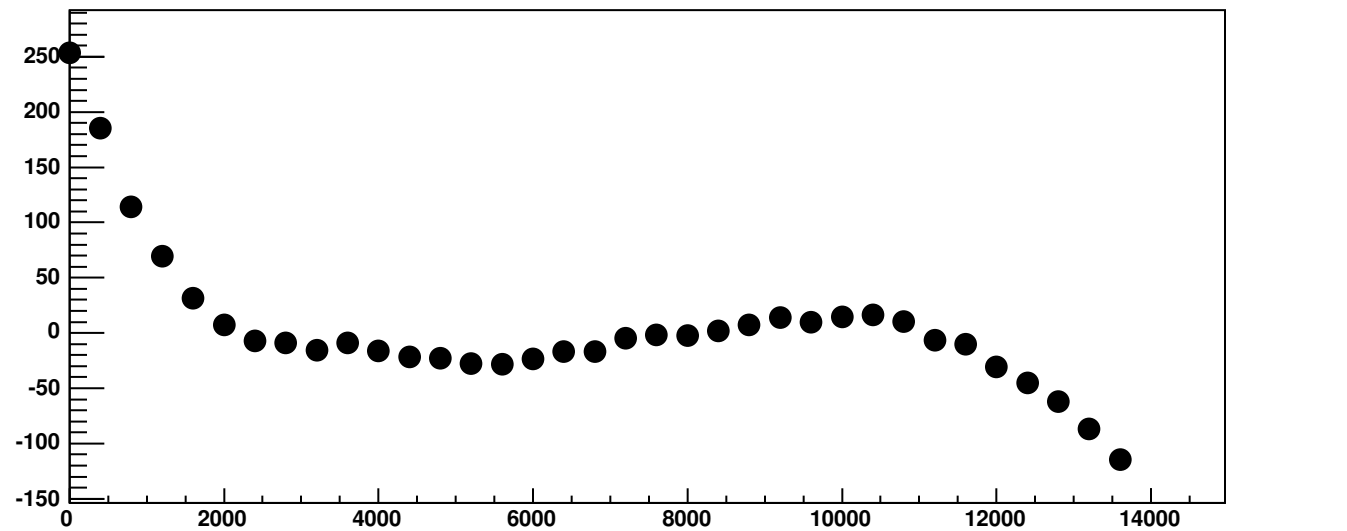
Chip 1, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC



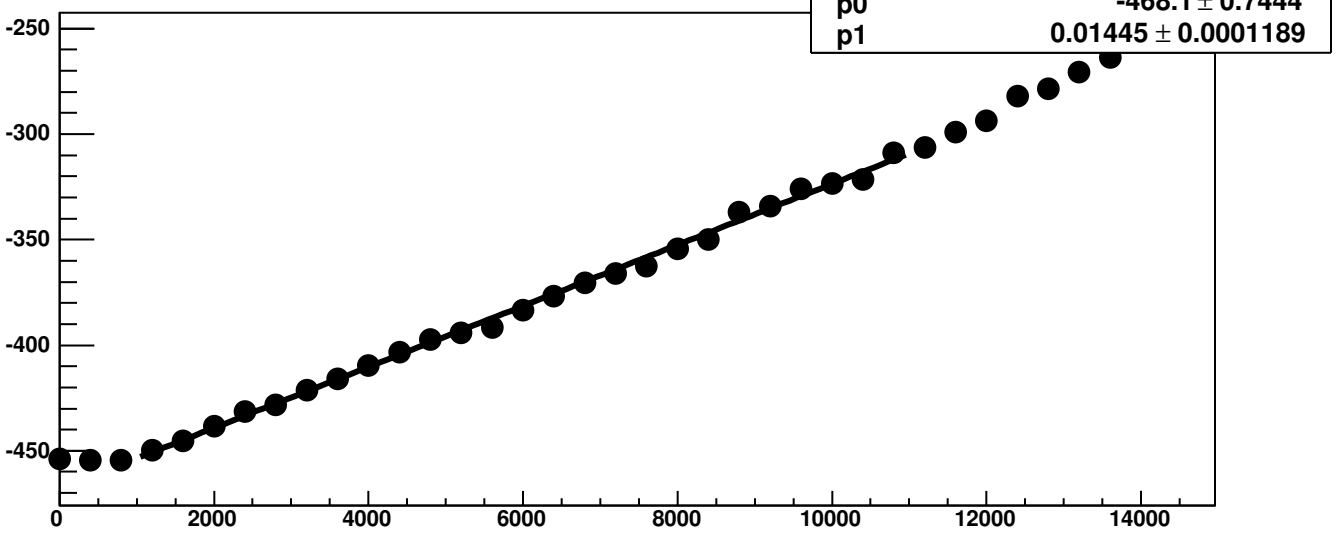
Chip 1, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



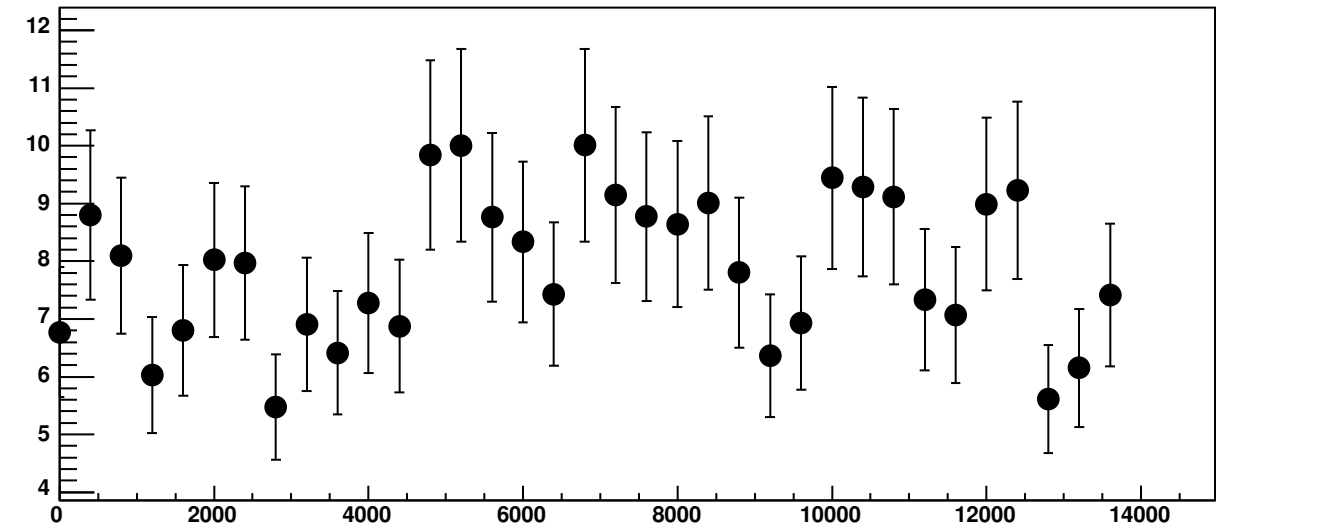
Chip 1, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC



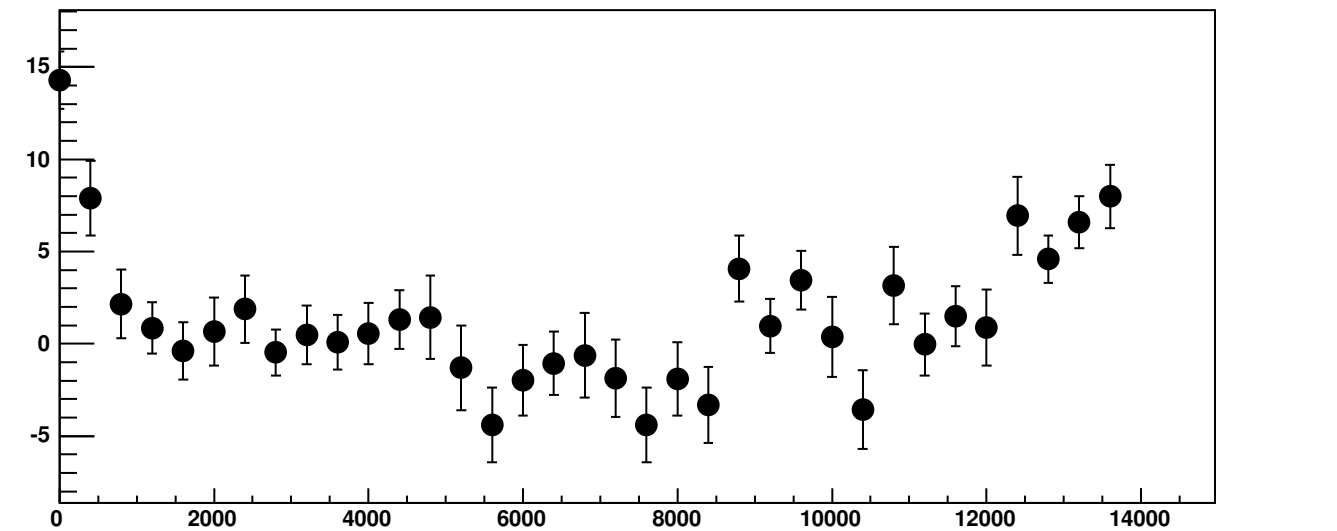
Chip 1, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

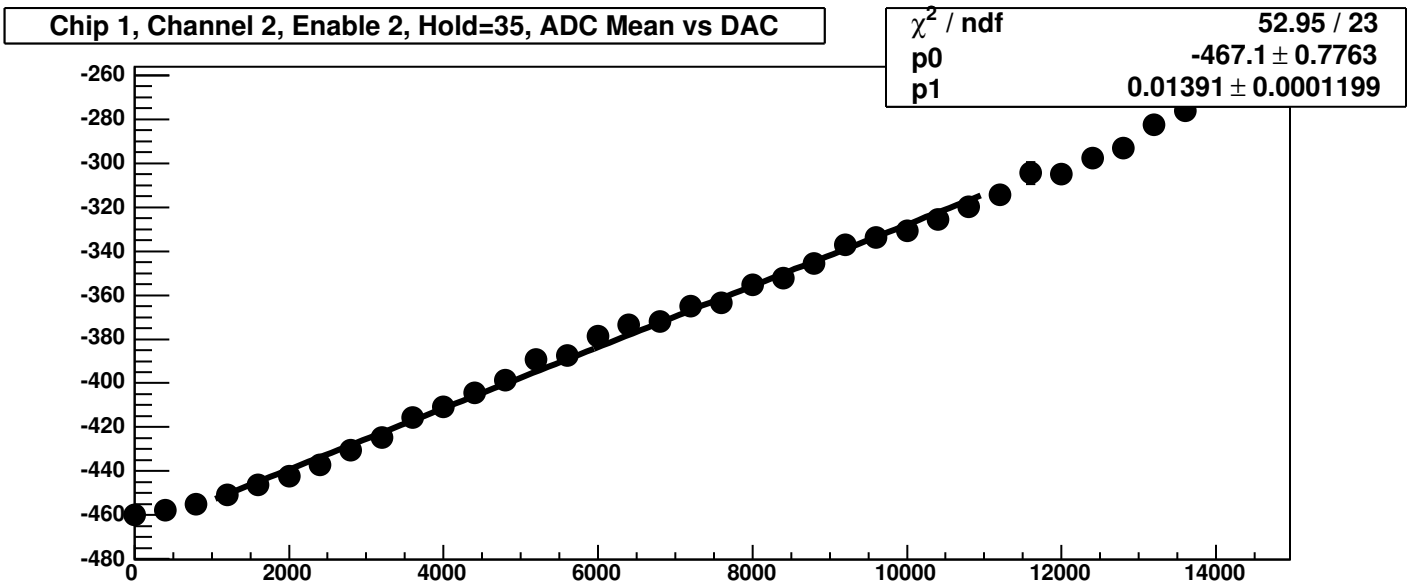


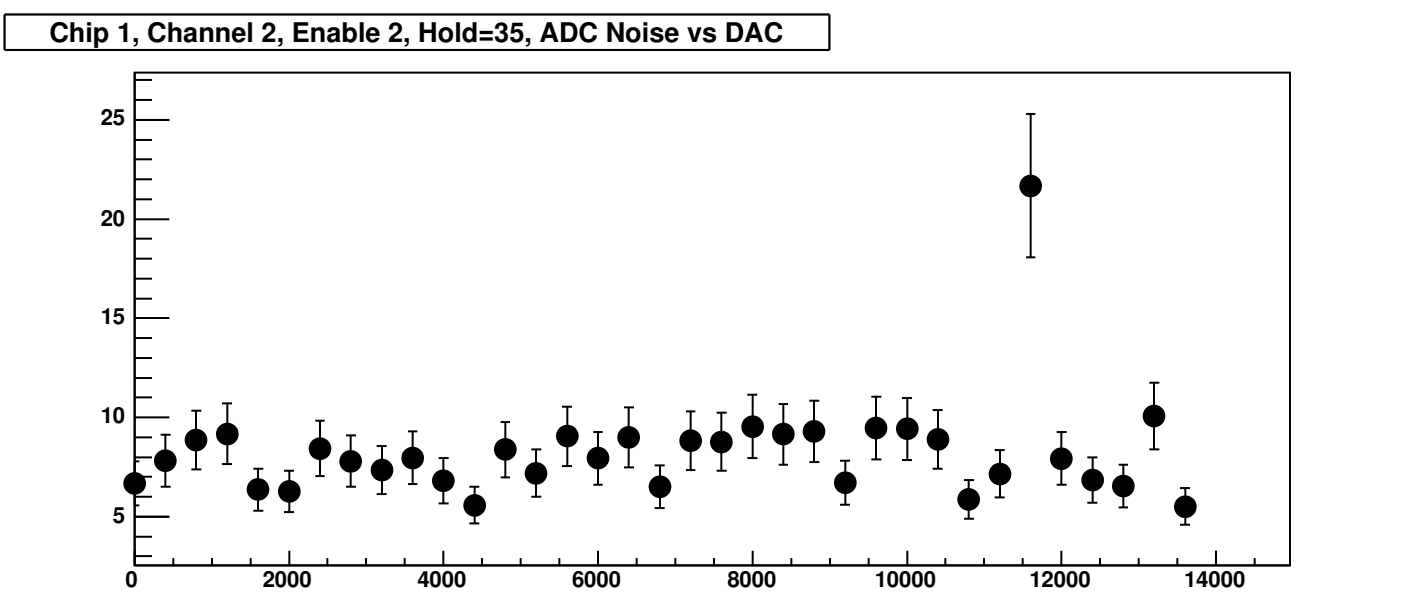
Chip 1, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC

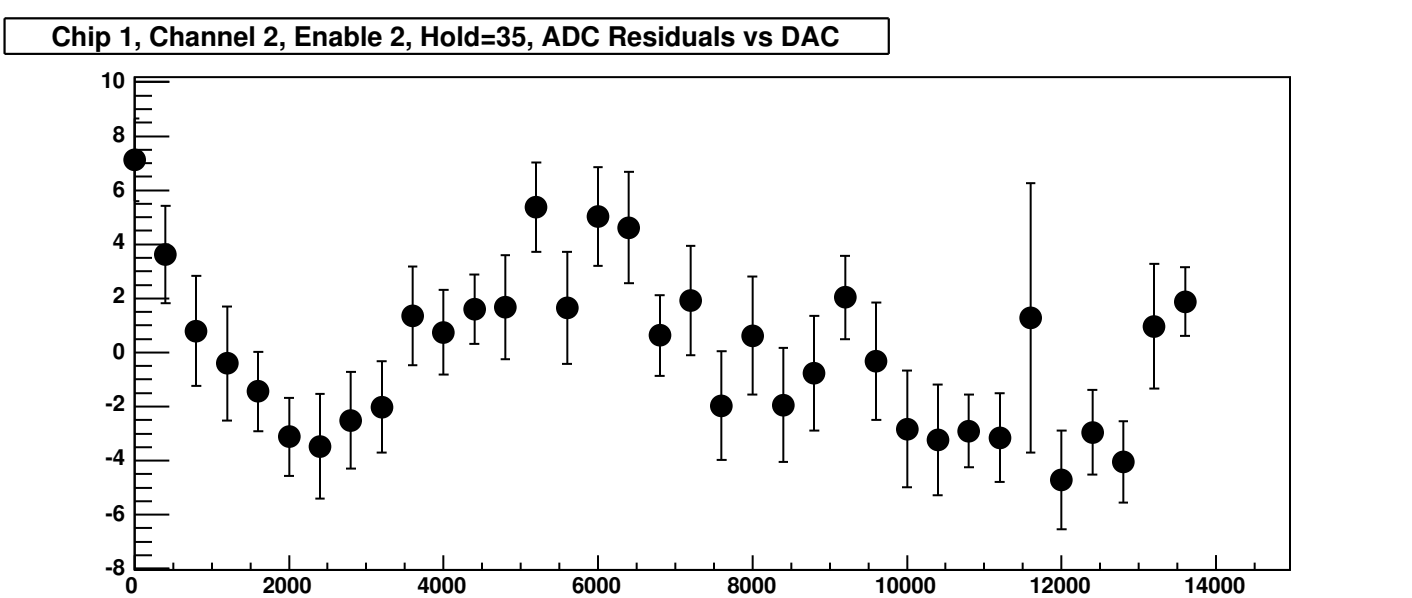


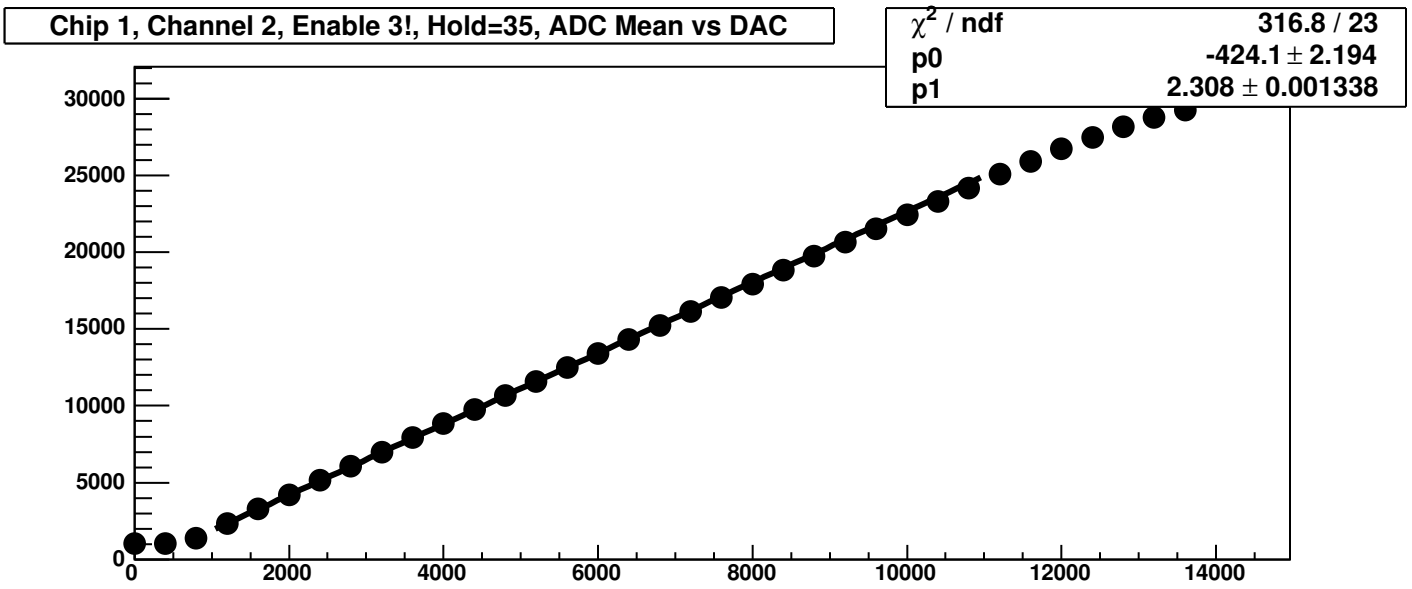
Chip 1, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

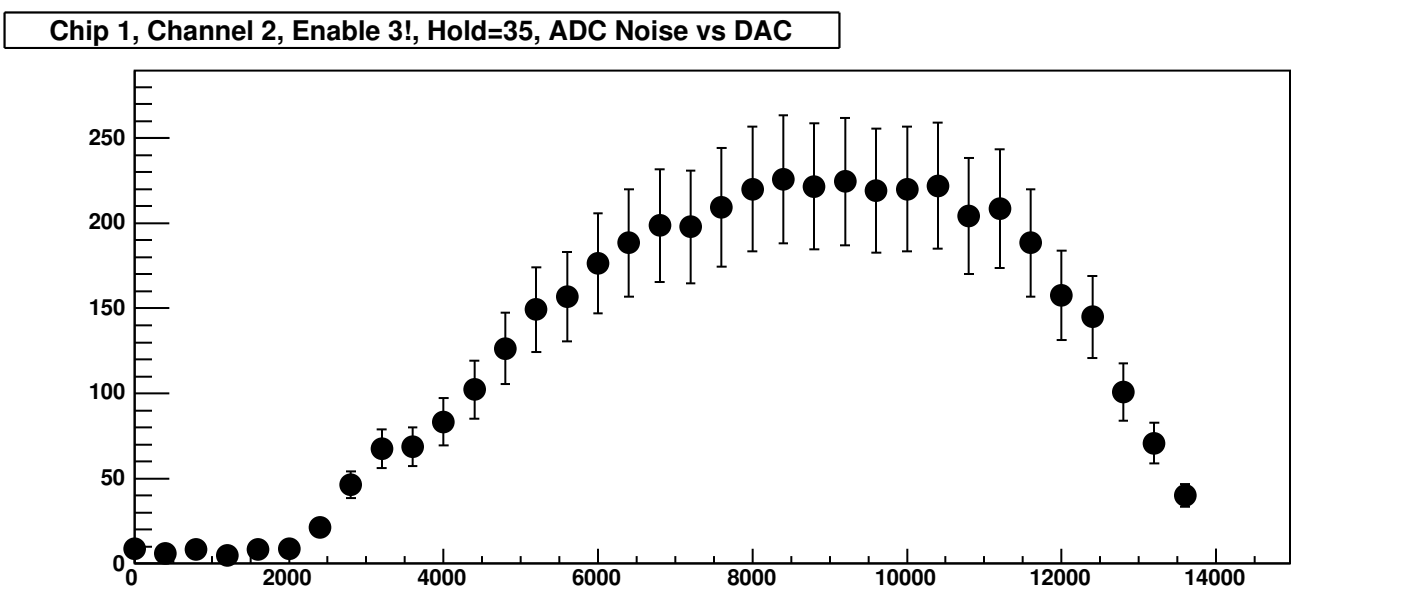


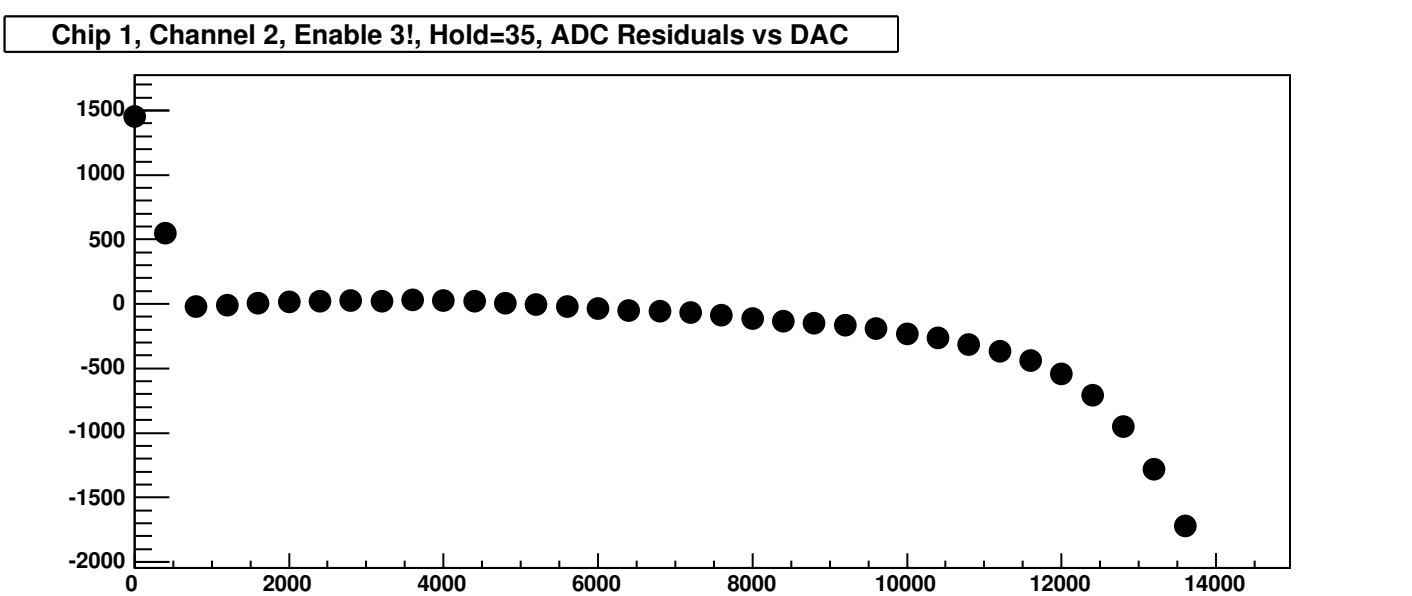

Chip 1, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 52.95 / 23
p0 -467.1 ± 0.7763
p1 0.01391 ± 0.0001199
-260
-280
-300
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

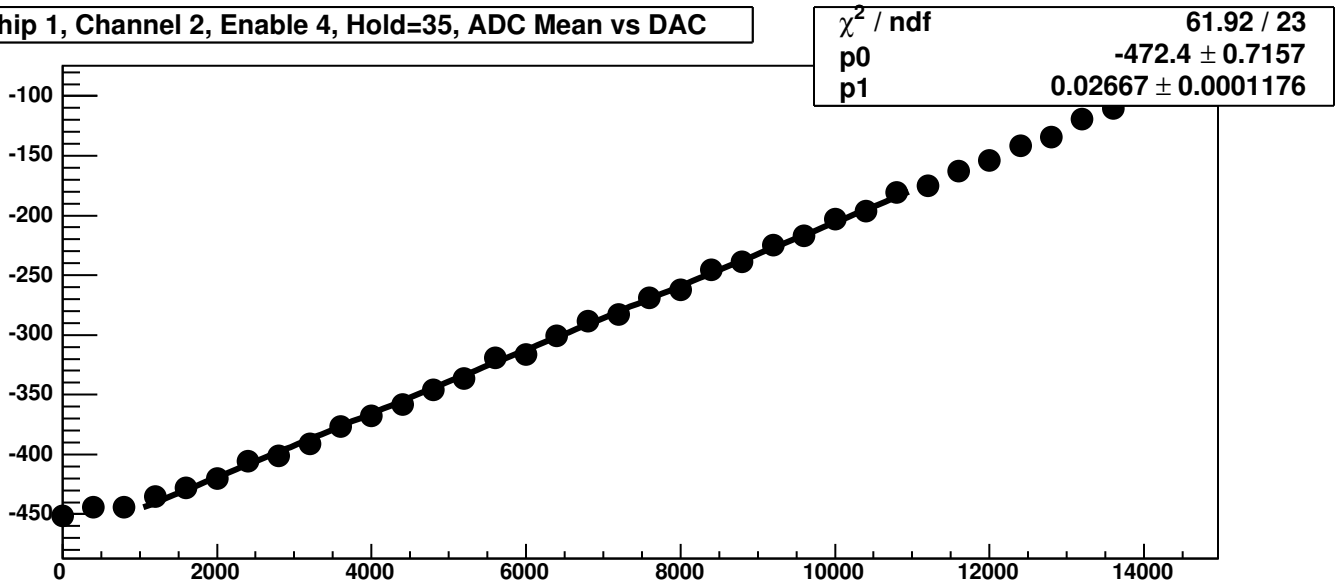

Chip 1, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 316.8 / 23
p0 -424.1 ± 2.194
p1 2.308 ± 0.001338
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

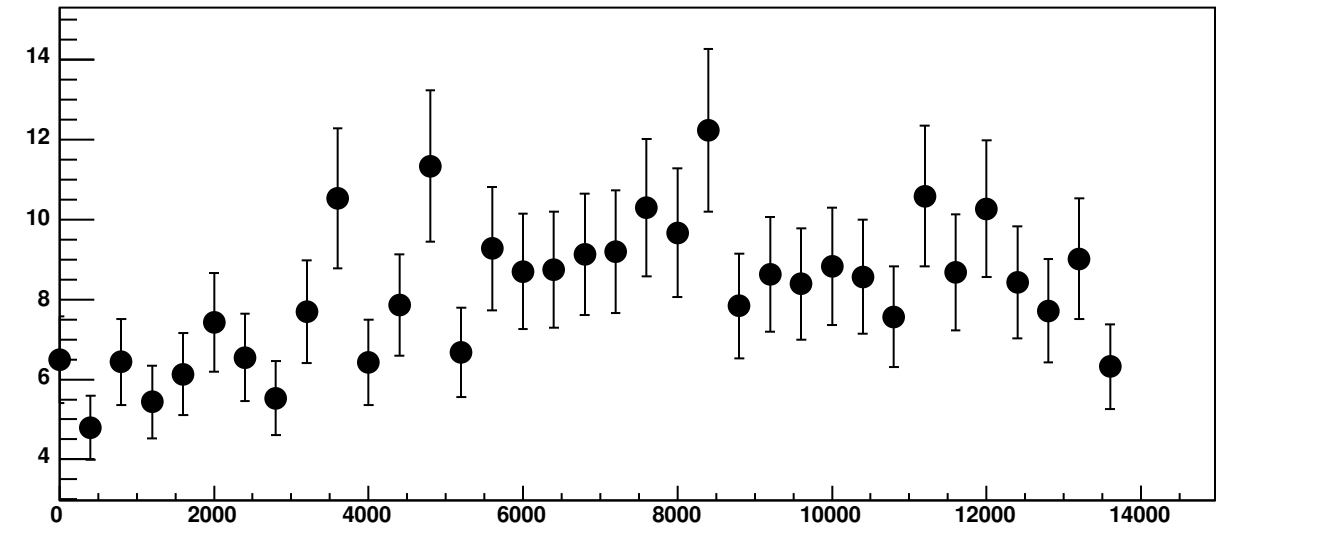

Chip 1, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

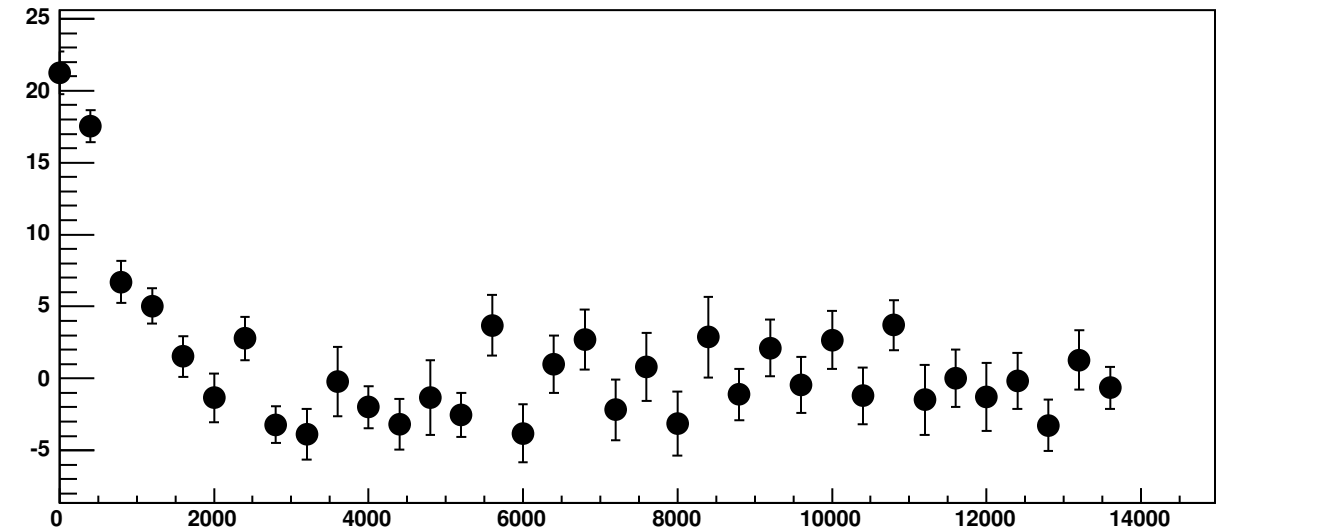
Chip 1, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

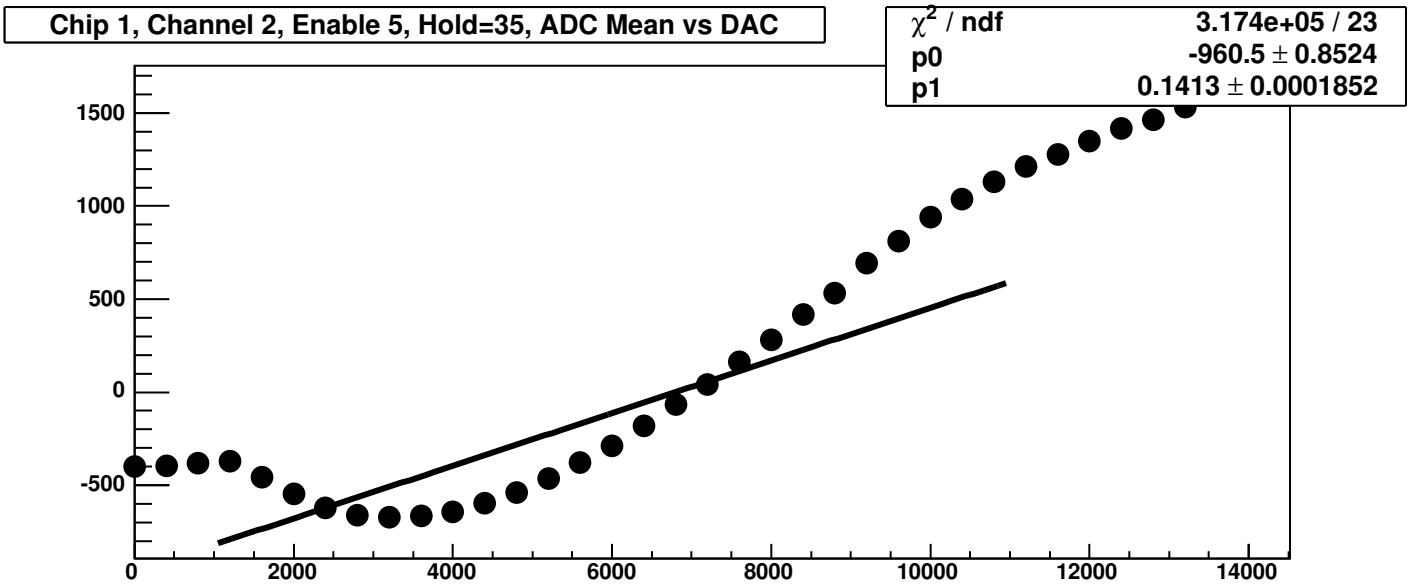


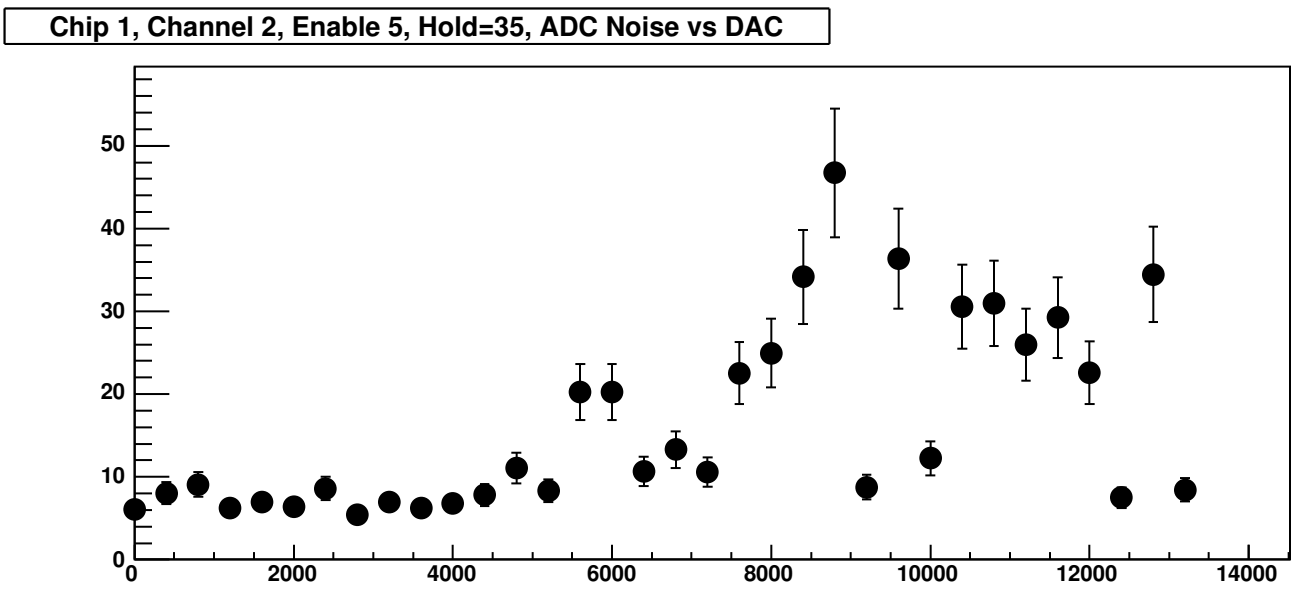
Chip 1, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

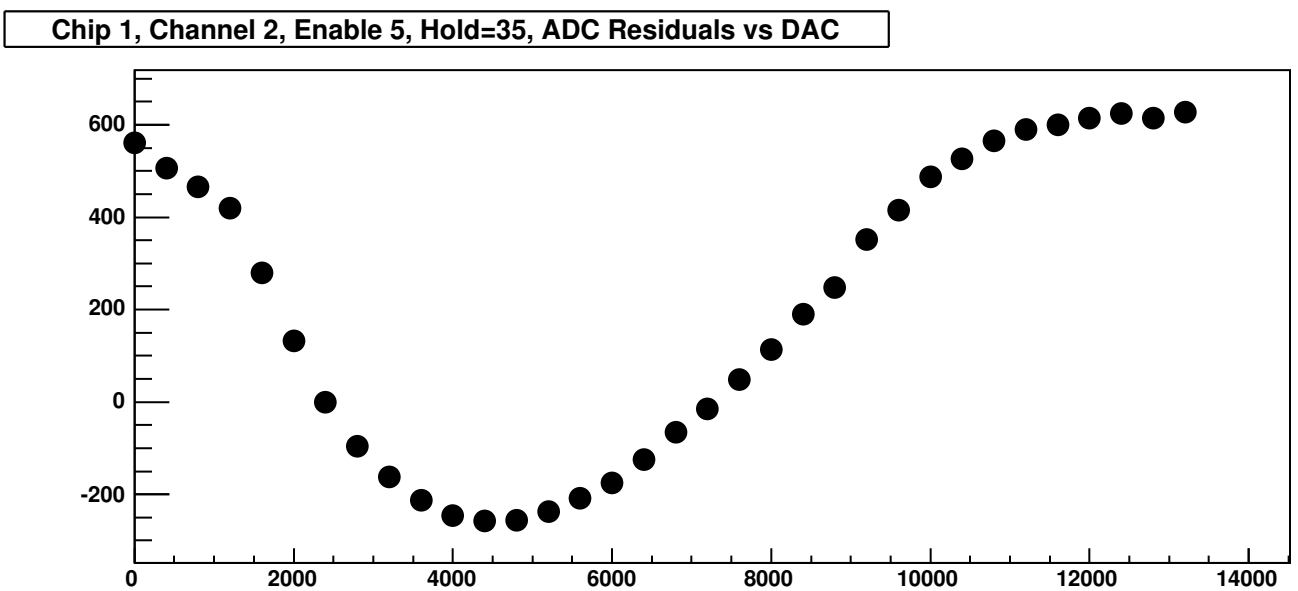


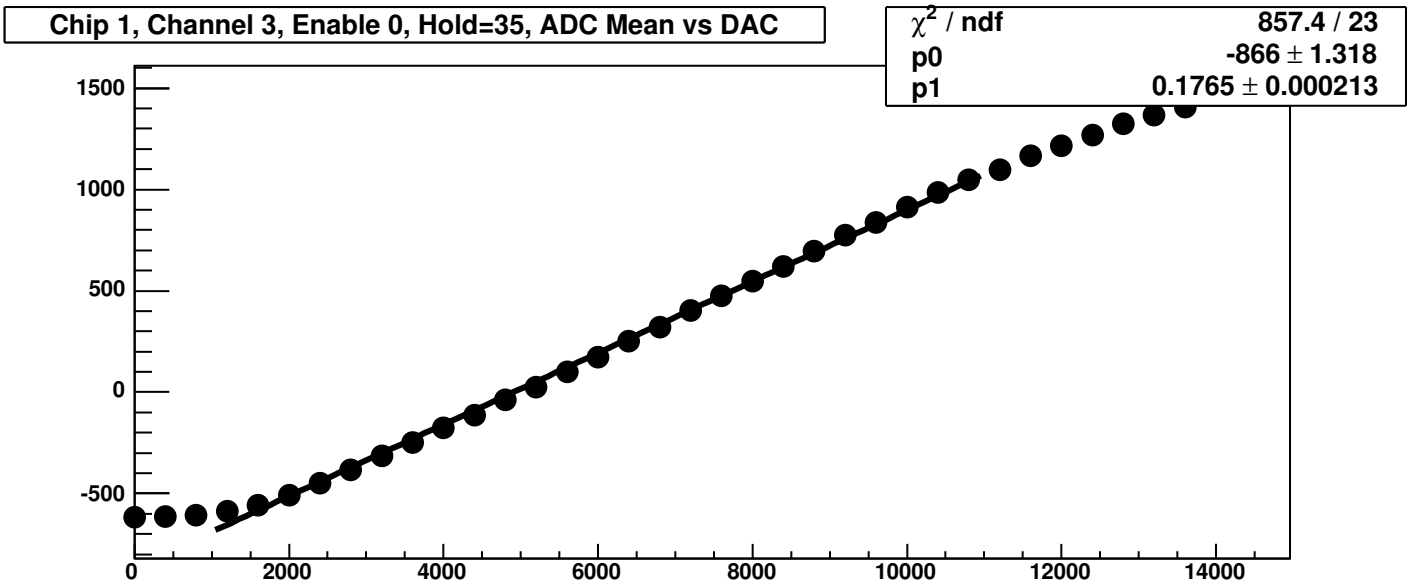
Chip 1, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

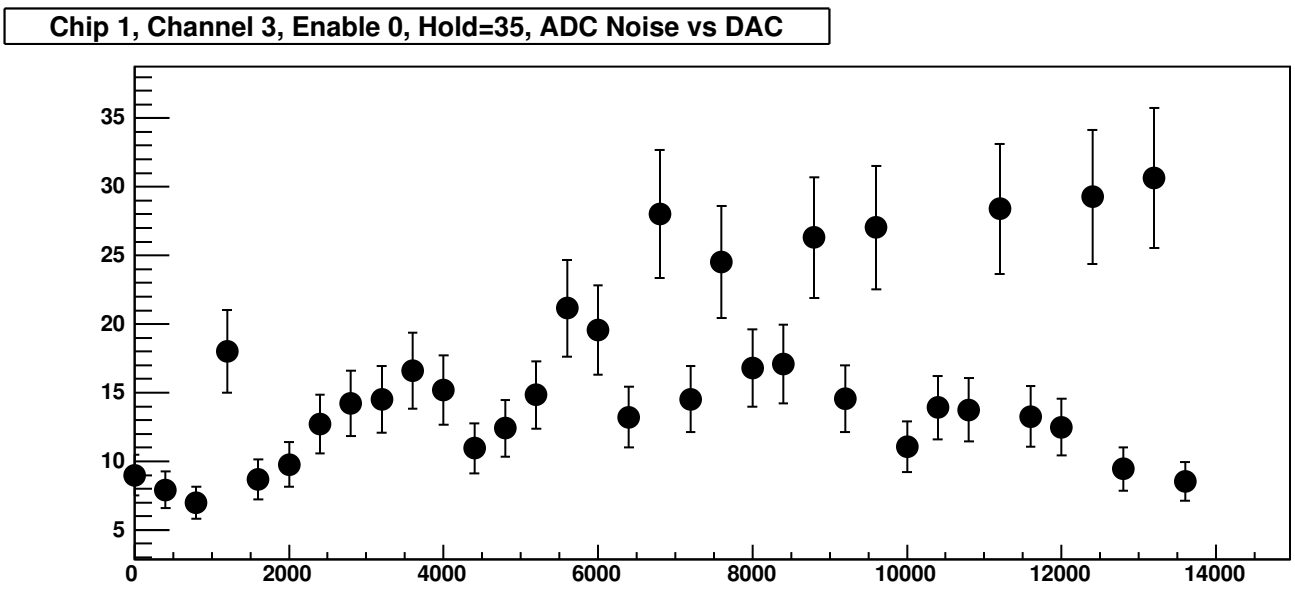


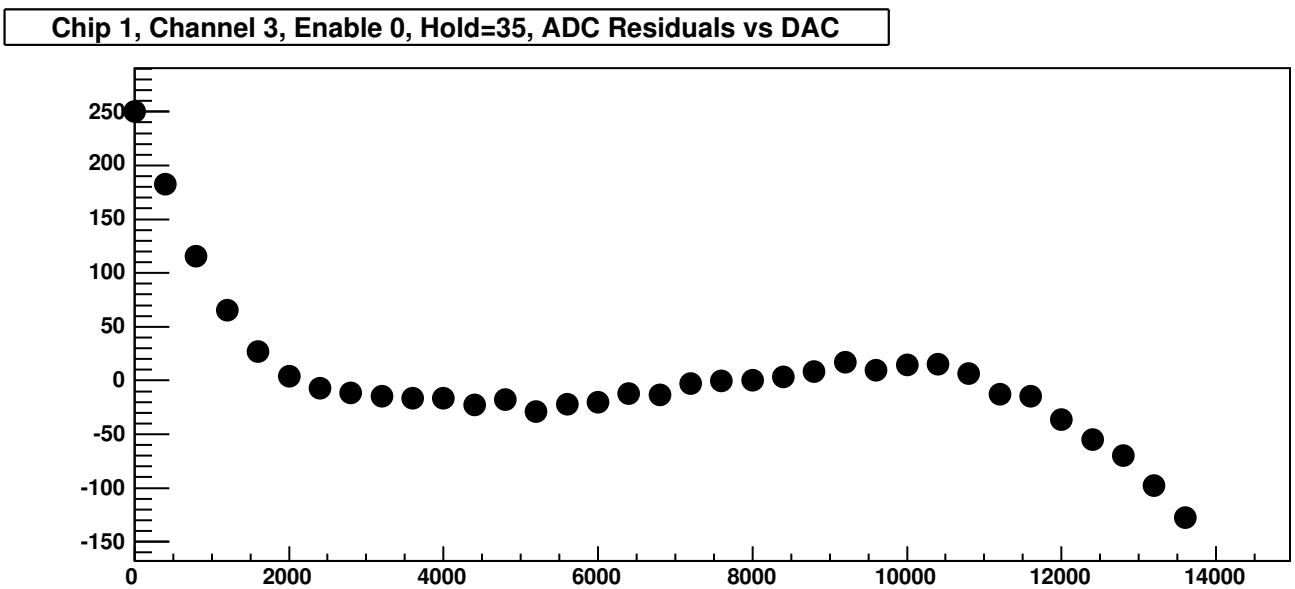

Chip 1, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.174e+05 / 23
p0 -960.5 ± 0.8524
p1 0.1413 ± 0.0001852
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

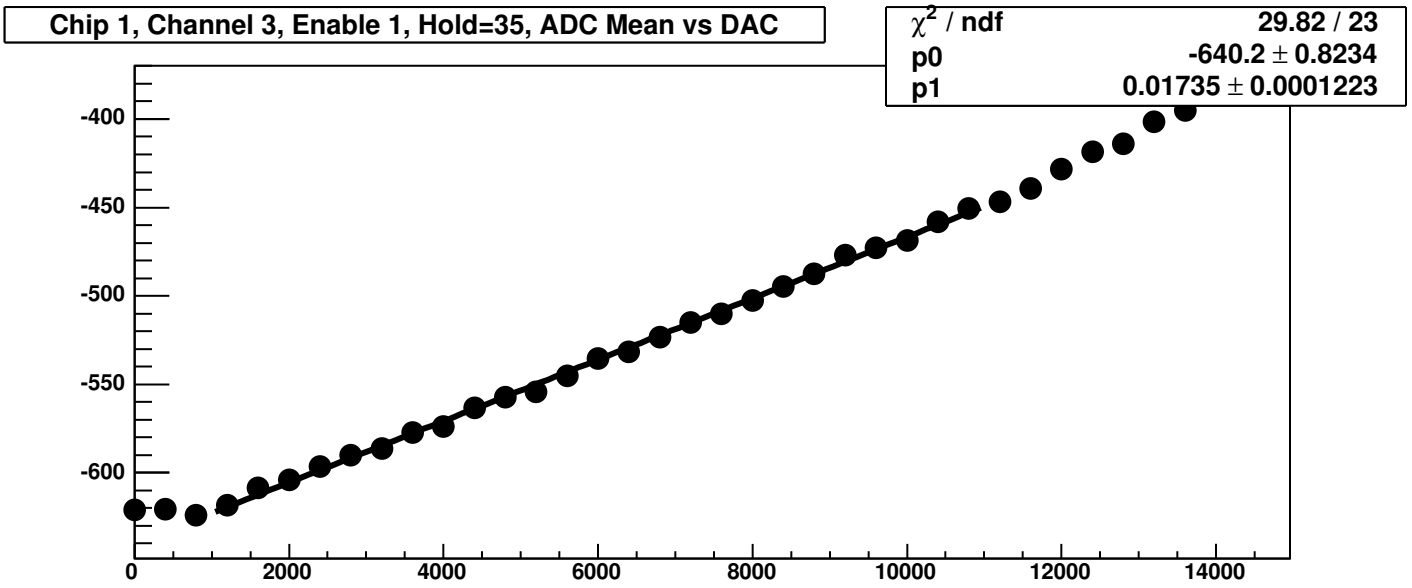

Chip 1, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

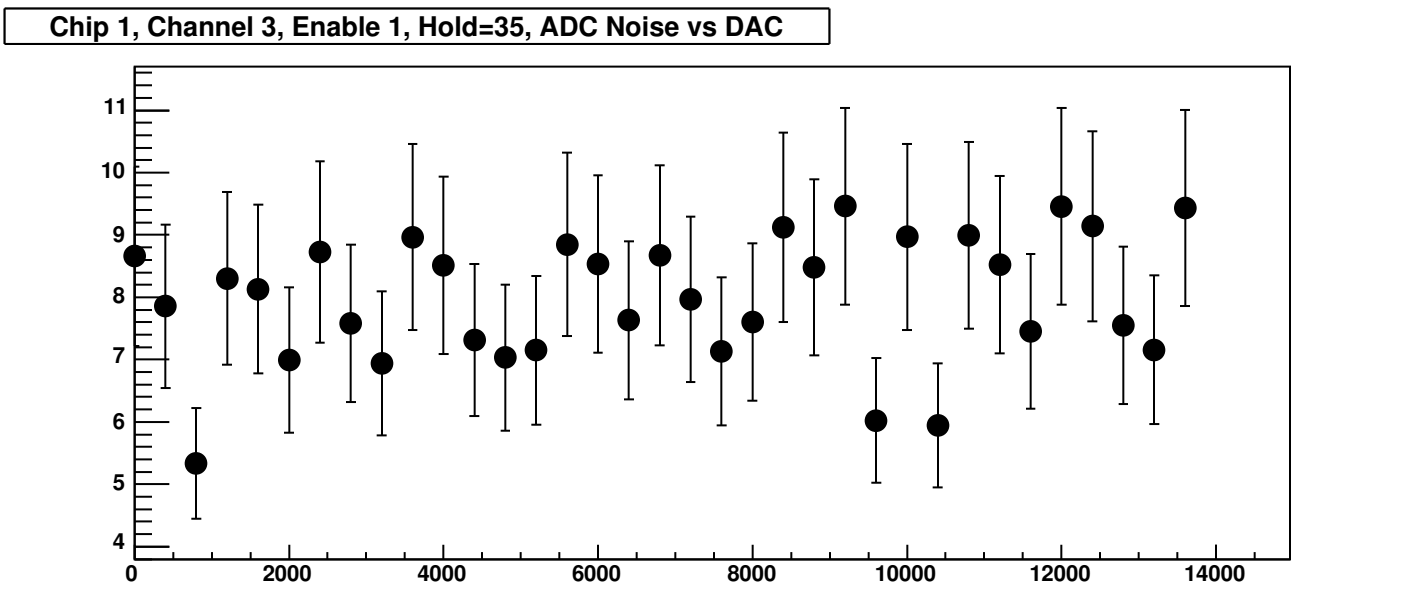

Chip 1, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

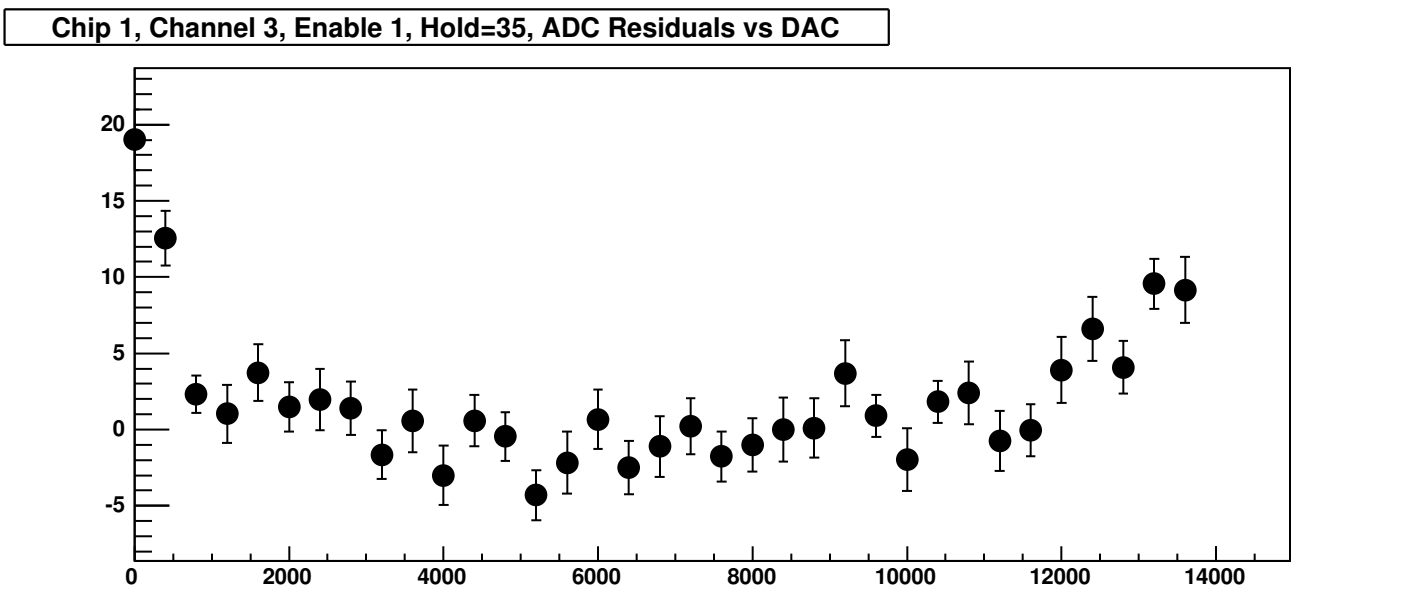

Chip 1, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 857.4 / 23
p0 -866 ± 1.318
p1 0.1765 ± 0.000213
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

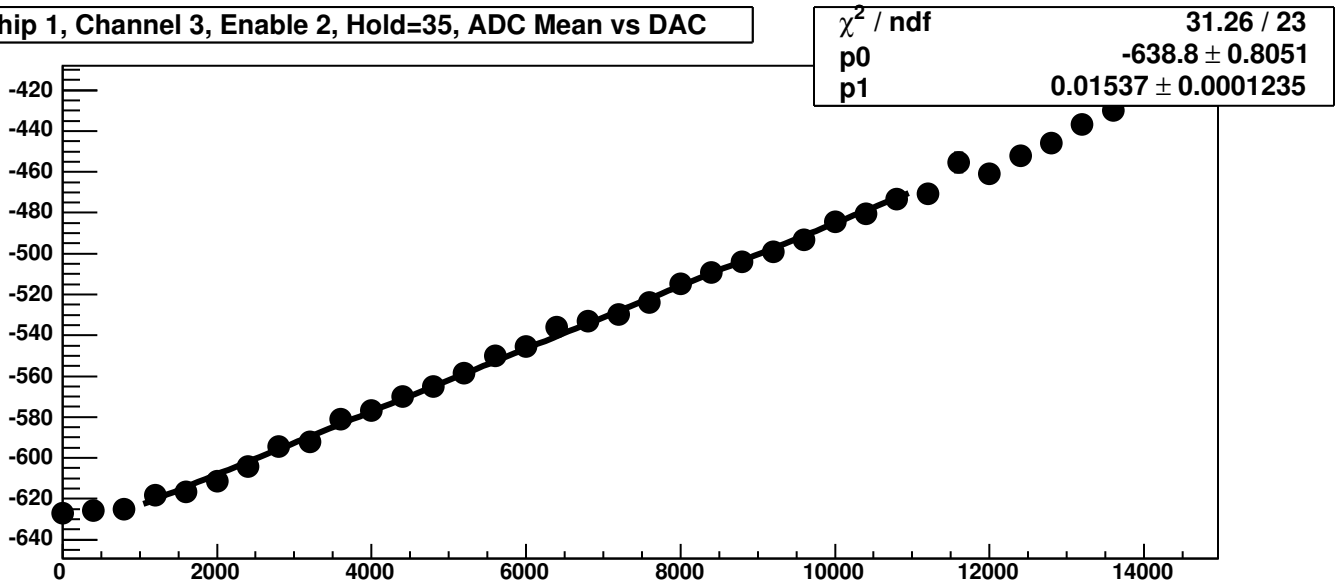

Chip 1, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 29.82 / 23
p0 -640.2 ± 0.8234
p1 0.01735 ± 0.0001223
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

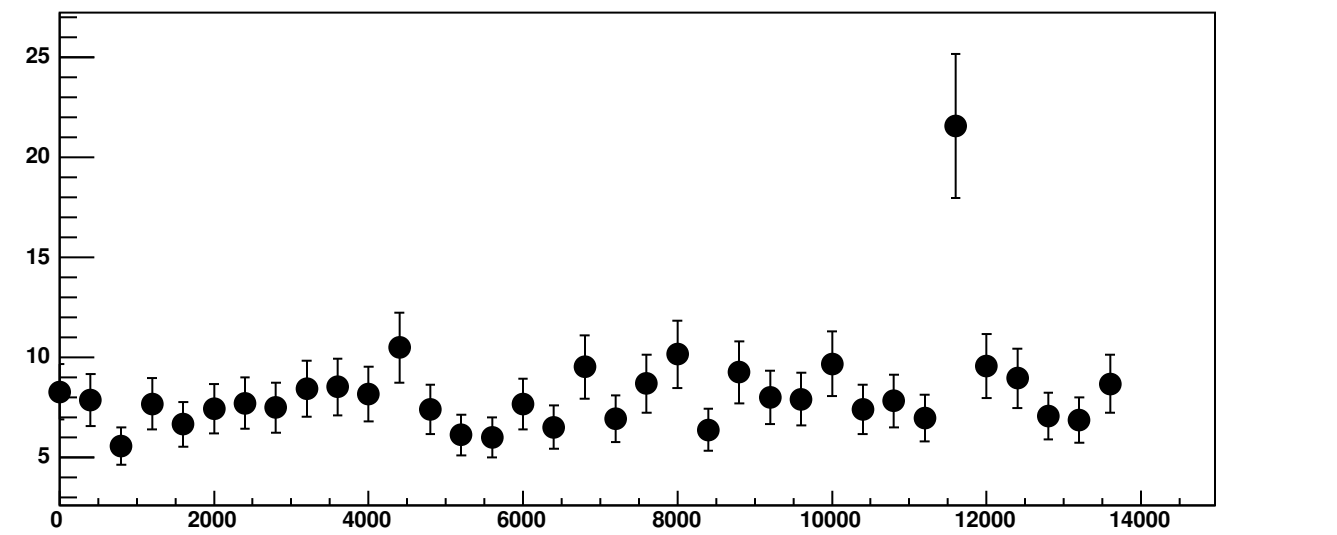

Chip 1, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

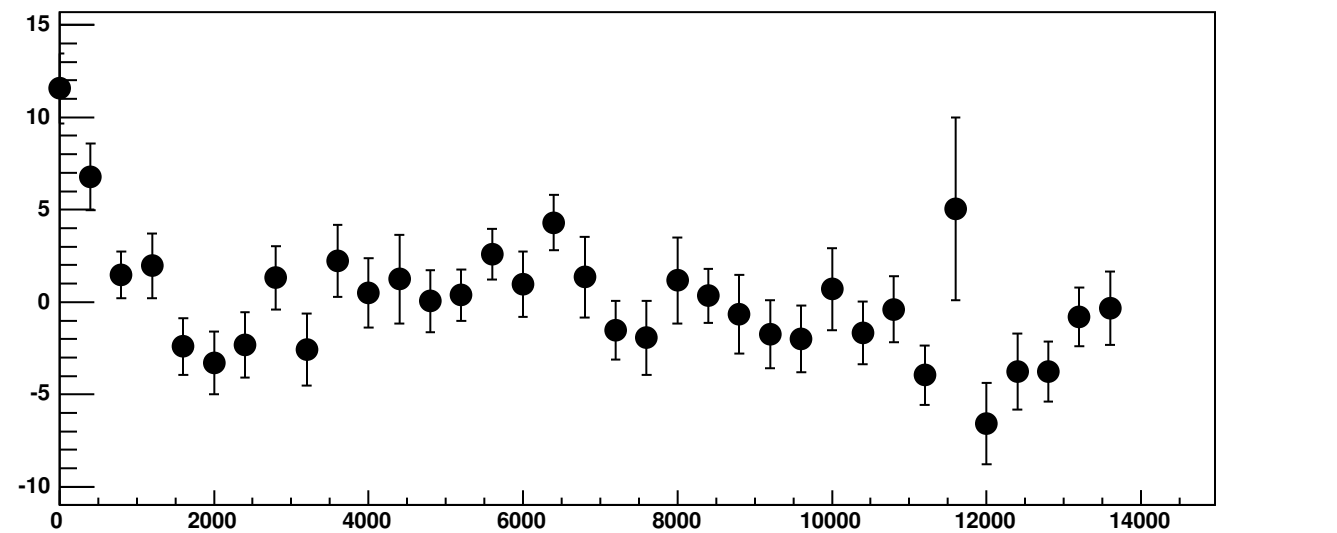
Chip 1, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

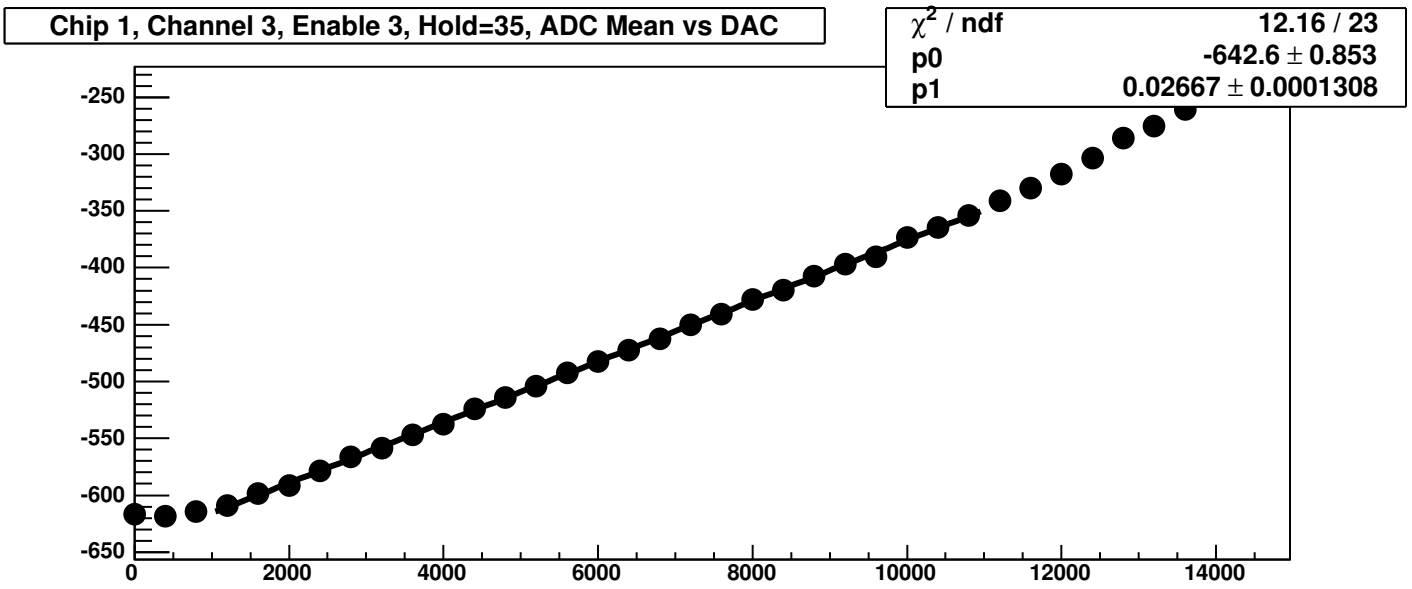


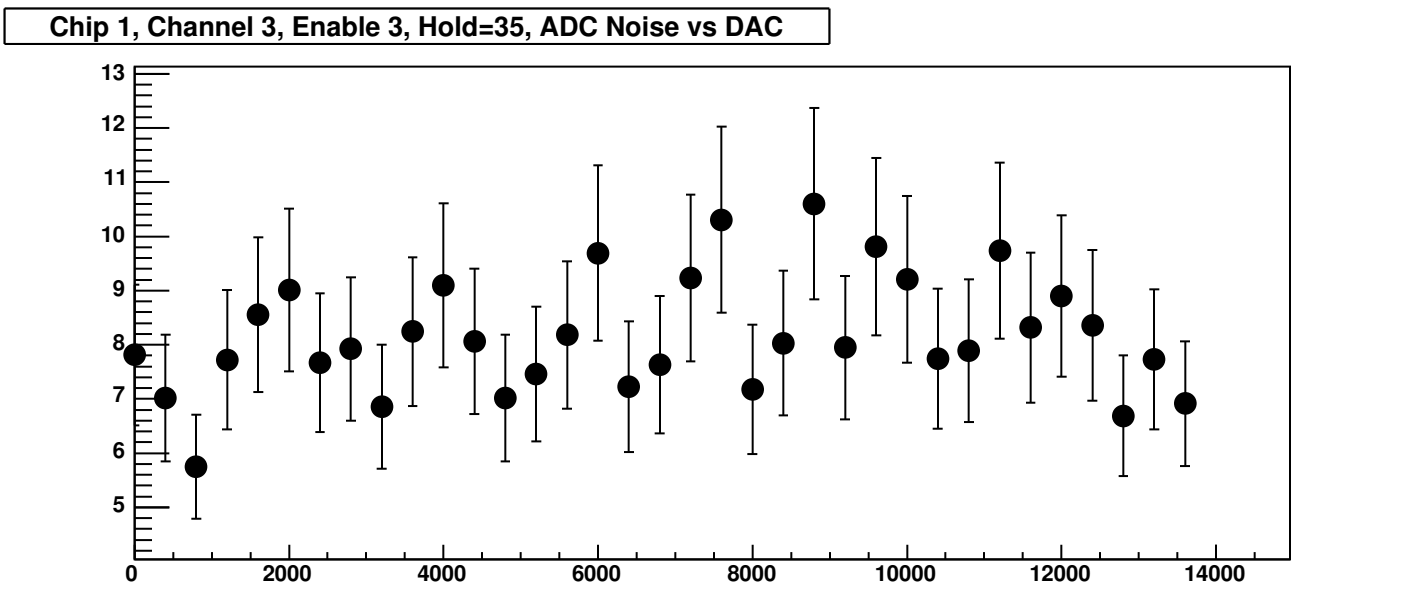
Chip 1, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC

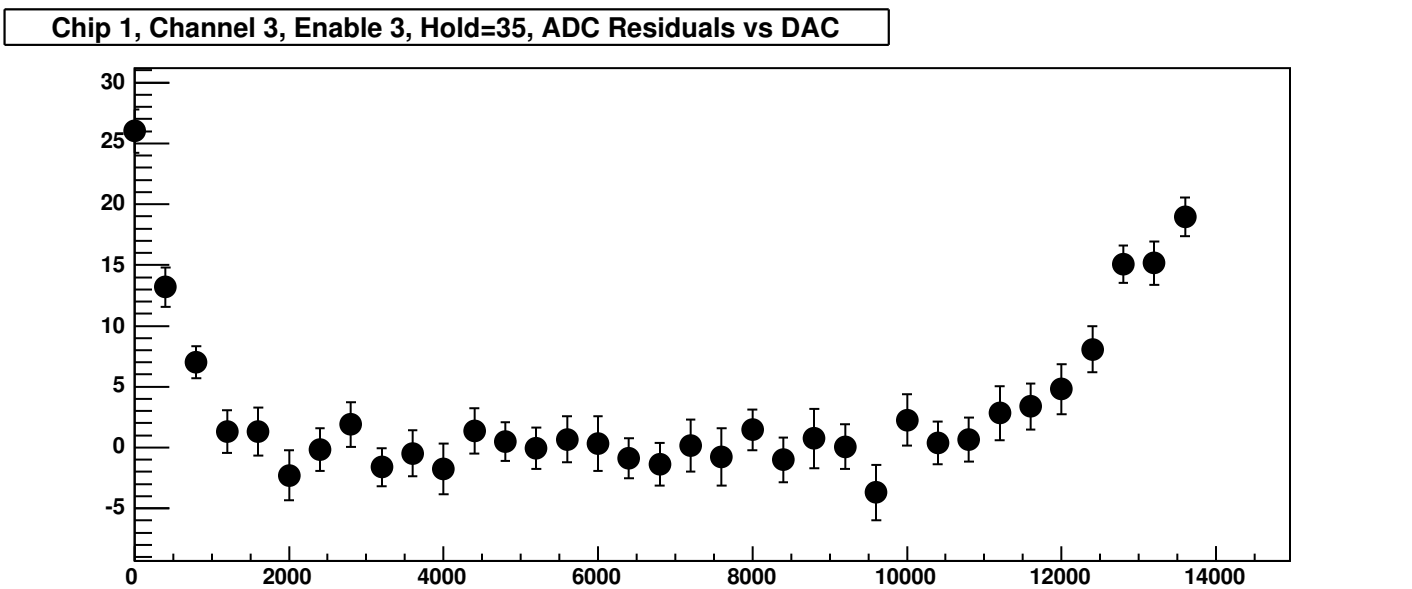


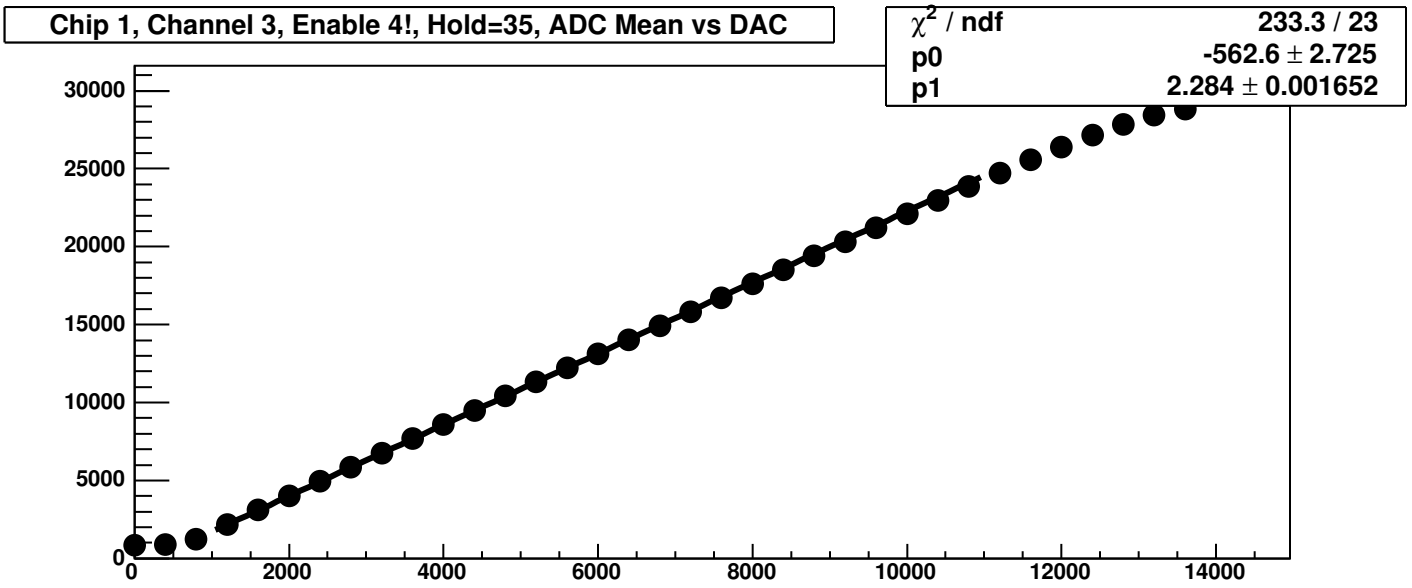
Chip 1, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

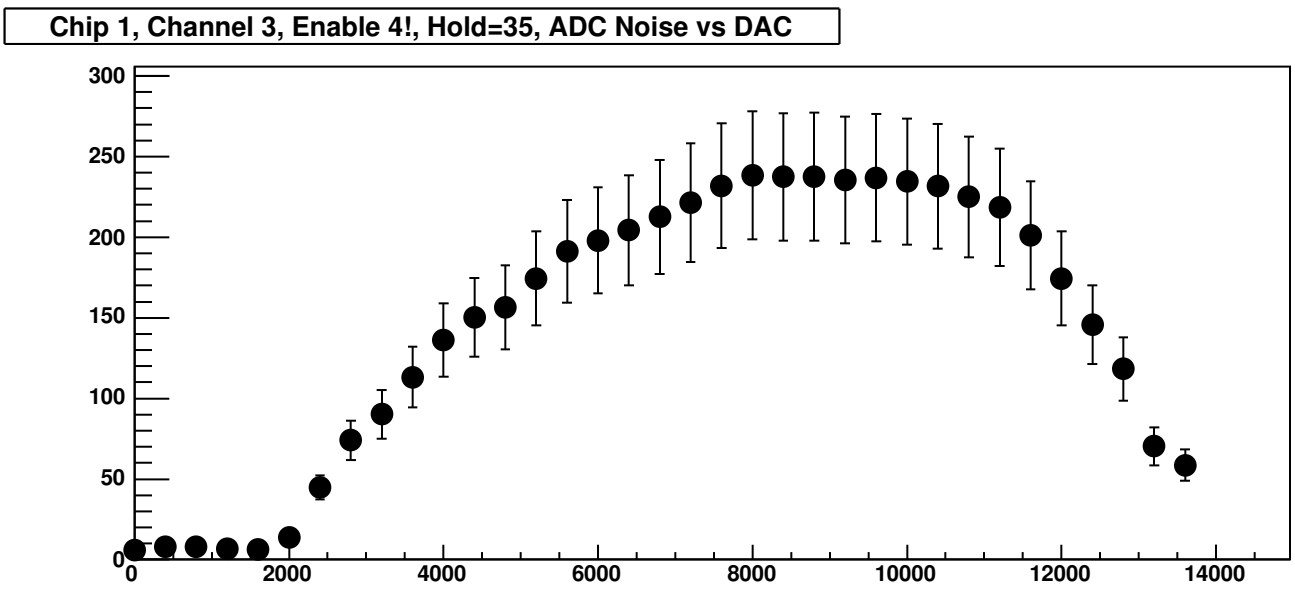


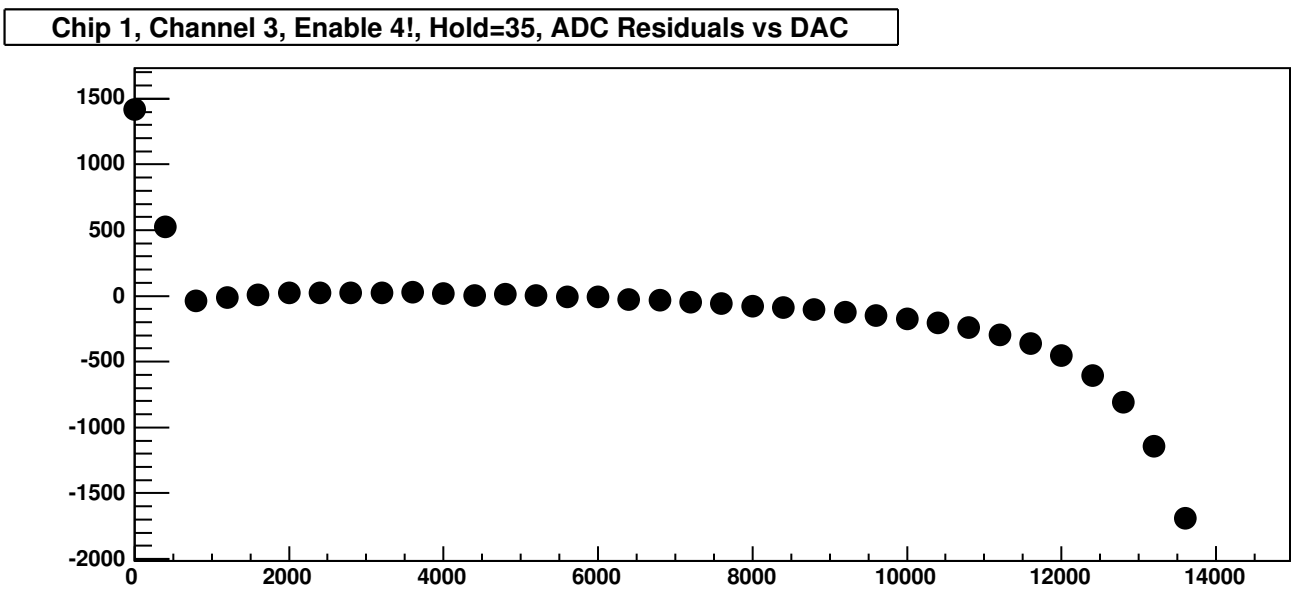

Chip 1, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 12.16 / 23
p0 -642.6 ± 0.853
p1 0.02667 ± 0.0001308
-250
-300
-350
-400
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

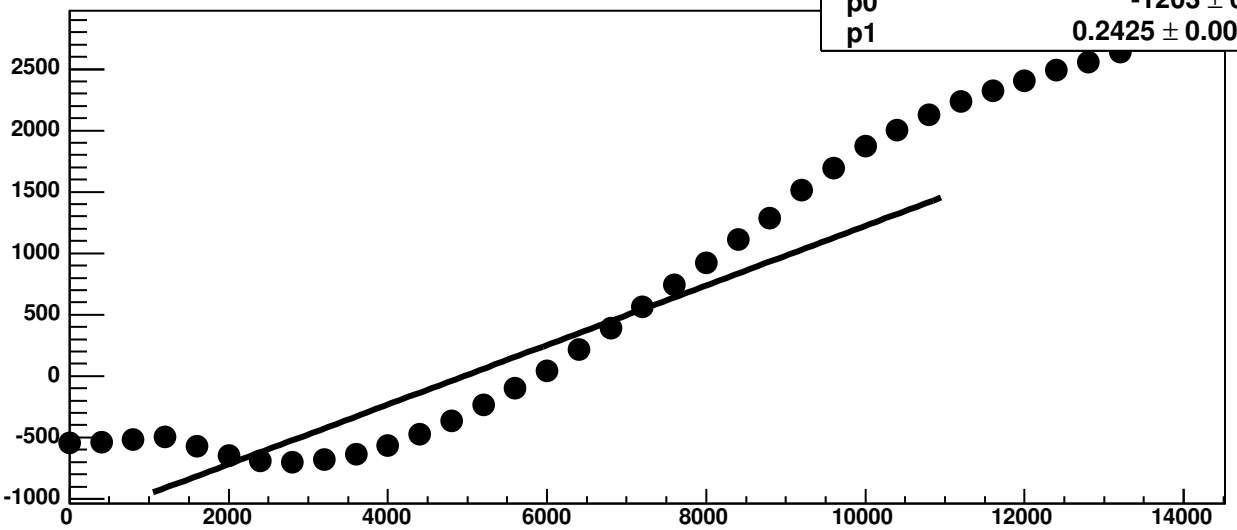

Chip 1, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 233.3 / 23
p0 -562.6 ± 2.725
p1 2.284 ± 0.001652
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

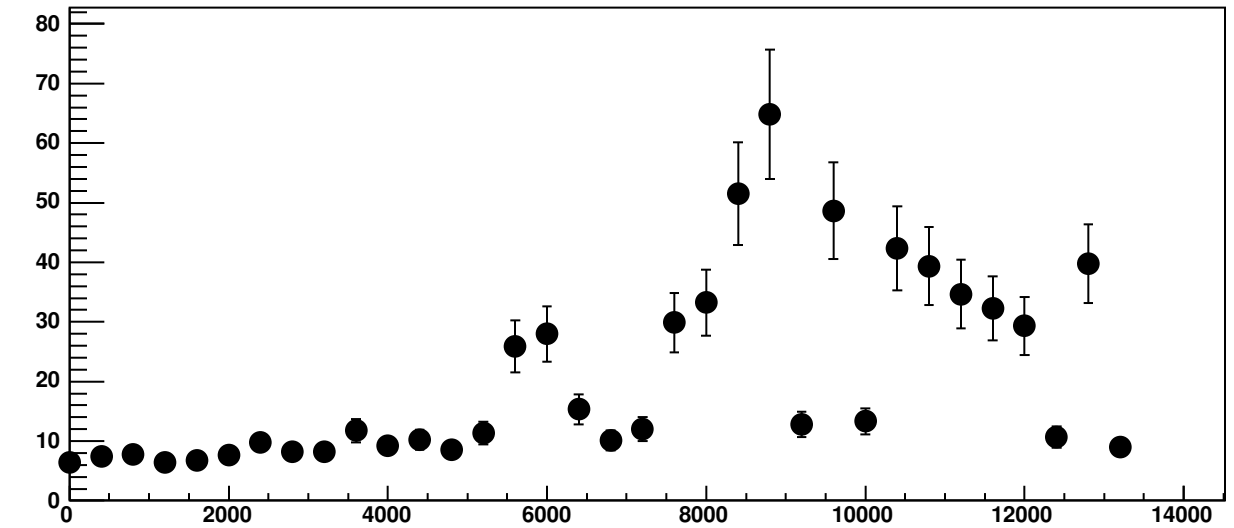

Chip 1, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

Chip 1, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

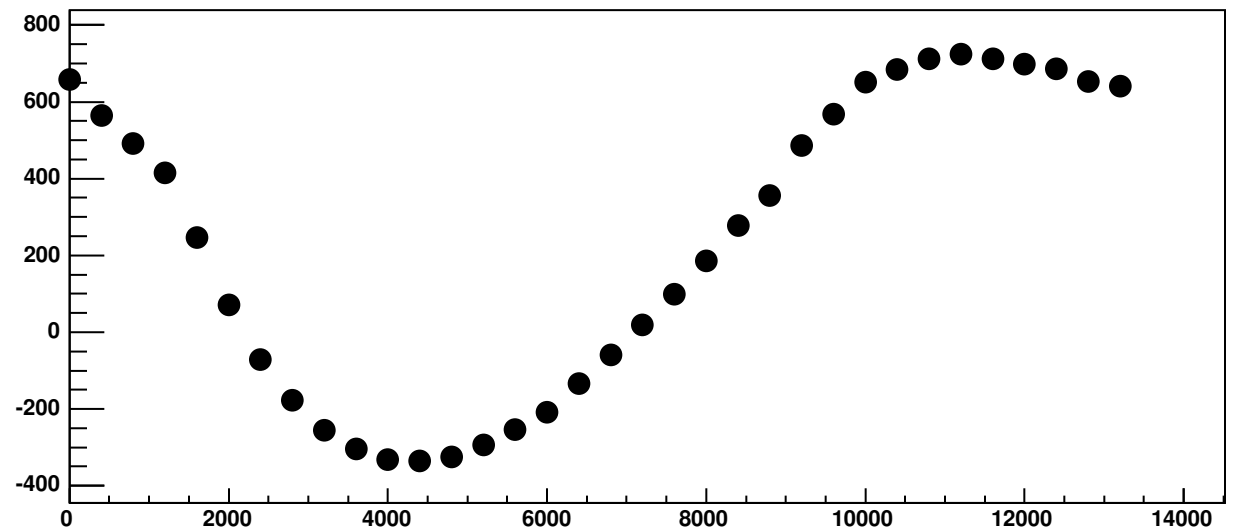
χ^2 / ndf	3.254e+05 / 23
p0	-1203 ± 0.9874
p1	0.2425 ± 0.0002173

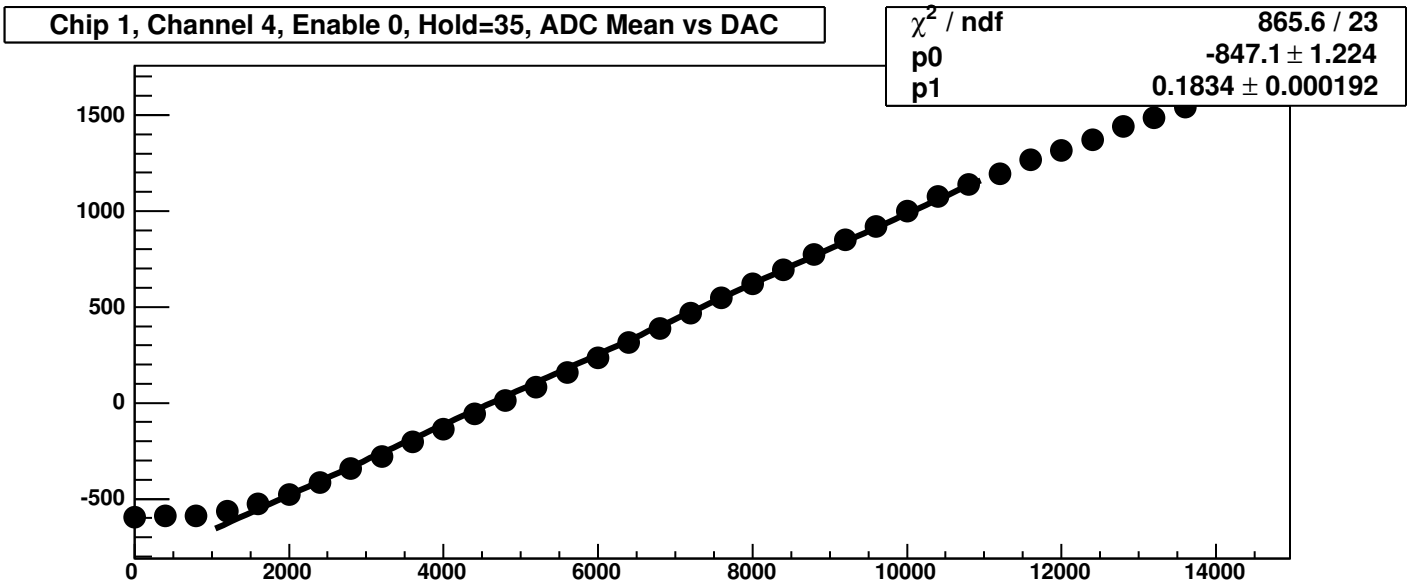


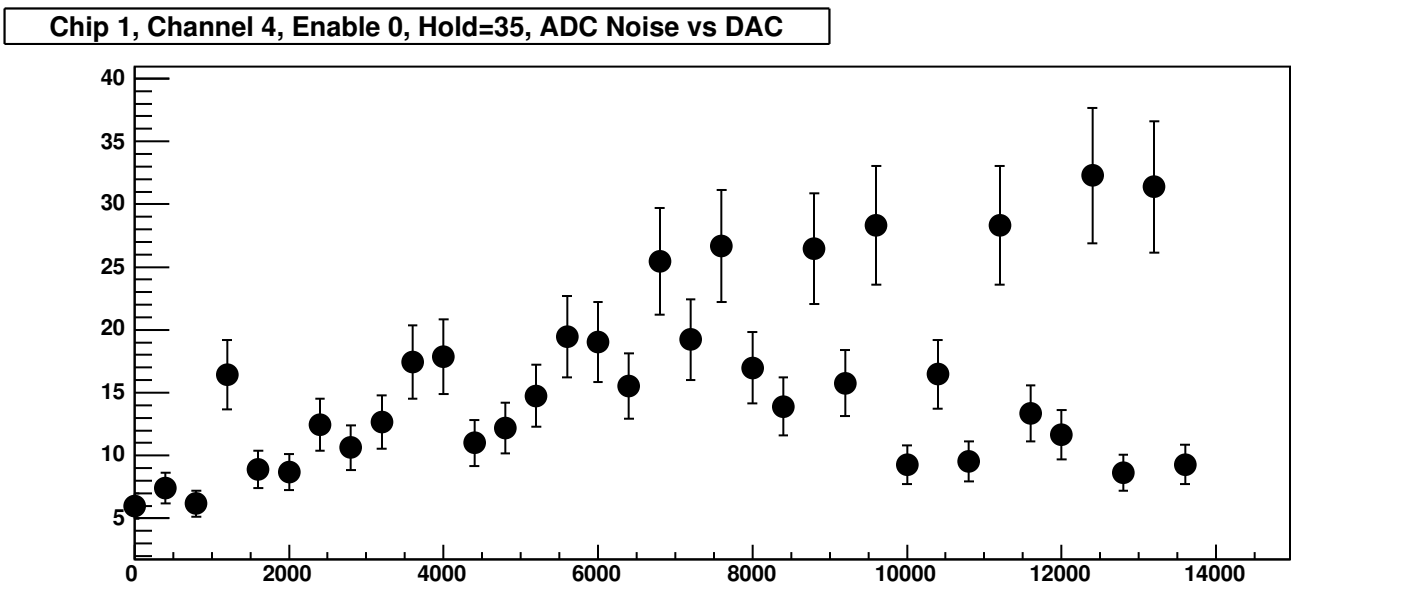
Chip 1, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC

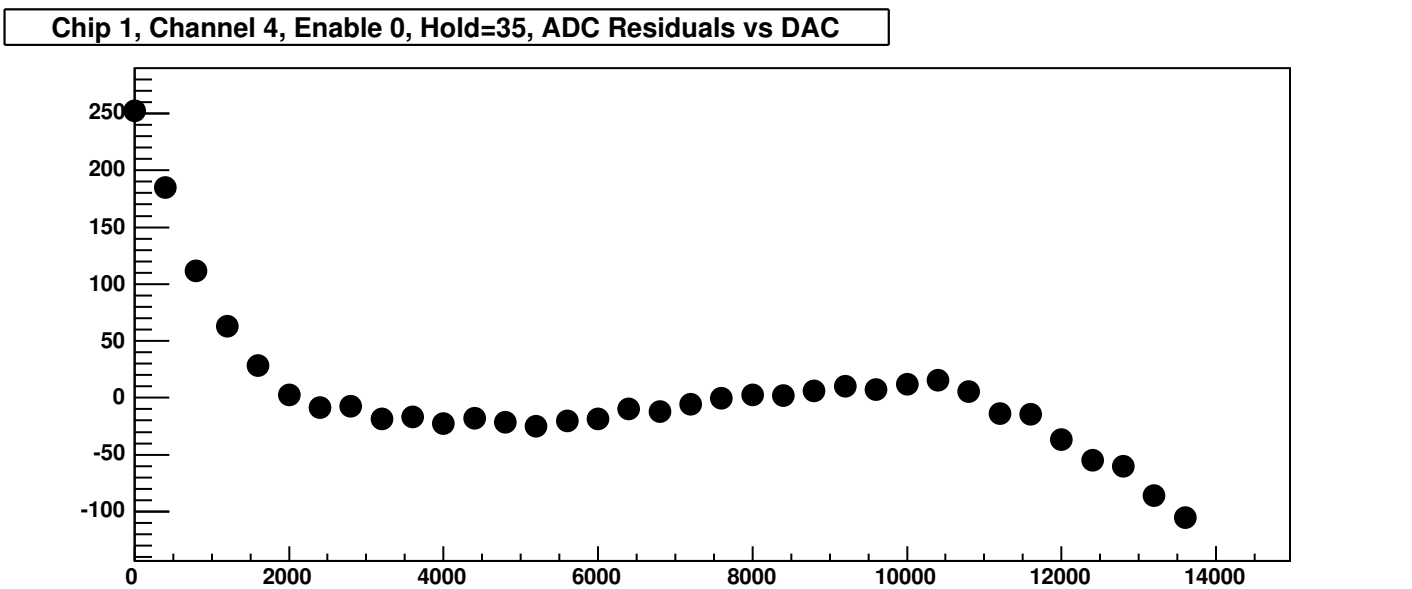


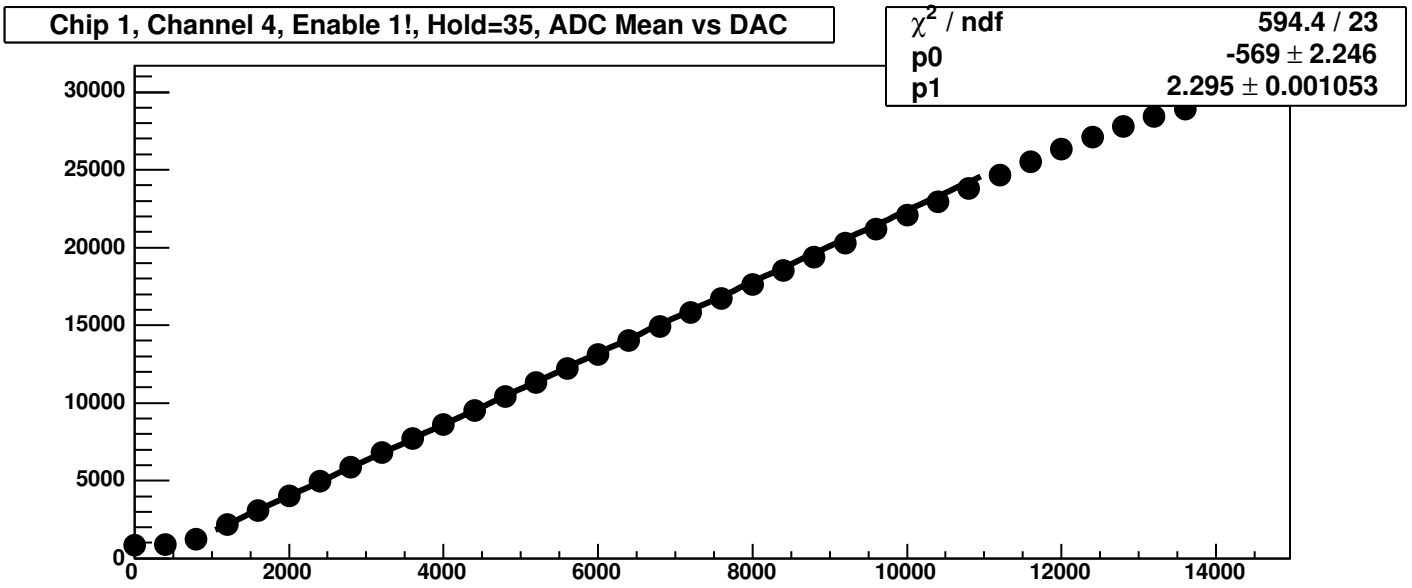
Chip 1, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

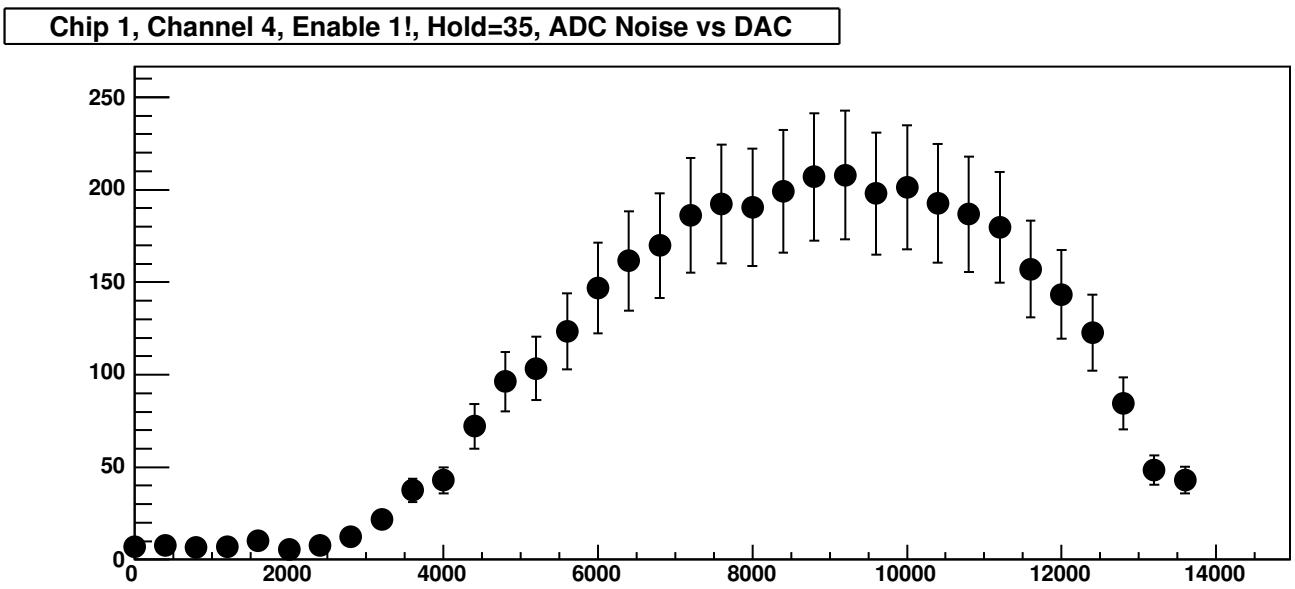


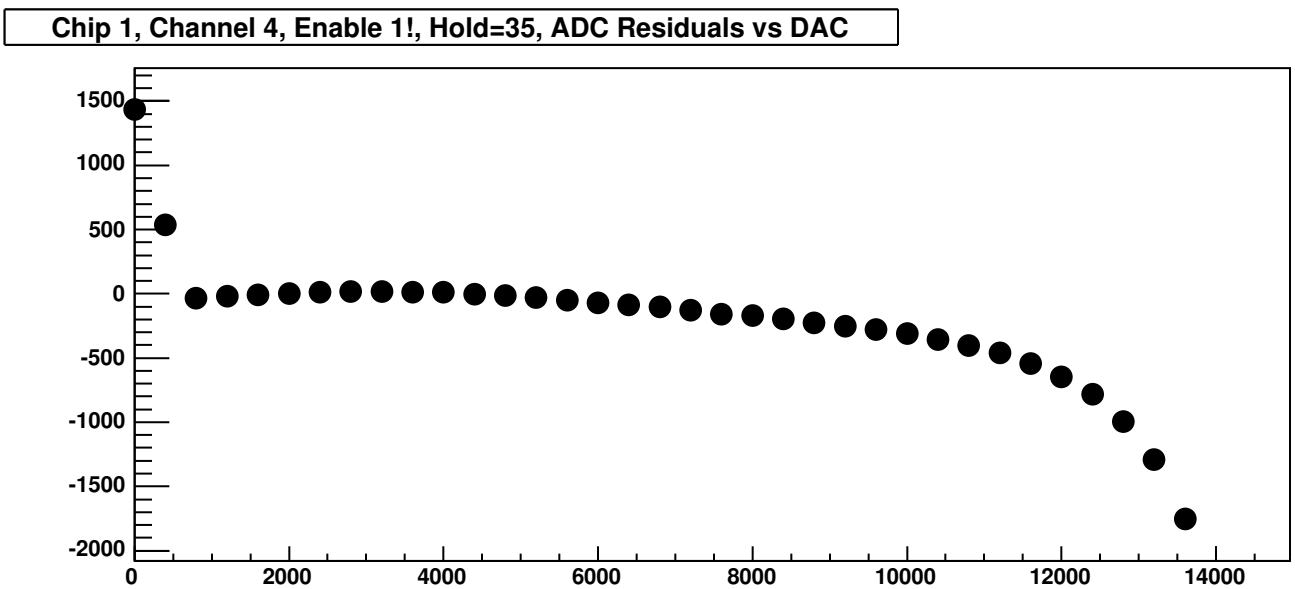

Chip 1, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 865.6 / 23
p0 -847.1 ± 1.224
p1 0.1834 ± 0.000192
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

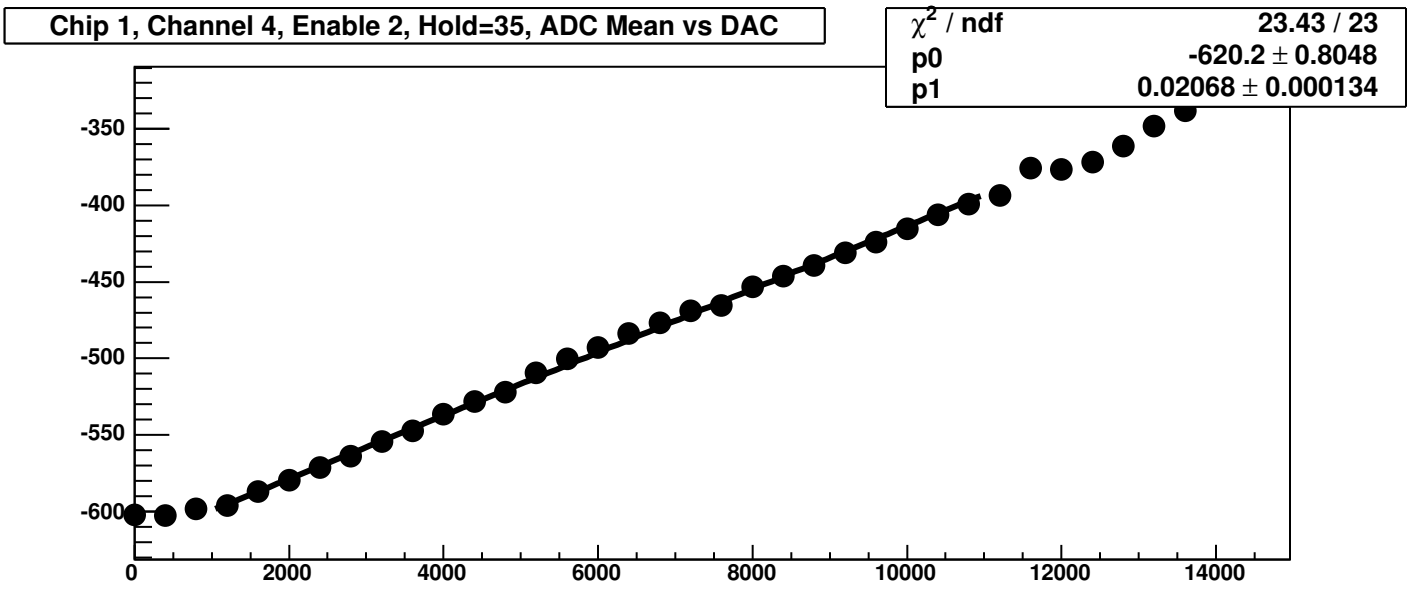

Chip 1, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

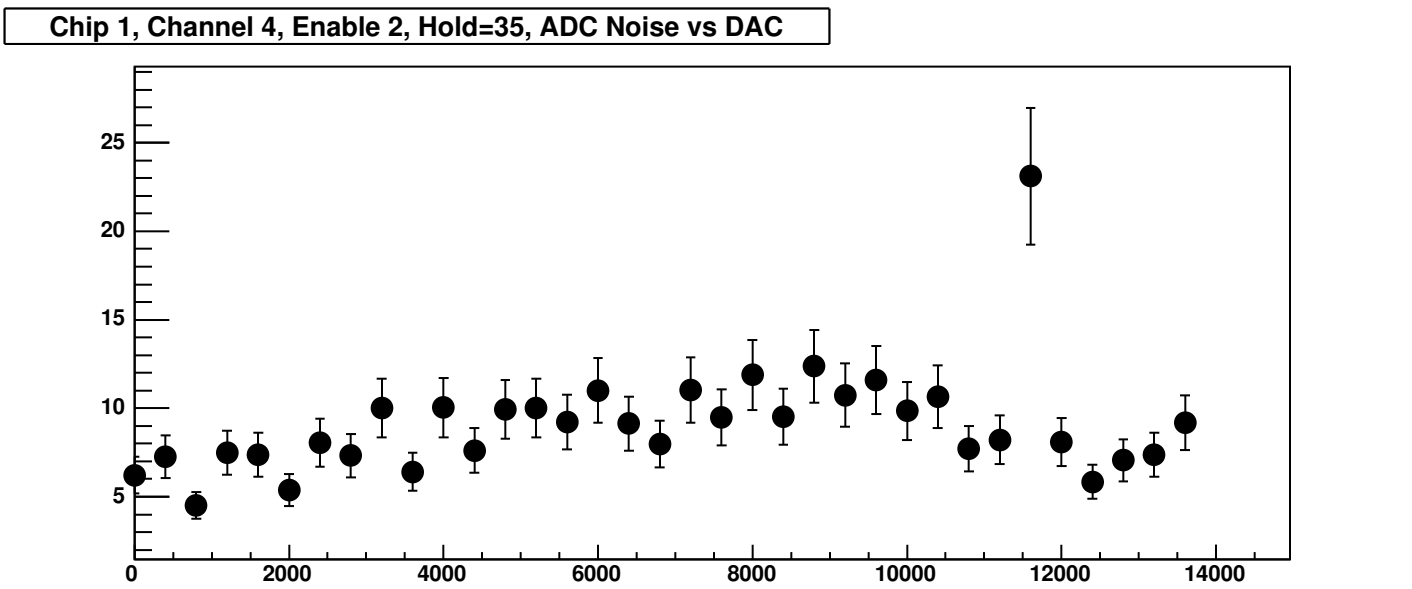

Chip 1, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

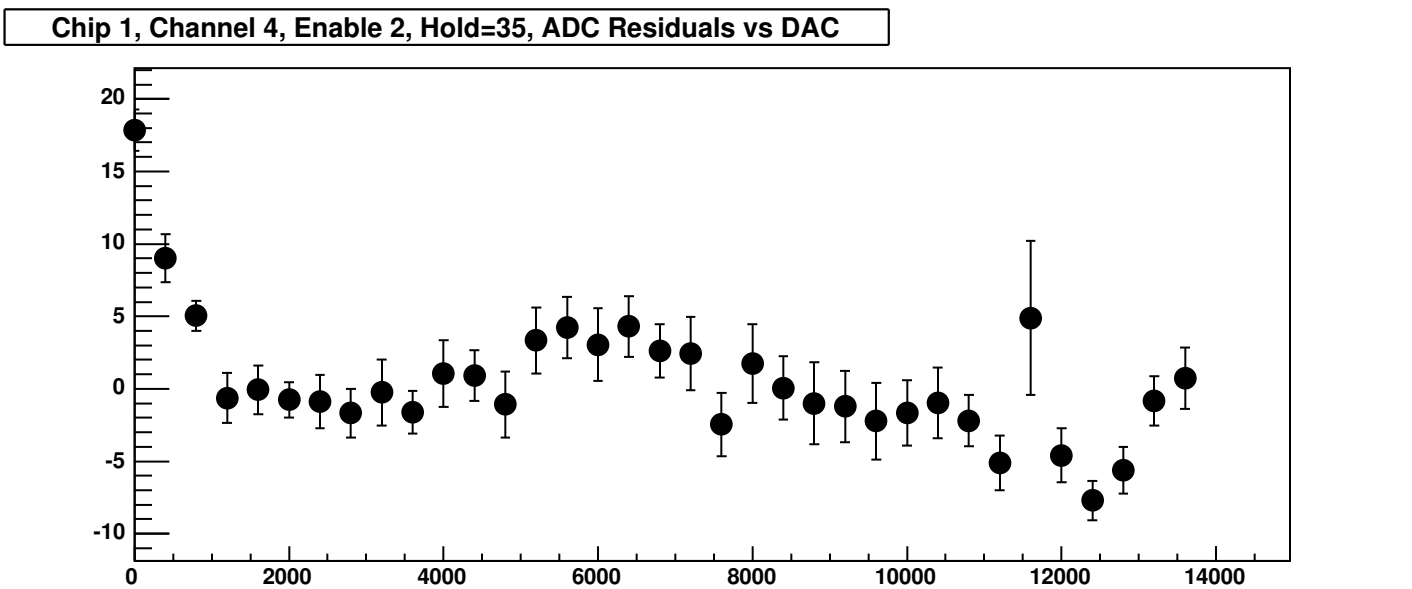

Chip 1, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 594.4 / 23
p0 -569 ± 2.246
p1 2.295 ± 0.001053
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

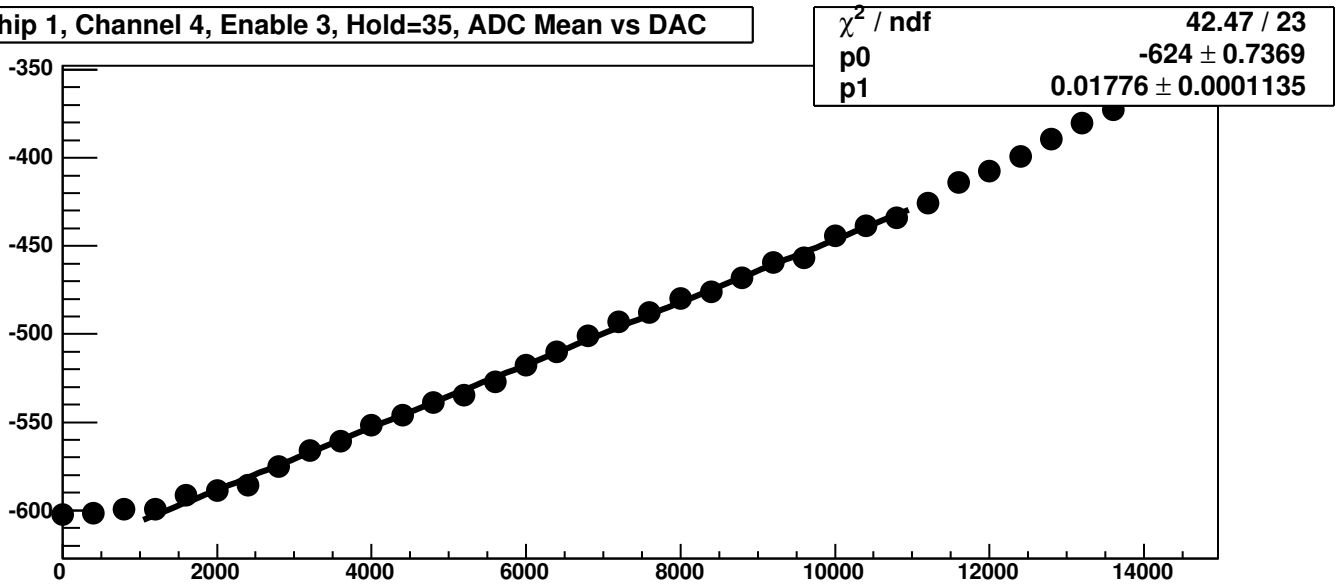

Chip 1, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 23.43 / 23
p0 -620.2 ± 0.8048
p1 0.02068 ± 0.000134
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

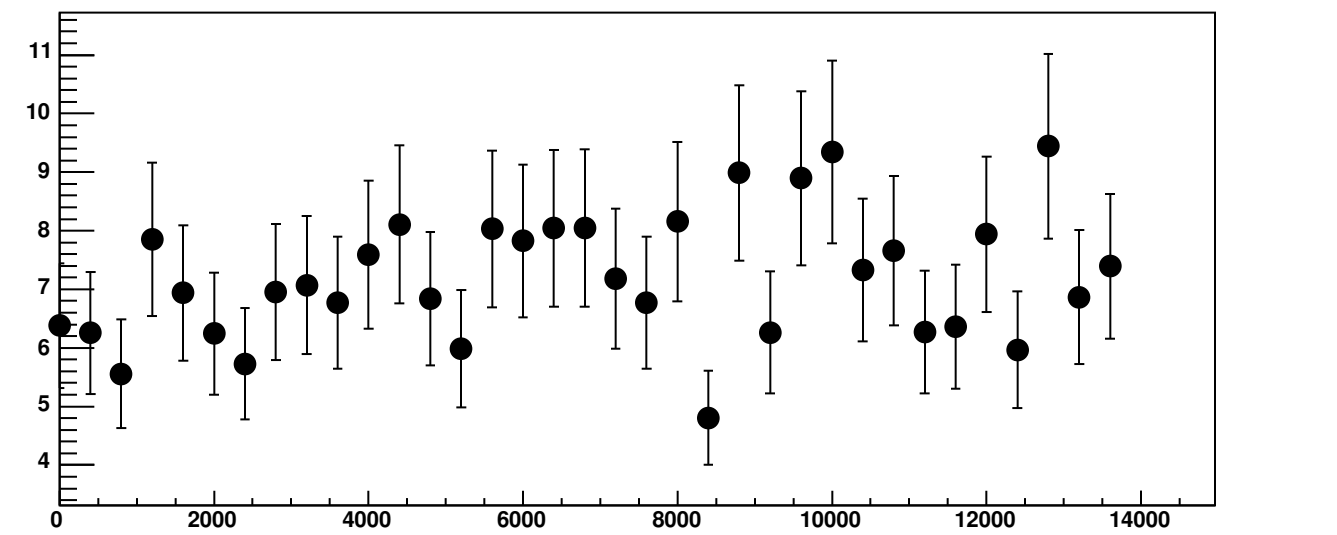

Chip 1, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

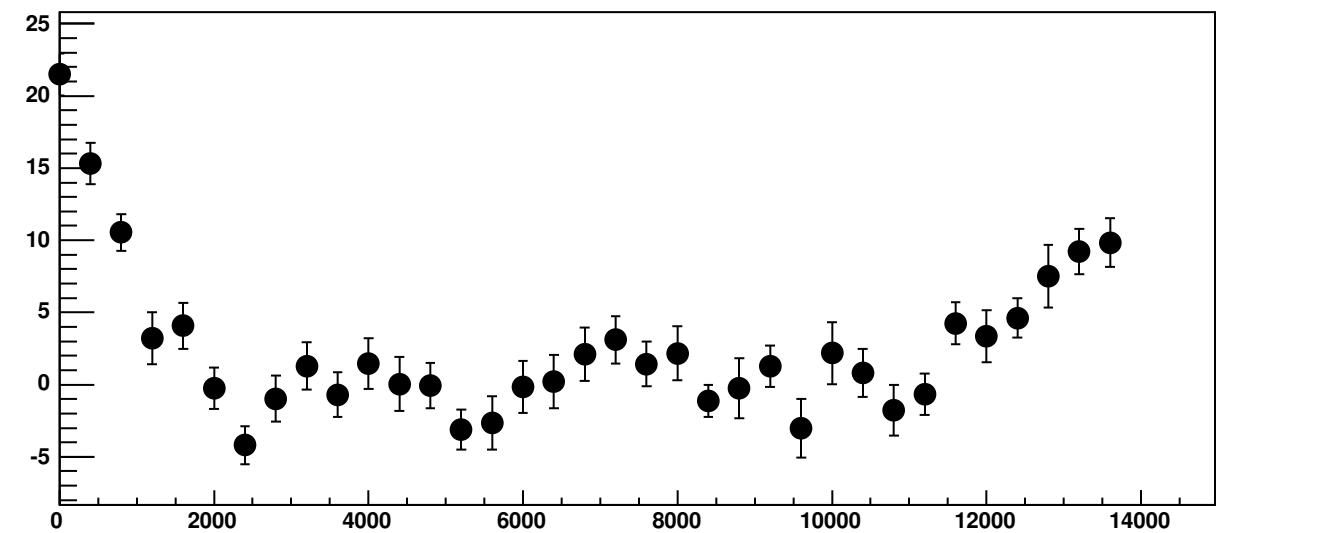
Chip 1, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



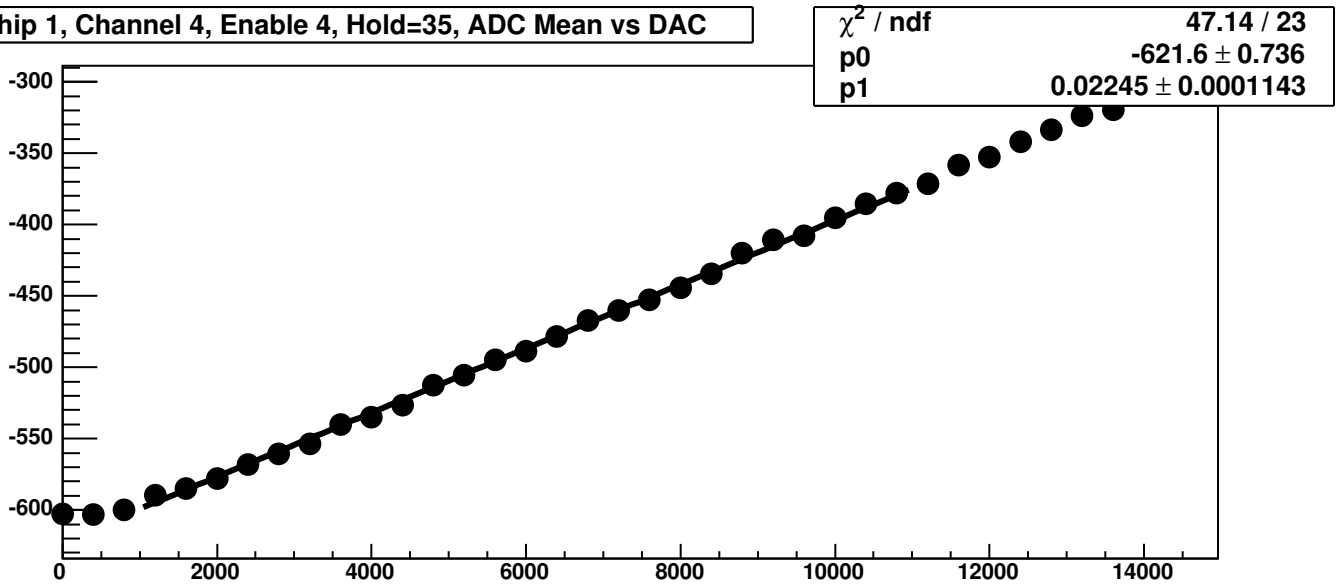
Chip 1, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



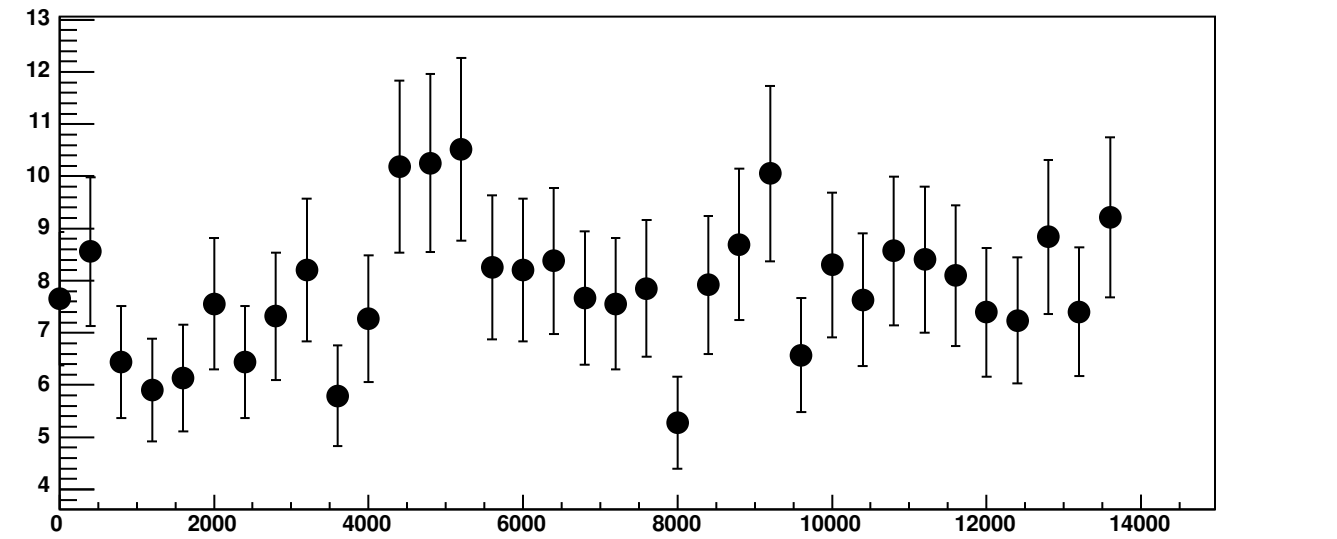
Chip 1, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



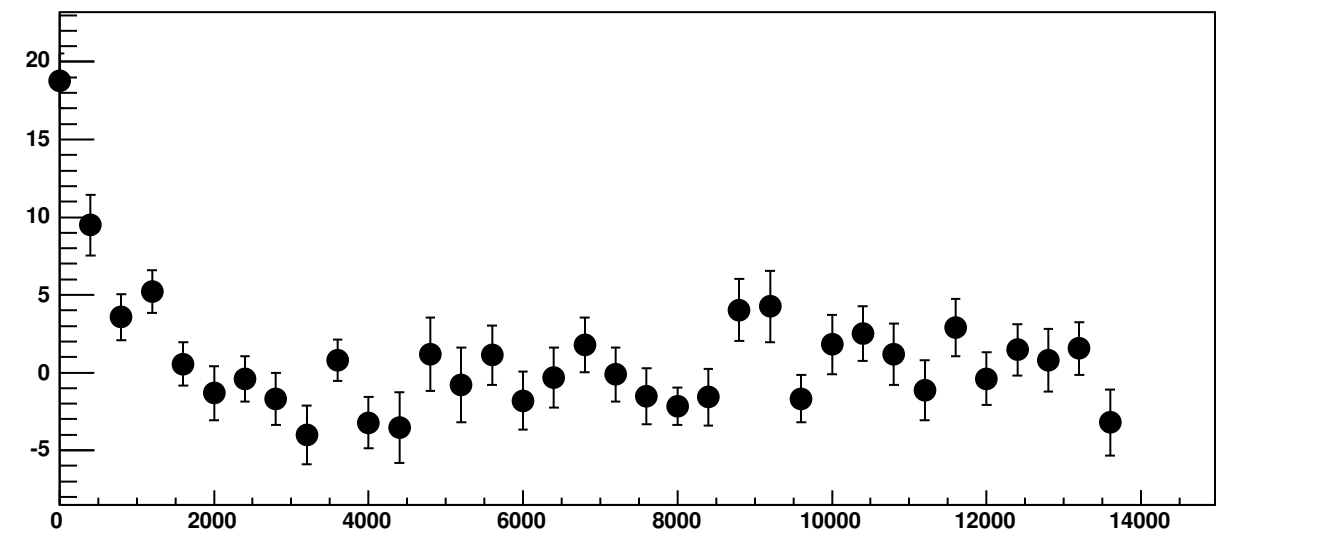
Chip 1, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC



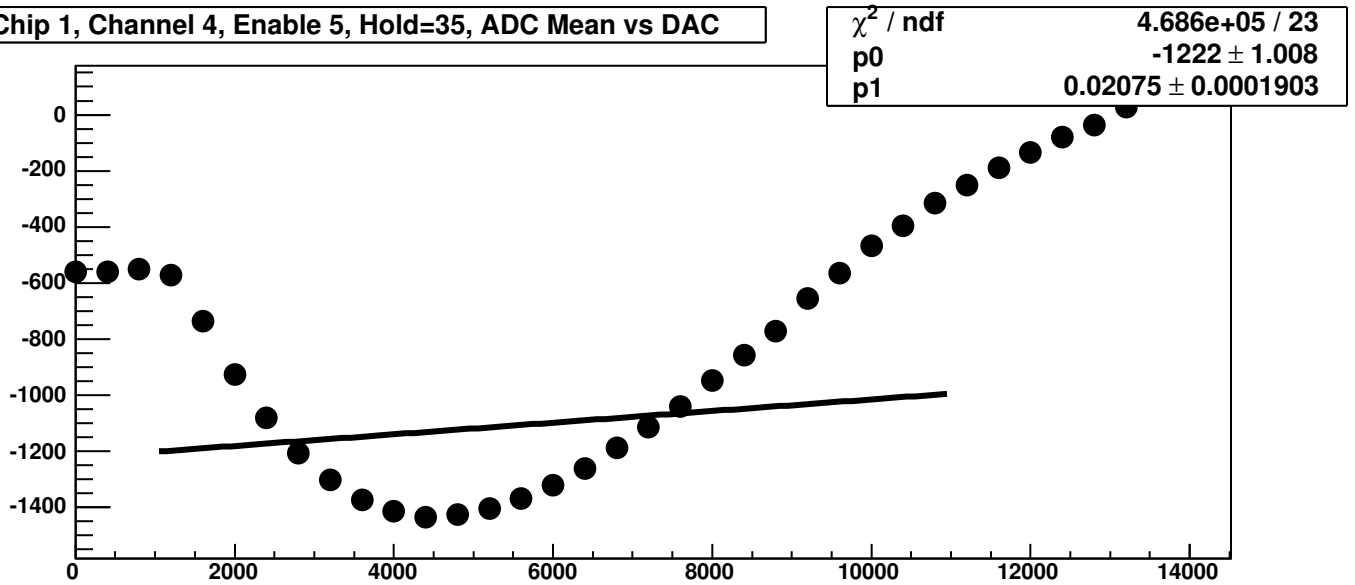
Chip 1, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



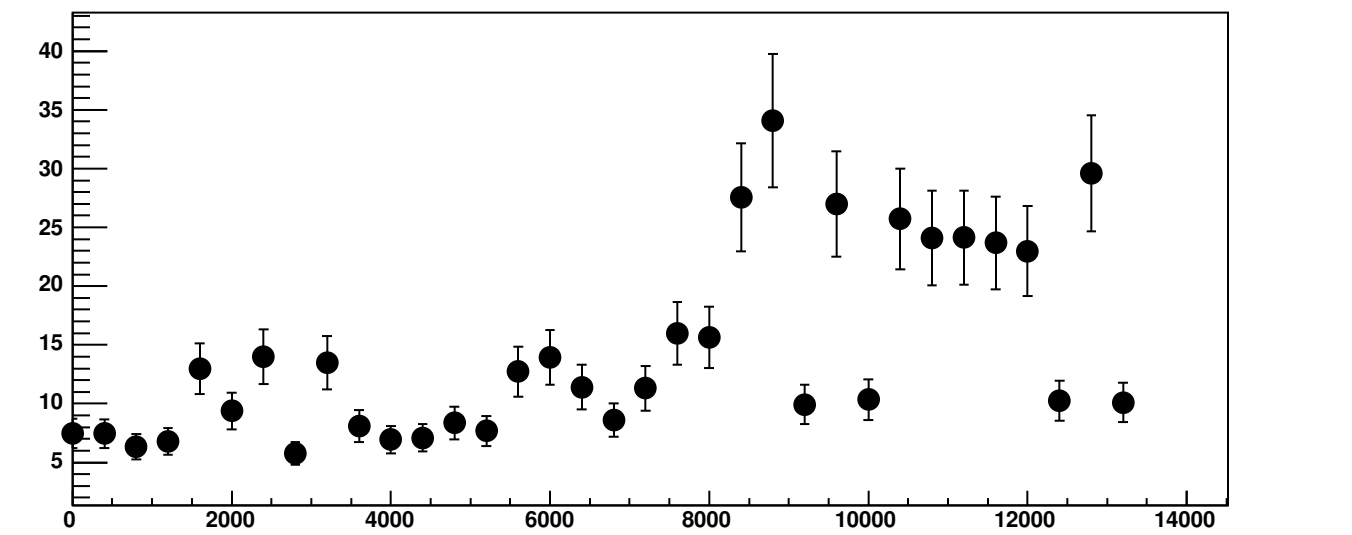
Chip 1, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC



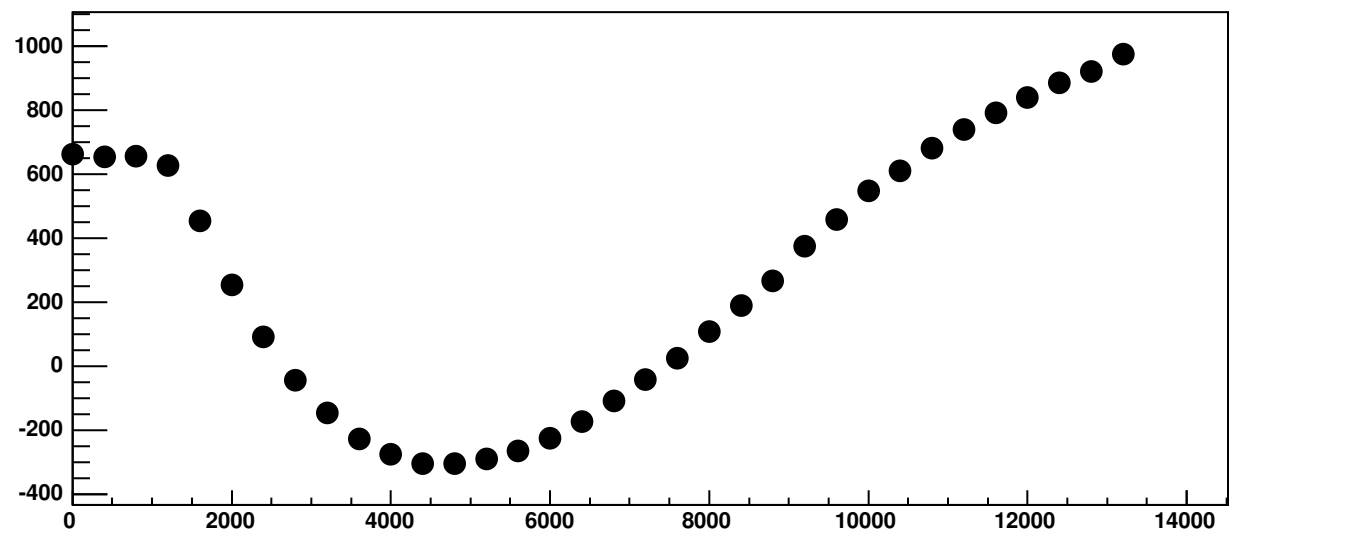
Chip 1, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC



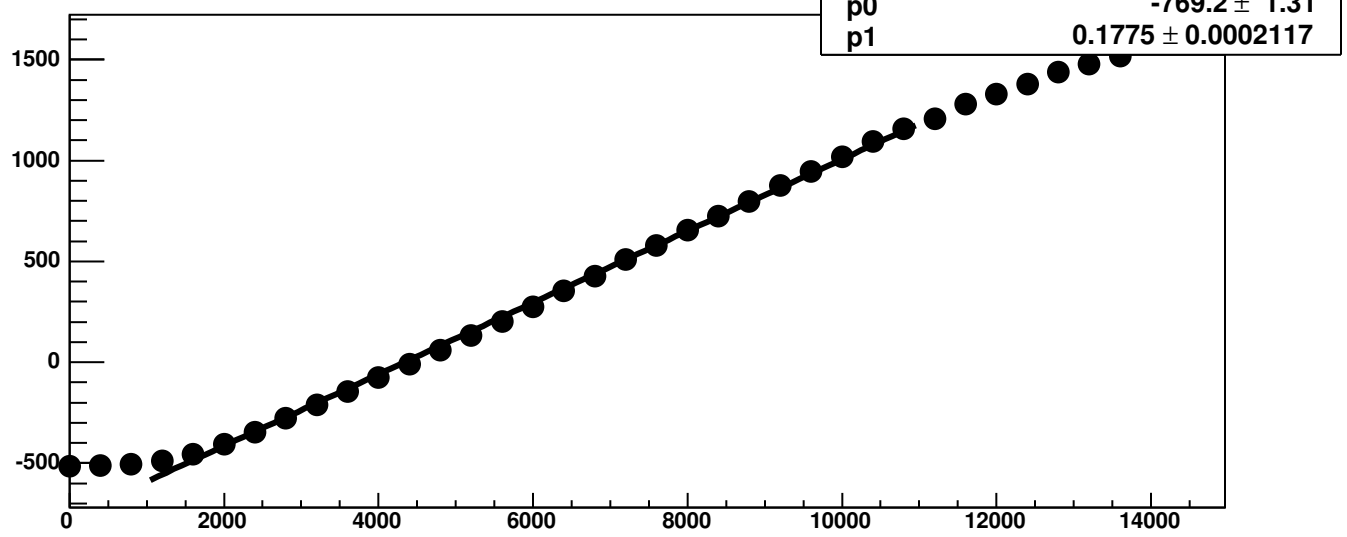
Chip 1, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC



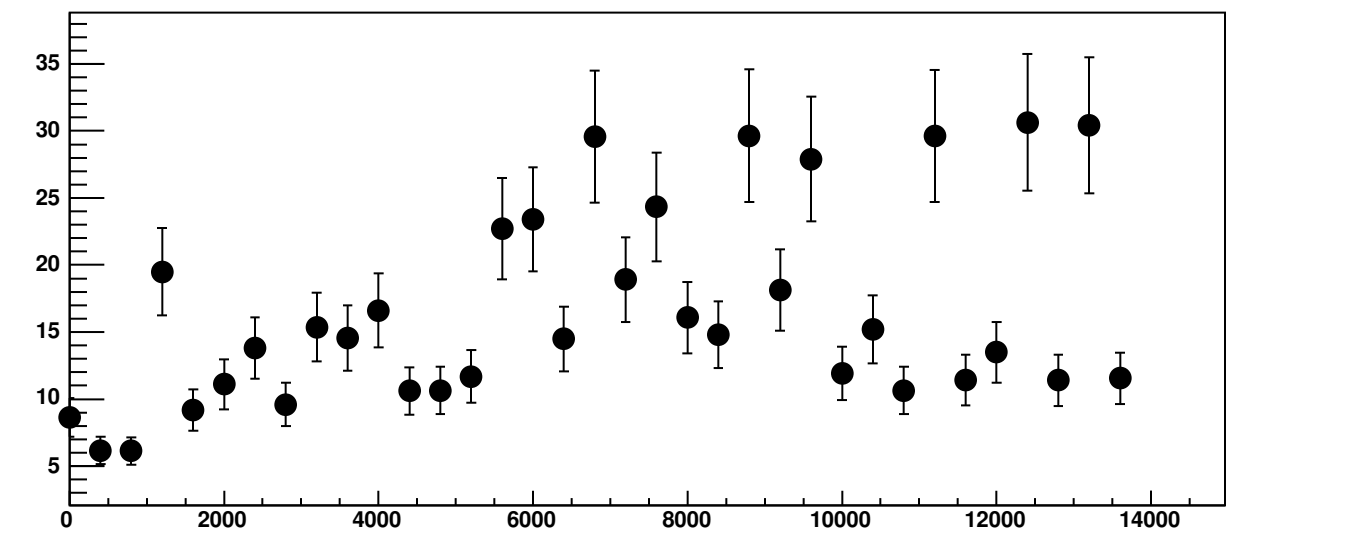
Chip 1, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC



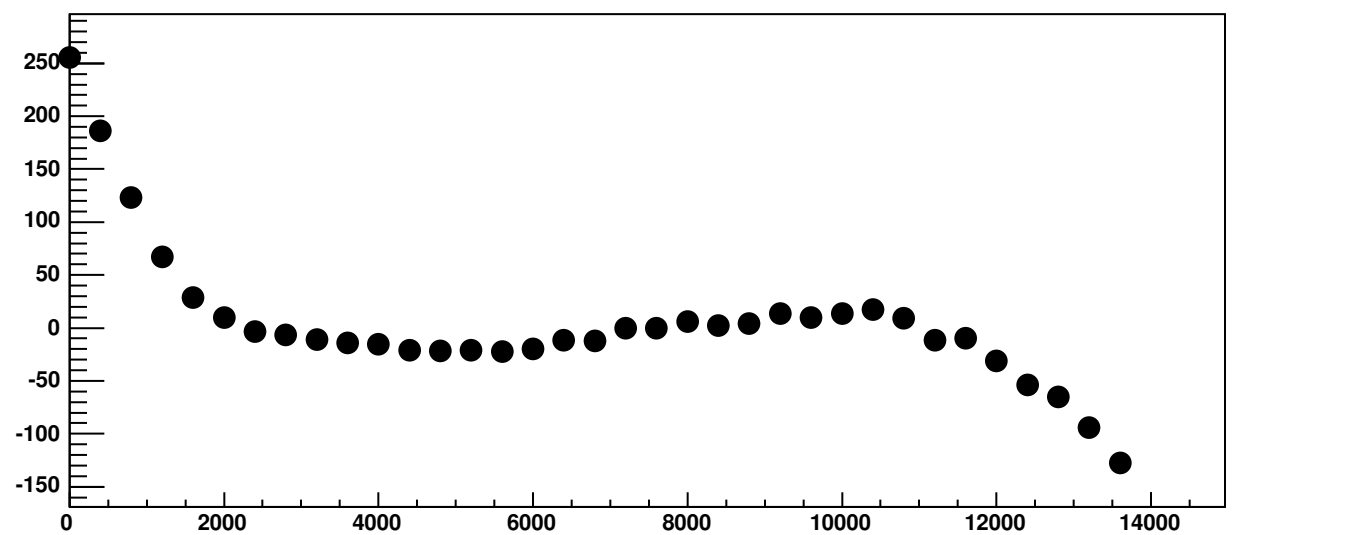
Chip 1, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

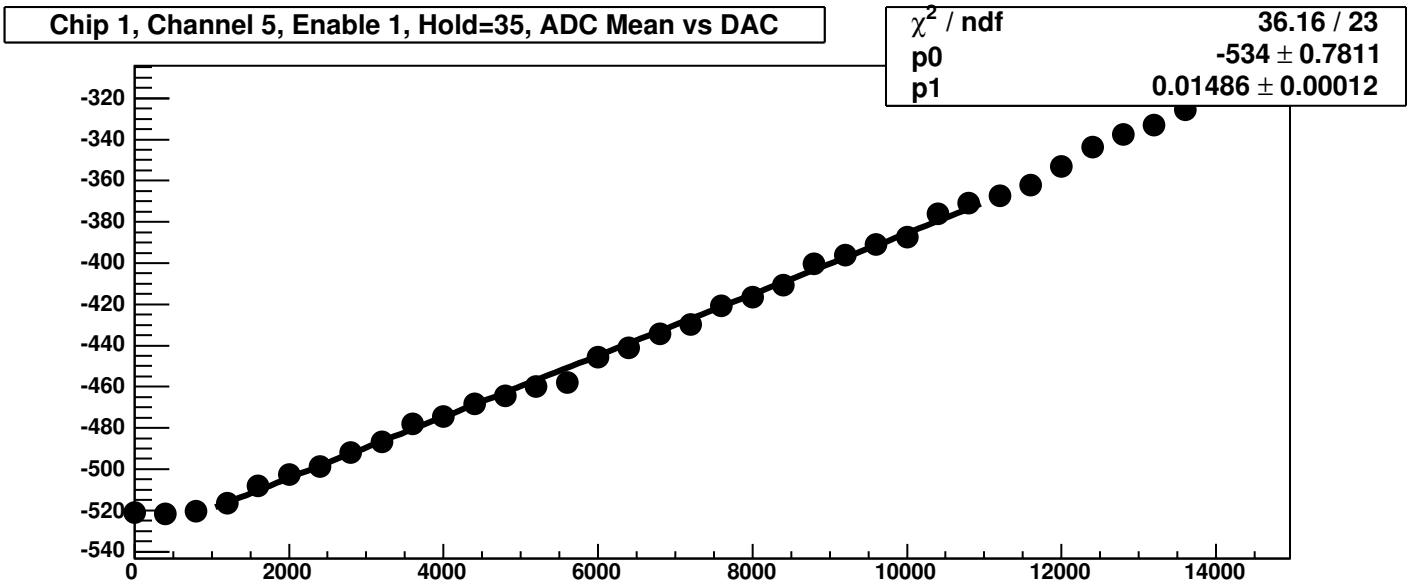


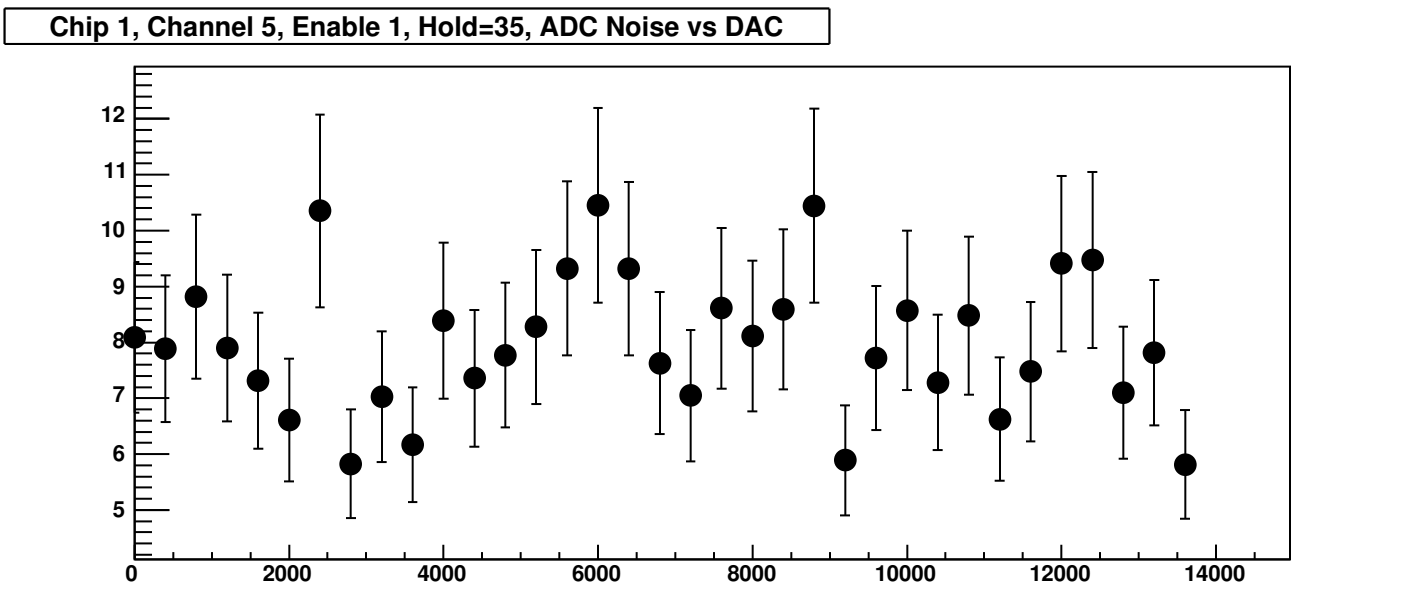
Chip 1, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC

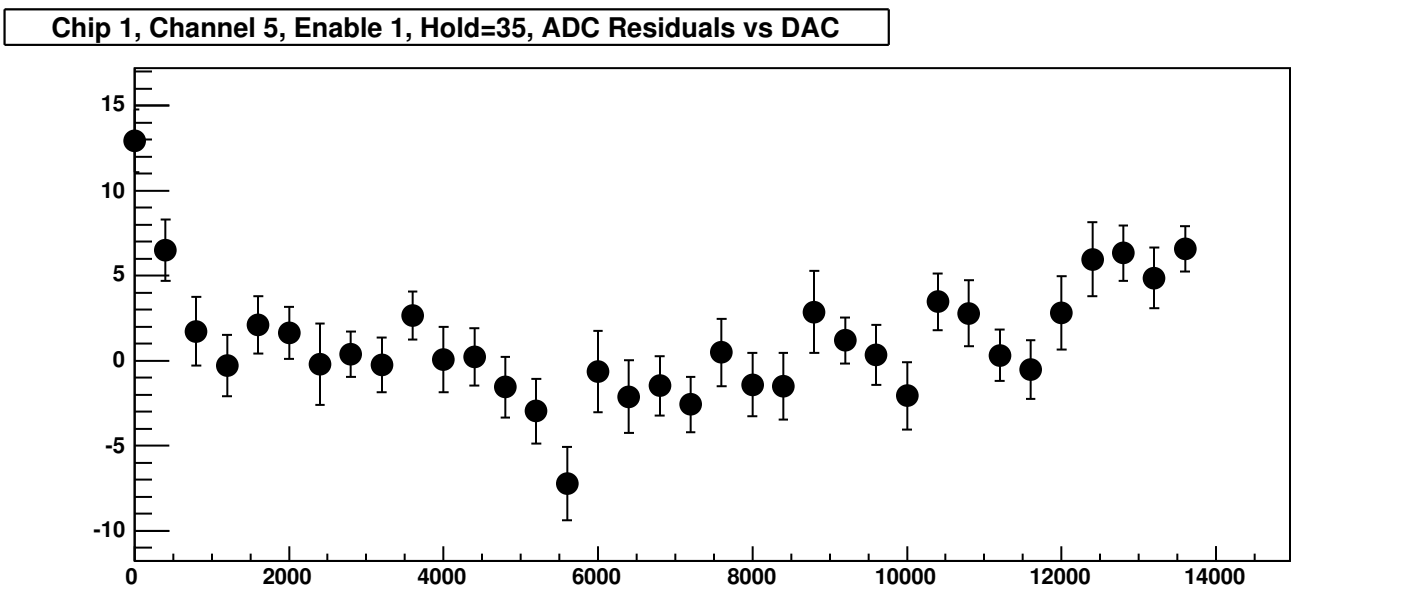


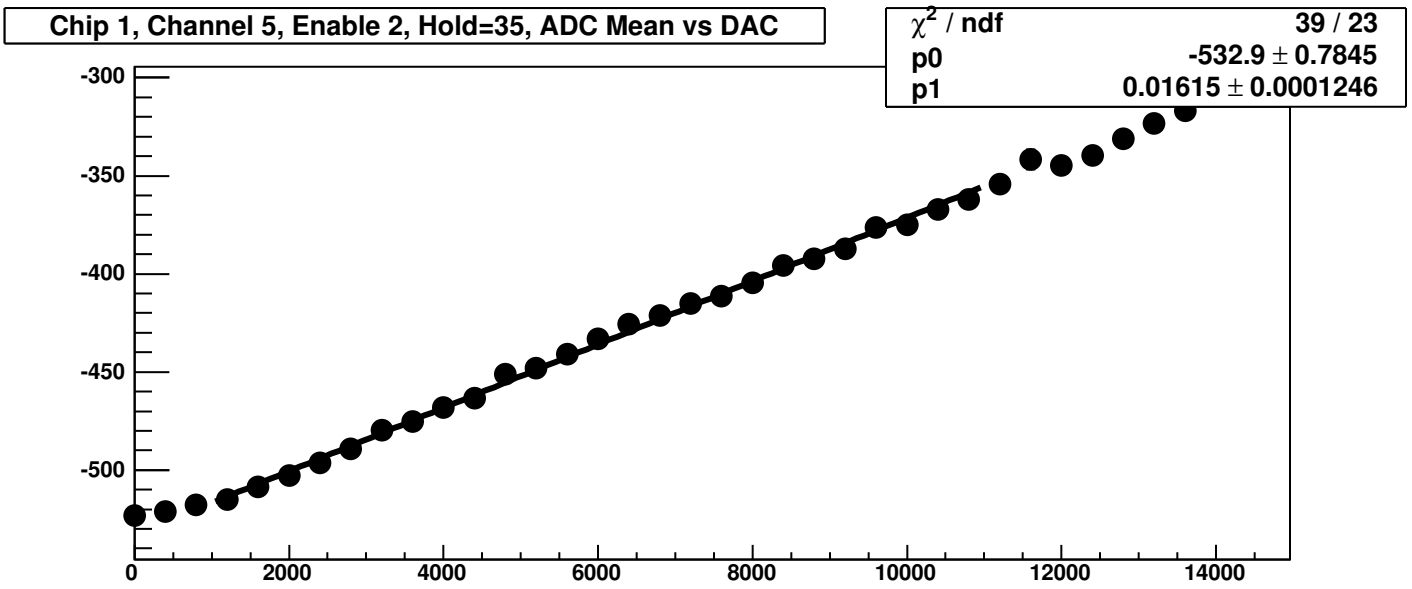
Chip 1, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

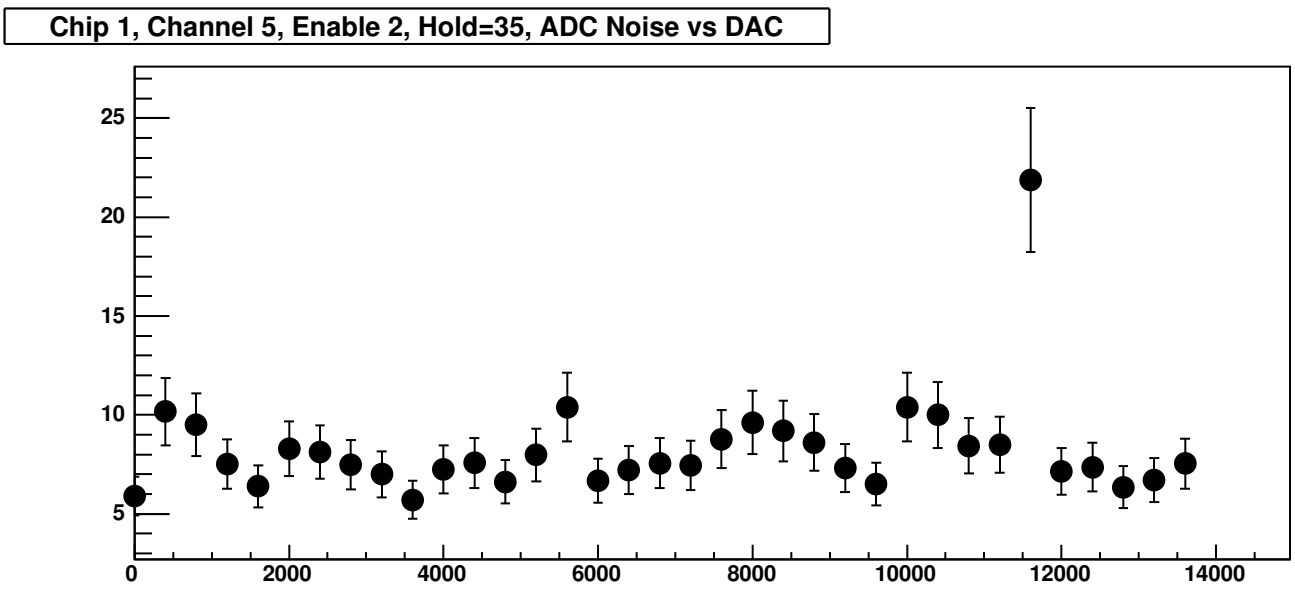


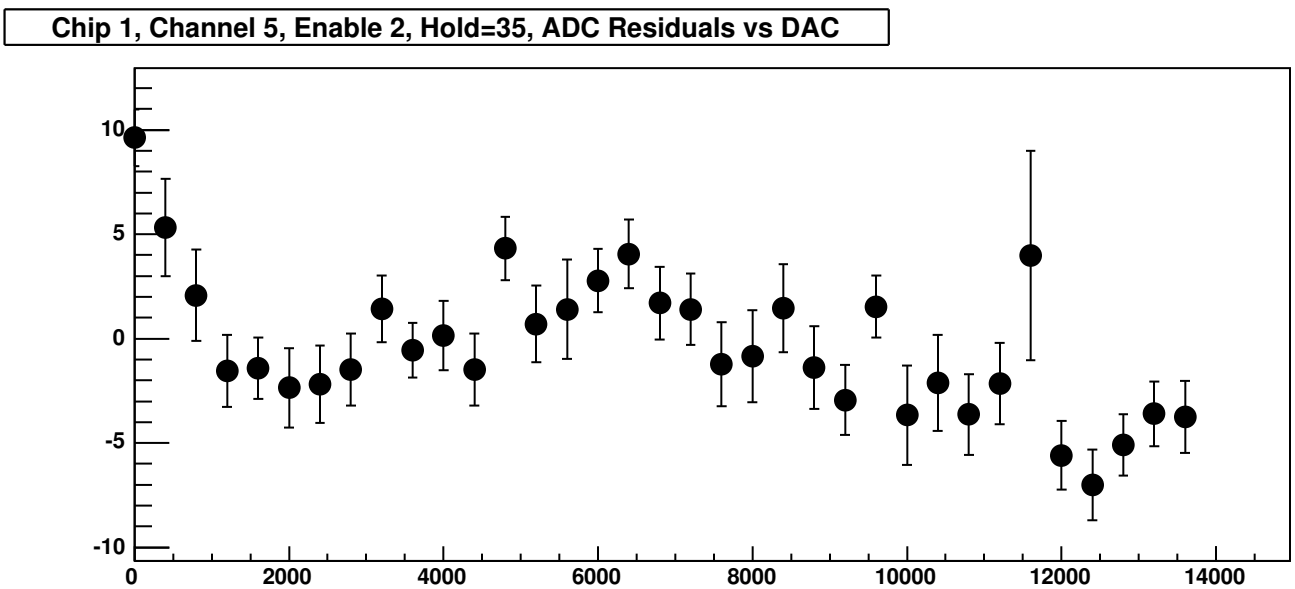

Chip 1, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 36.16 / 23
p0 -534 ± 0.7811
p1 0.01486 ± 0.00012
-320
-340
-360
-380
-400
-420
-440
-460
-480
-500
-520
-540
0
2000
4000
6000
8000
10000
12000
14000

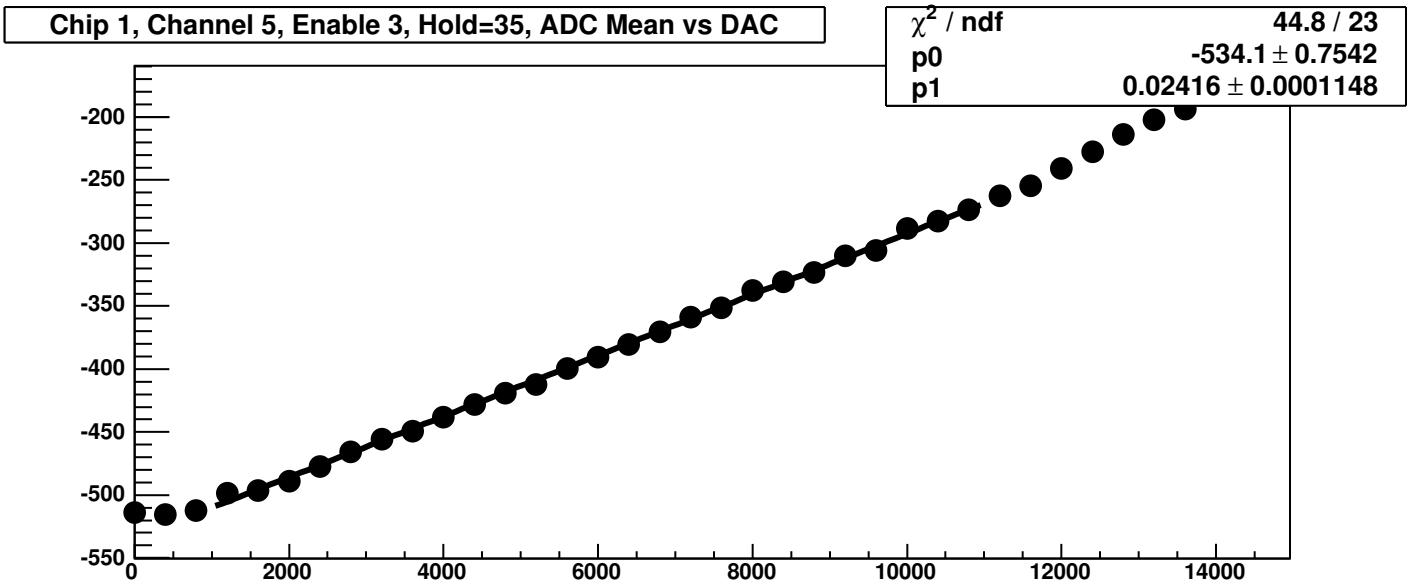

Chip 1, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

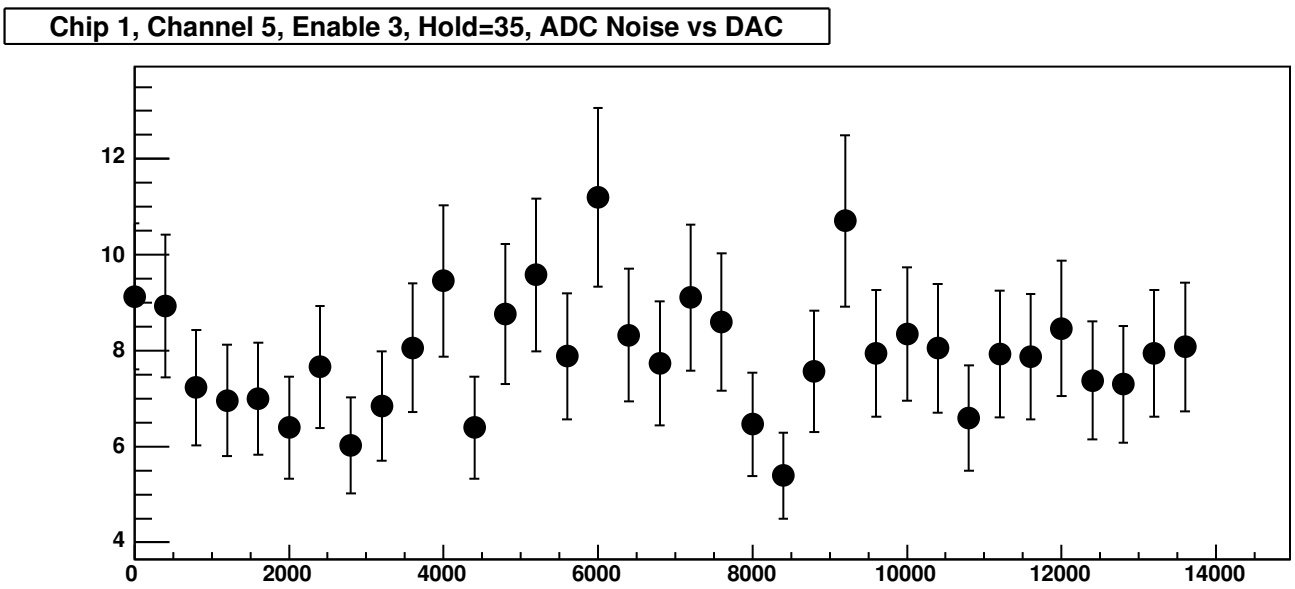

Chip 1, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

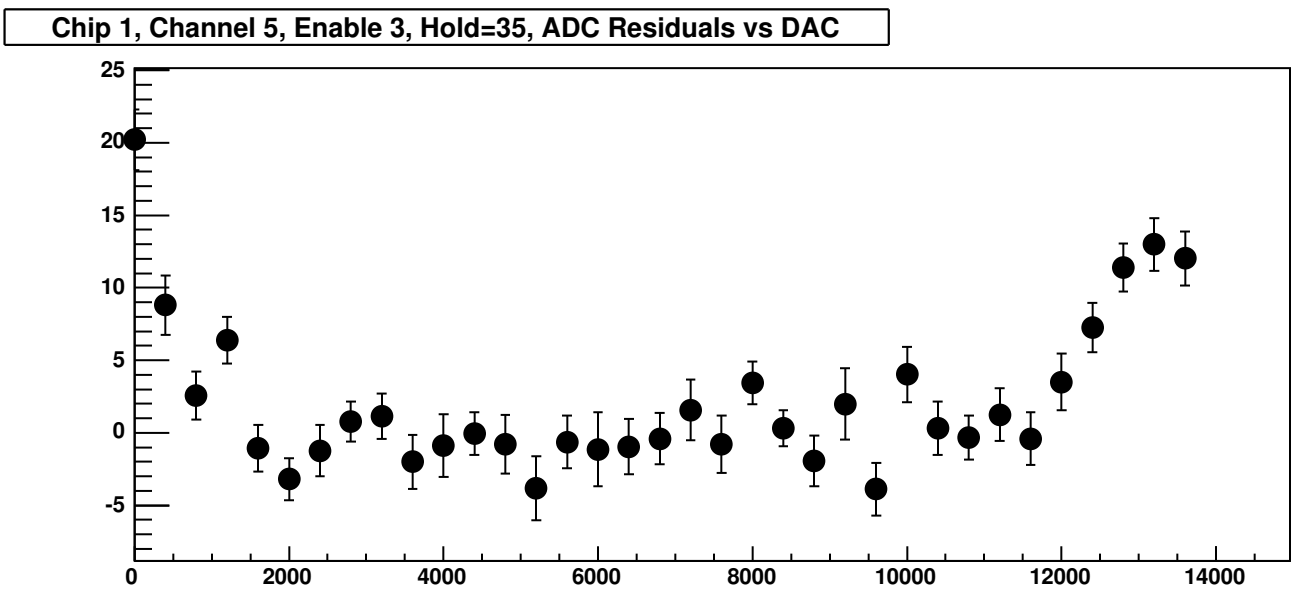

Chip 1, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 39 / 23
p0 -532.9 ± 0.7845
p1 0.01615 ± 0.0001246
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

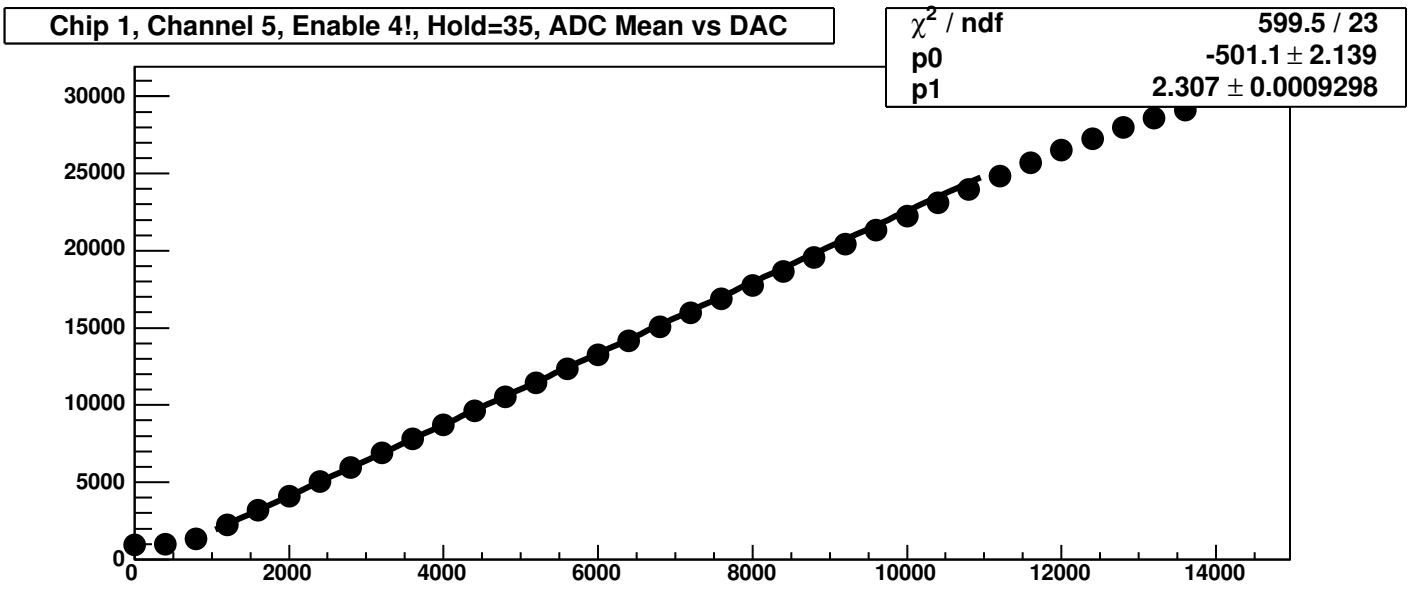

Chip 1, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

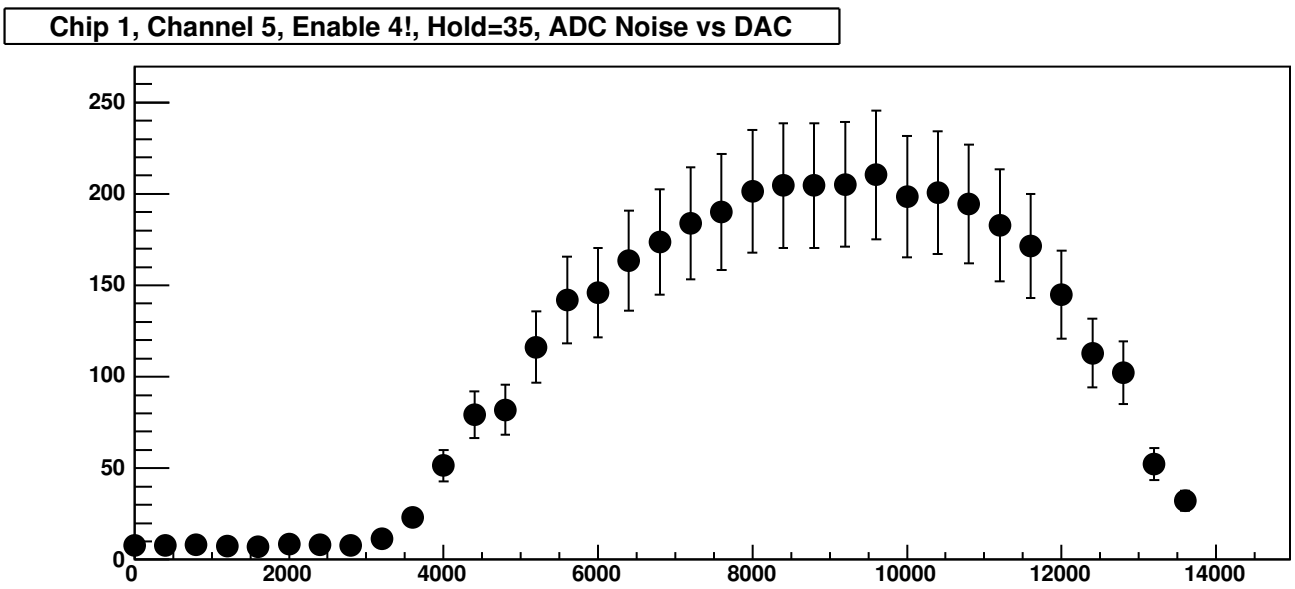

Chip 1, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

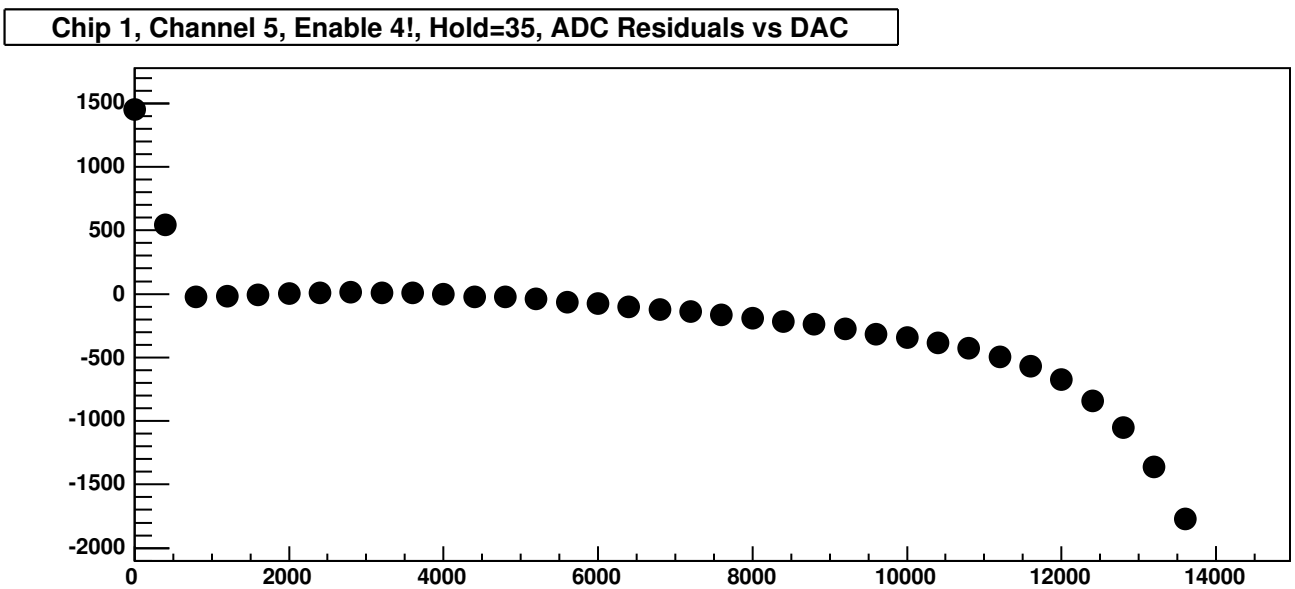

Chip 1, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 44.8 / 23
p0 -534.1 ± 0.7542
p1 0.02416 ± 0.0001148
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

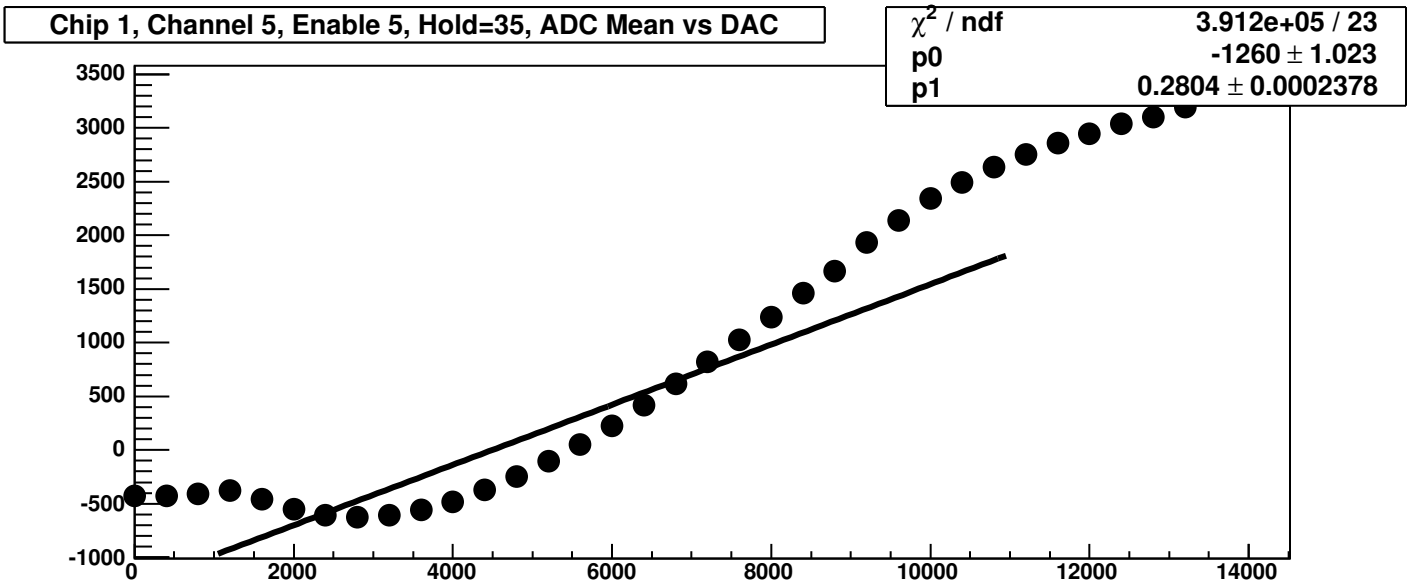

Chip 1, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

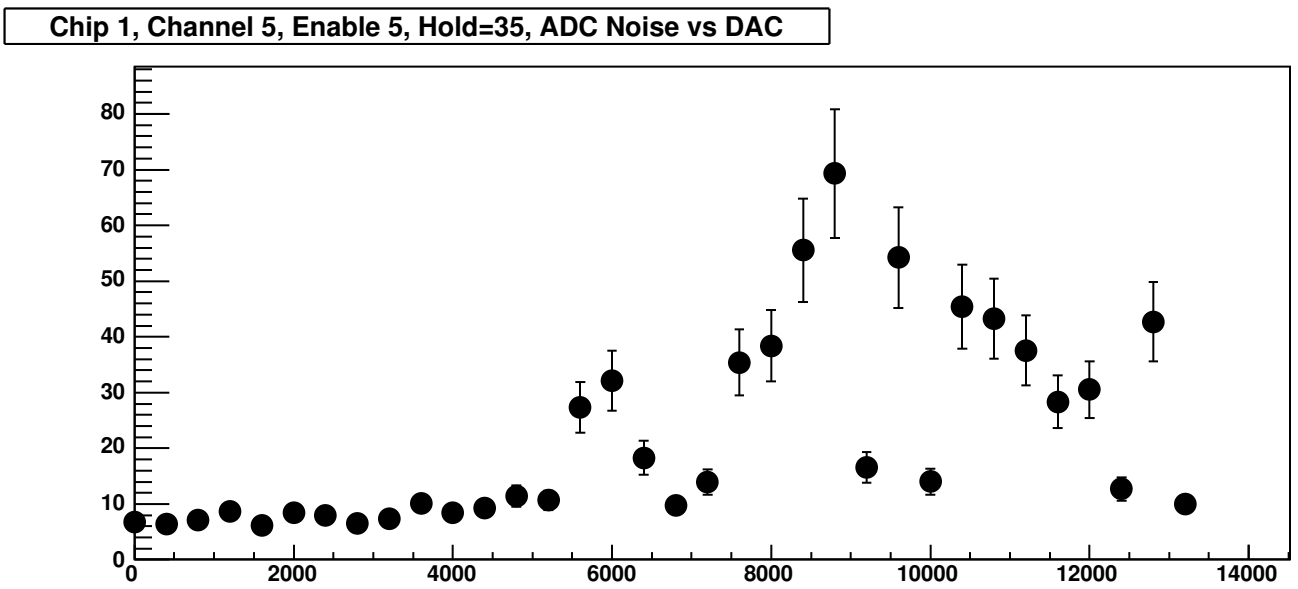

Chip 1, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

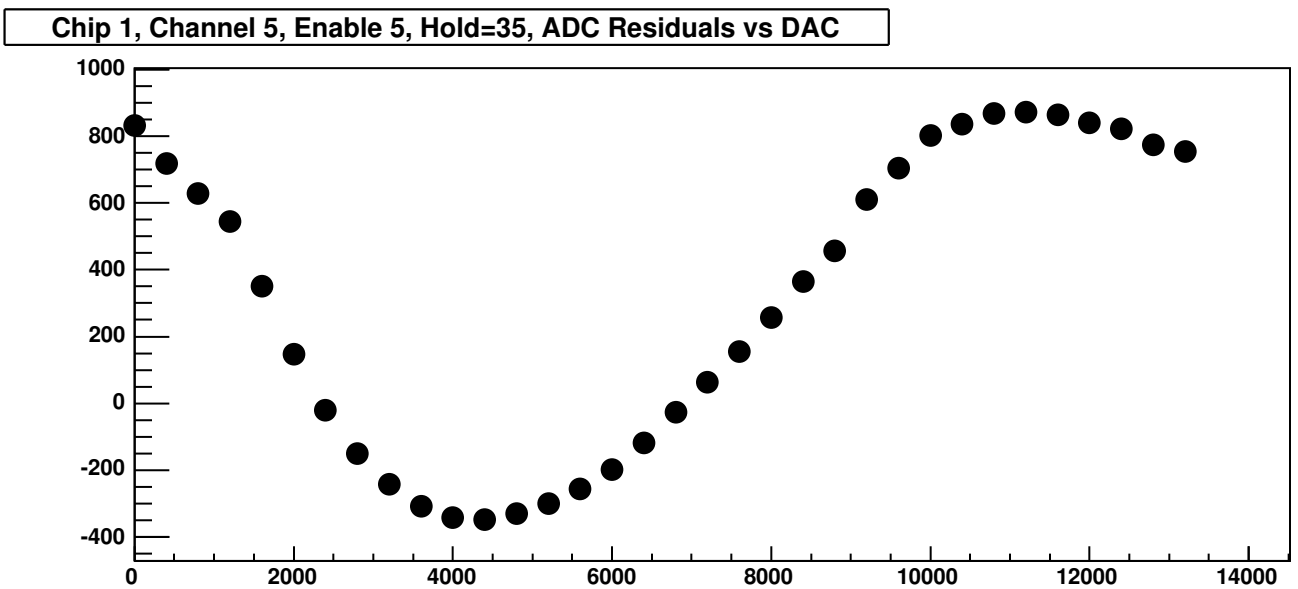

Chip 1, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 599.5 / 23
p0 -501.1 ± 2.139
p1 2.307 ± 0.0009298
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

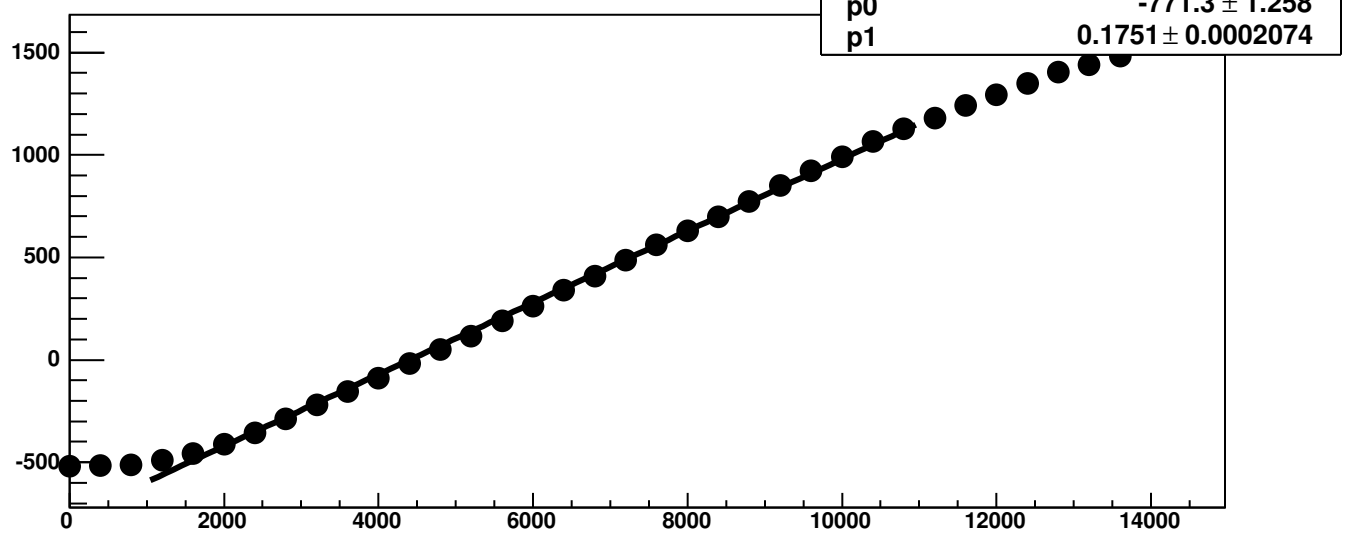

Chip 1, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.912e+05 / 23
p0 -1260 ± 1.023
p1 0.2804 ± 0.0002378
3500
3000
2500
2000
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

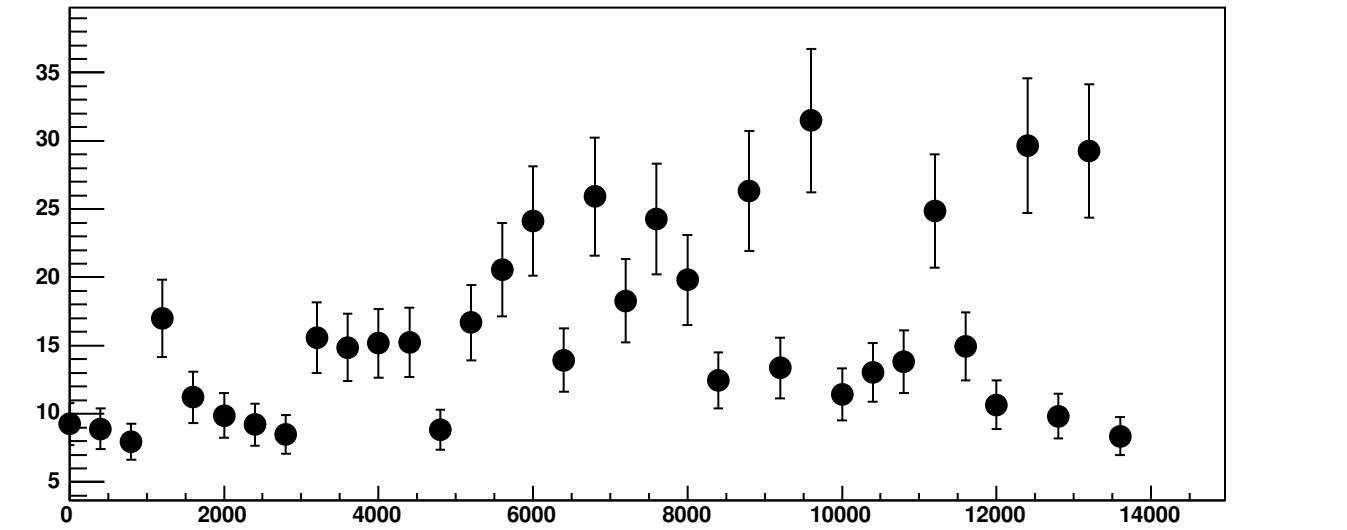

Chip 1, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

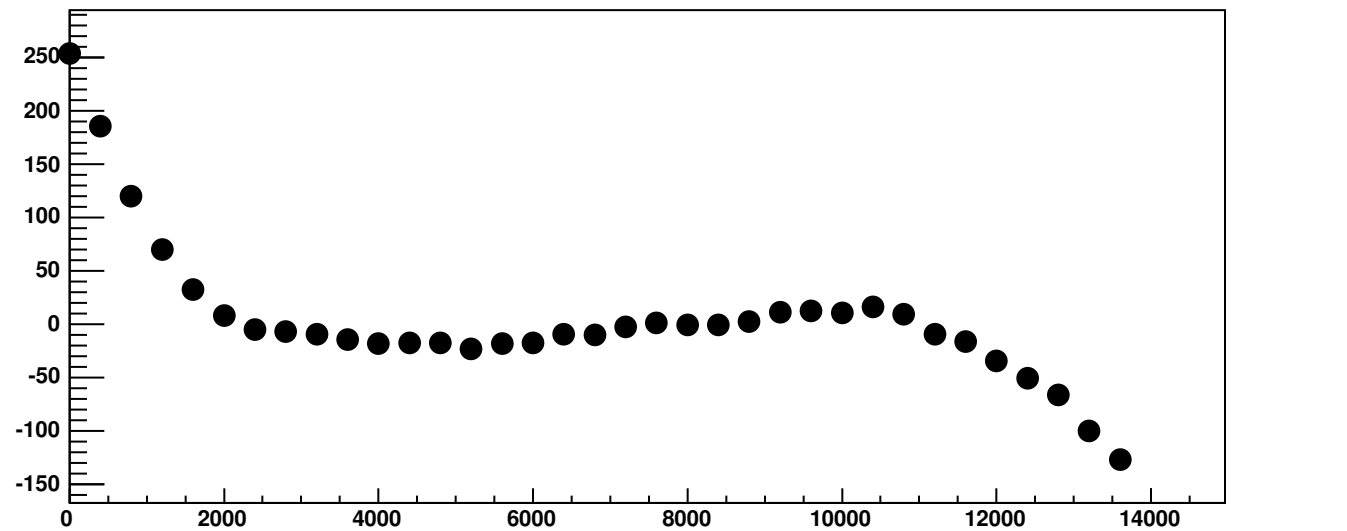
Chip 1, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



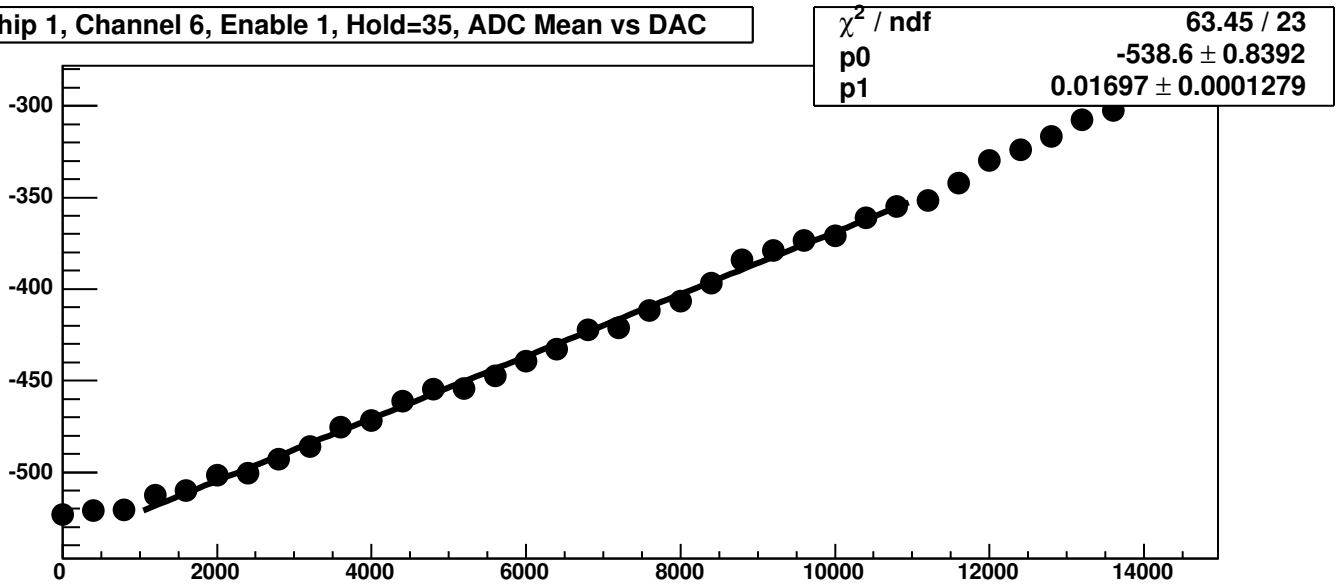
Chip 1, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



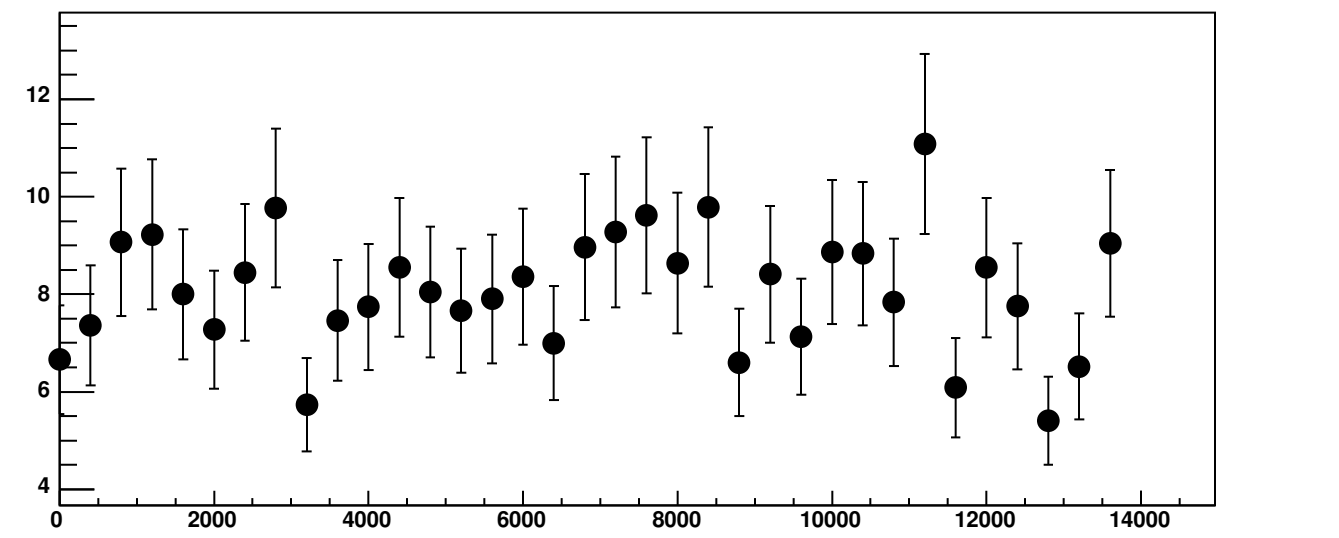
Chip 1, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



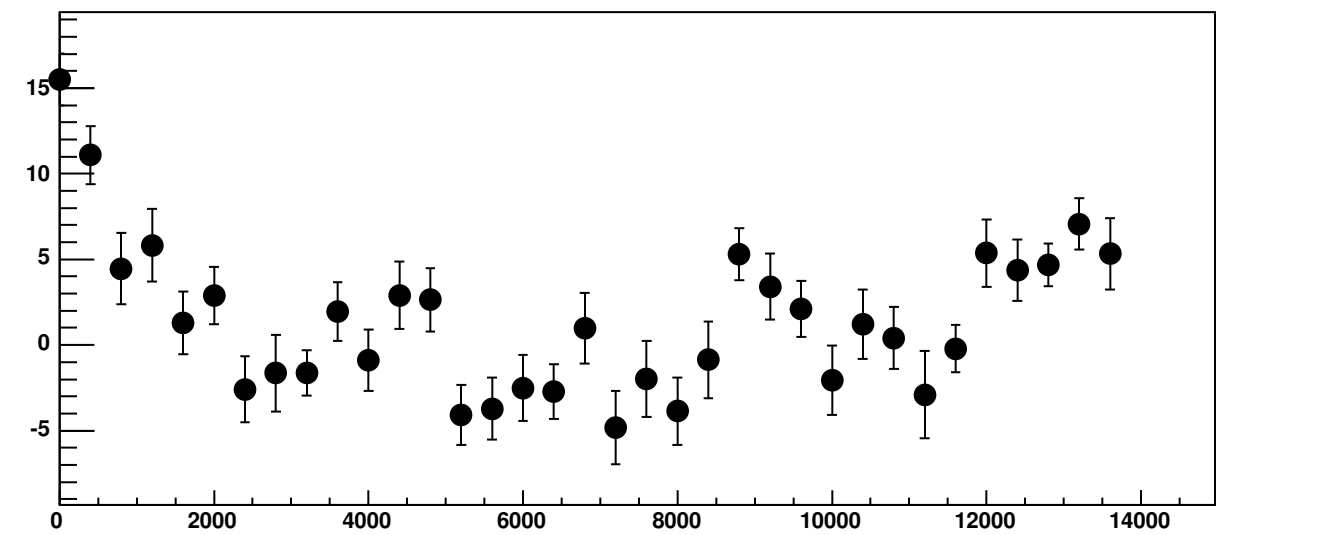
Chip 1, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



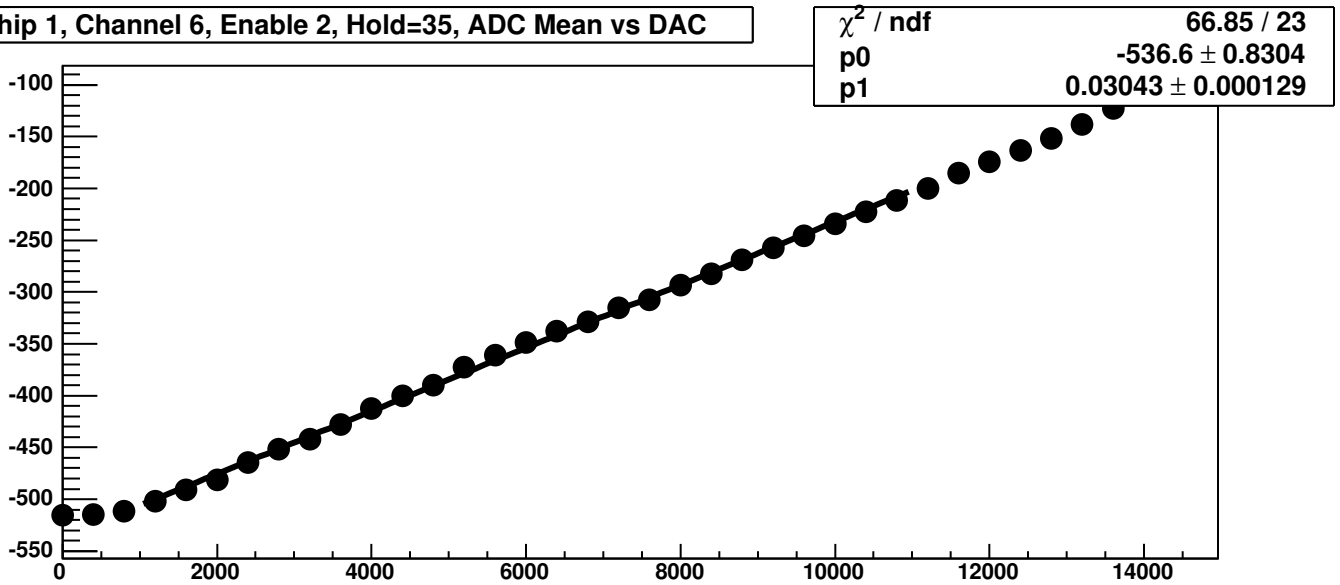
Chip 1, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



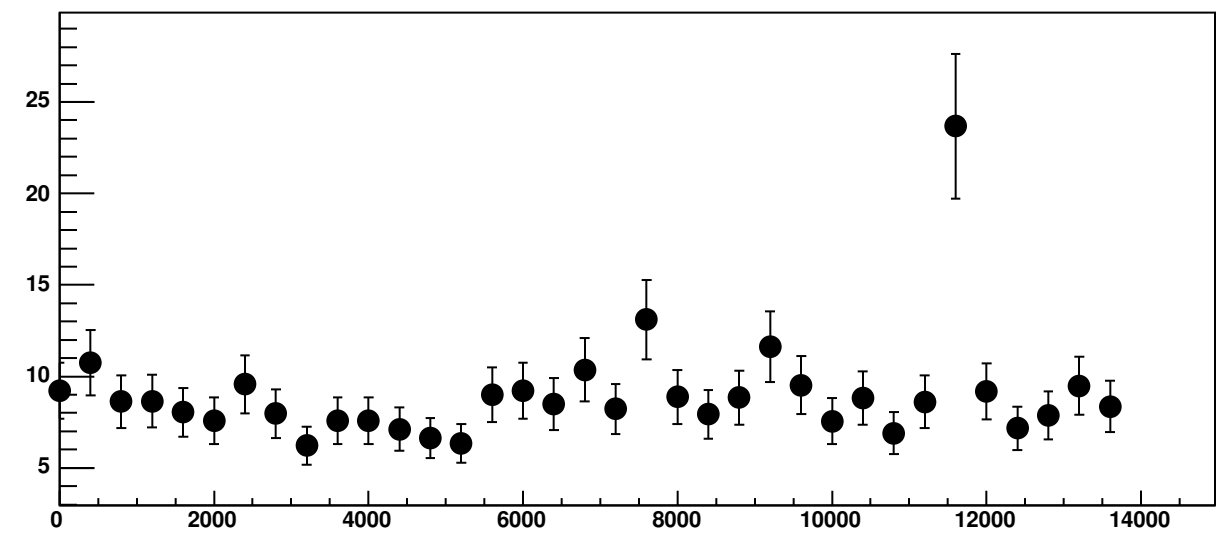
Chip 1, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



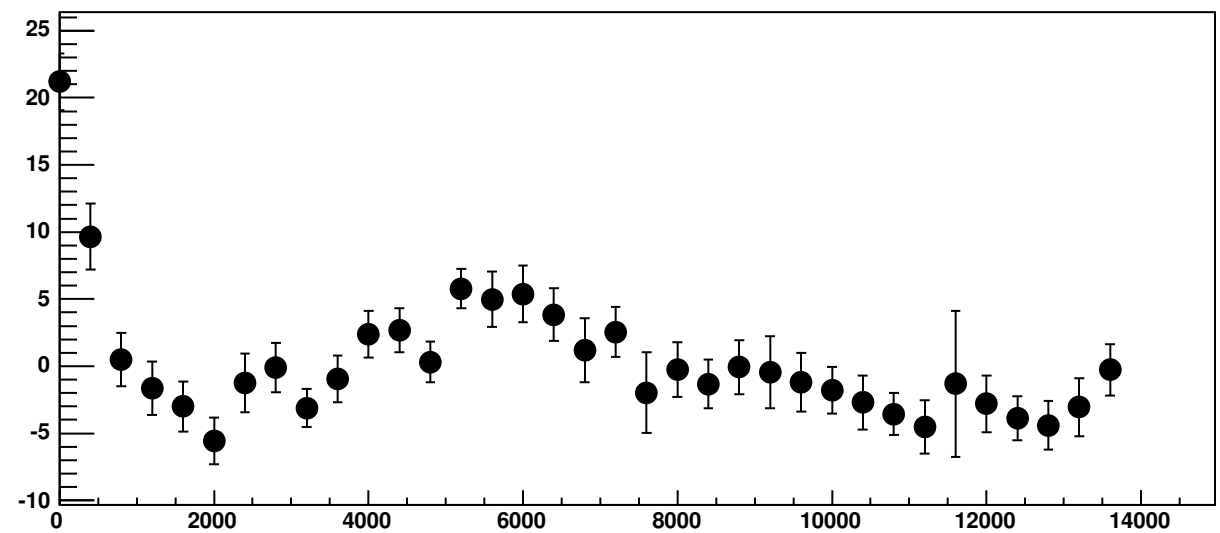
Chip 1, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC



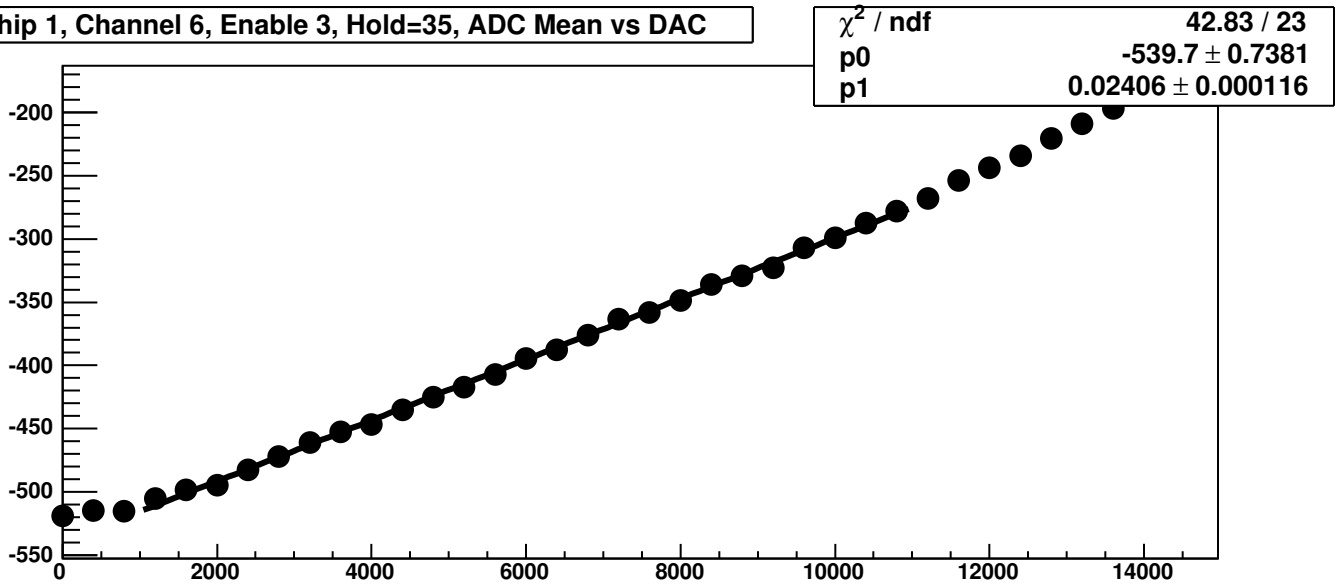
Chip 1, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC



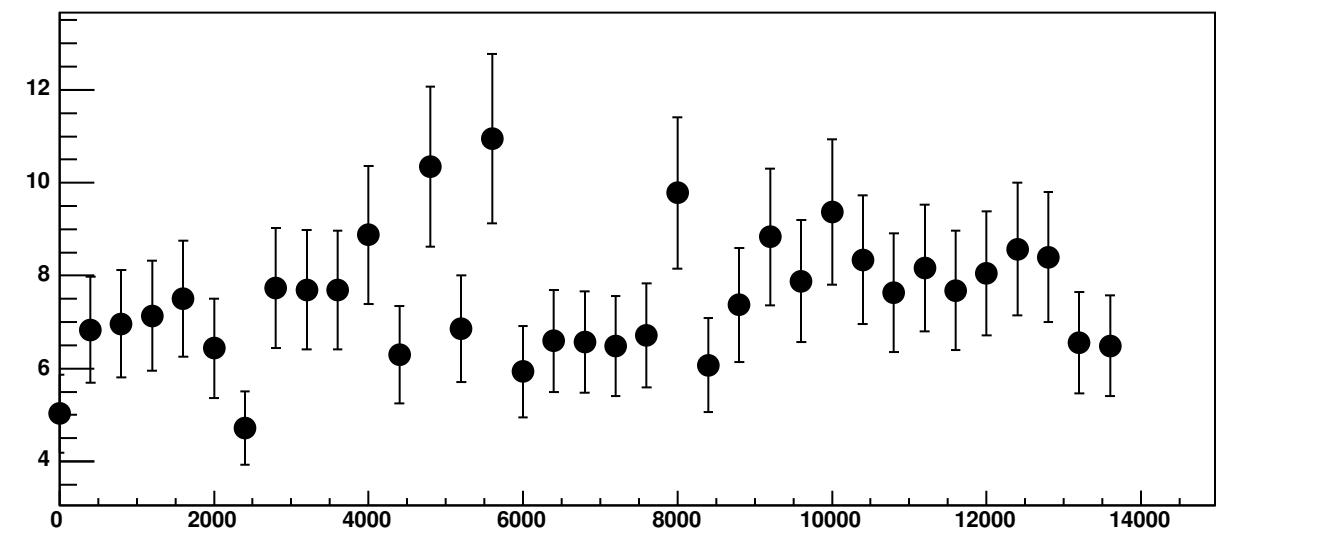
Chip 1, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC



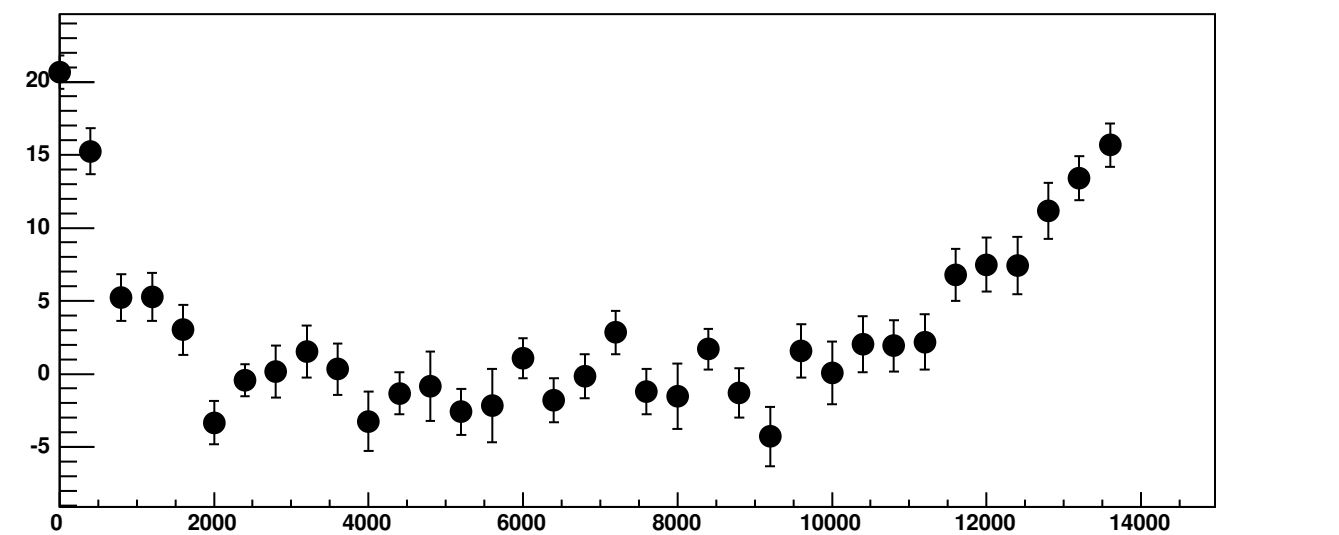
Chip 1, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



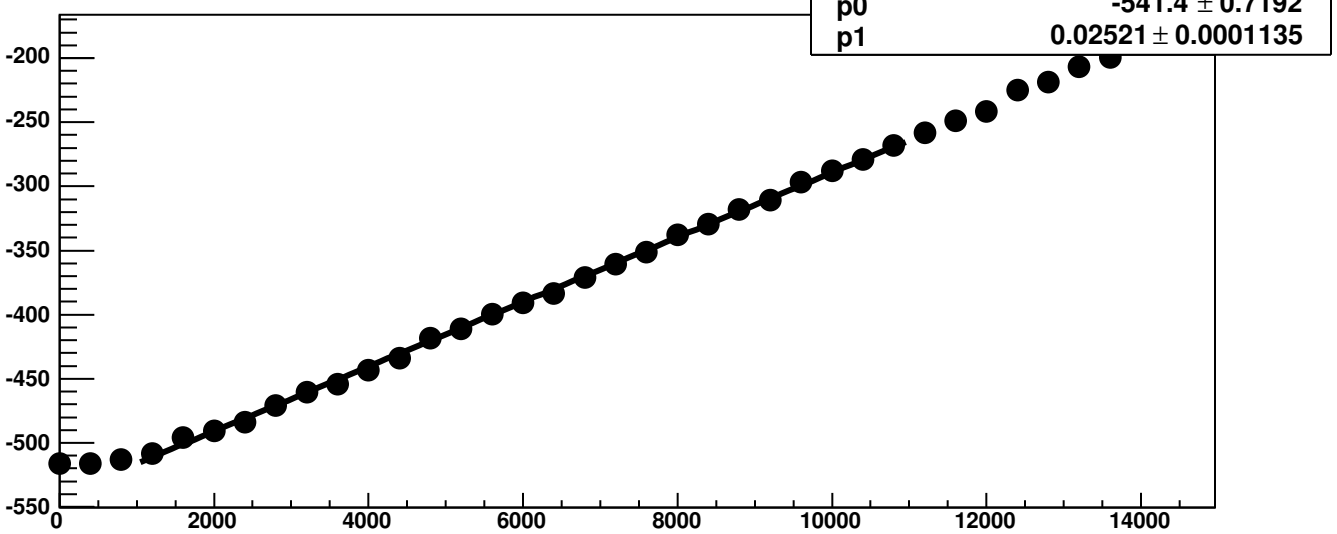
Chip 1, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



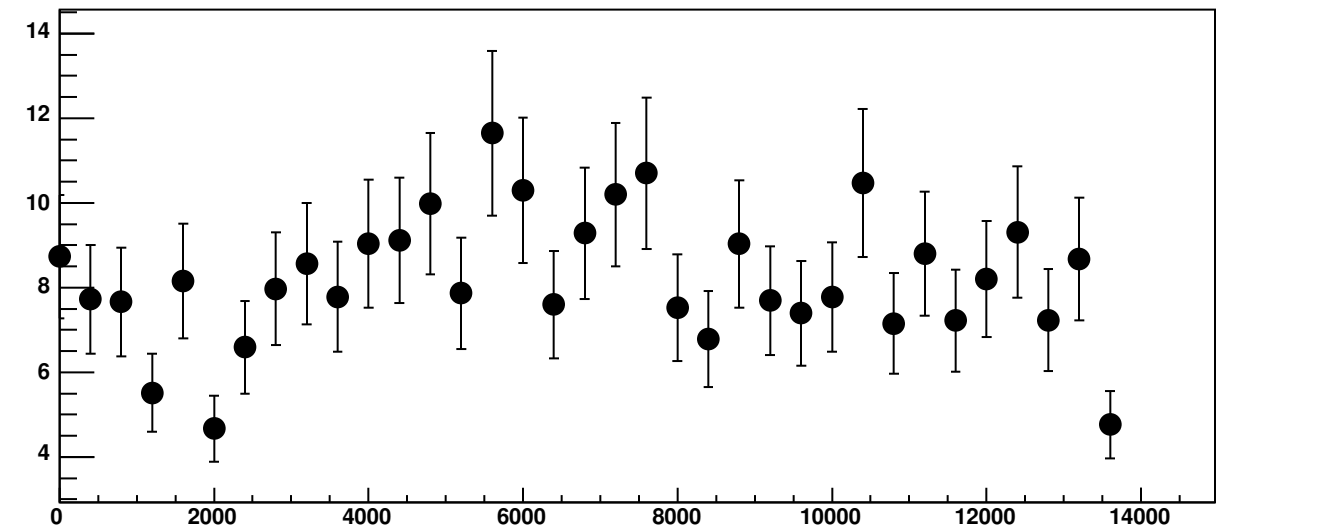
Chip 1, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



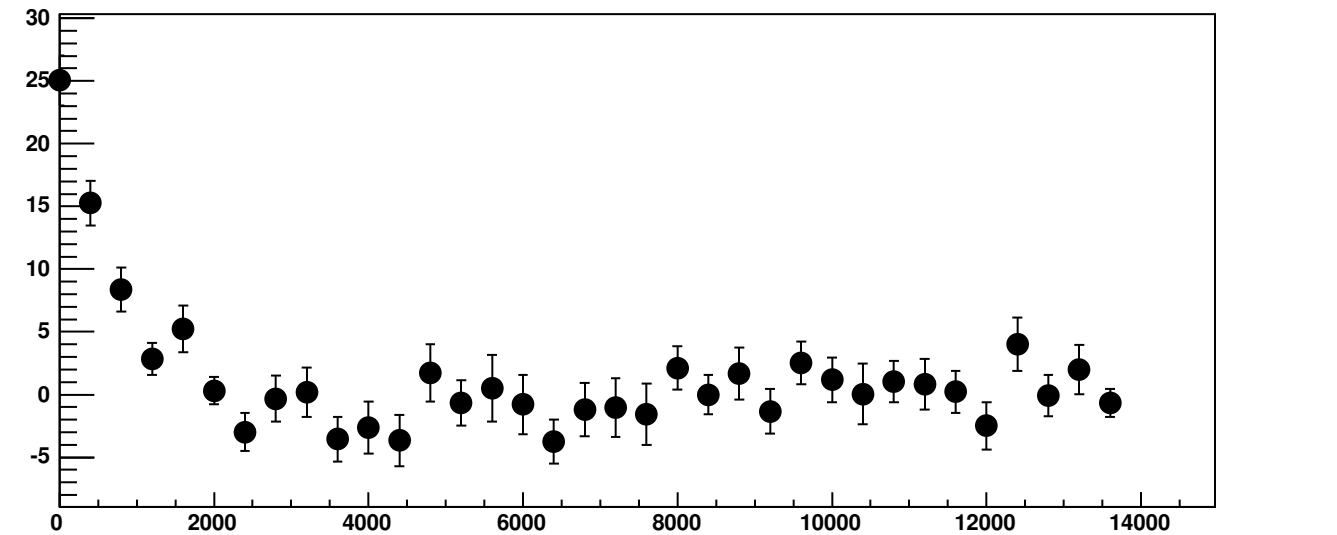
Chip 1, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

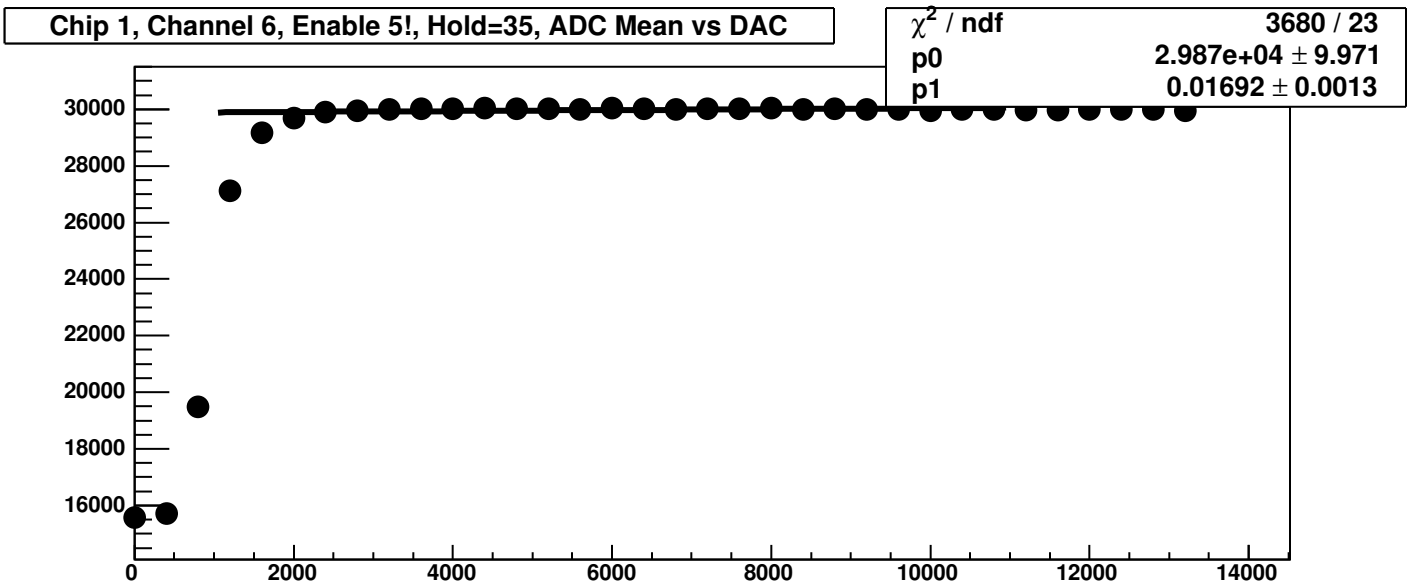


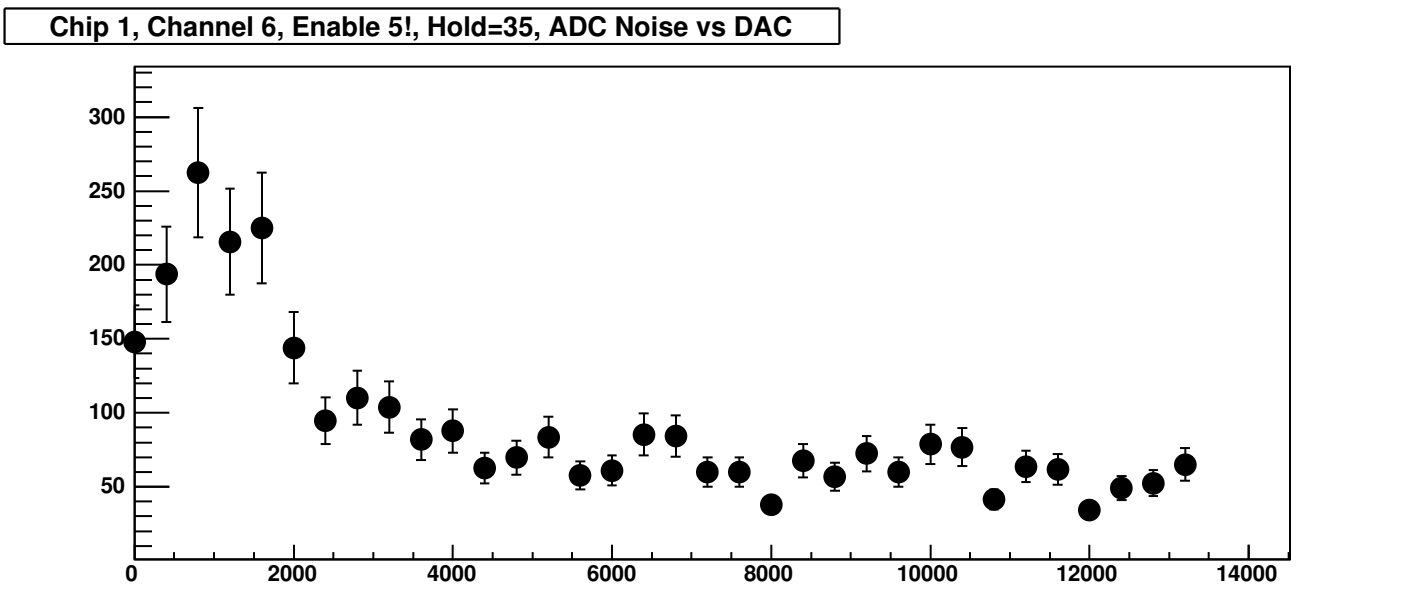
Chip 1, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

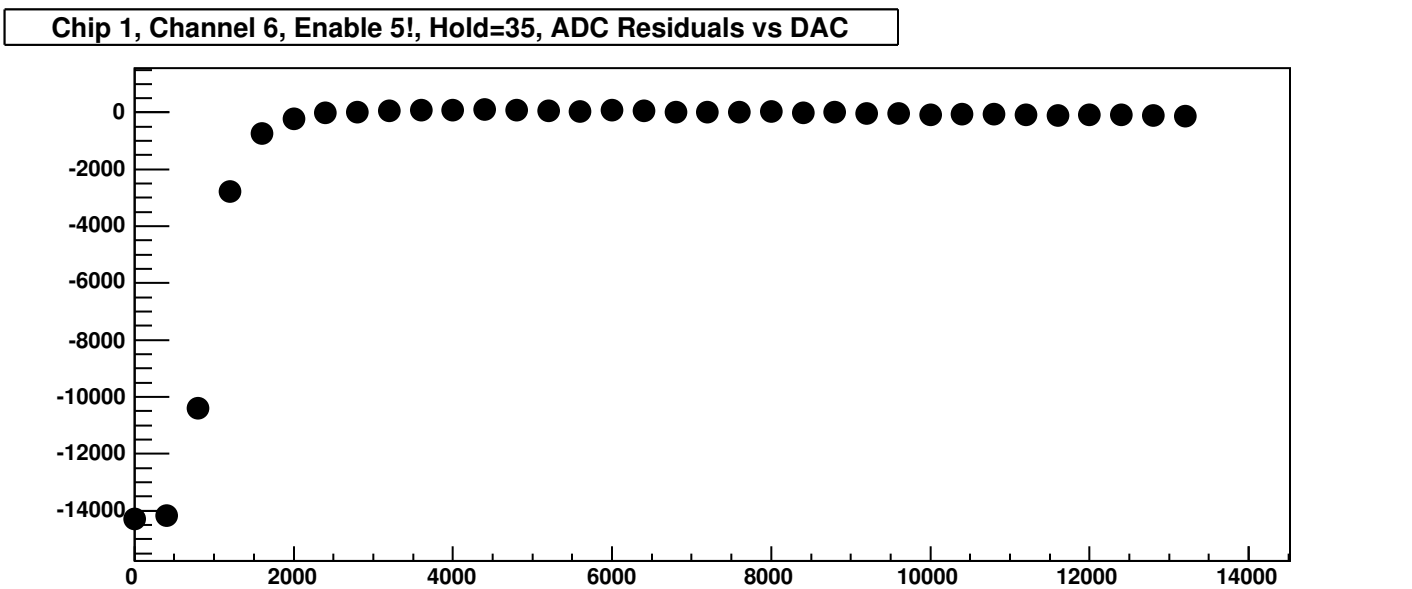


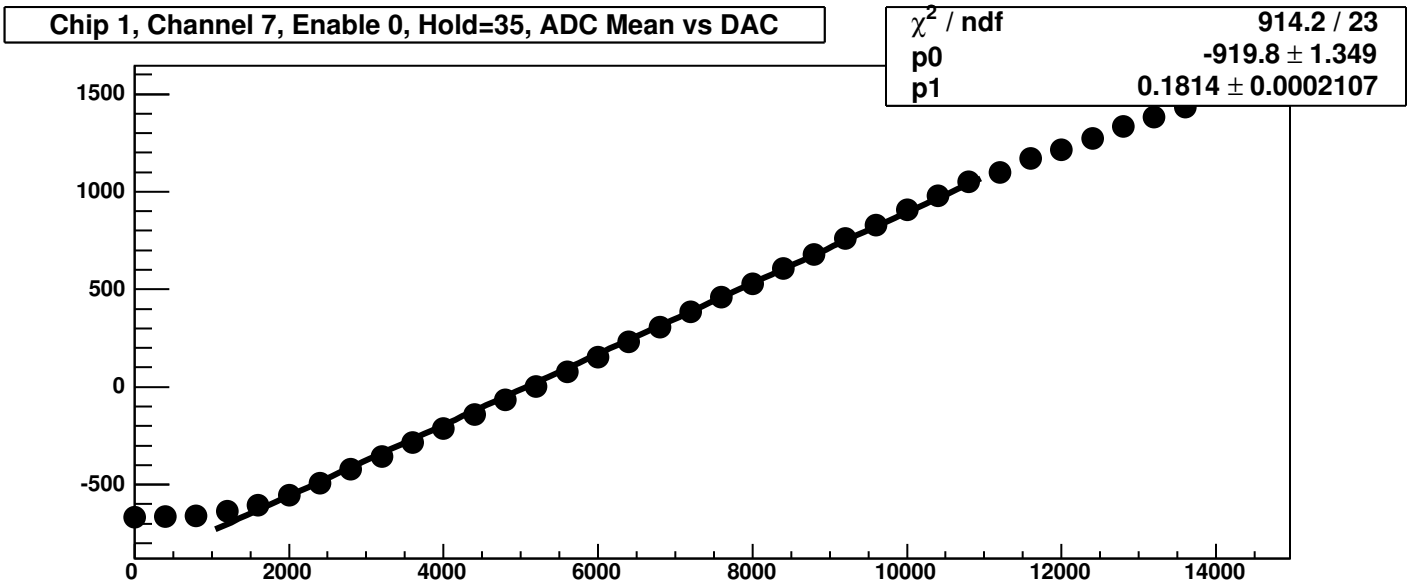
Chip 1, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

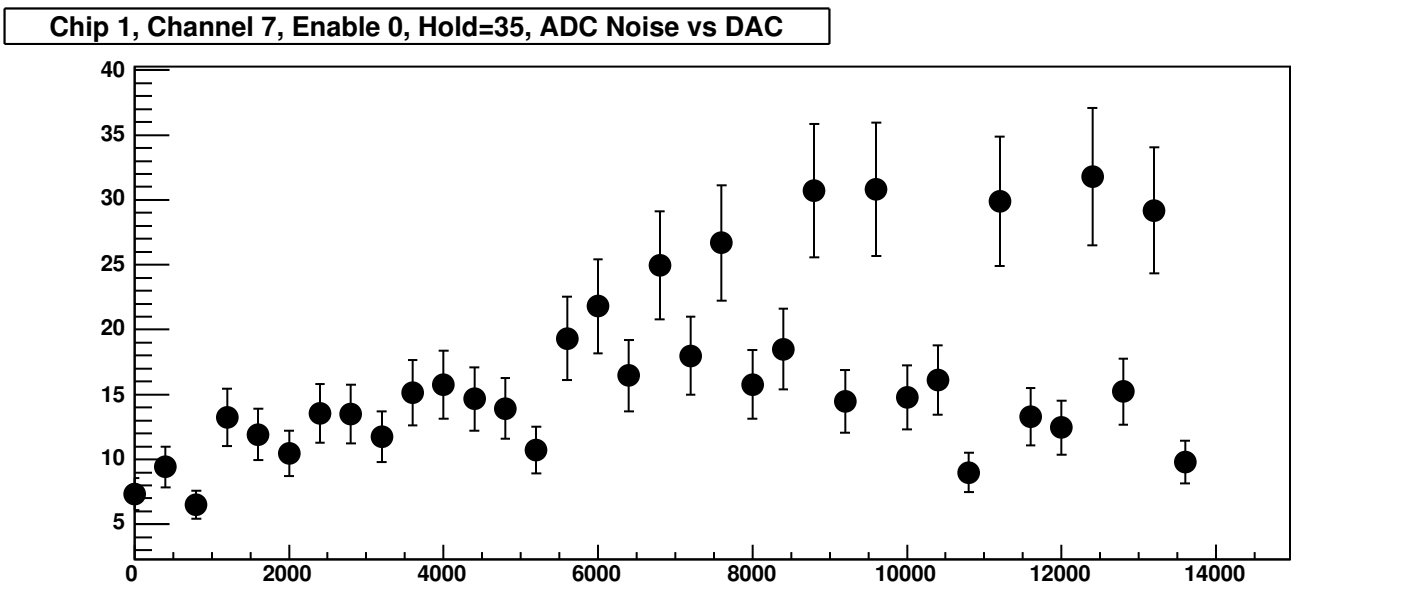


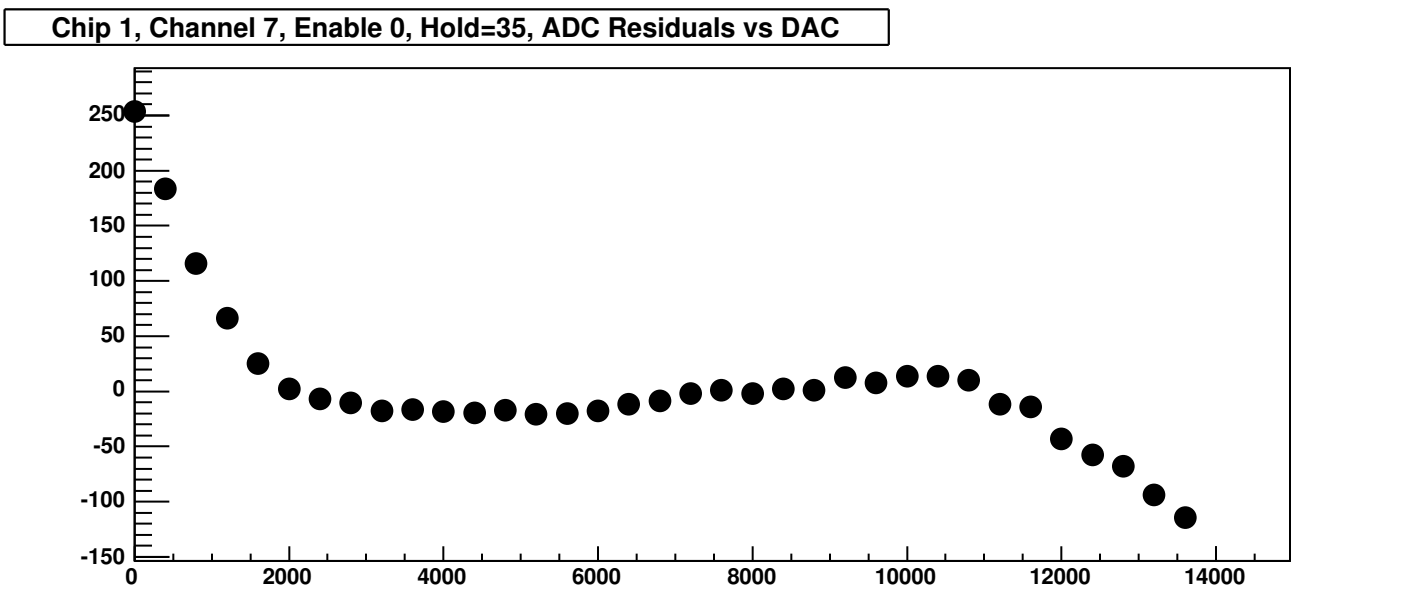

Chip 1, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ^2 / ndf 3680 / 23
p0 2.987e+04 ± 9.971
p1 0.01692 ± 0.0013
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

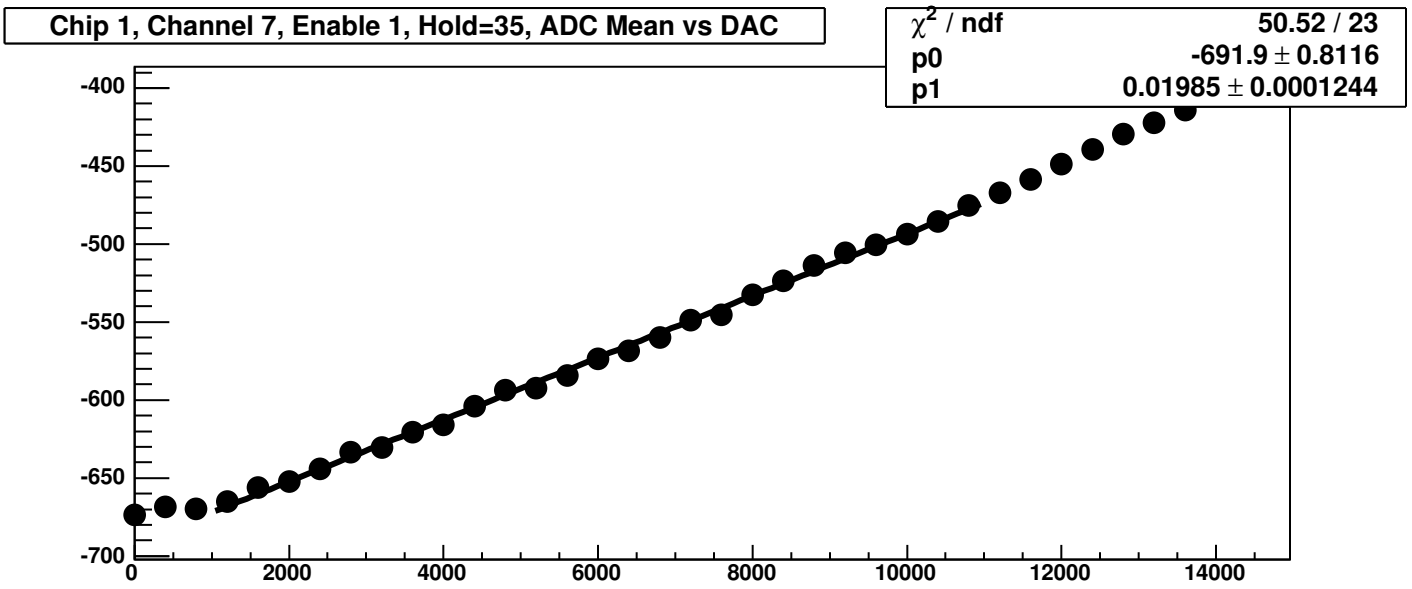

Chip 1, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

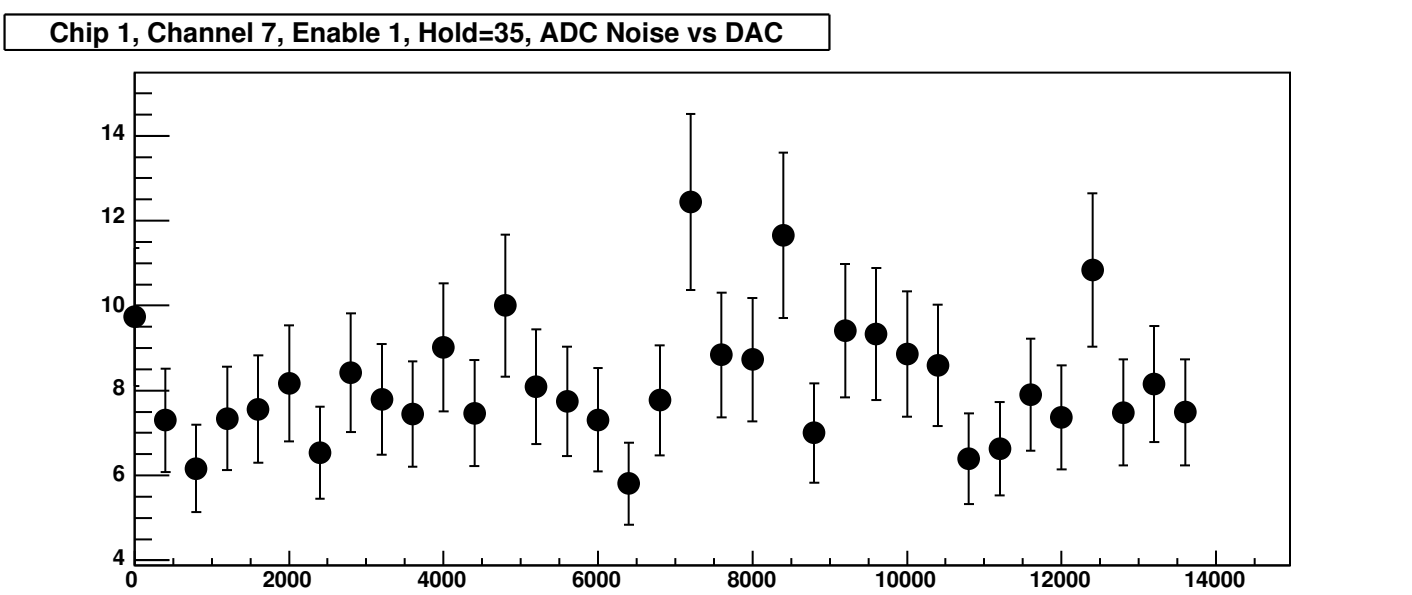

Chip 1, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

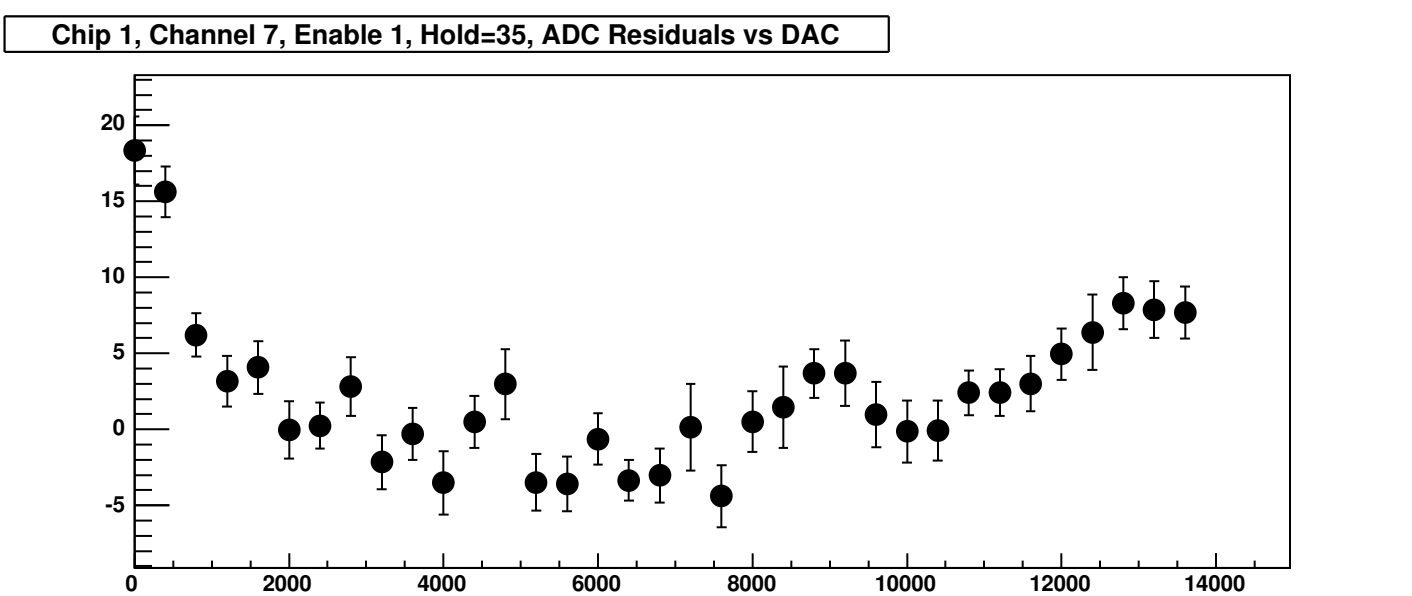

Chip 1, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 914.2 / 23
p0 -919.8 ± 1.349
p1 0.1814 ± 0.0002107
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

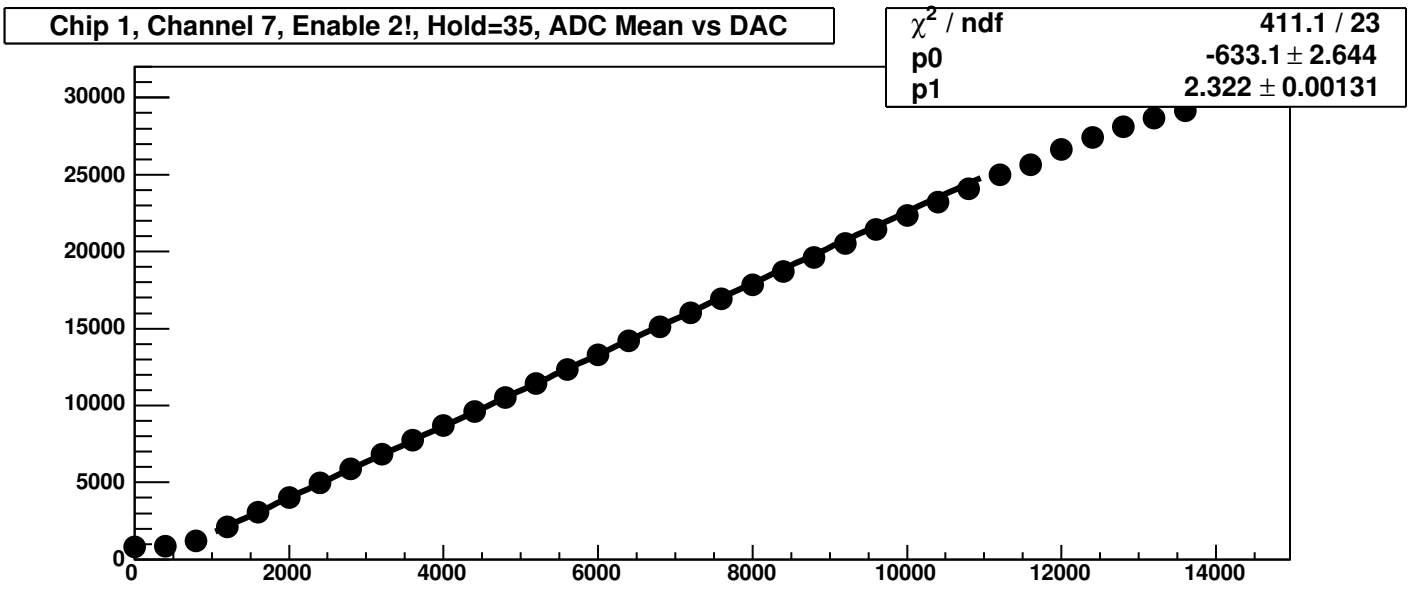

Chip 1, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

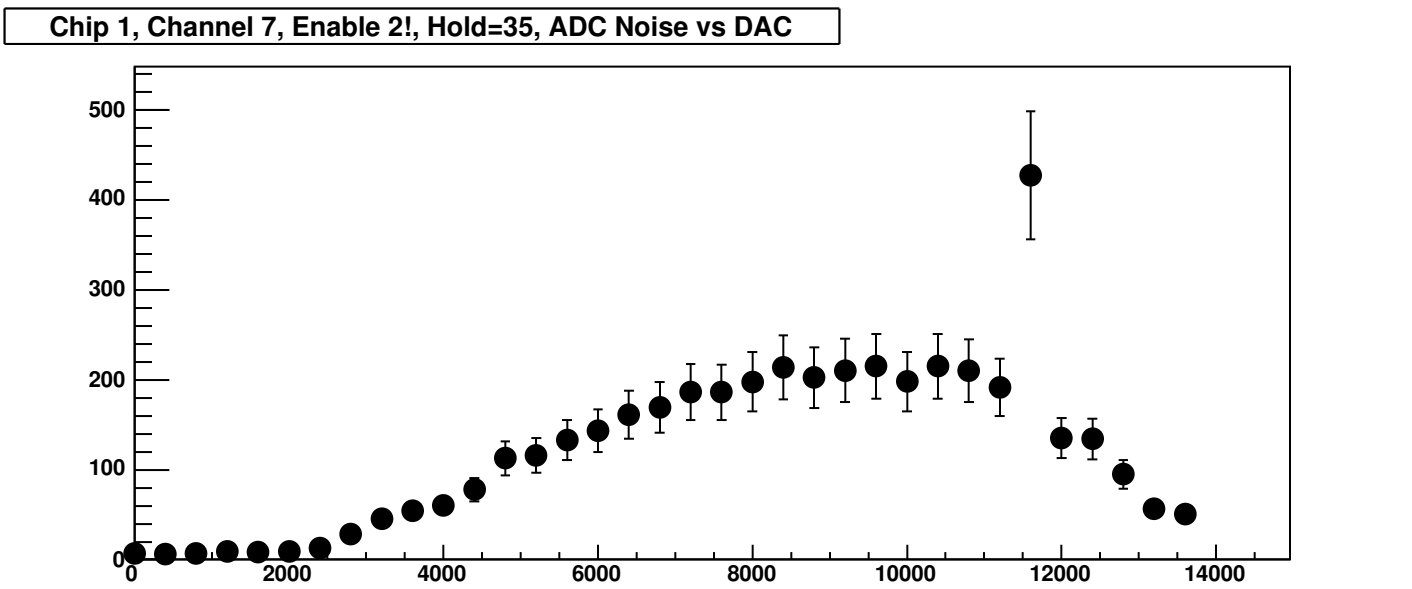

Chip 1, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

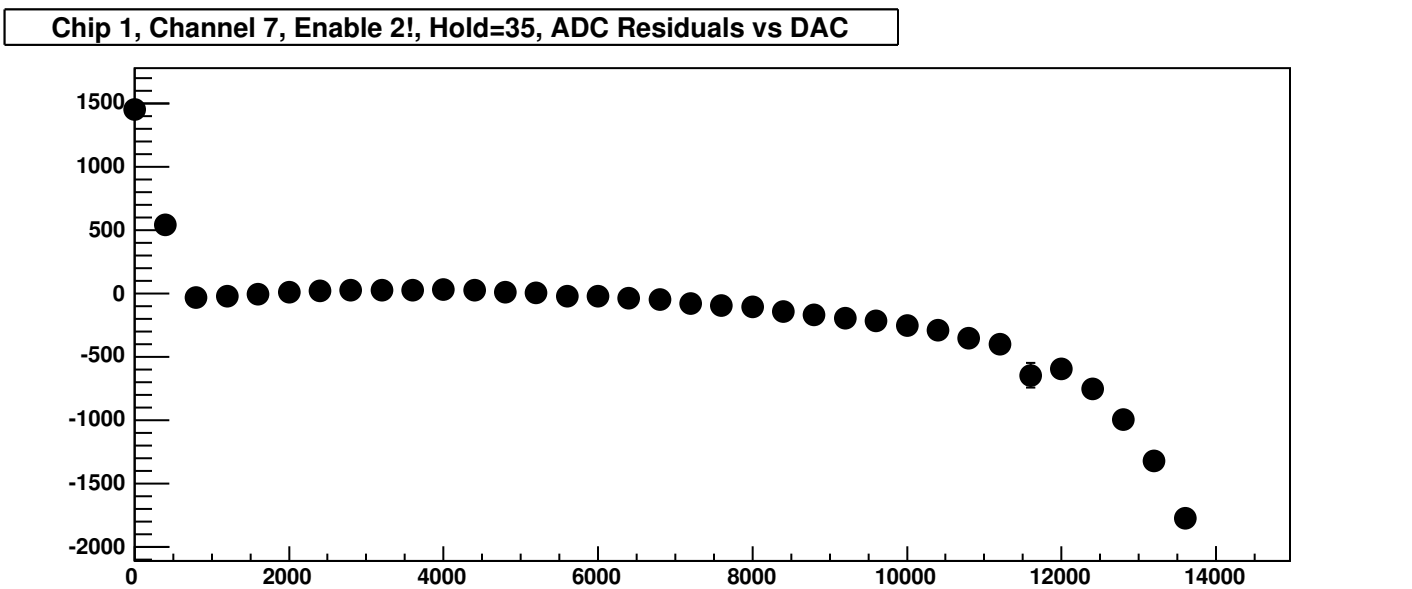

Chip 1, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 50.52 / 23
p0 -691.9 ± 0.8116
p1 0.01985 ± 0.0001244
-400
-450
-500
-550
-600
-650
-700
0
2000
4000
6000
8000
10000
12000
14000

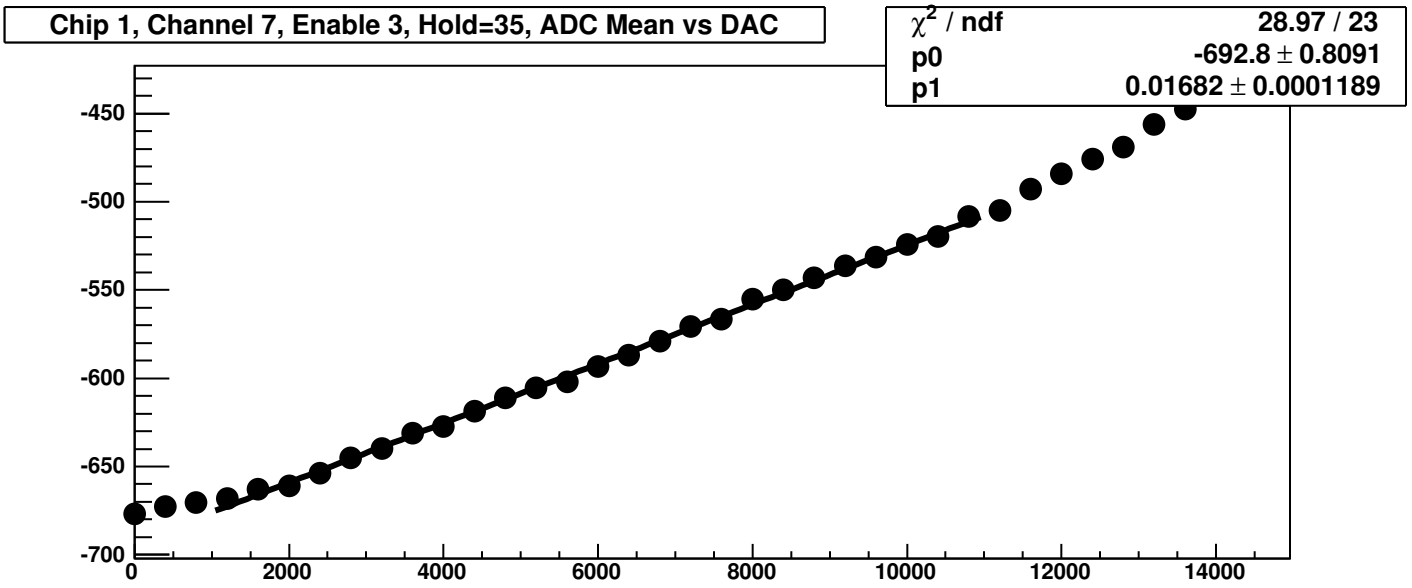

Chip 1, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

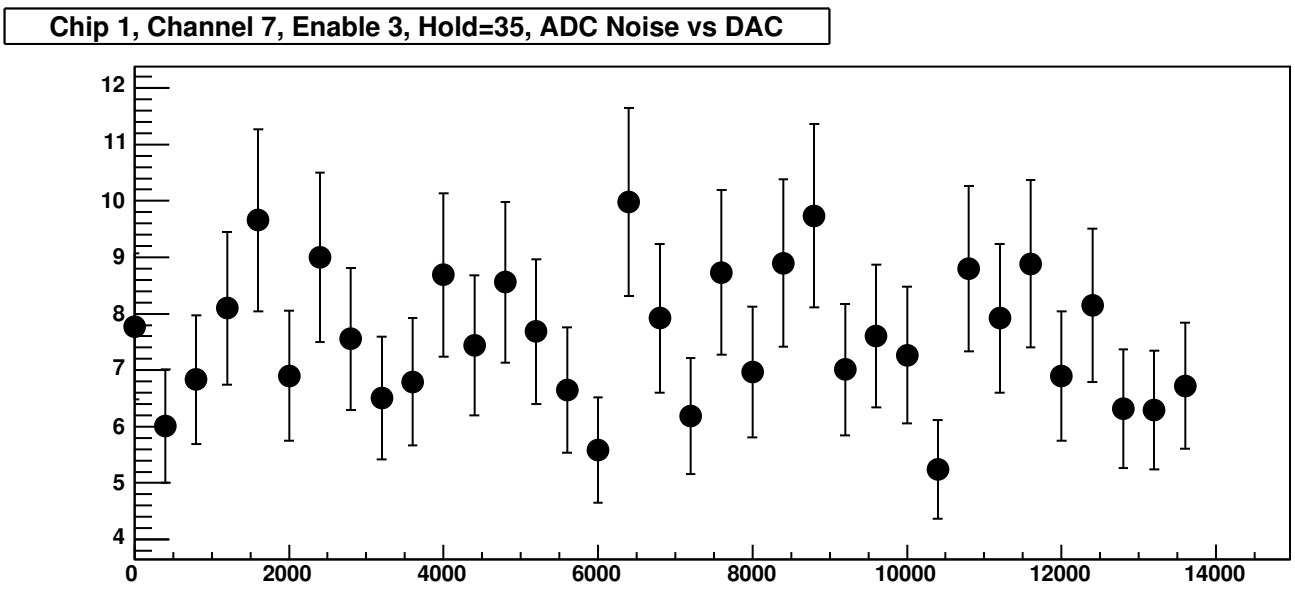

Chip 1, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

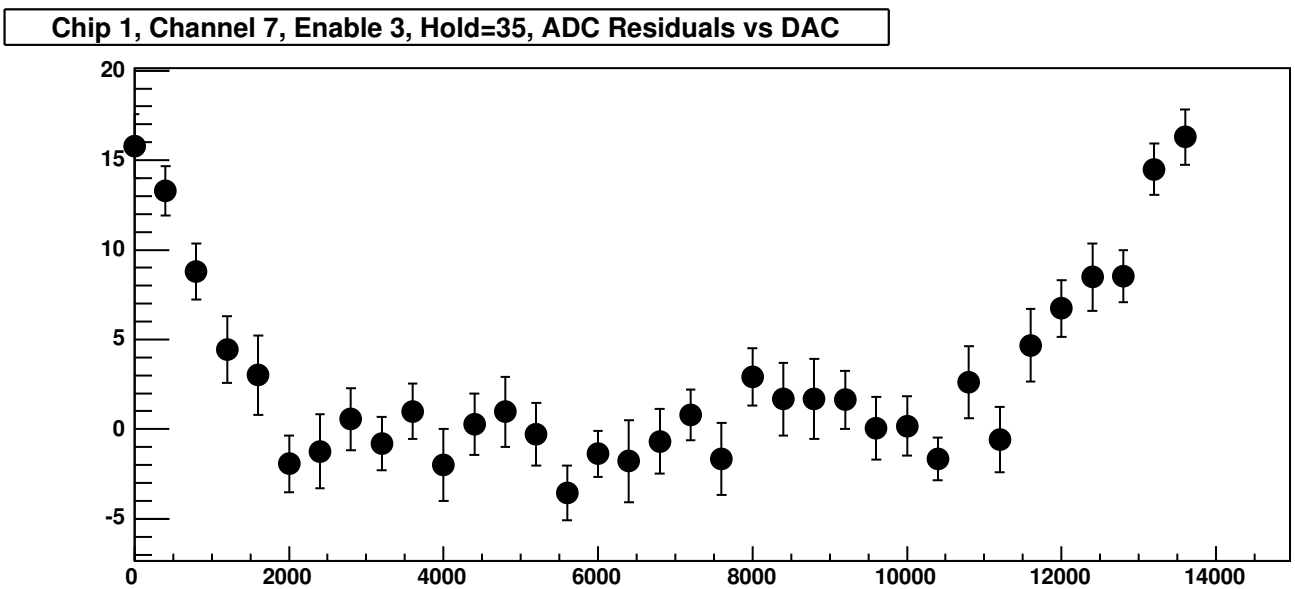

Chip 1, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 411.1 / 23
p0 -633.1 ± 2.644
p1 2.322 ± 0.00131
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

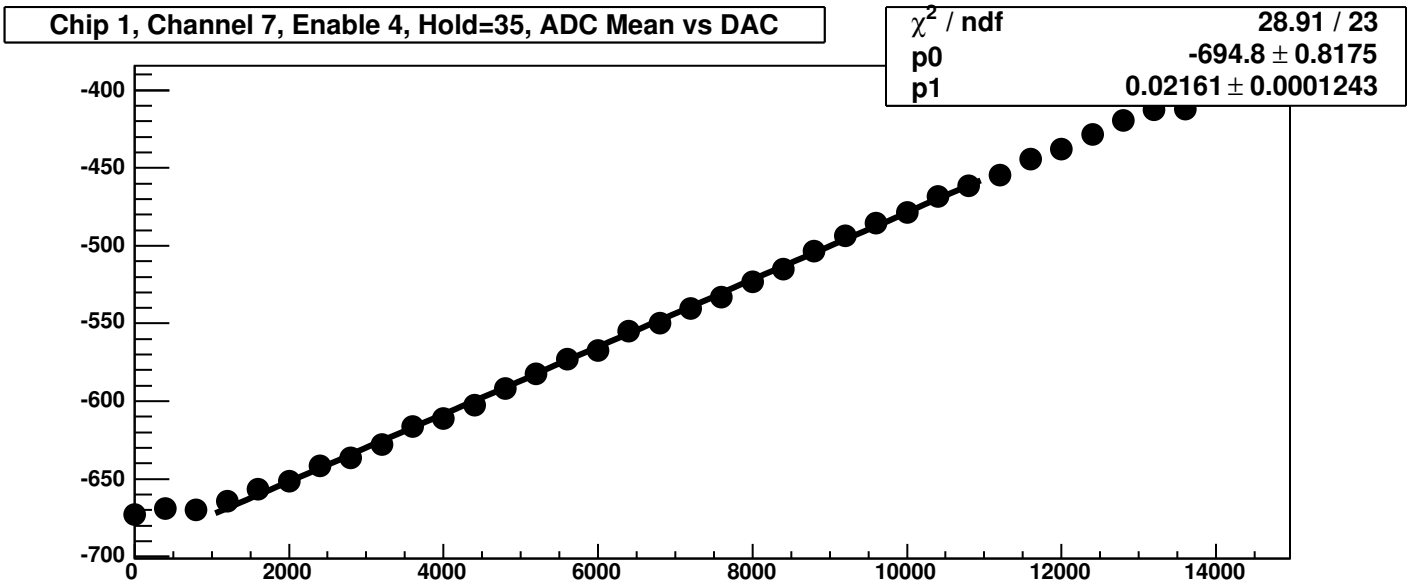

Chip 1, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

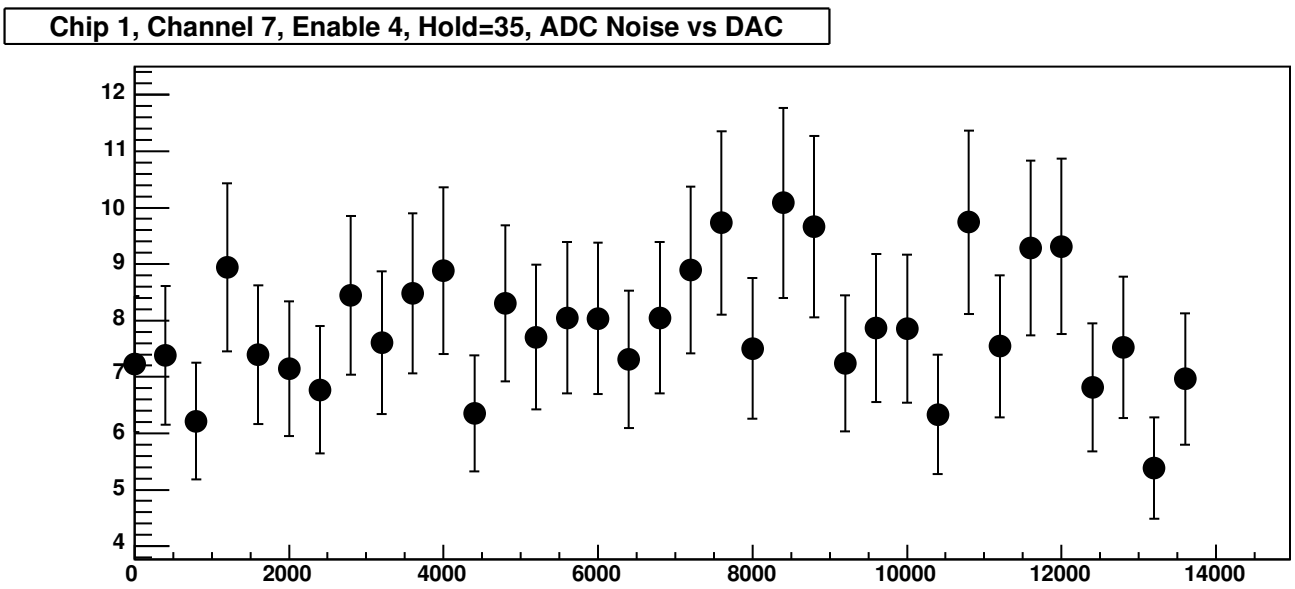

Chip 1, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

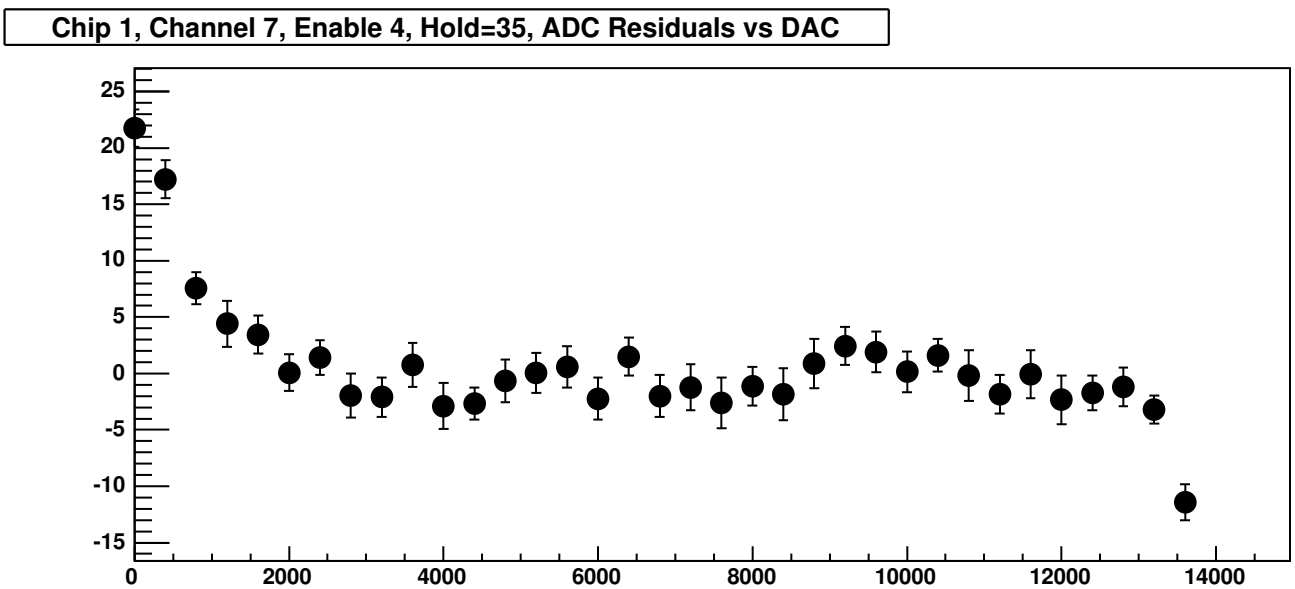

Chip 1, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.97 / 23
p0 -692.8 ± 0.8091
p1 0.01682 ± 0.0001189
-450
-500
-550
-600
-650
-700
0
2000
4000
6000
8000
10000
12000
14000

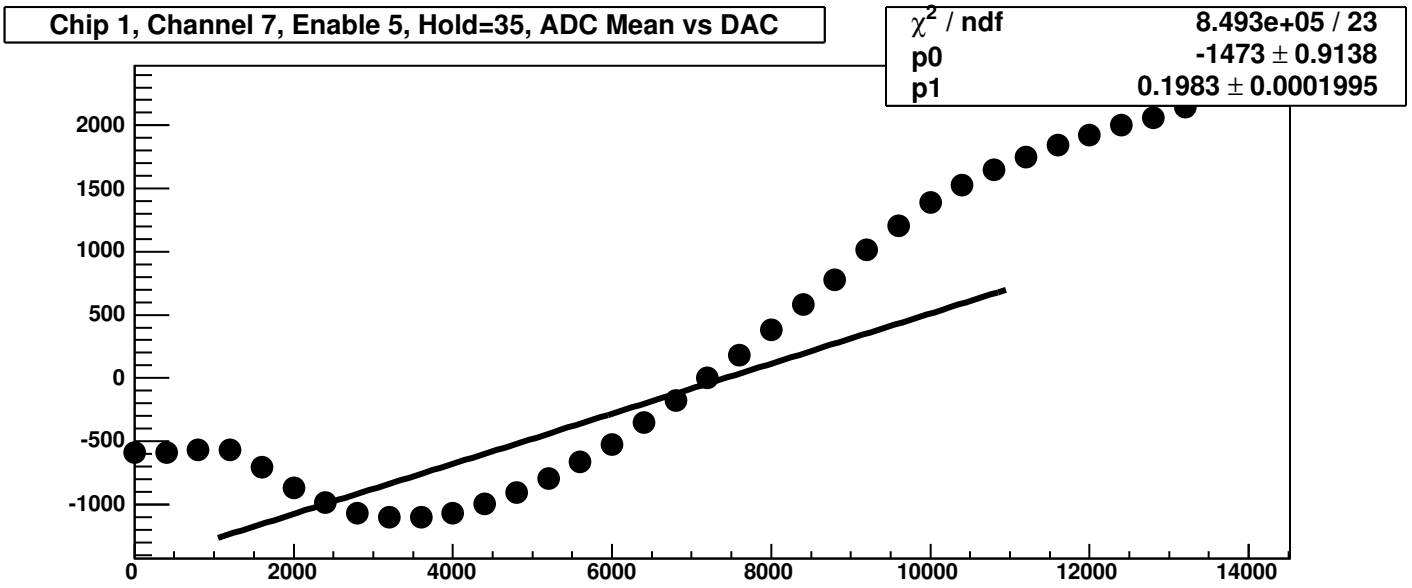

Chip 1, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

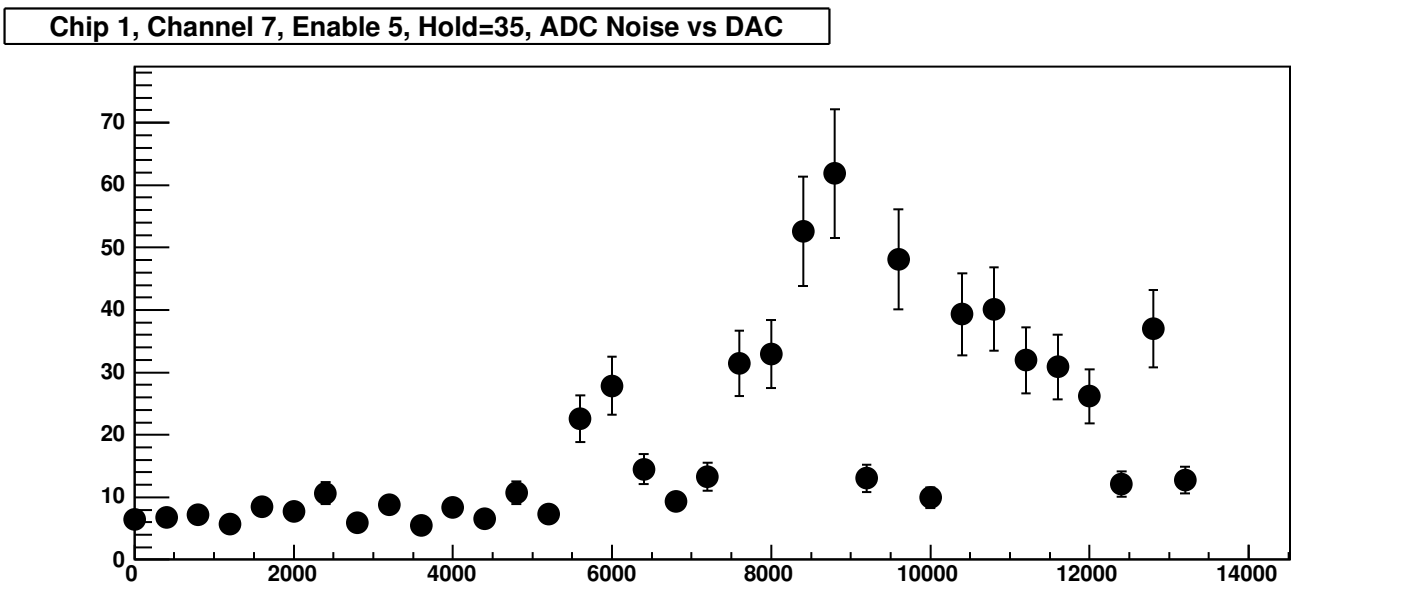

Chip 1, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

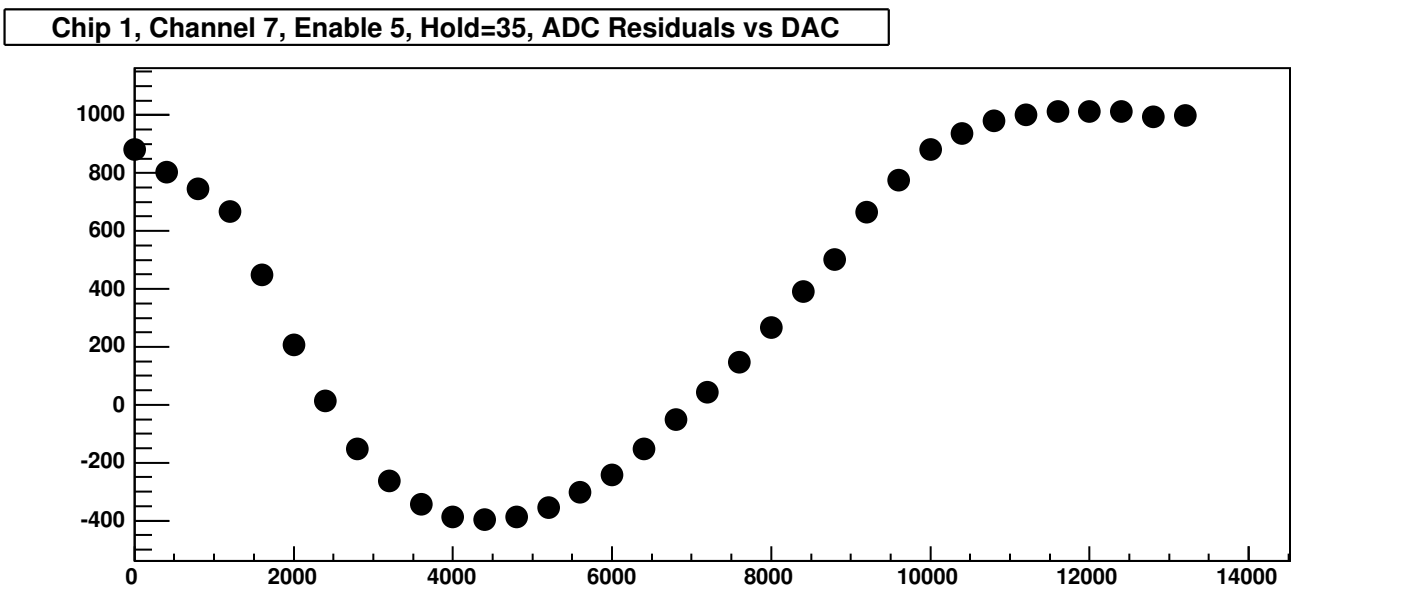

Chip 1, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 28.91 / 23
p0 -694.8 ± 0.8175
p1 0.02161 ± 0.0001243
-400
-450
-500
-550
-600
-650
-700
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

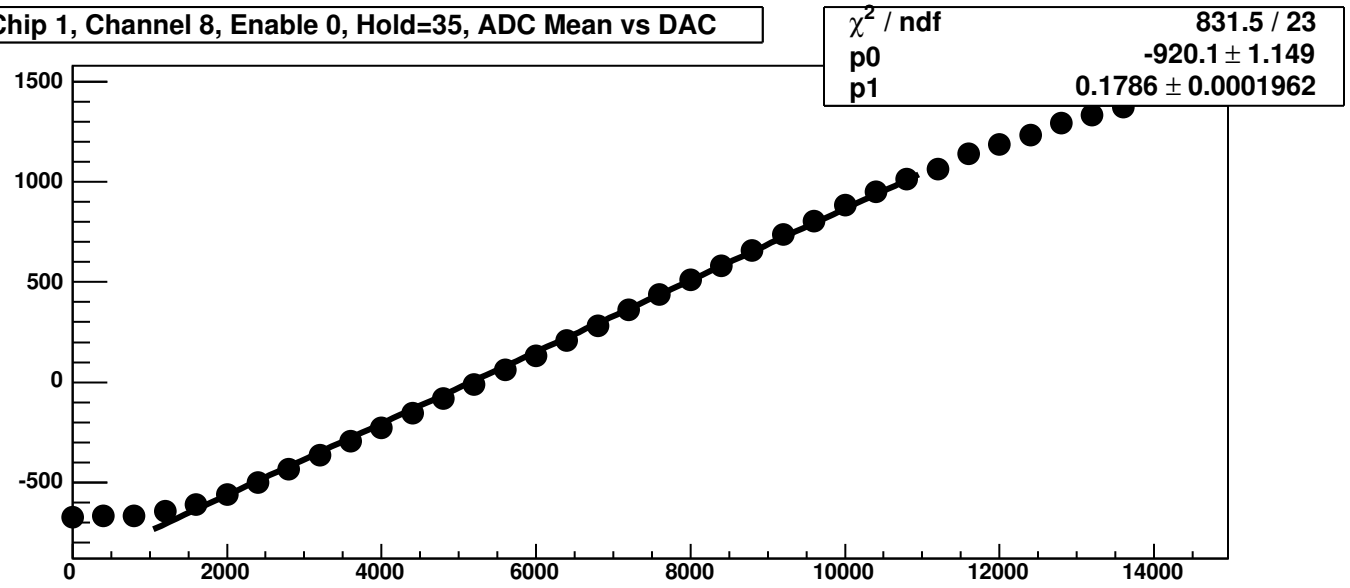

Chip 1, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 8.493e+05 / 23
p0 -1473 ± 0.9138
p1 0.1983 ± 0.0001995
2000
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

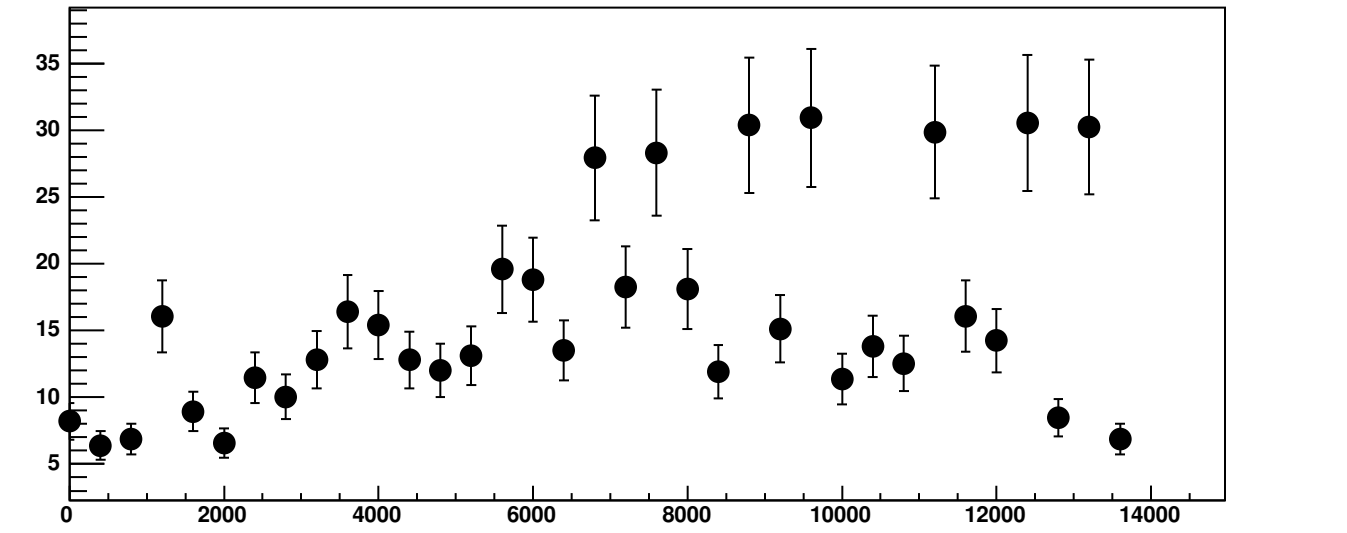

Chip 1, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

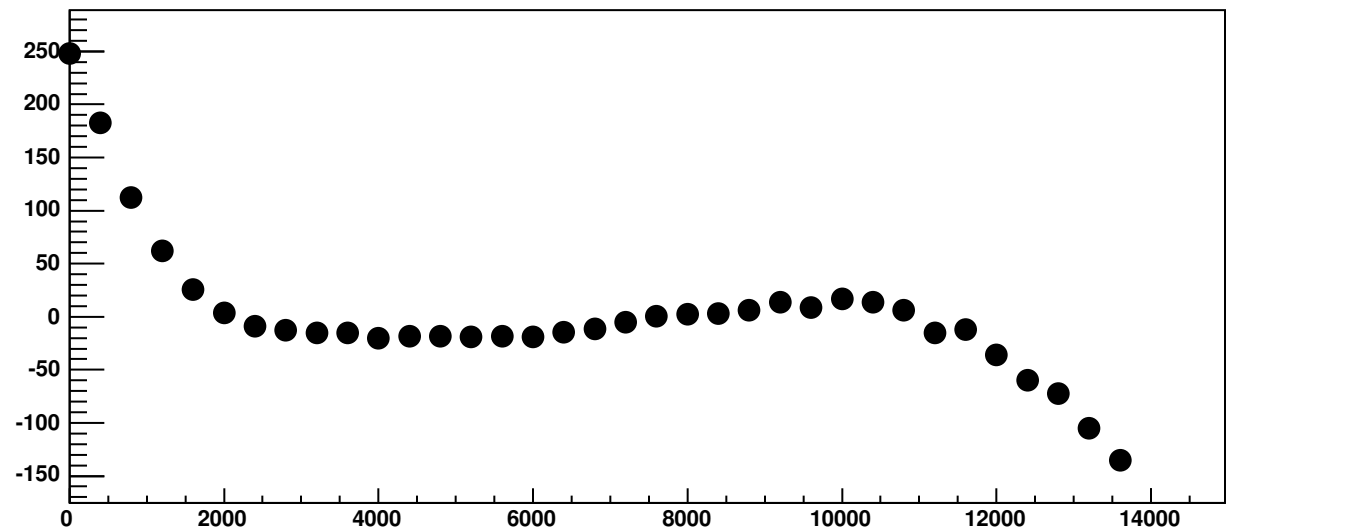
Chip 1, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



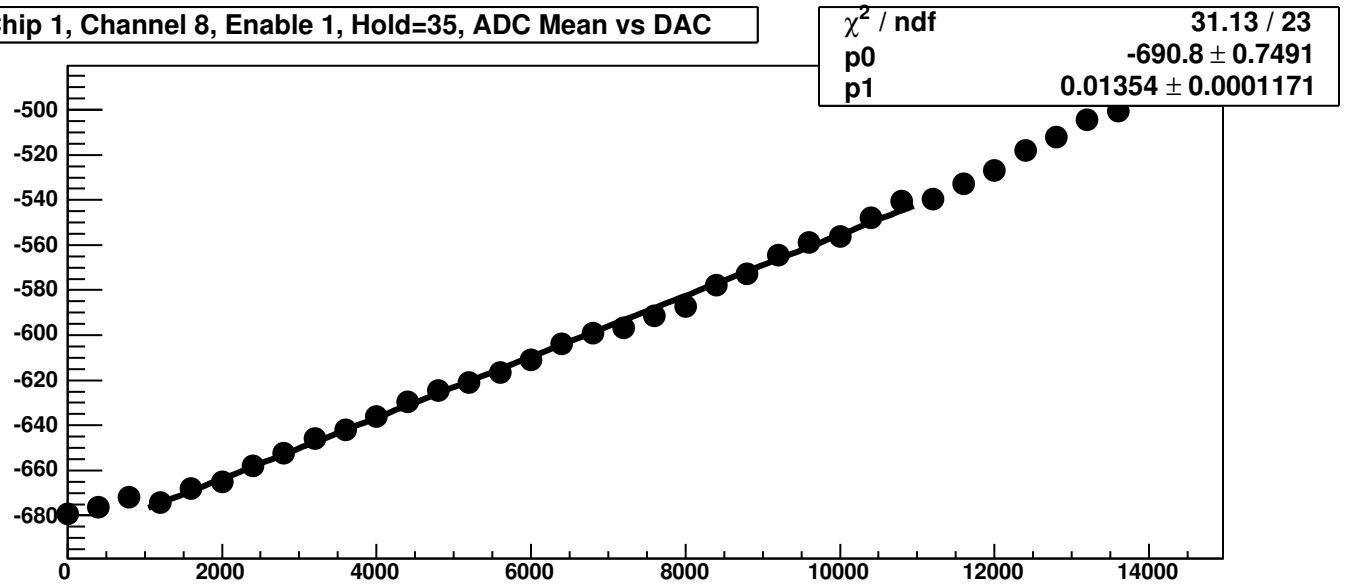
Chip 1, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



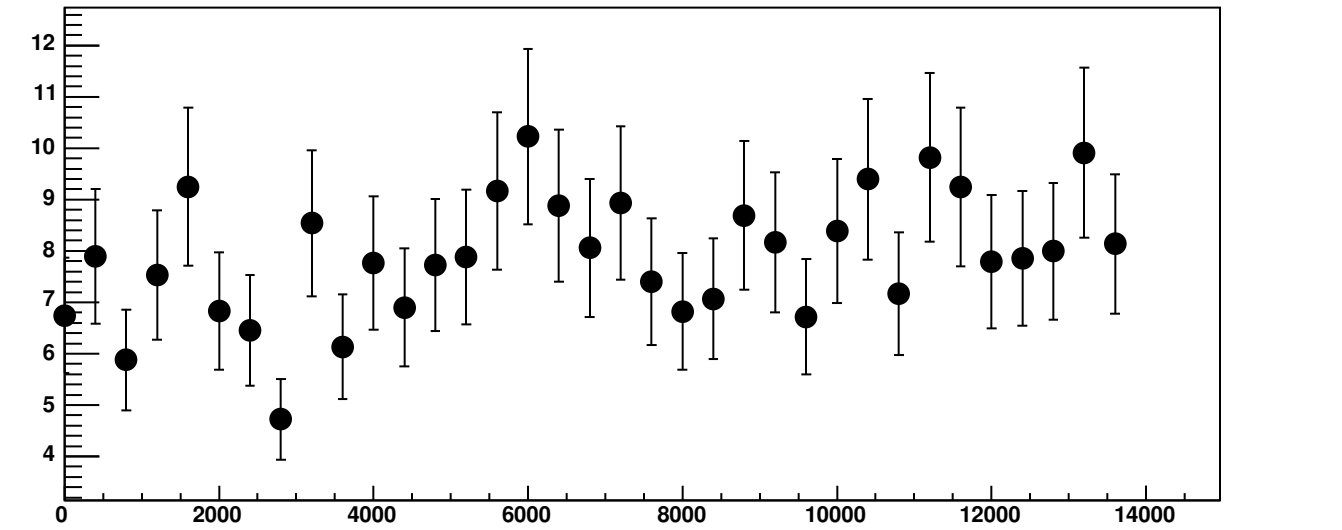
Chip 1, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



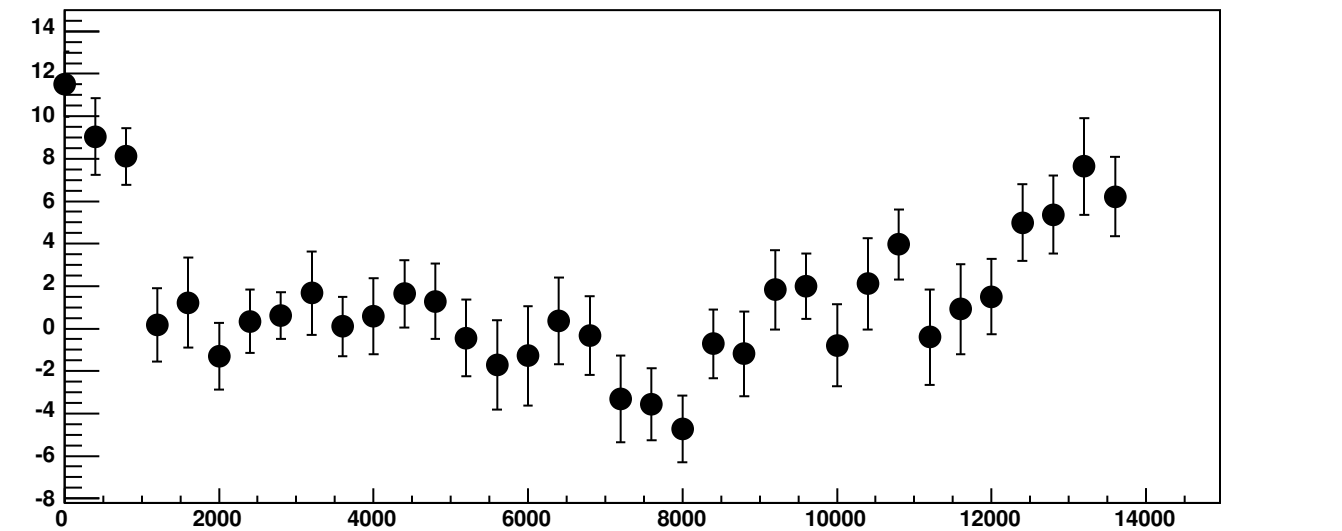
Chip 1, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC



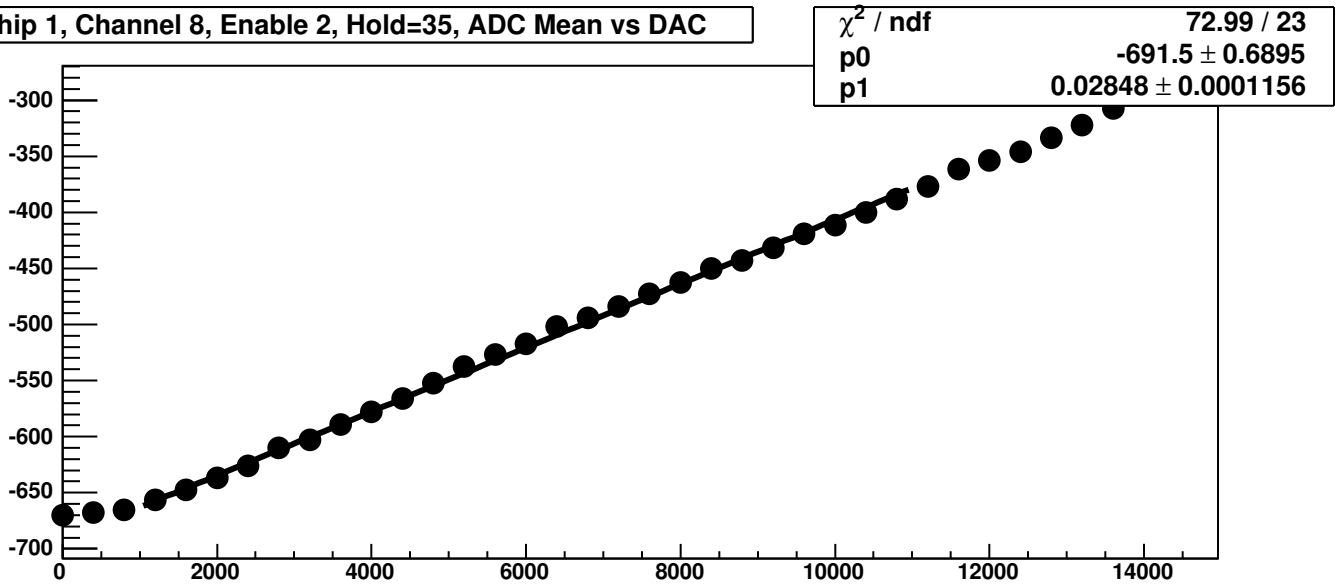
Chip 1, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC



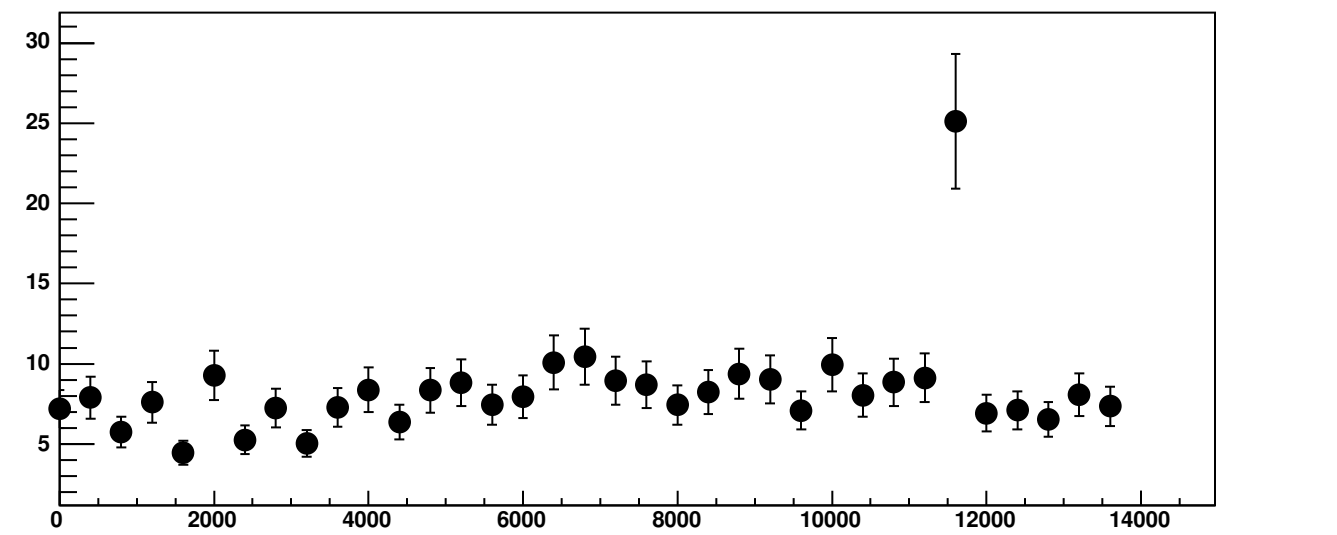
Chip 1, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC



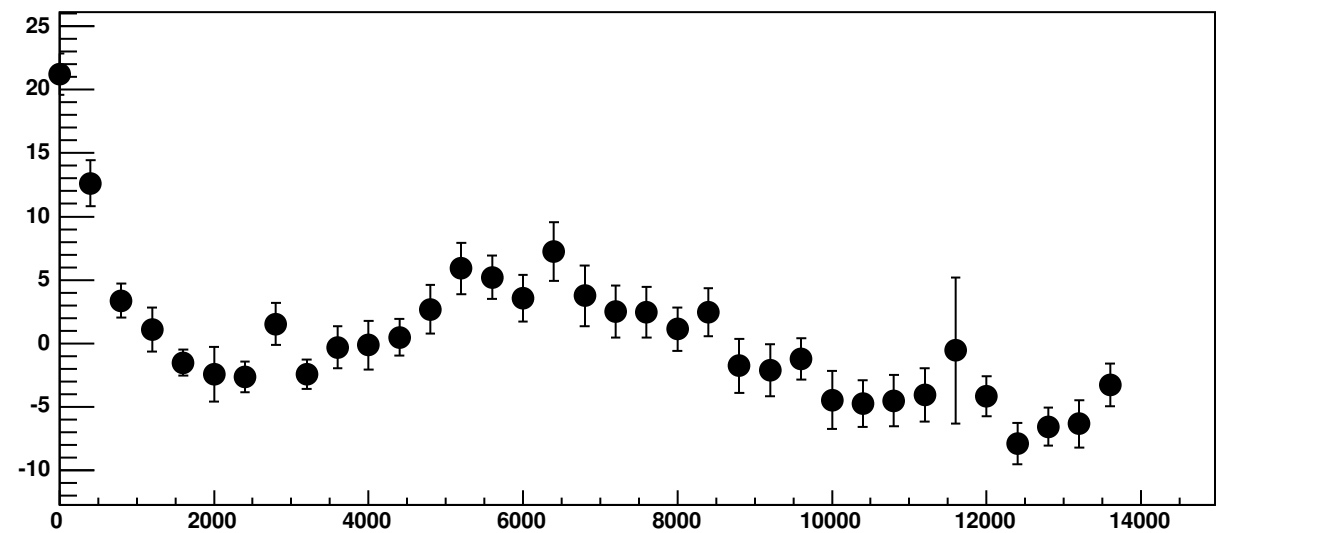
Chip 1, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC

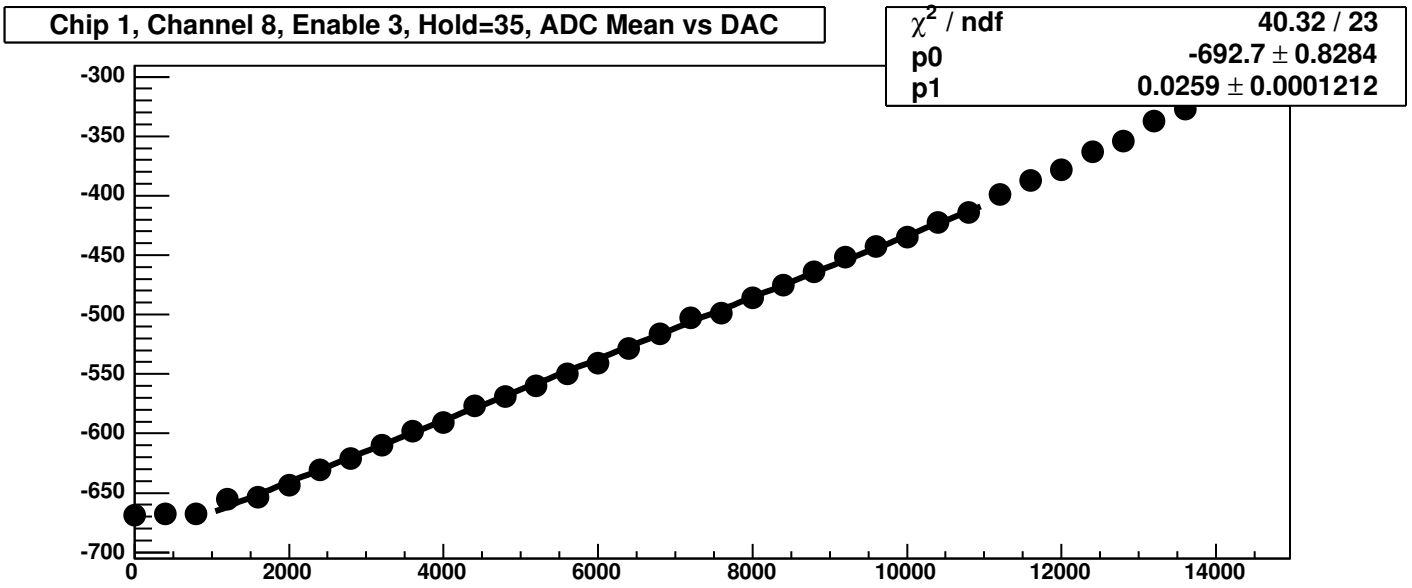


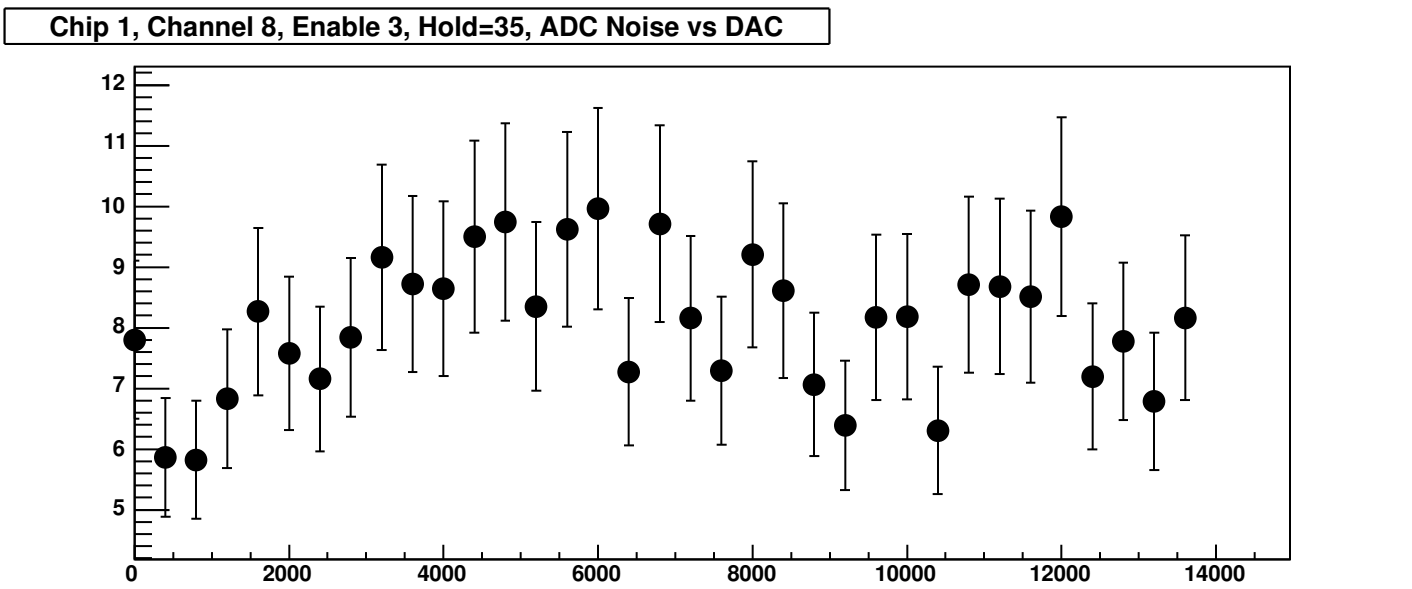
Chip 1, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC

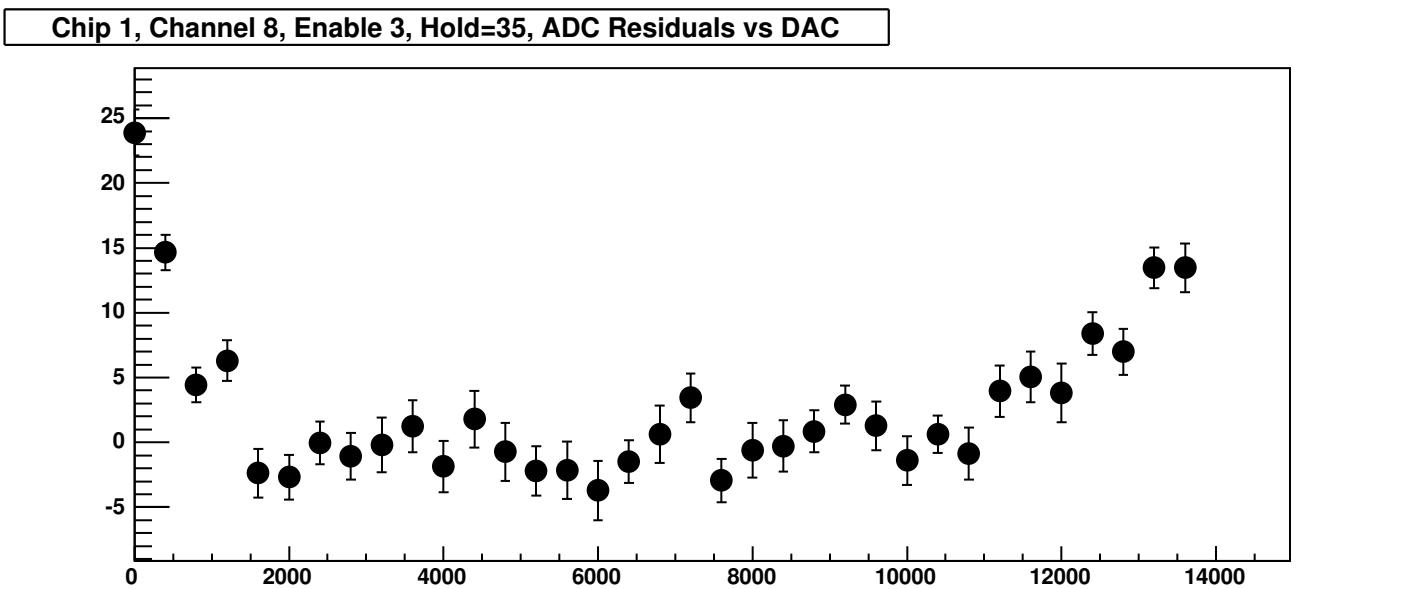


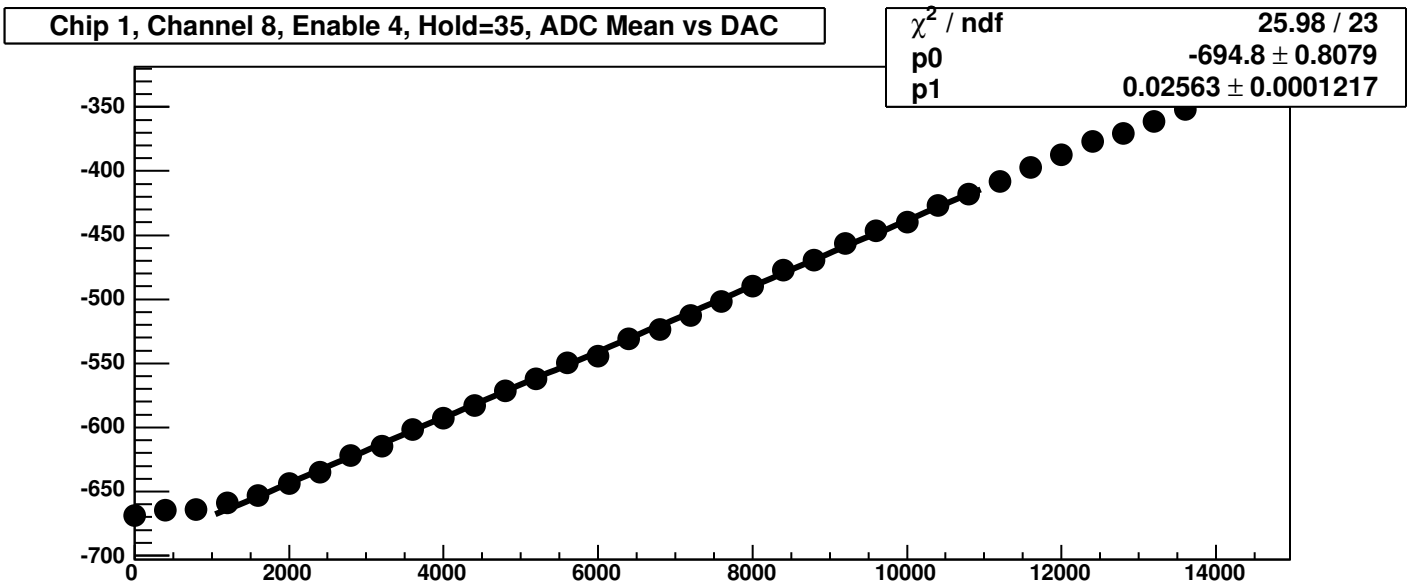
Chip 1, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC

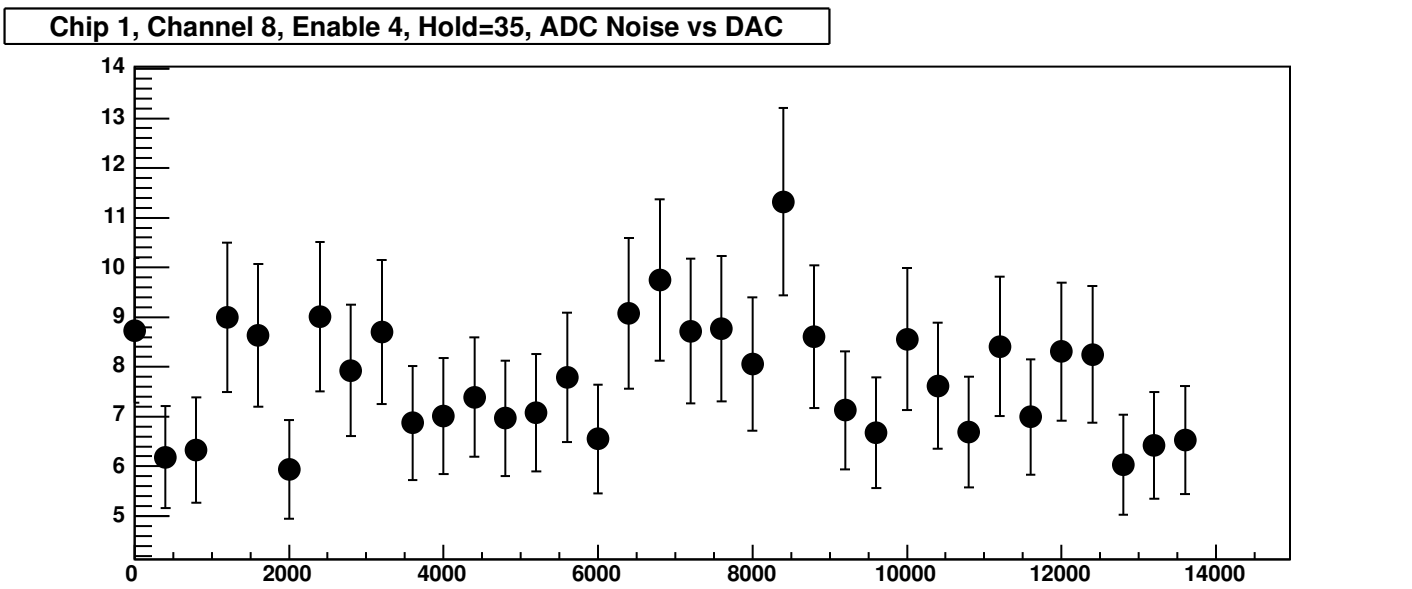


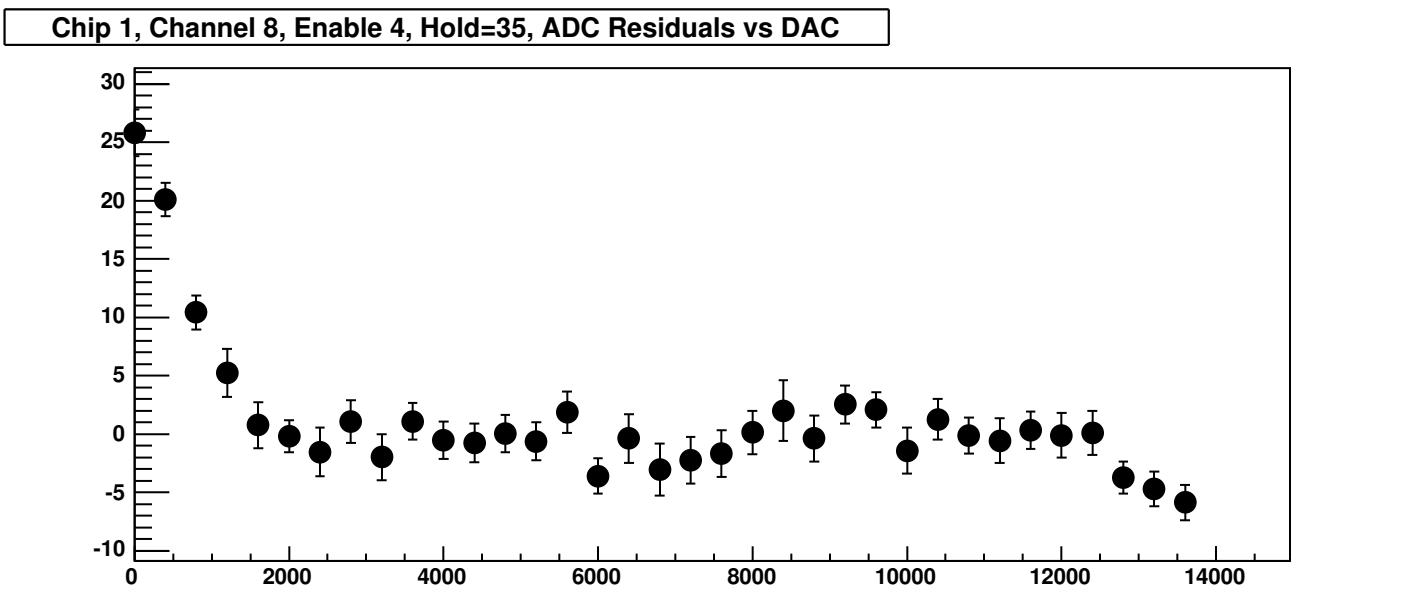

Chip 1, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 40.32 / 23
p0 -692.7 ± 0.8284
p1 0.0259 ± 0.0001212
-300
-350
-400
-450
-500
-550
-600
-650
-700
0
2000
4000
6000
8000
10000
12000
14000

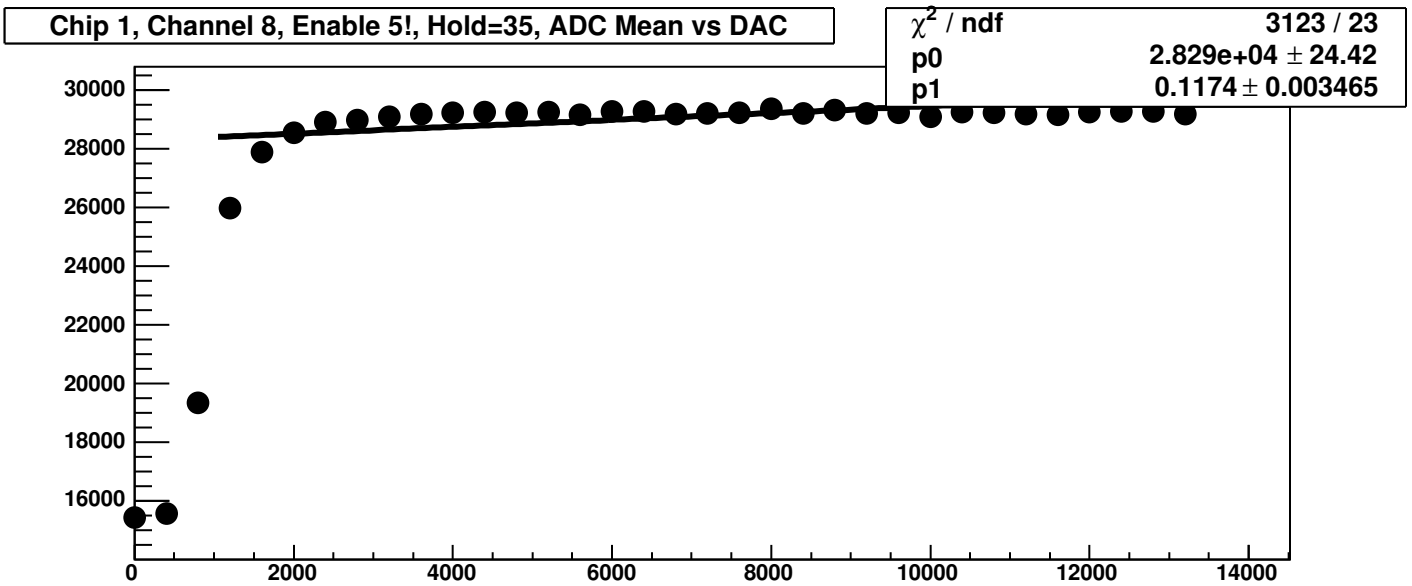

Chip 1, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

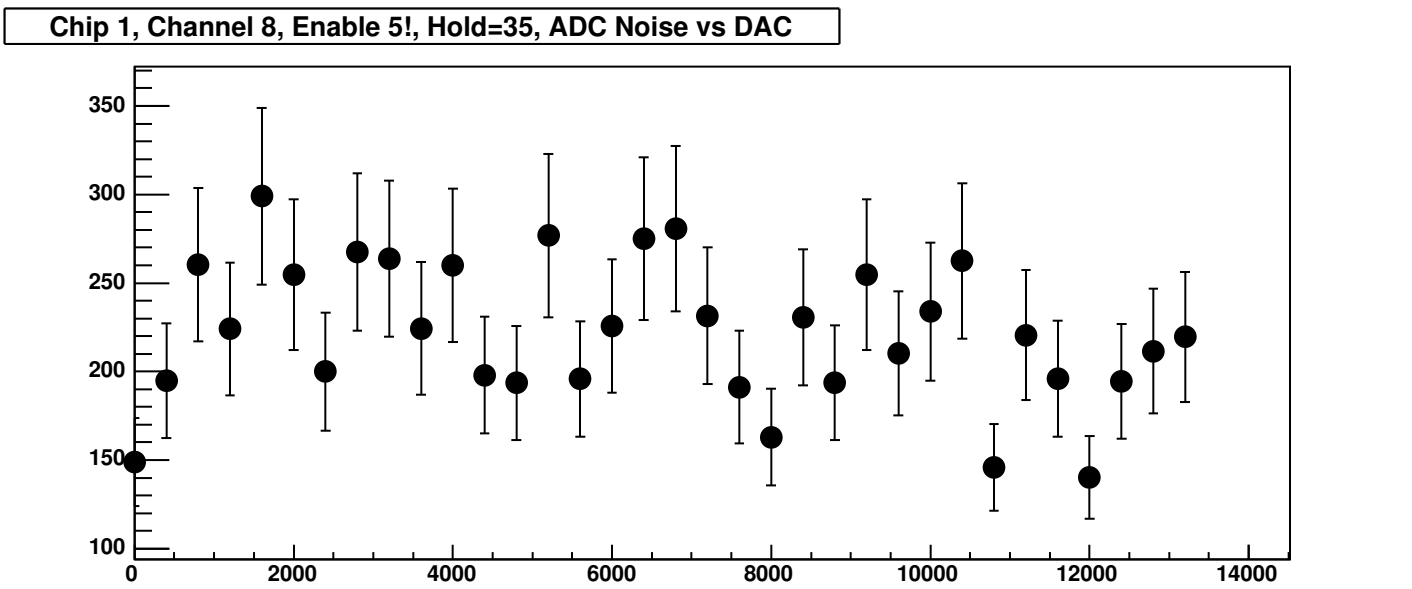

Chip 1, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

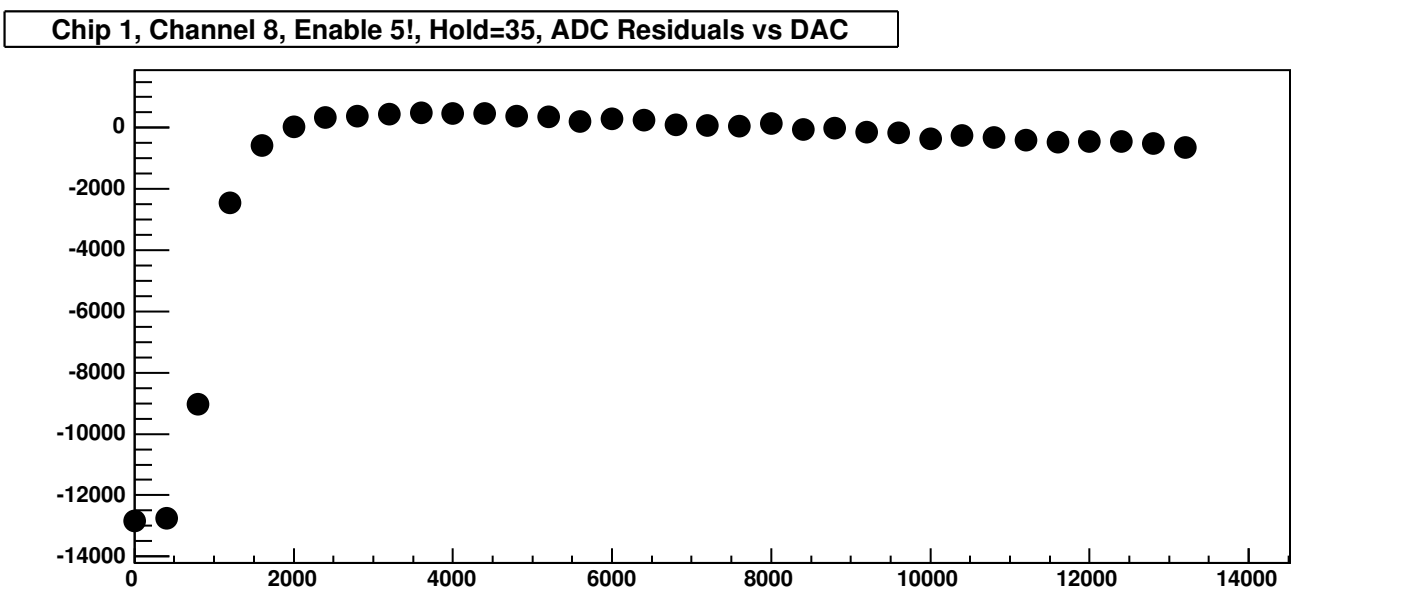

Chip 1, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 25.98 / 23
p0 -694.8 ± 0.8079
p1 0.02563 ± 0.0001217
-350
-400
-450
-500
-550
-600
-650
-700
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
14
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

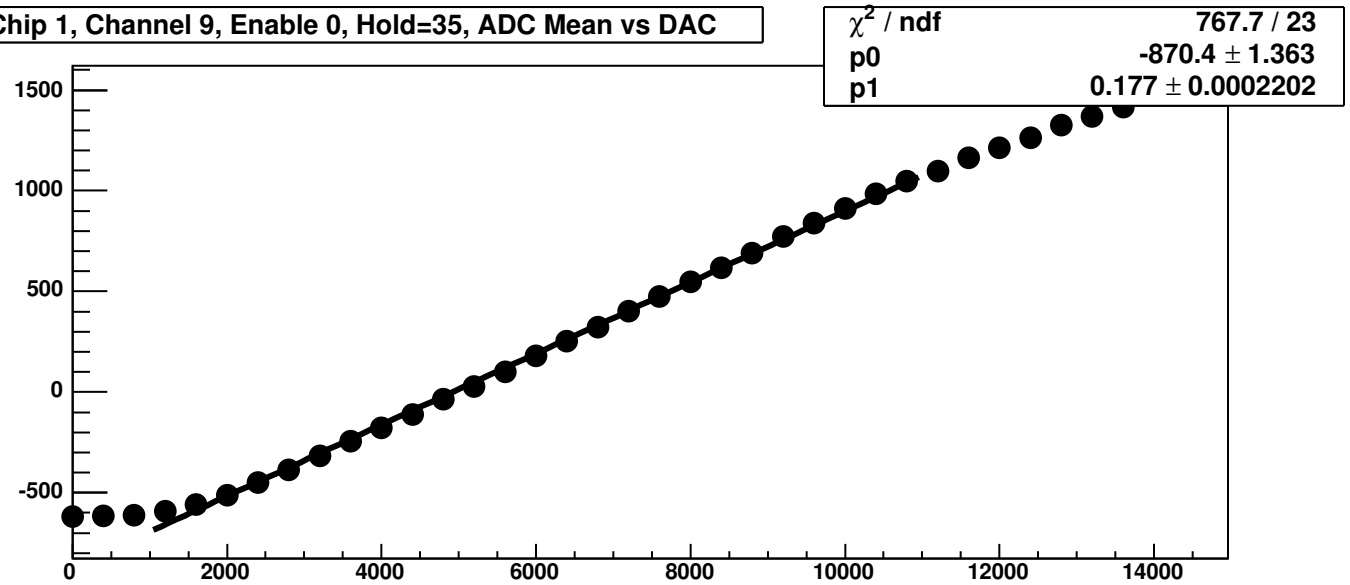

Chip 1, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3123 / 23
p0 2.829e+04 ± 24.42
p1 0.1174 ± 0.003465
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

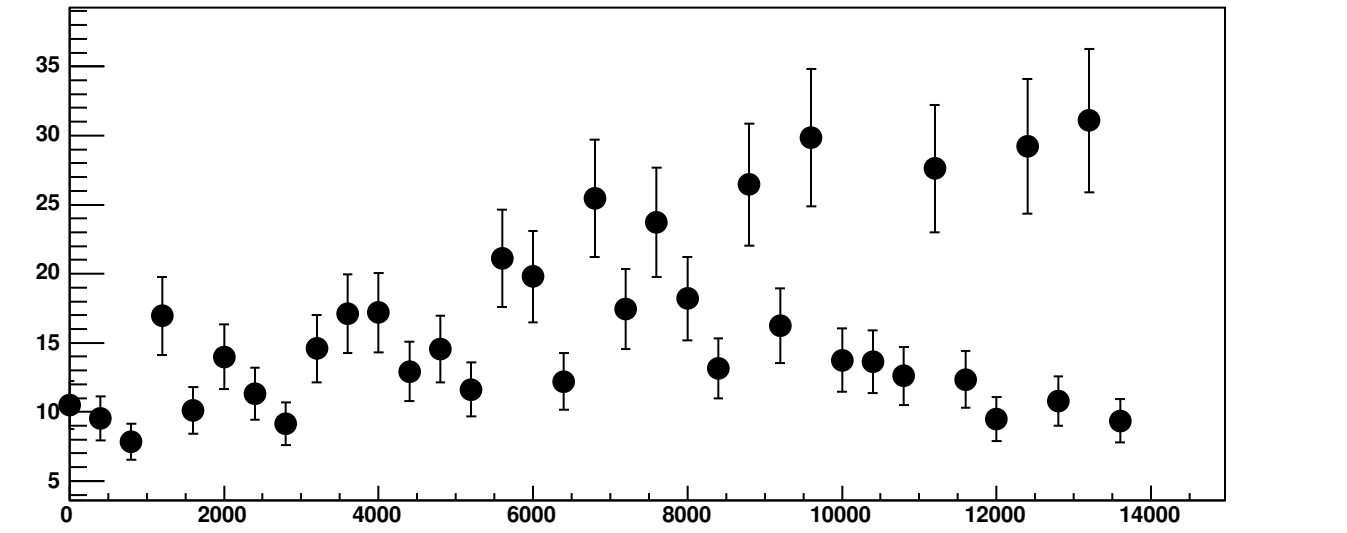

Chip 1, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

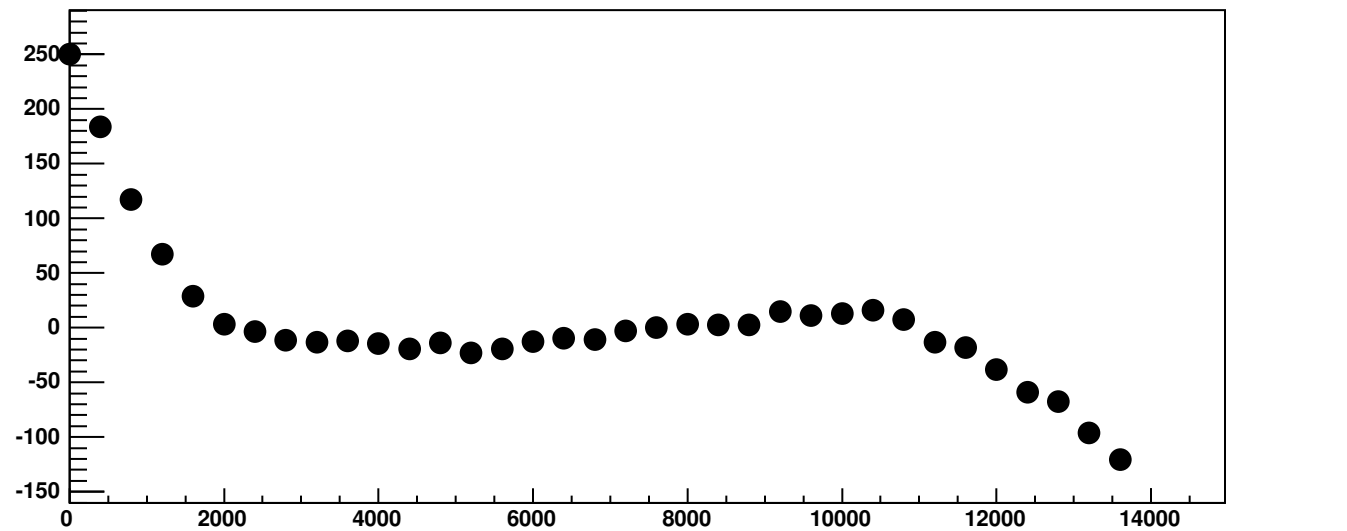
Chip 1, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

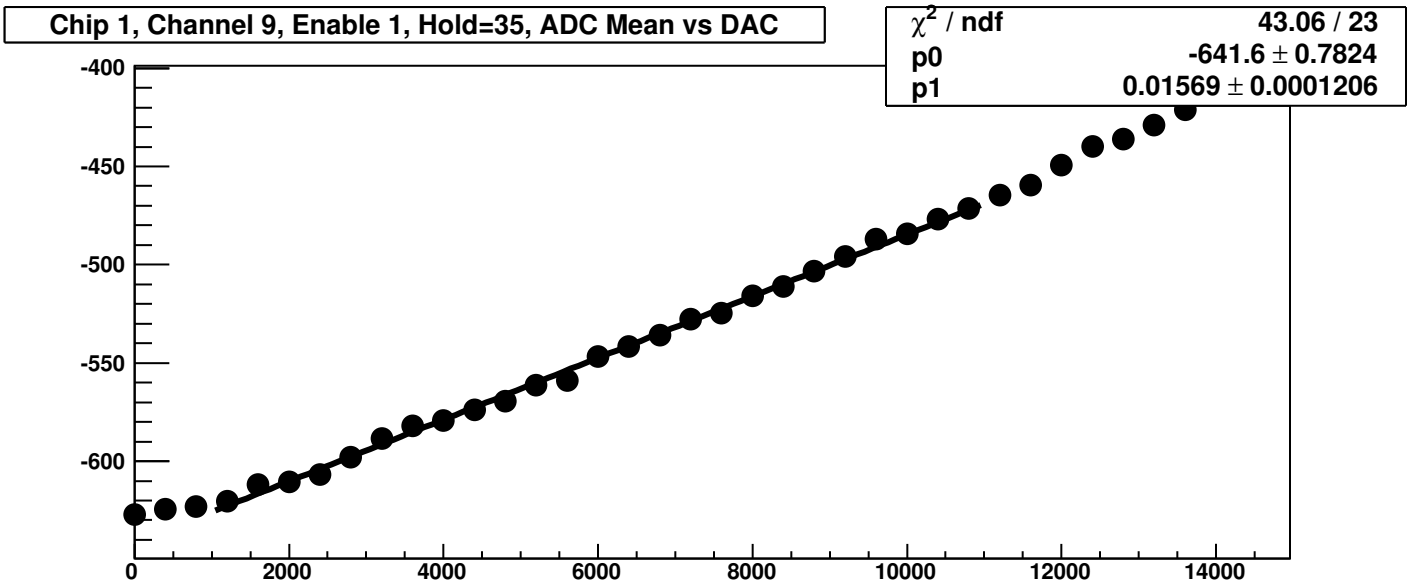


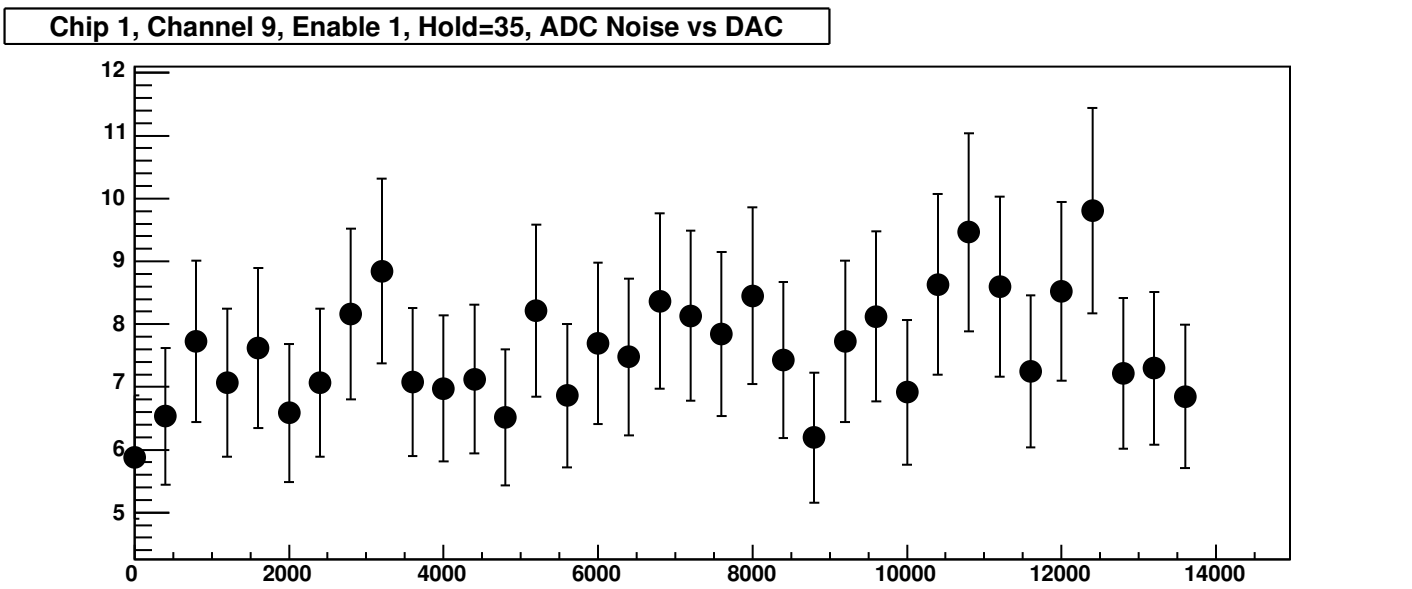
Chip 1, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC

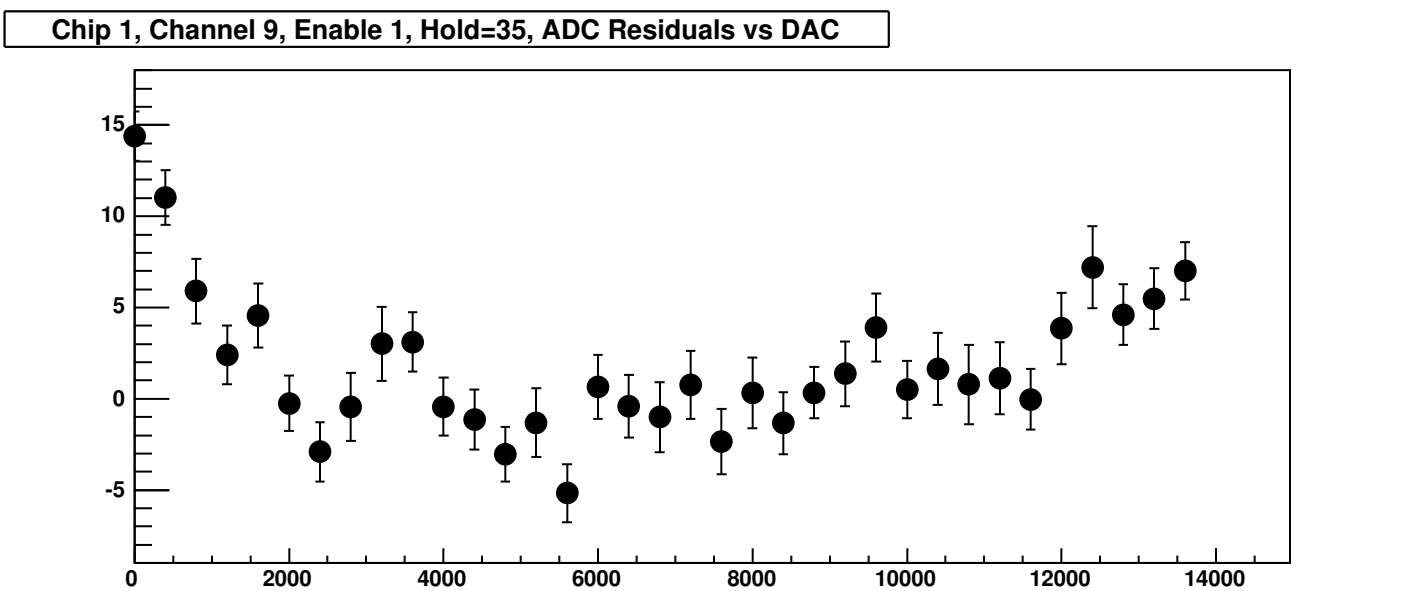


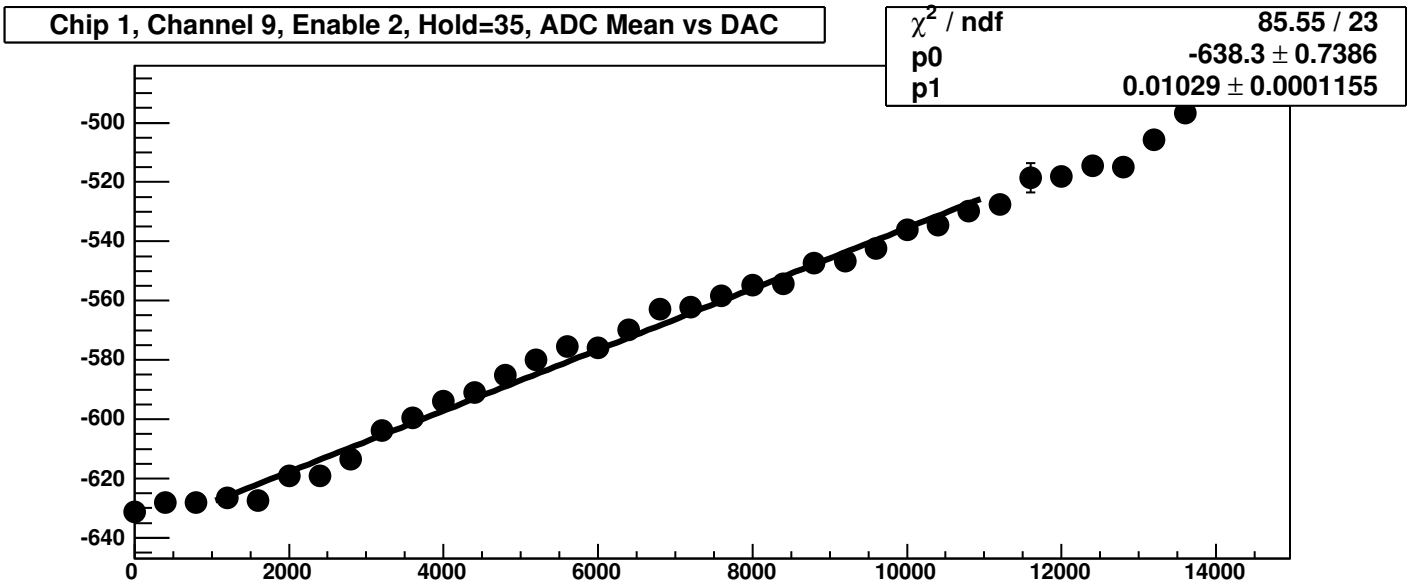
Chip 1, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

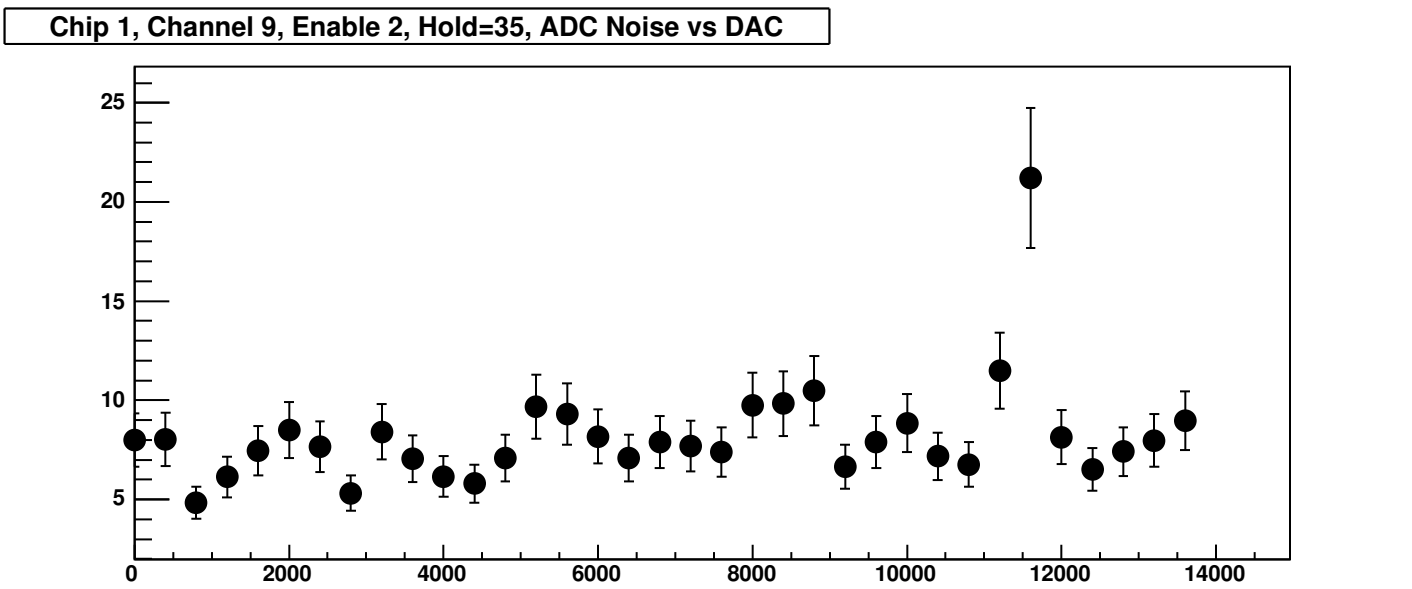


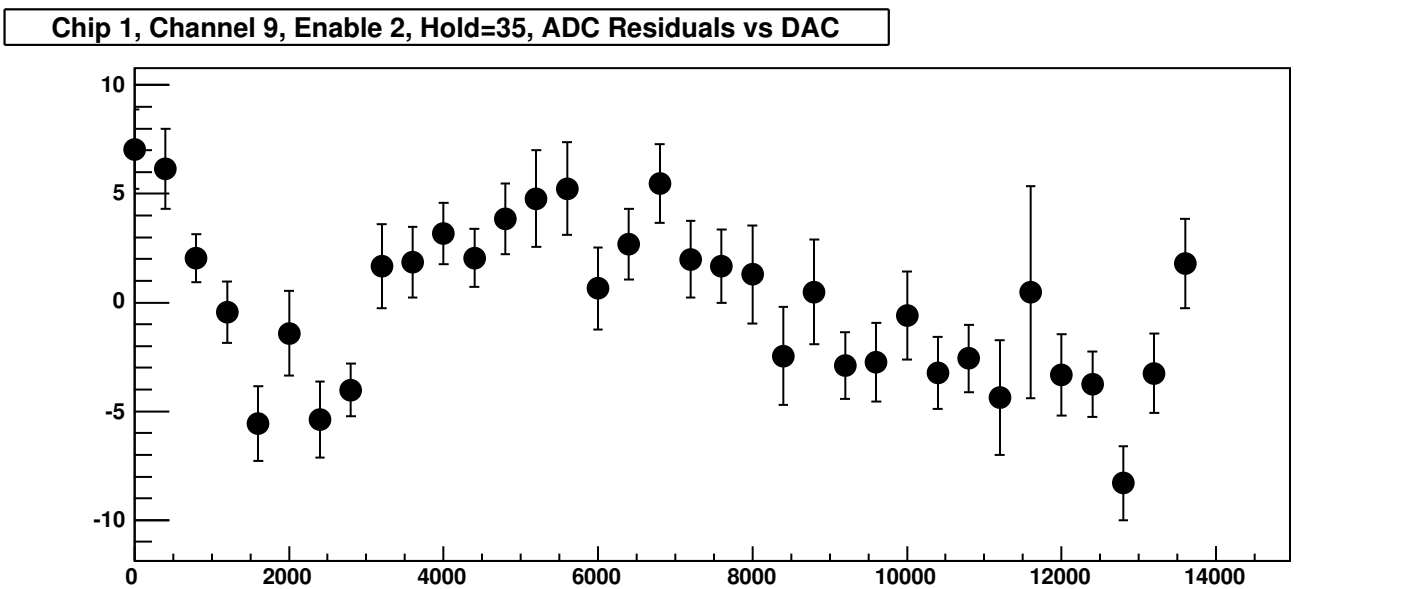

Chip 1, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 43.06 / 23
p0 -641.6 ± 0.7824
p1 0.01569 ± 0.0001206
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

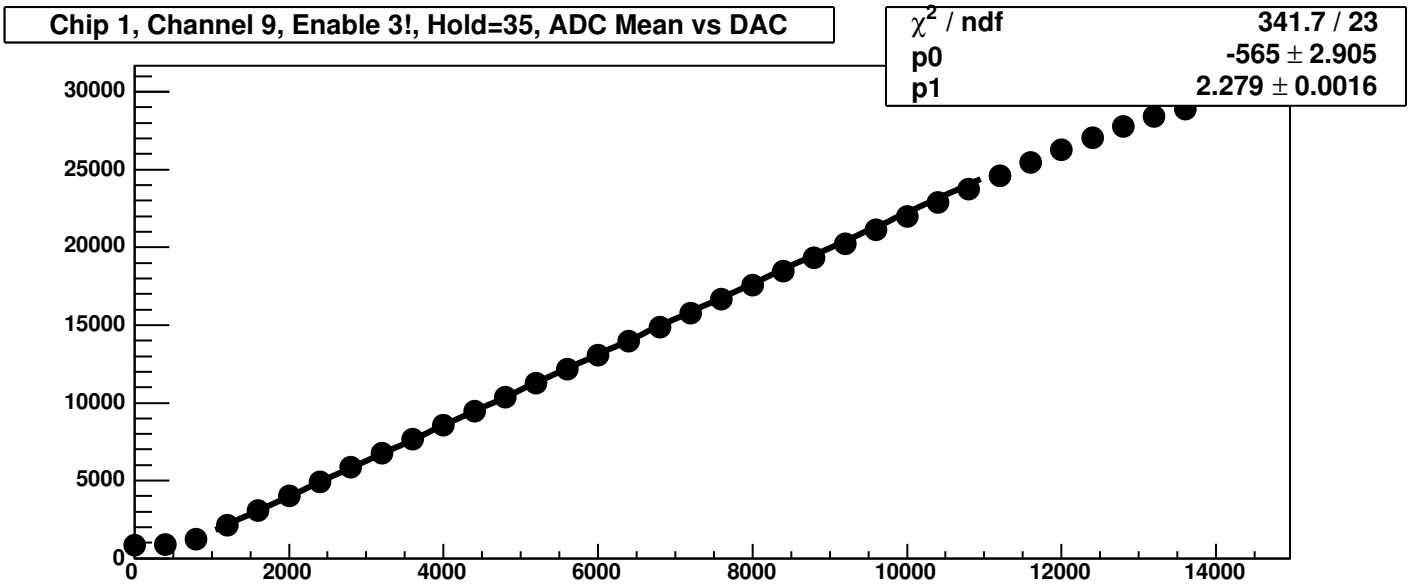

Chip 1, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

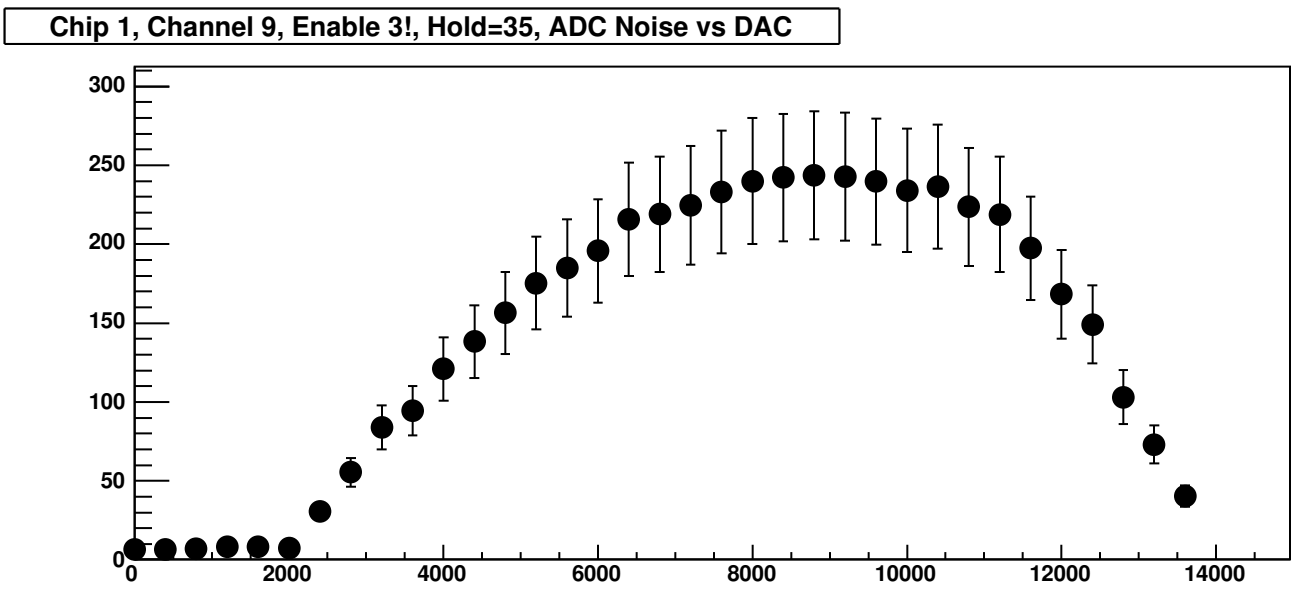

Chip 1, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

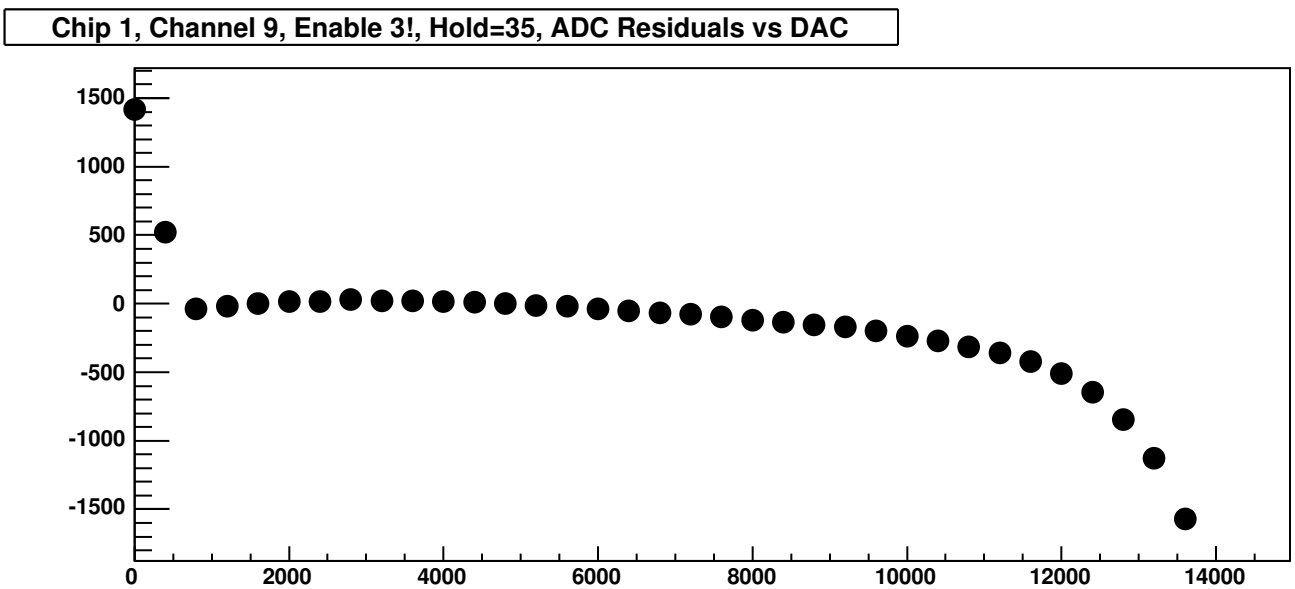

Chip 1, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 85.55 / 23
p0 -638.3 ± 0.7386
p1 0.01029 ± 0.0001155
-500
-520
-540
-560
-580
-600
-620
-640
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

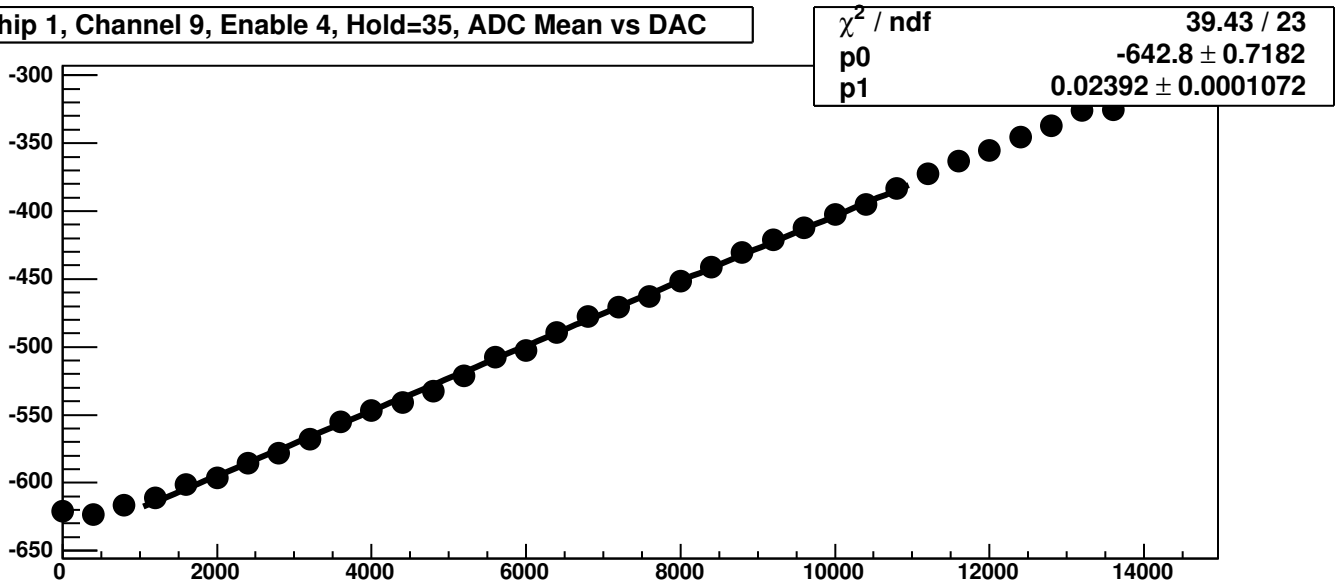

Chip 1, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 341.7 / 23
p0 -565 ± 2.905
p1 2.279 ± 0.0016
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

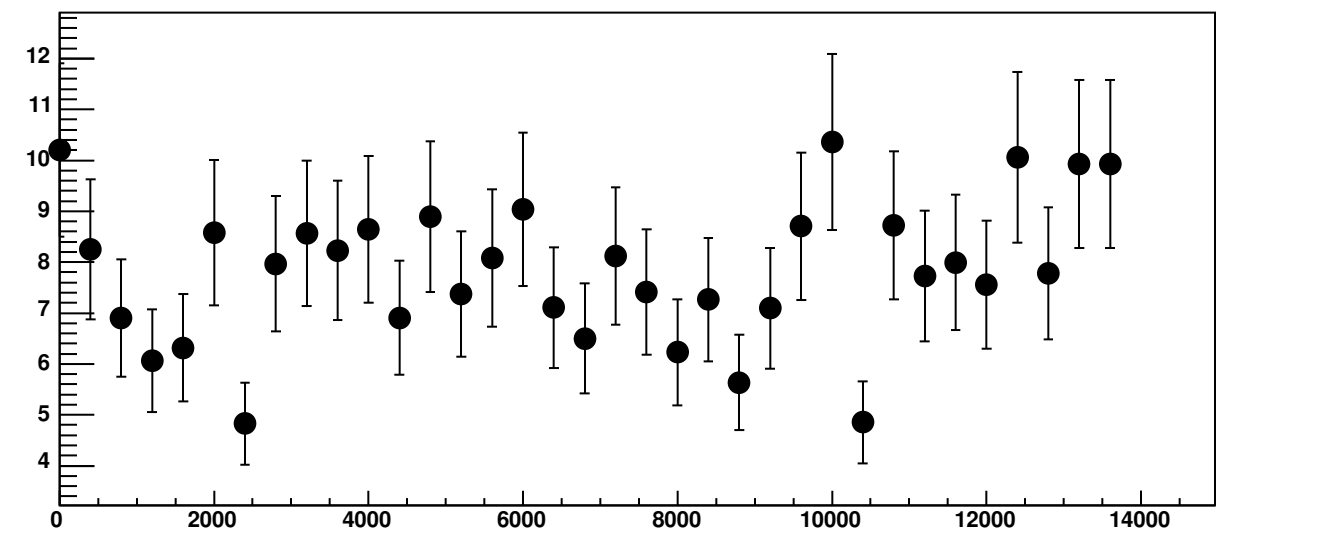

Chip 1, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

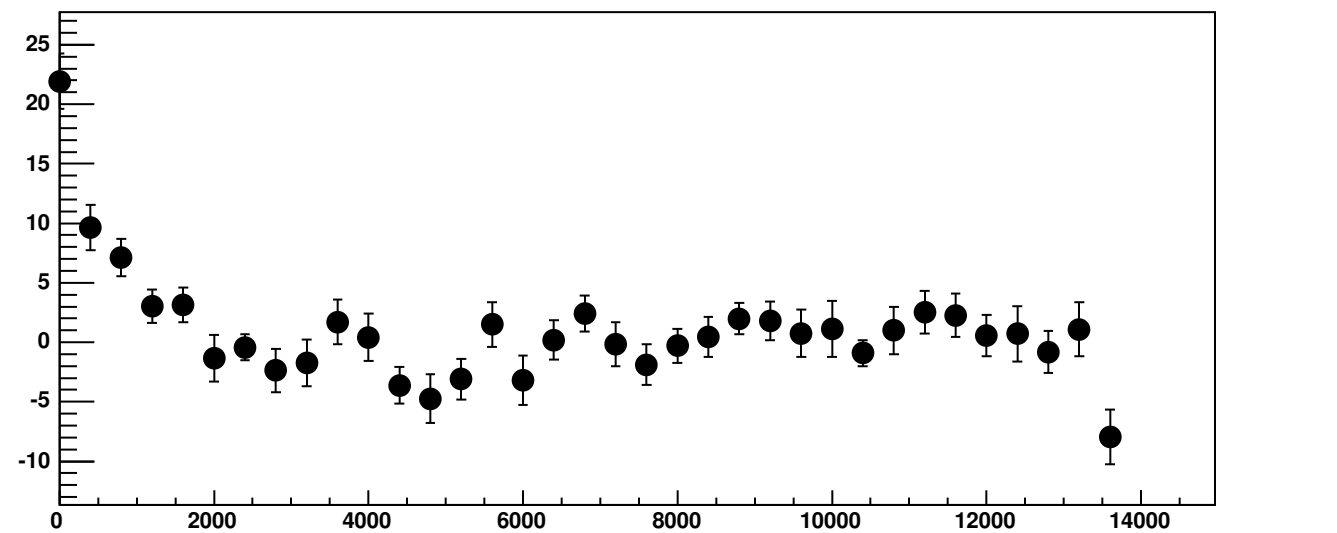
Chip 1, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

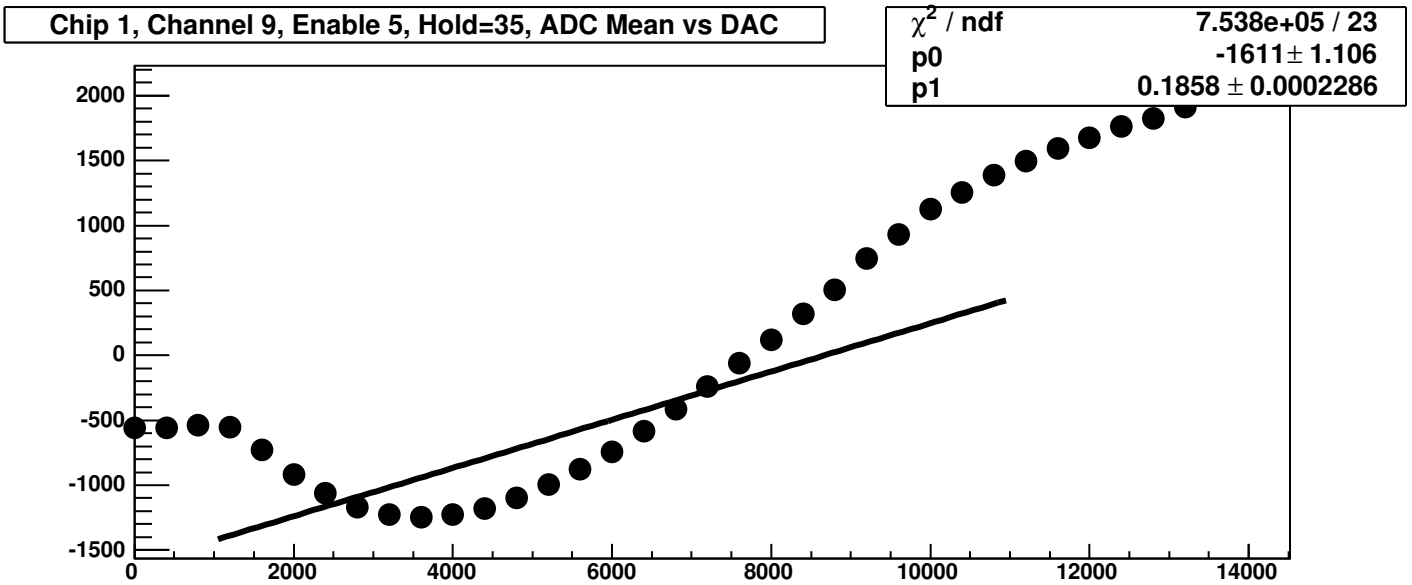


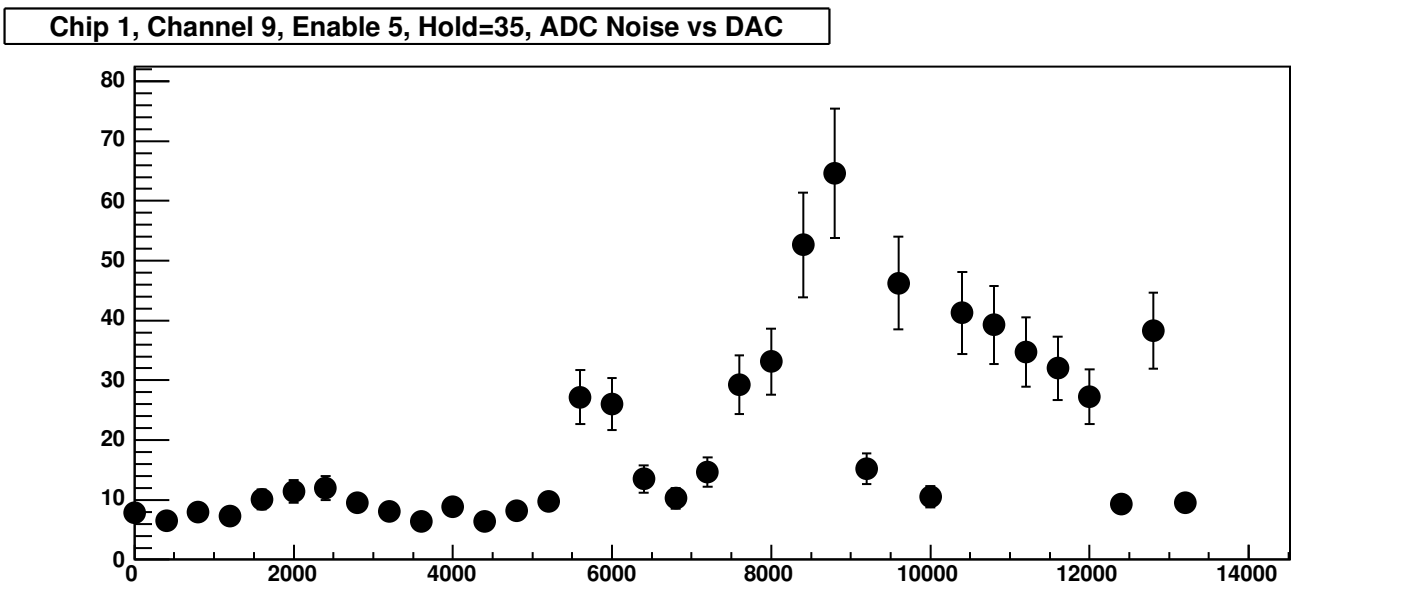
Chip 1, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC

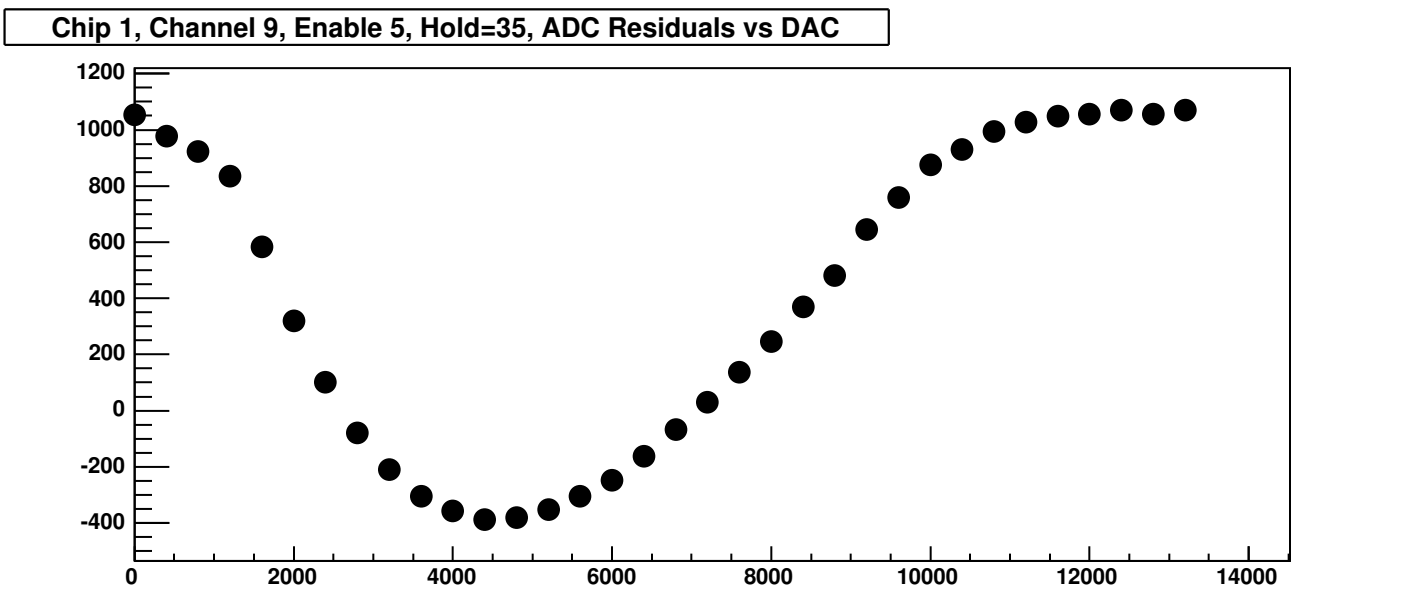


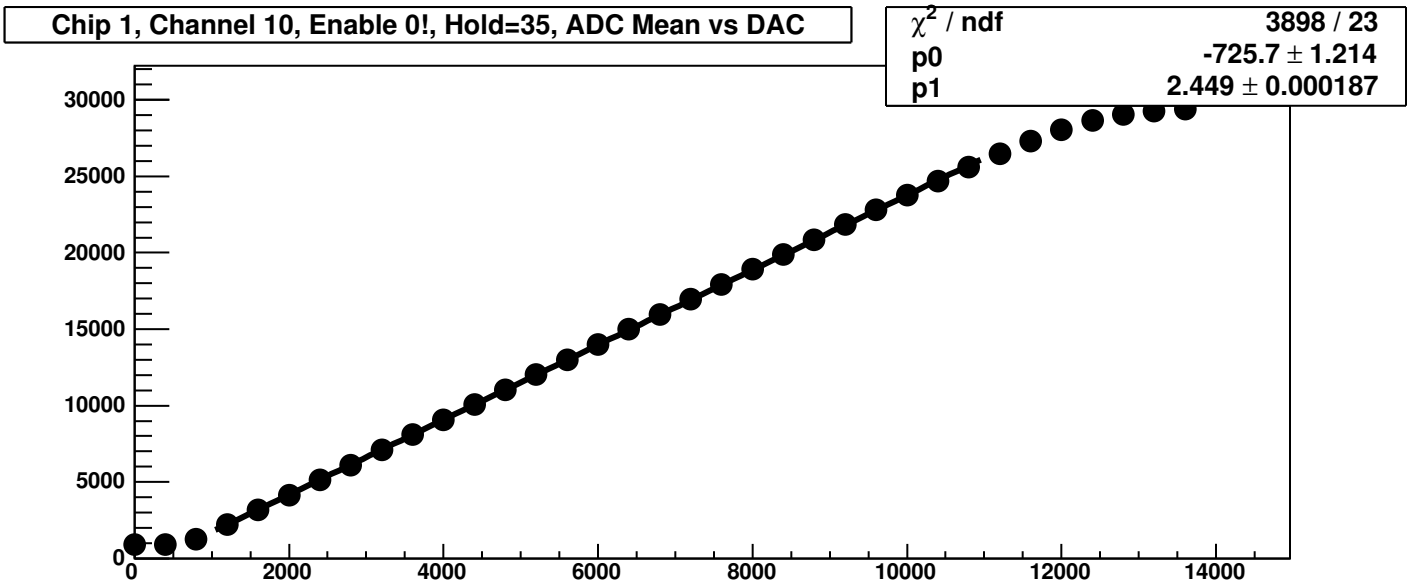
Chip 1, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

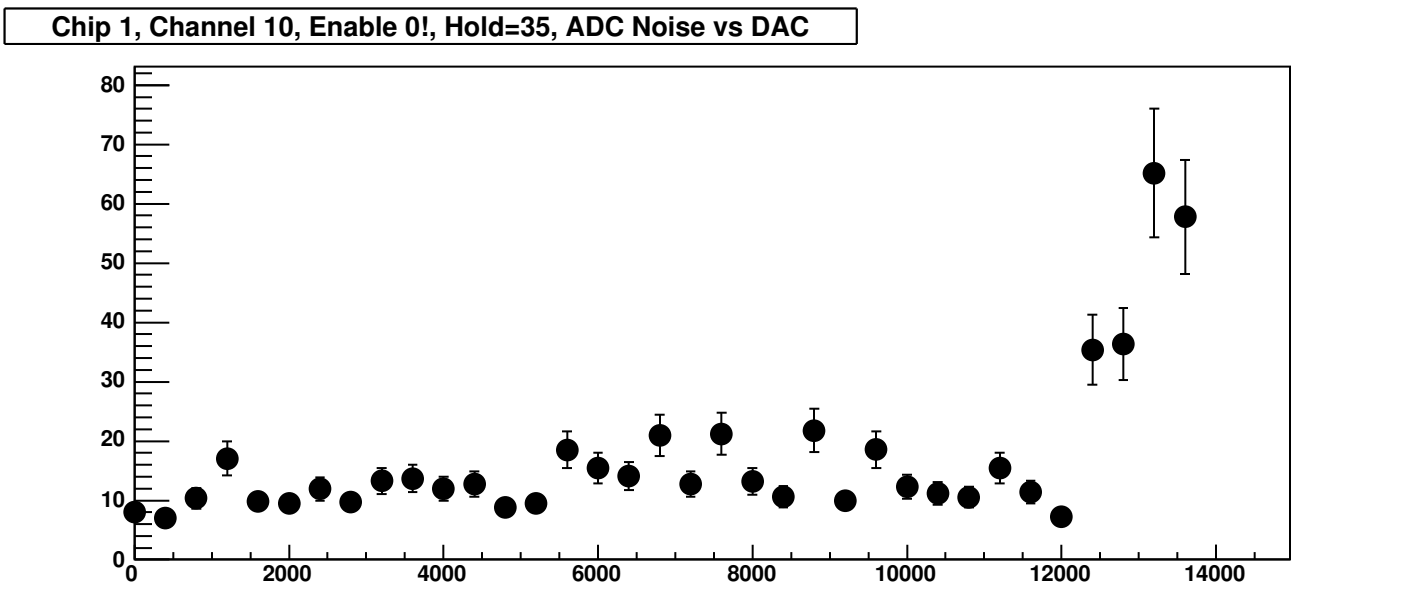


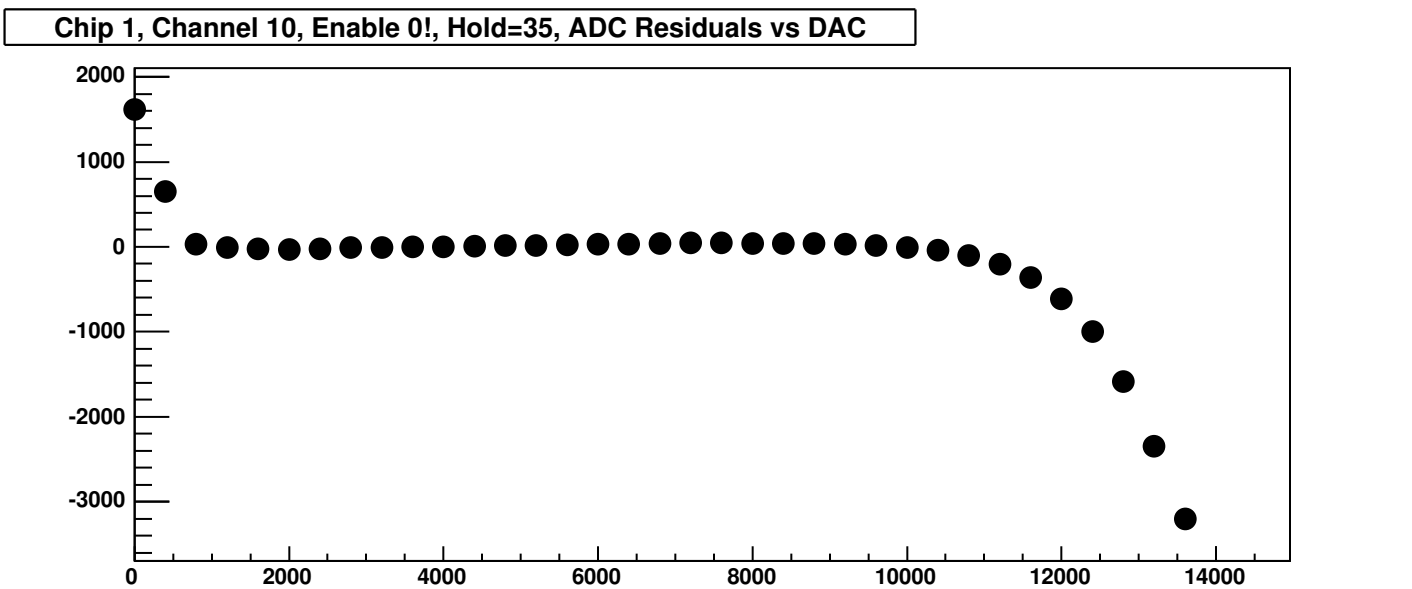

Chip 1, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 7.538e+05 / 23
p0 -1611 ± 1.106
p1 0.1858 ± 0.0002286
2000
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

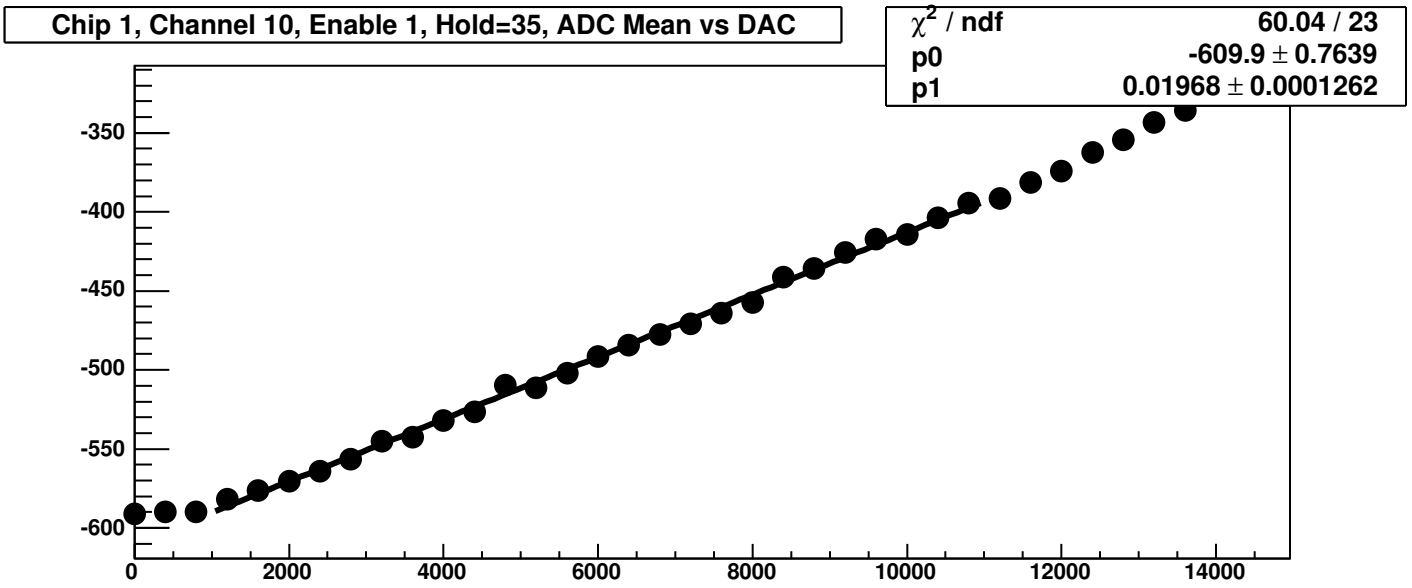

Chip 1, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

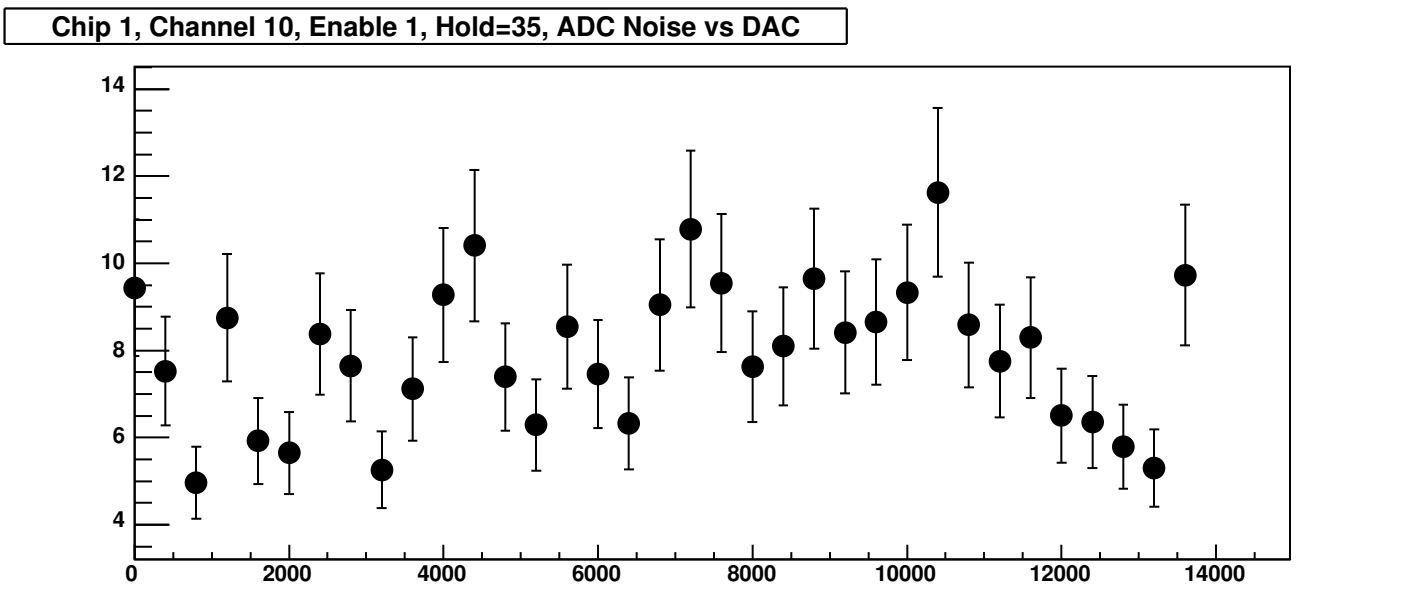

Chip 1, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

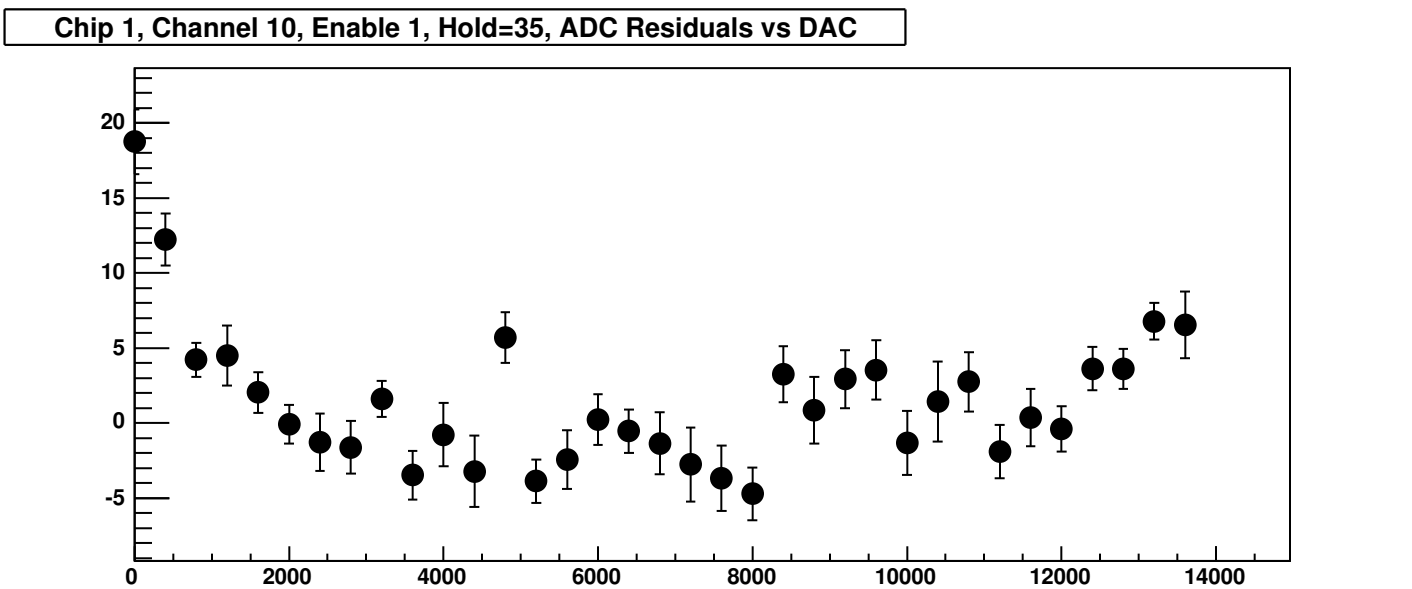

Chip 1, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 3898 / 23
p0 -725.7 ± 1.214
p1 2.449 ± 0.000187
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

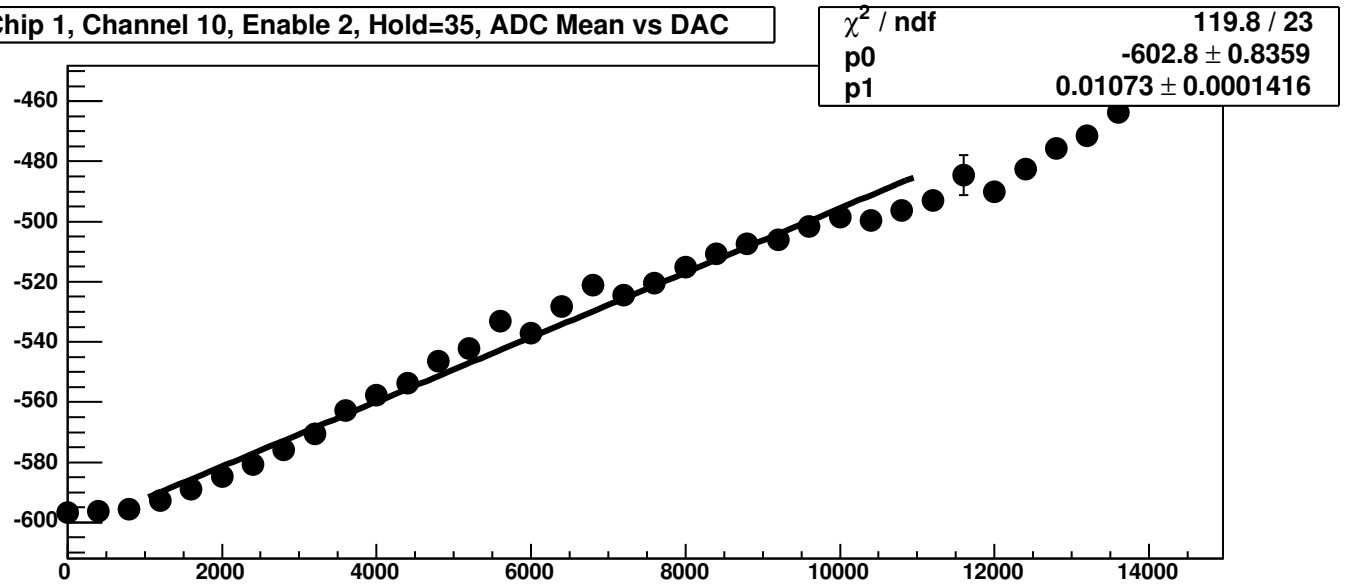

Chip 1, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 60.04 / 23
p0 -609.9 ± 0.7639
p1 0.01968 ± 0.0001262
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

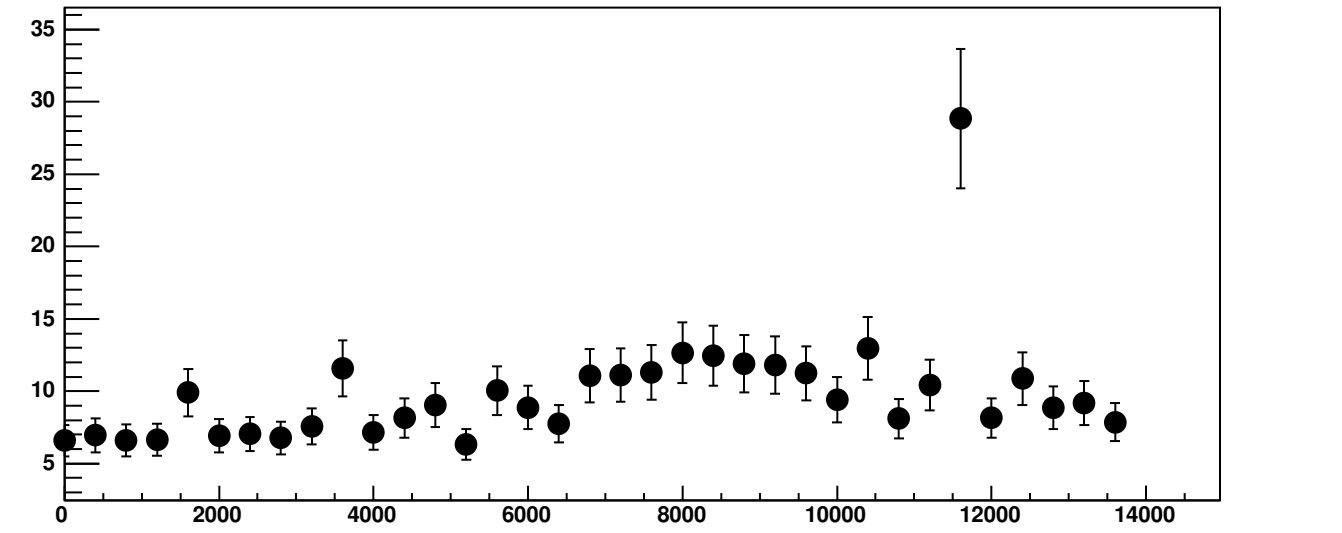

Chip 1, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

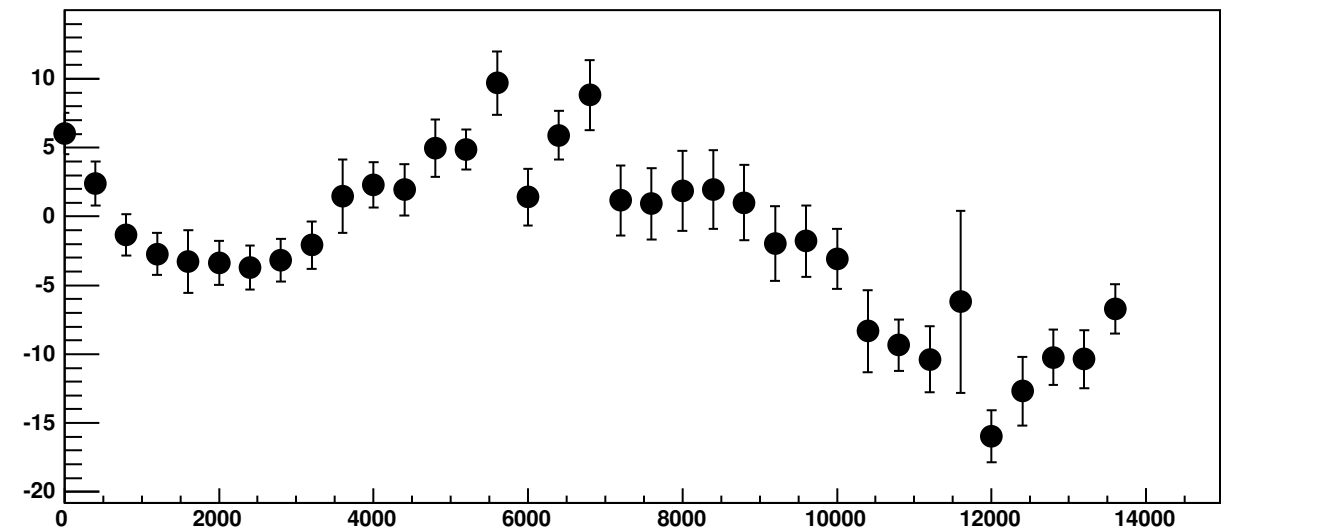
Chip 1, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

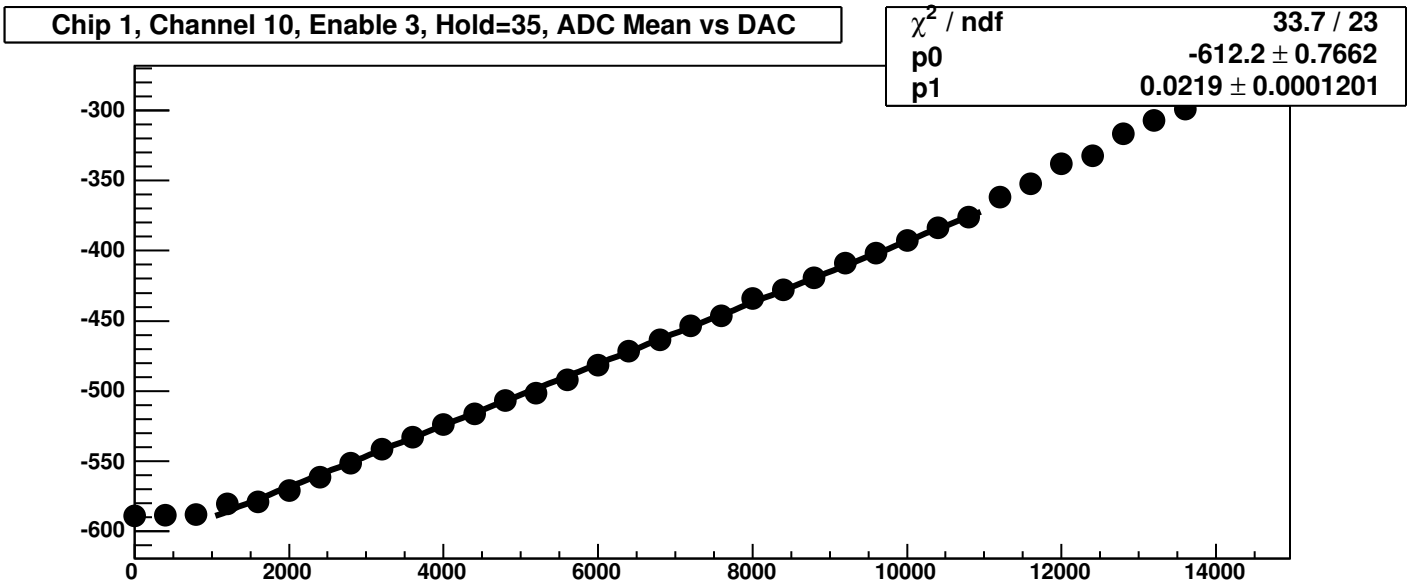


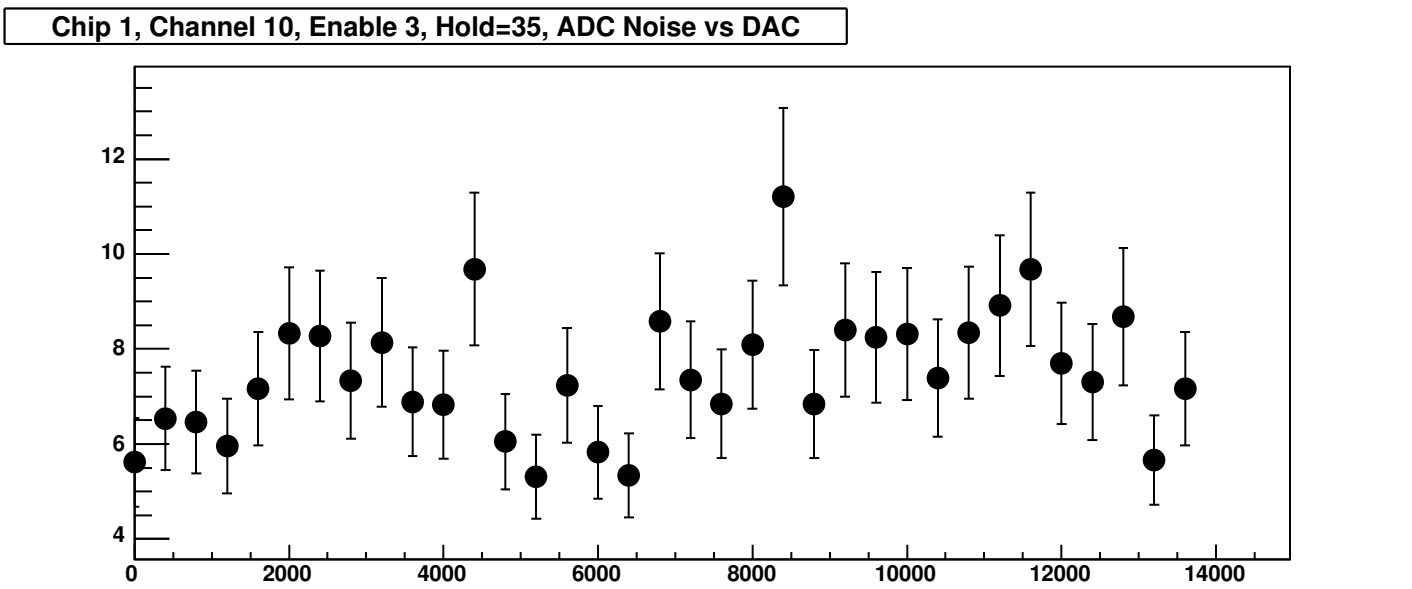
Chip 1, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

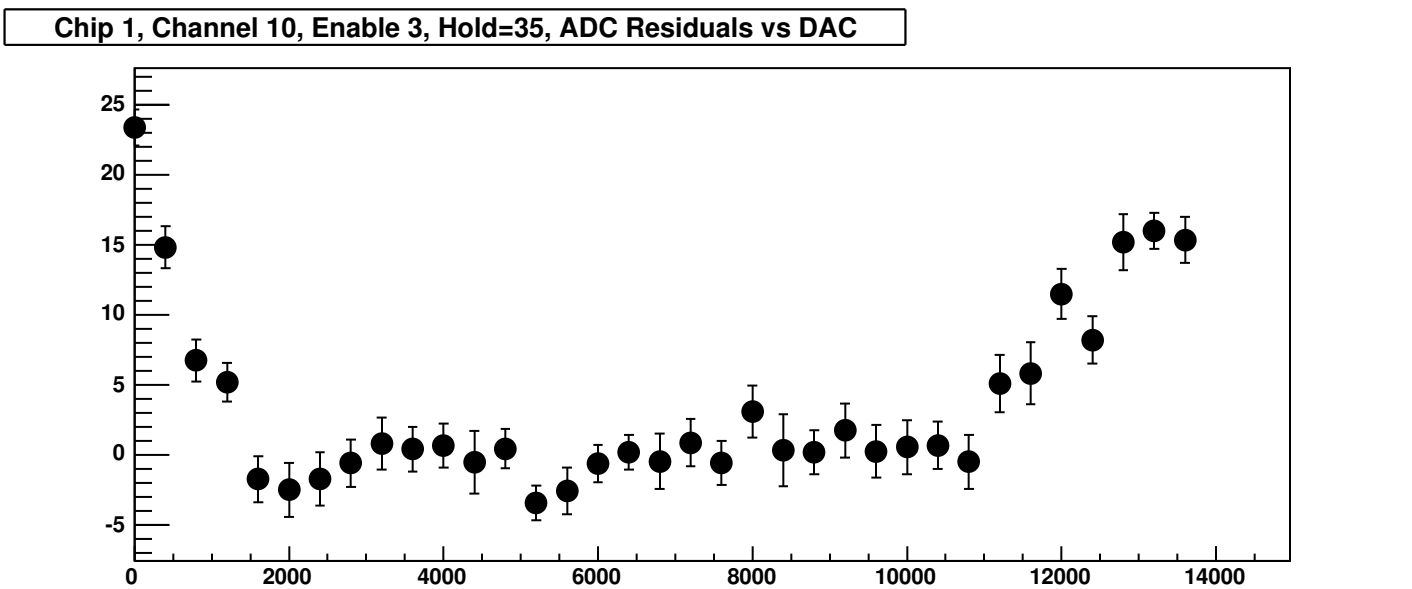


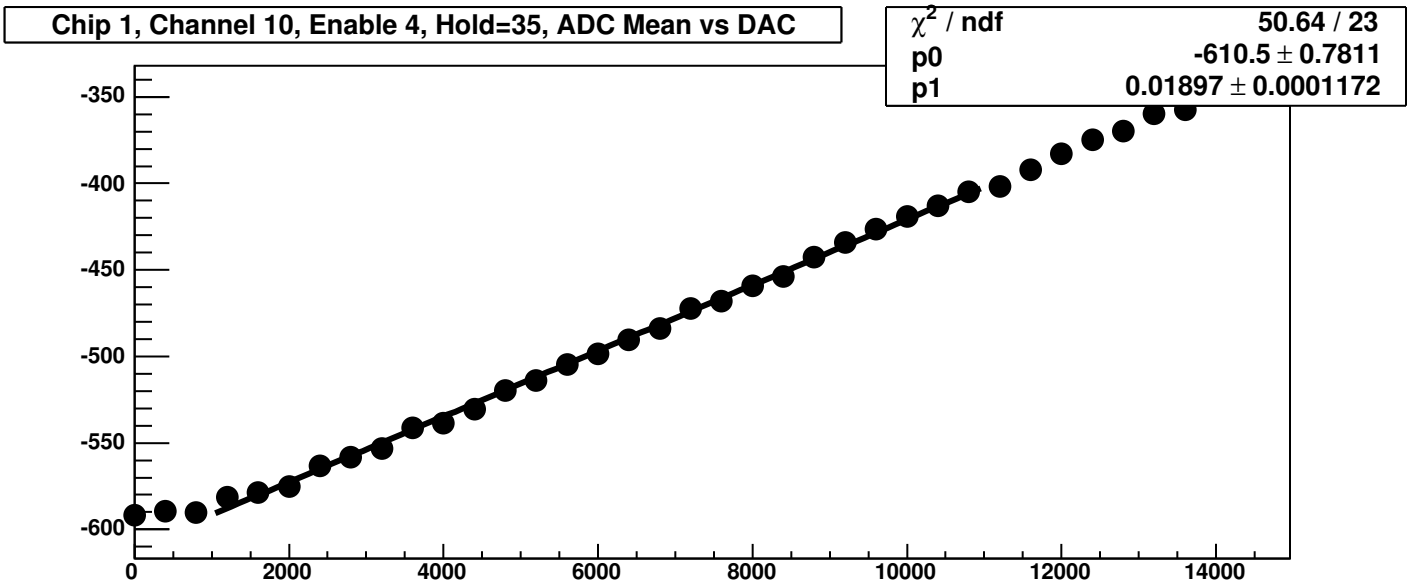
Chip 1, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

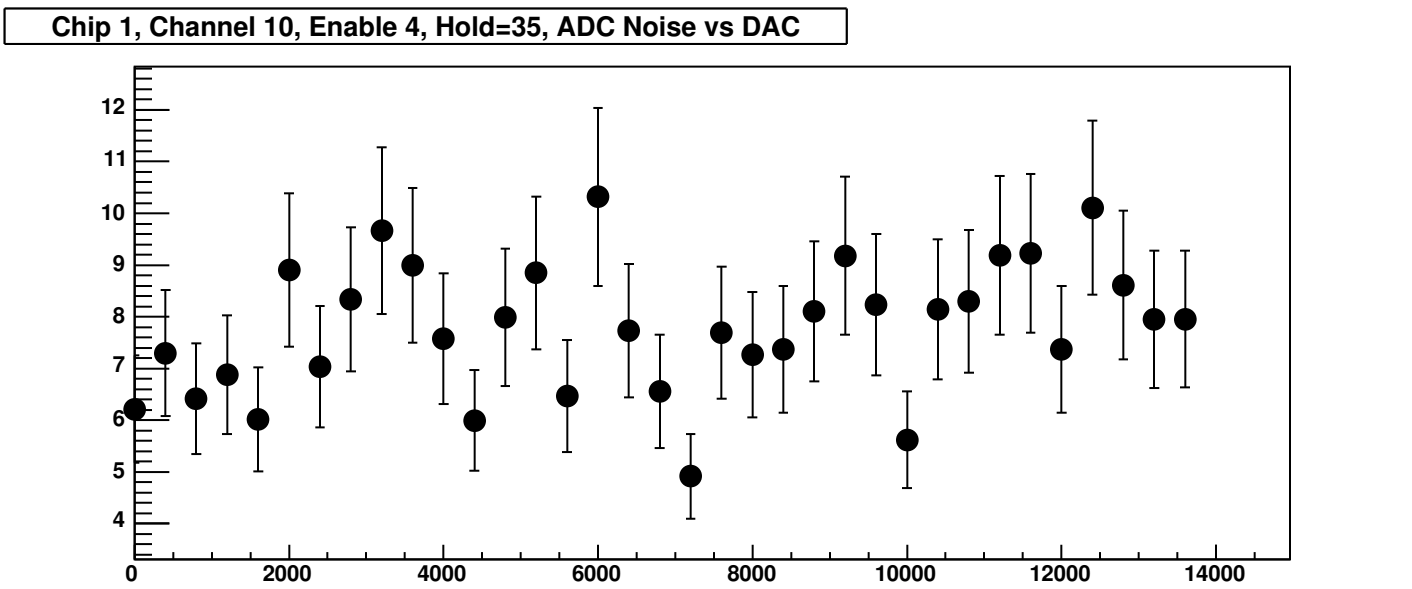


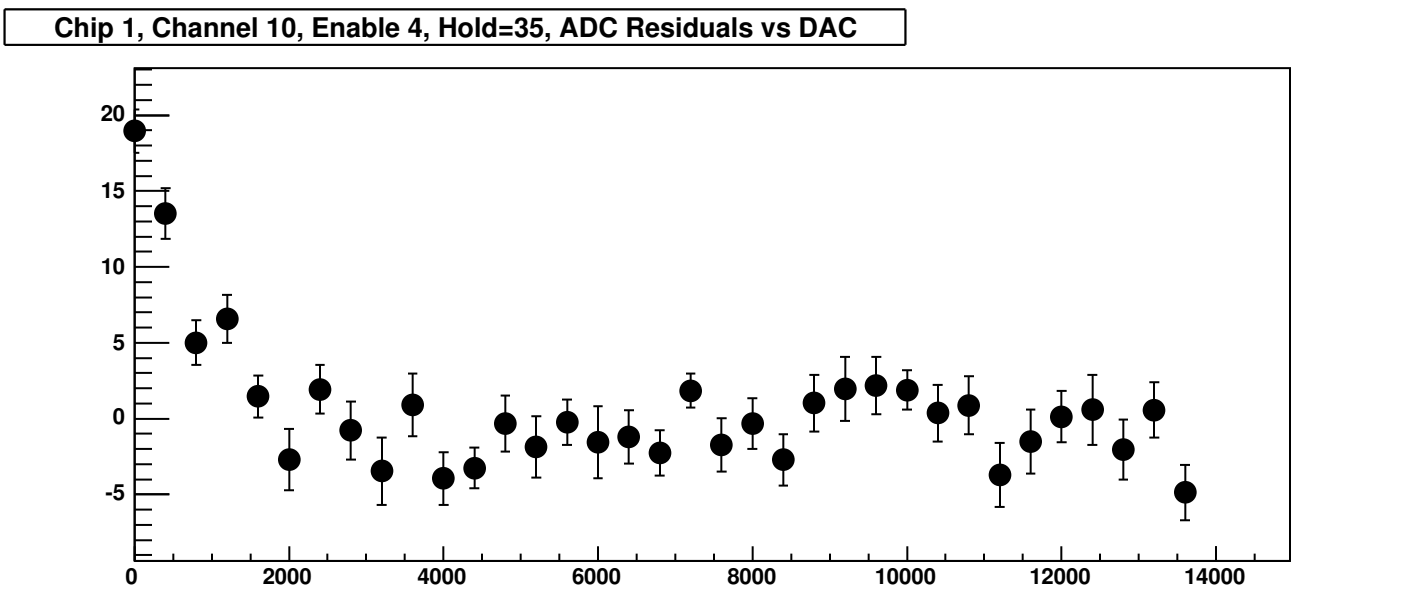

Chip 1, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 33.7 / 23
p0 -612.2 ± 0.7662
p1 0.0219 ± 0.0001201
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

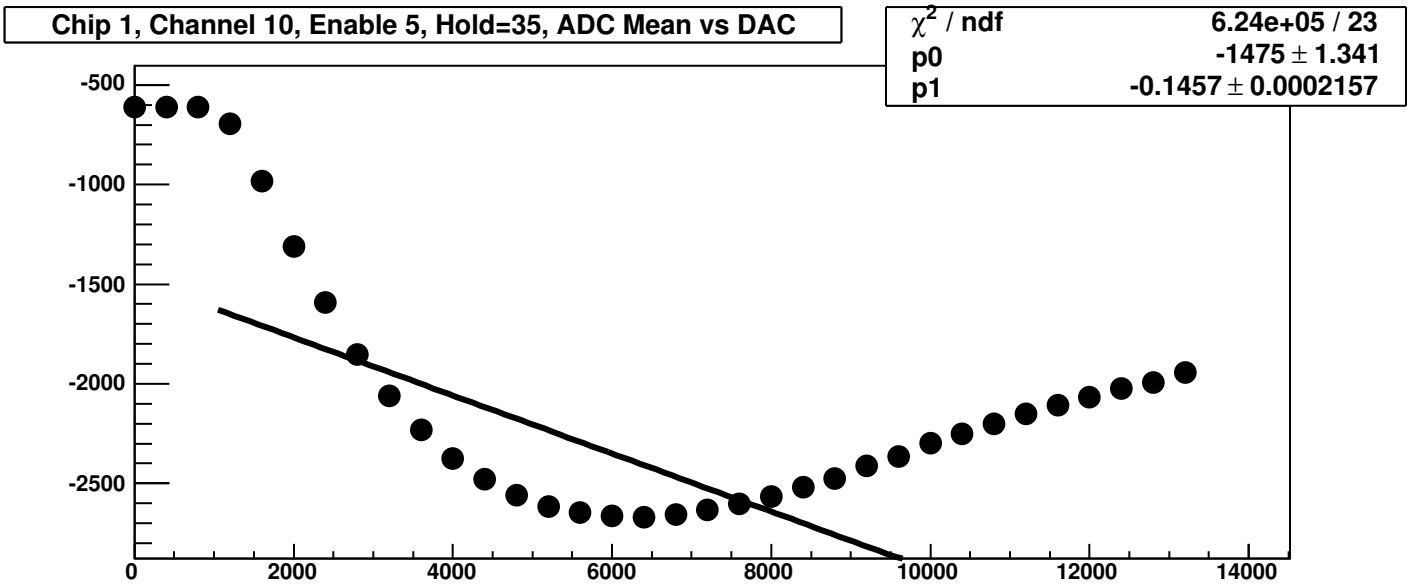

Chip 1, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

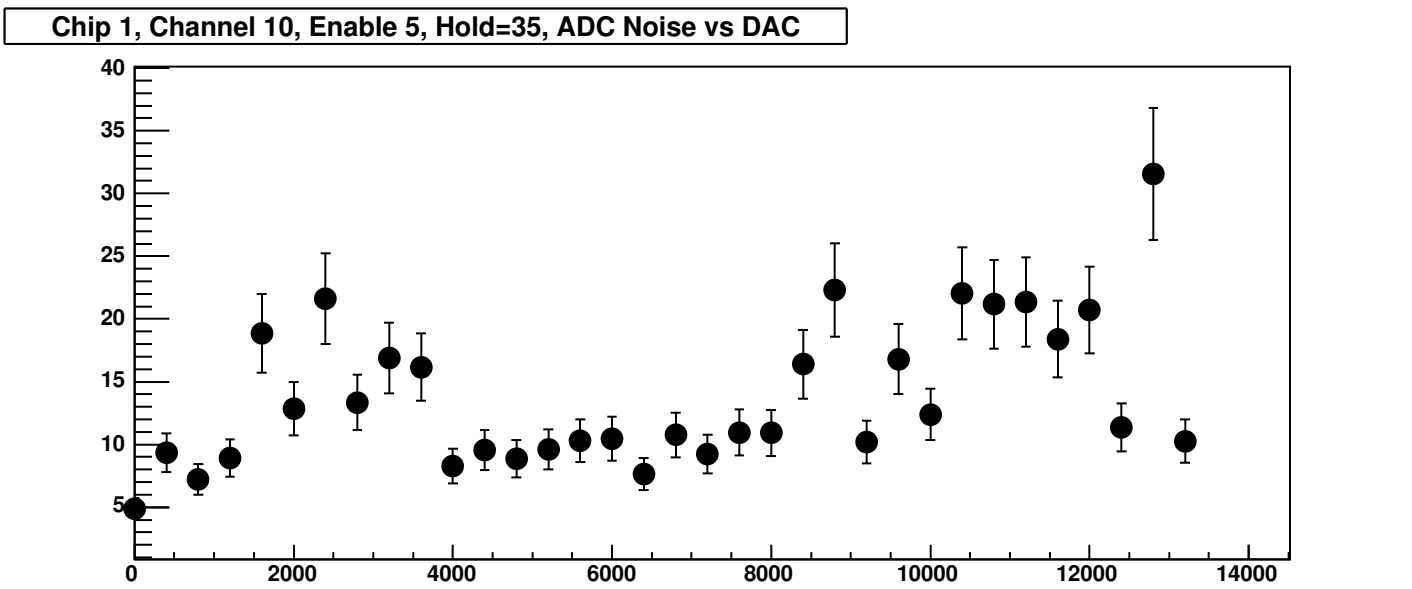

Chip 1, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

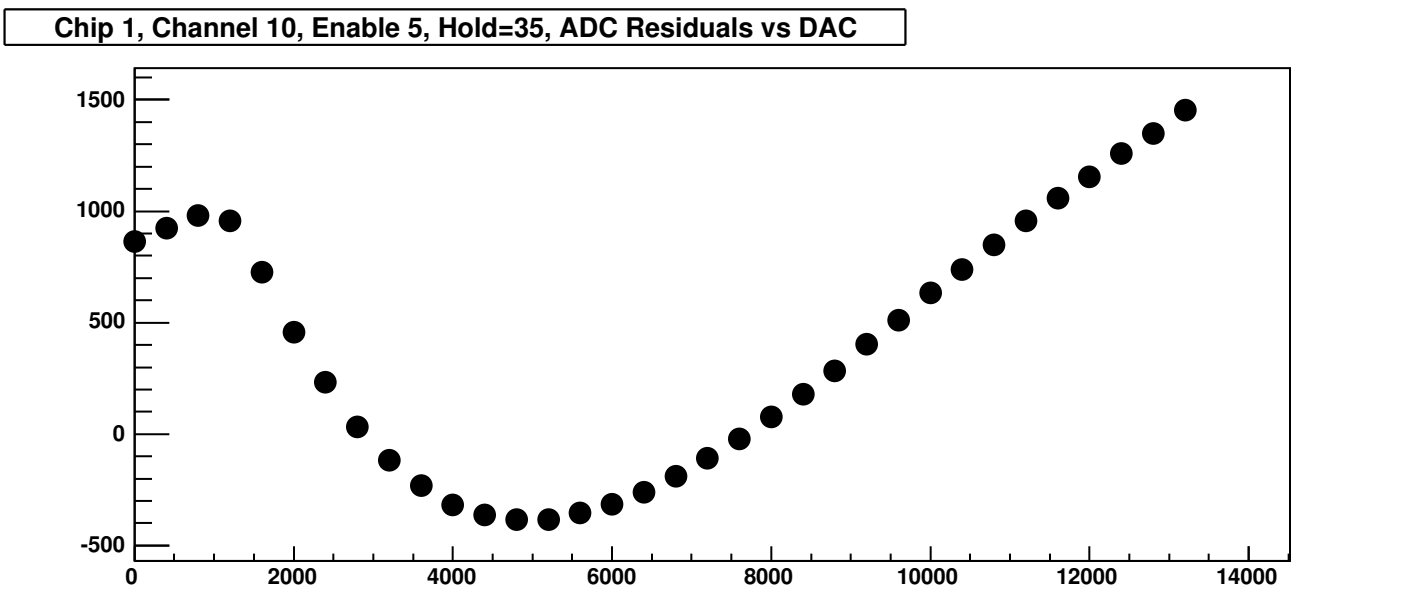

Chip 1, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 50.64 / 23
p0 -610.5 ± 0.7811
p1 0.01897 ± 0.0001172
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

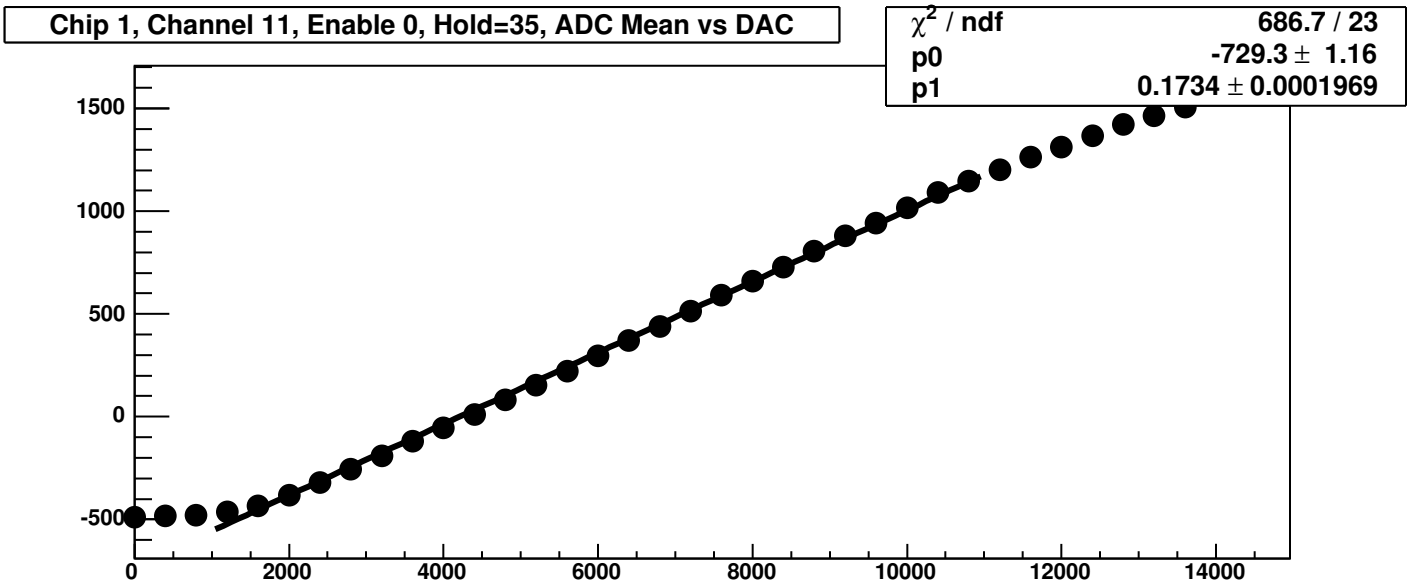

Chip 1, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

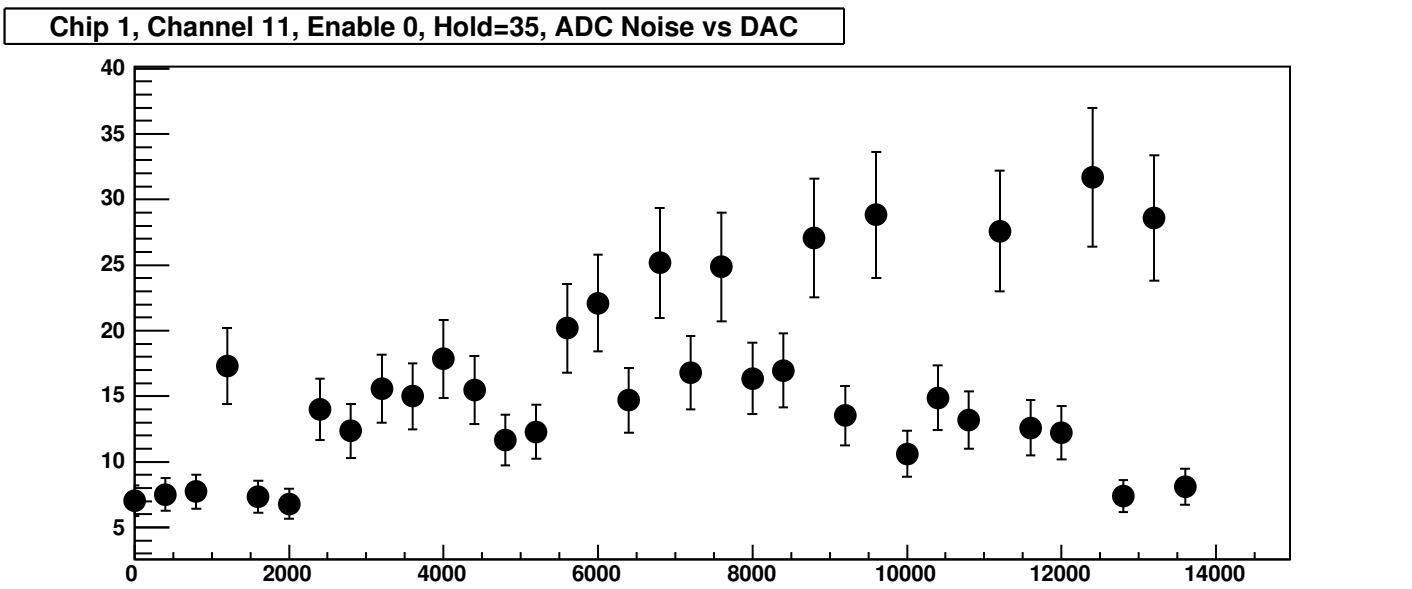

Chip 1, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

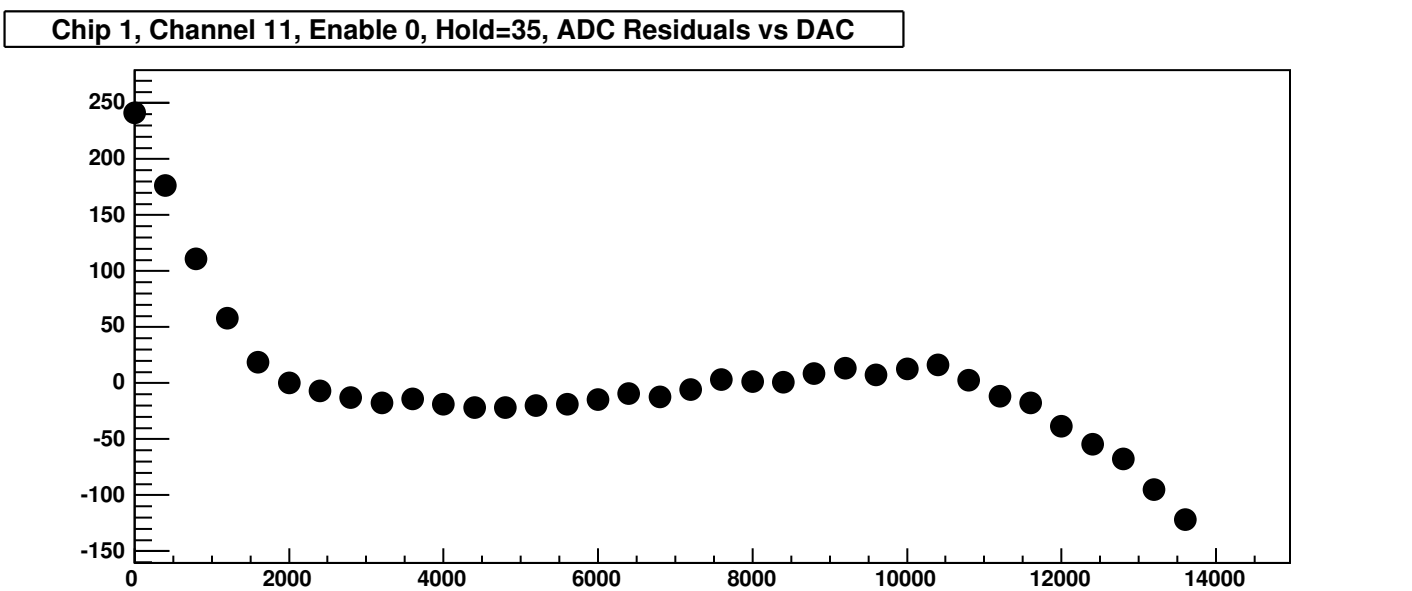

Chip 1, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 6.24e+05 / 23
p0 -1475 ± 1.341
p1 -0.1457 ± 0.0002157
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

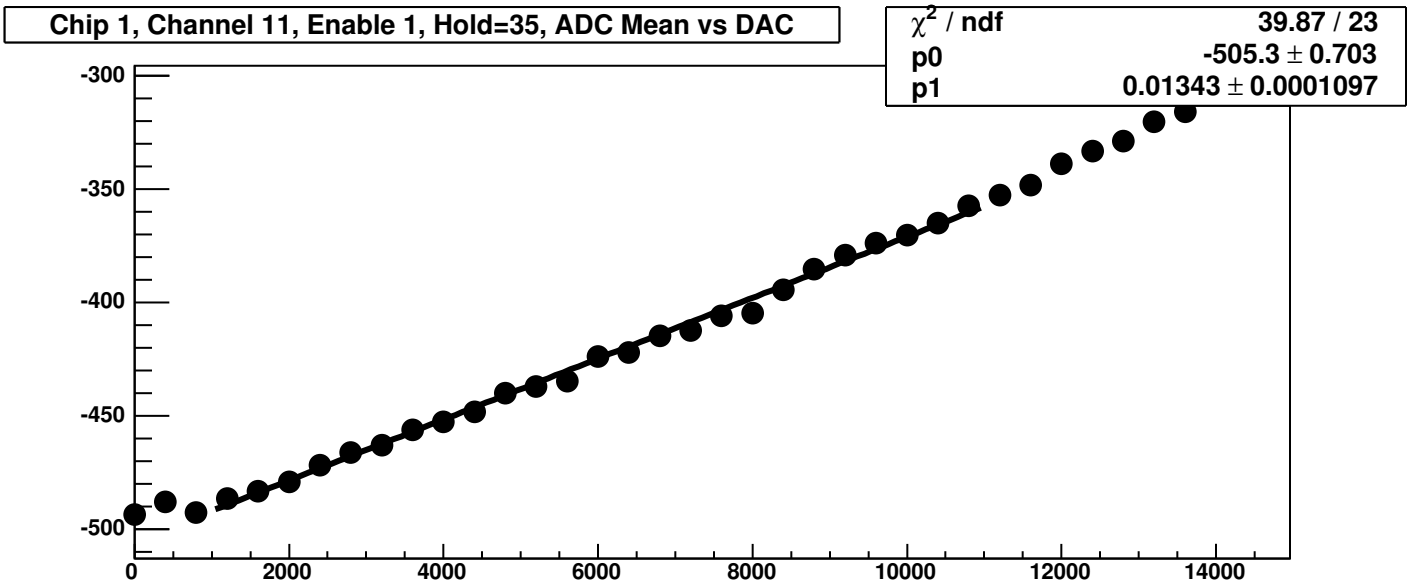

Chip 1, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

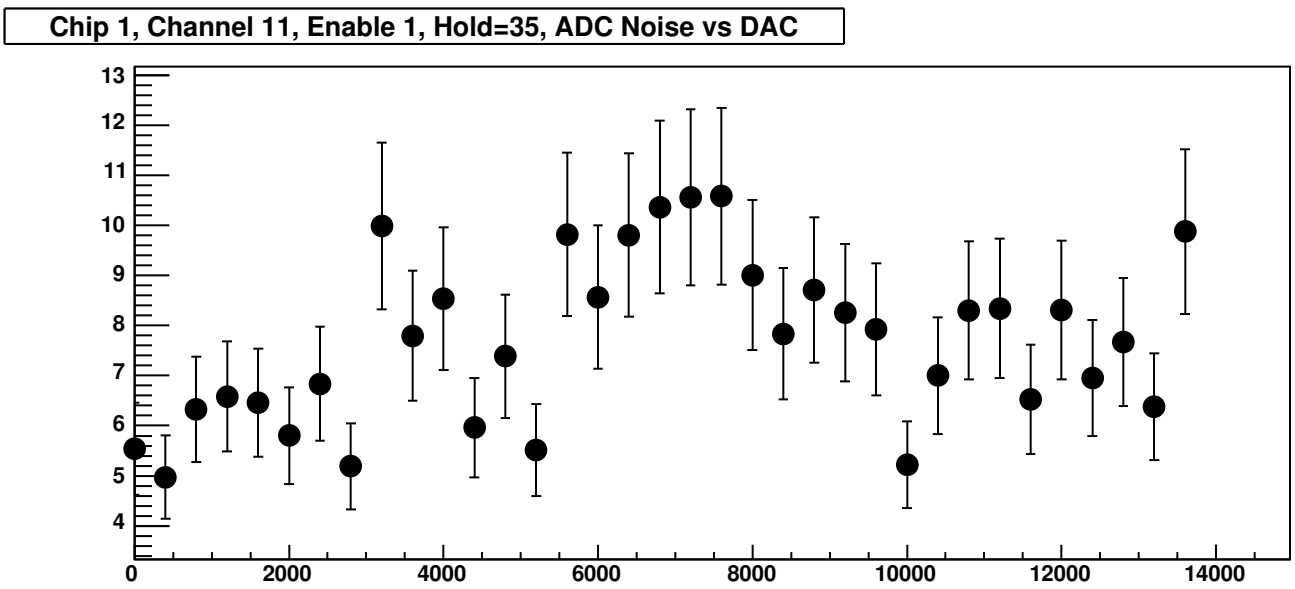

Chip 1, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

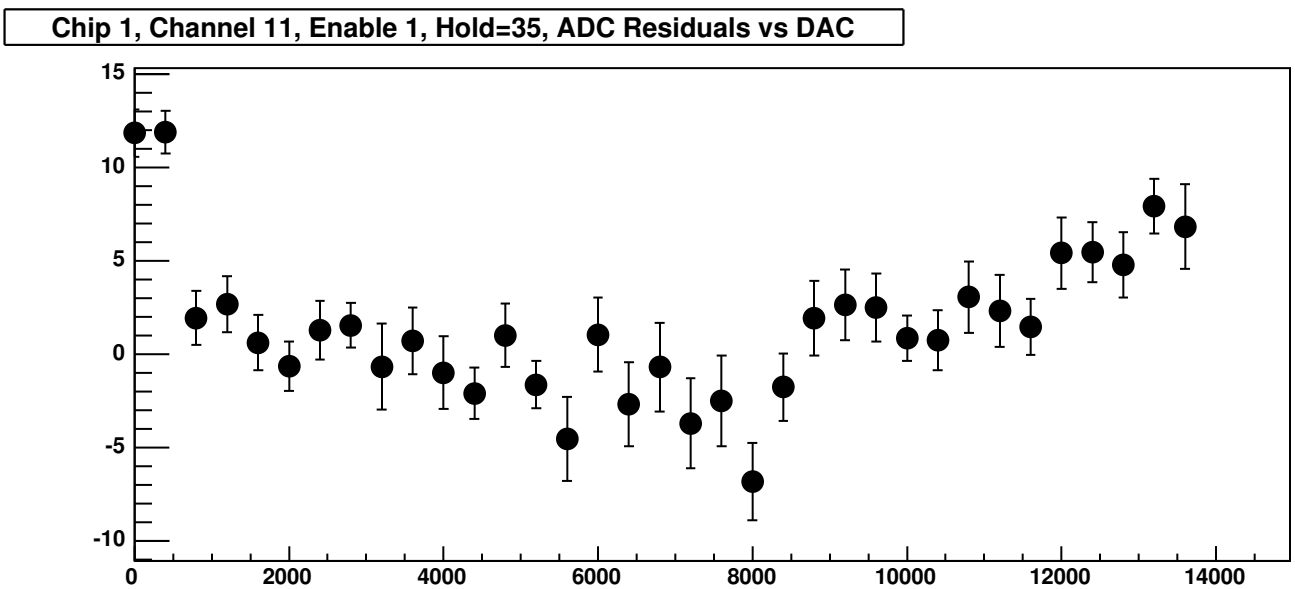

Chip 1, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 686.7 / 23
p0 -729.3 ± 1.16
p1 0.1734 ± 0.0001969
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

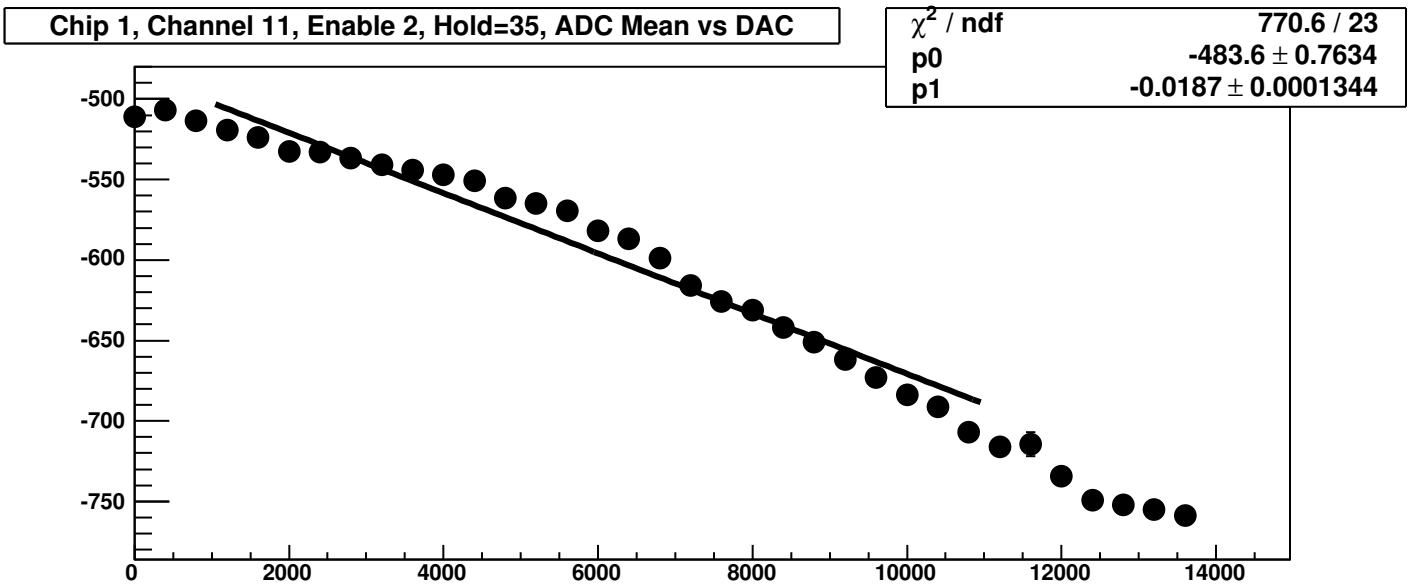

Chip 1, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

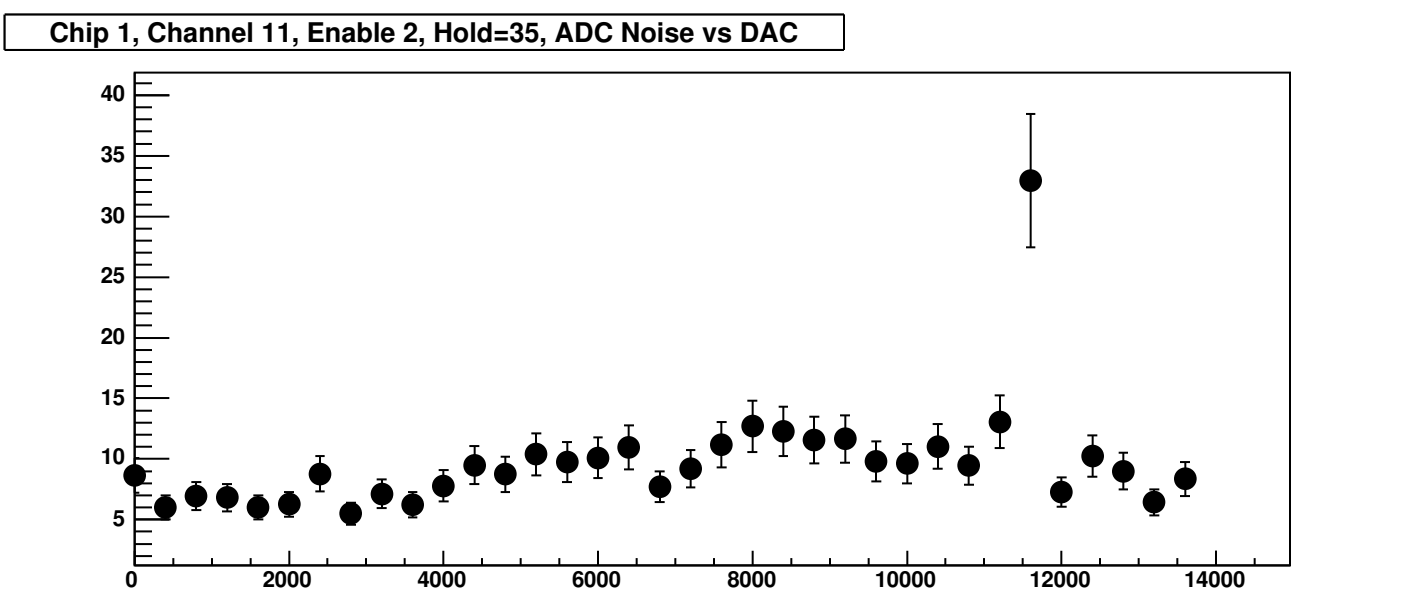

Chip 1, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

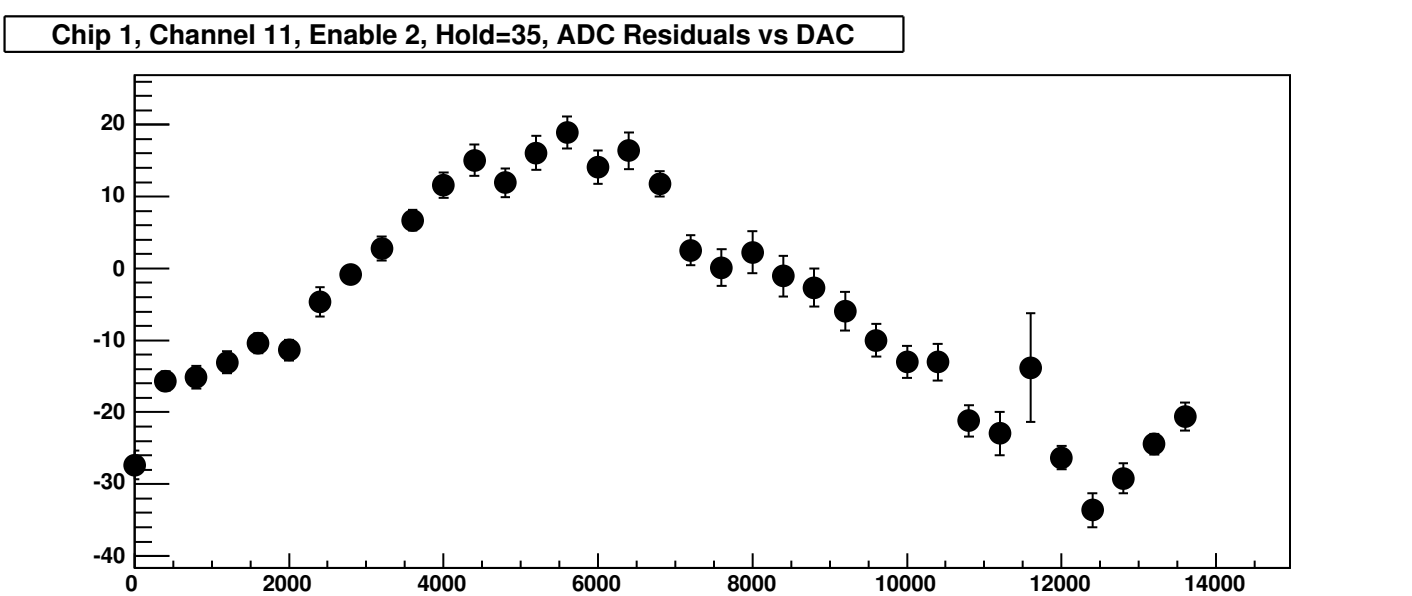

Chip 1, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 39.87 / 23
p0 -505.3 ± 0.703
p1 0.01343 ± 0.0001097
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

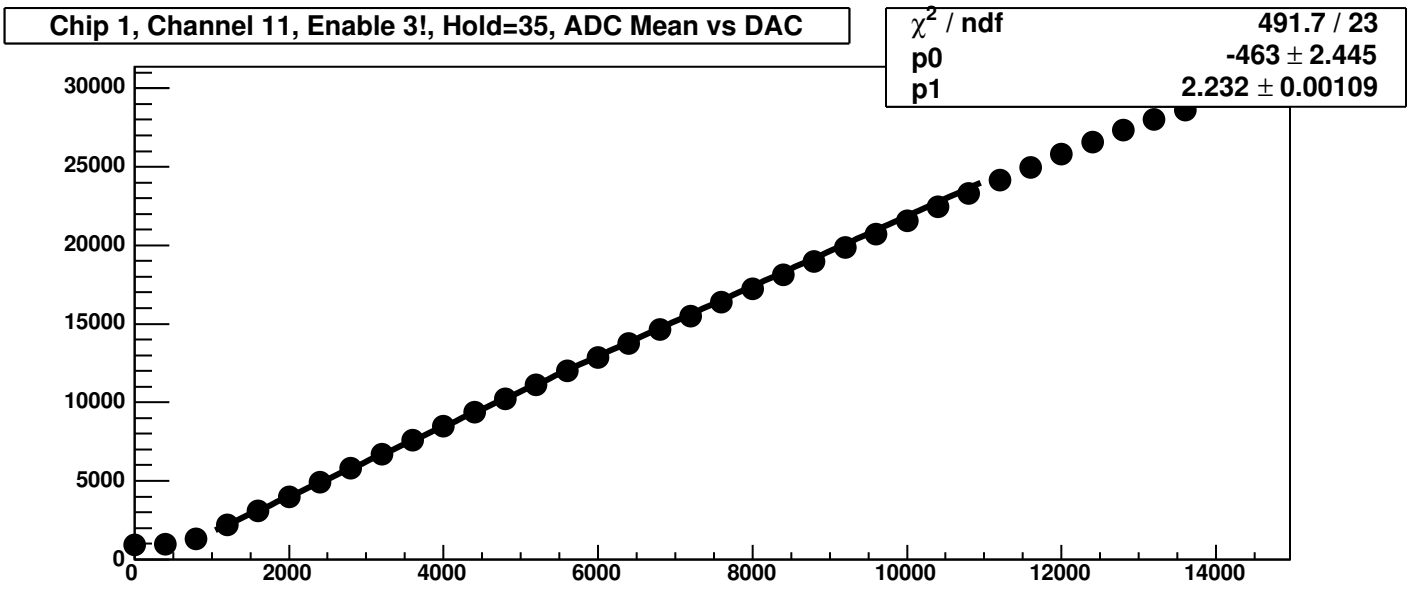

Chip 1, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

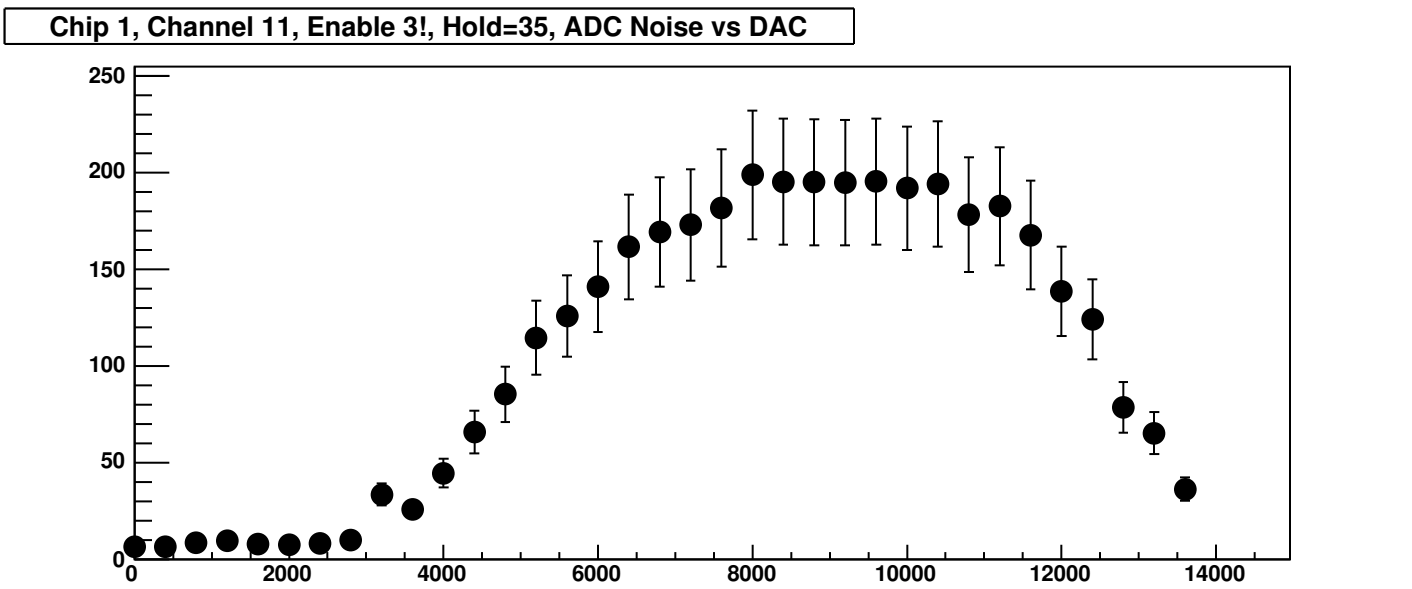

Chip 1, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

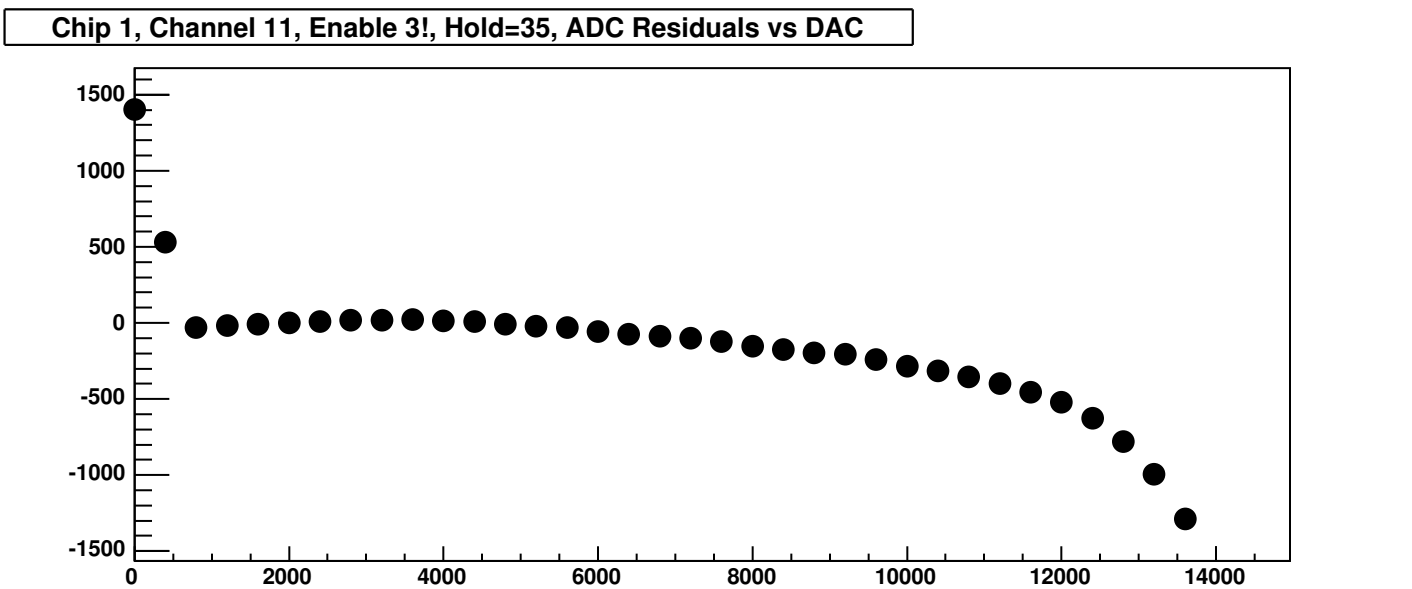

Chip 1, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 770.6 / 23
p0 -483.6 ± 0.7634
p1 -0.0187 ± 0.0001344
-500
-550
-600
-650
-700
-750
0
2000
4000
6000
8000
10000
12000
14000

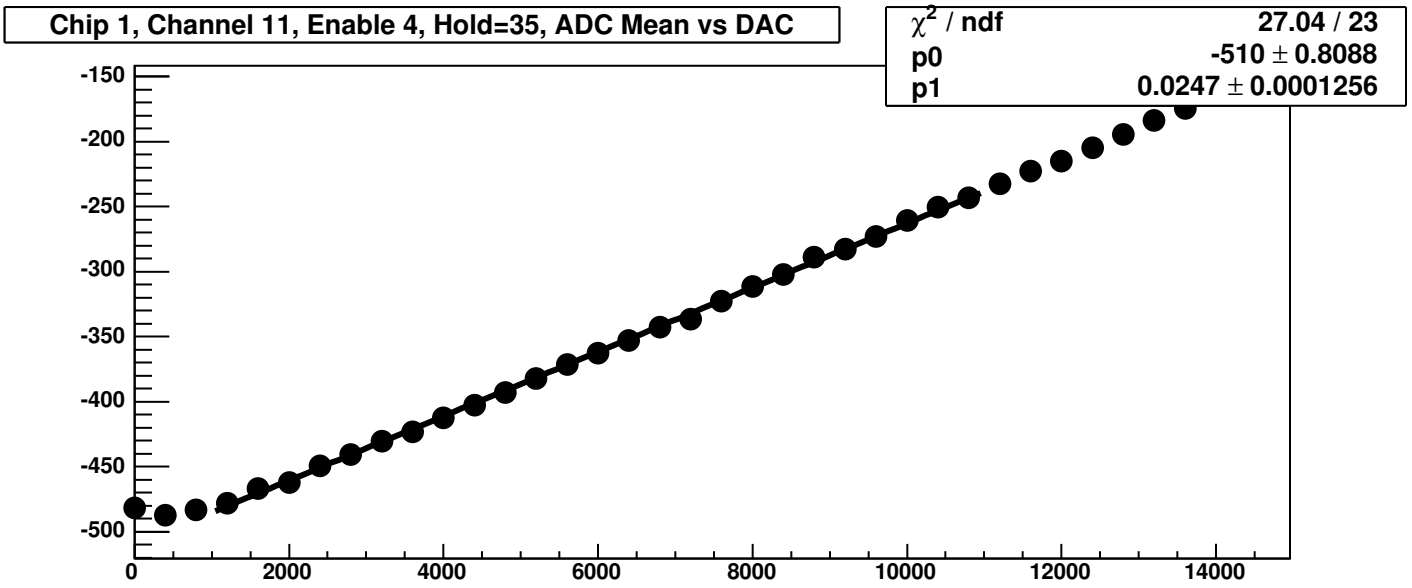

Chip 1, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

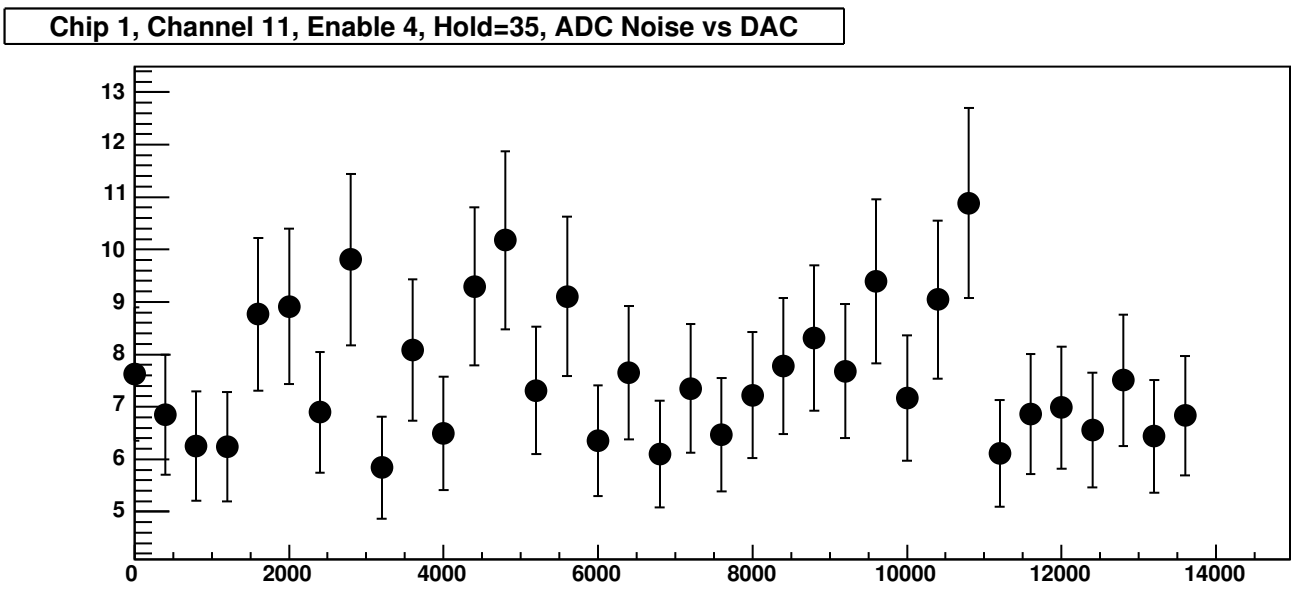

Chip 1, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

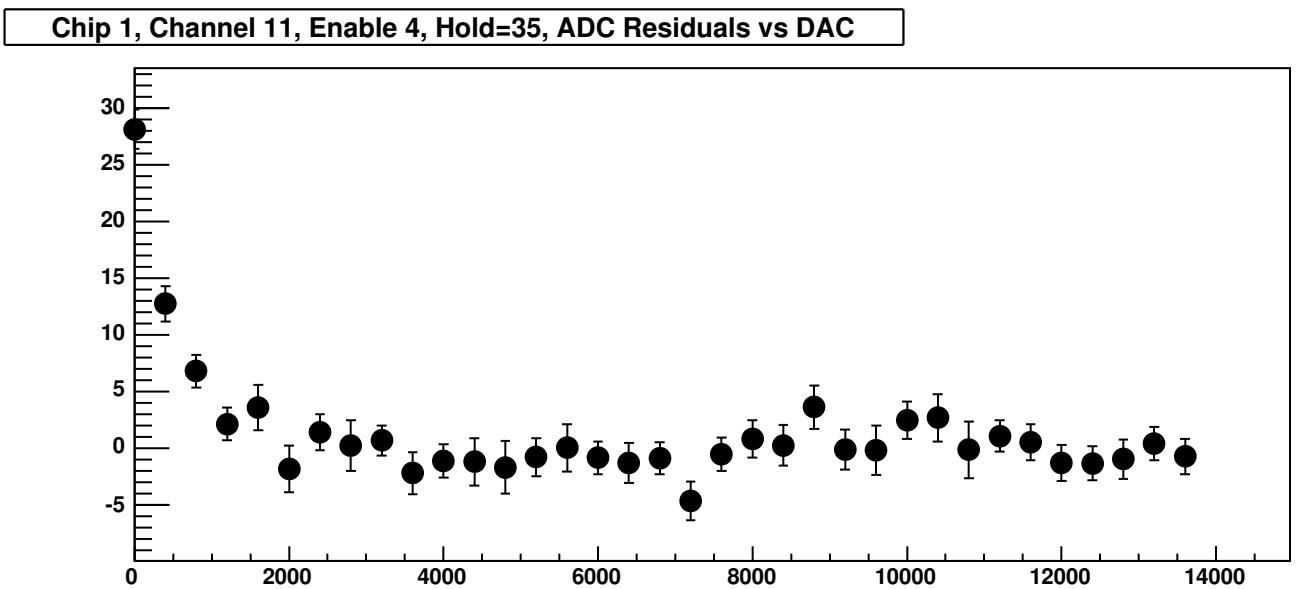

Chip 1, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 491.7 / 23
p0 -463 ± 2.445
p1 2.232 ± 0.00109
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

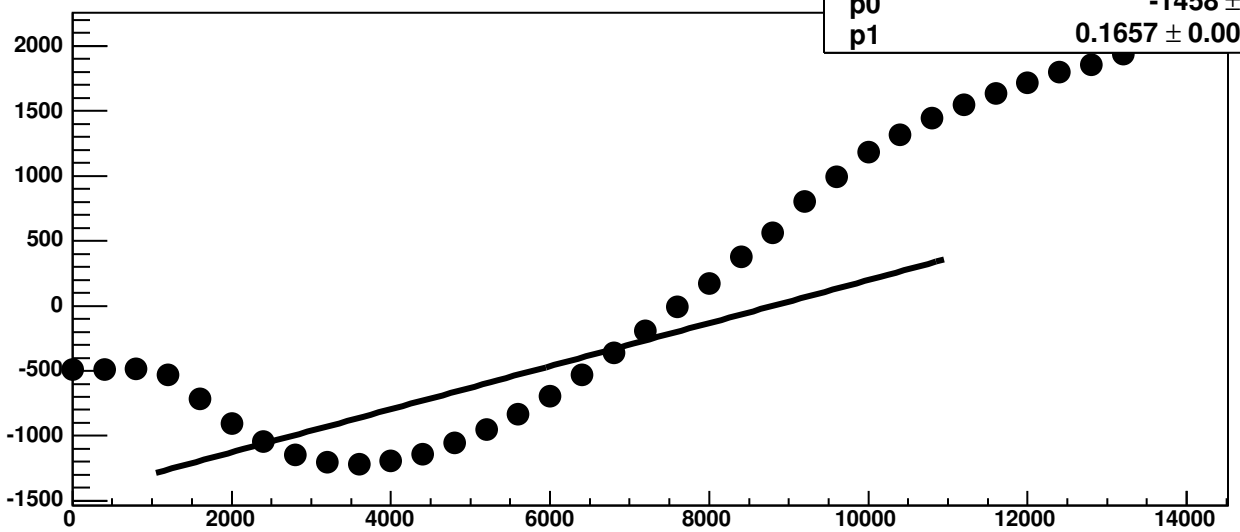

Chip 1, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.04 / 23
p0 -510 ± 0.8088
p1 0.0247 ± 0.0001256
-150
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

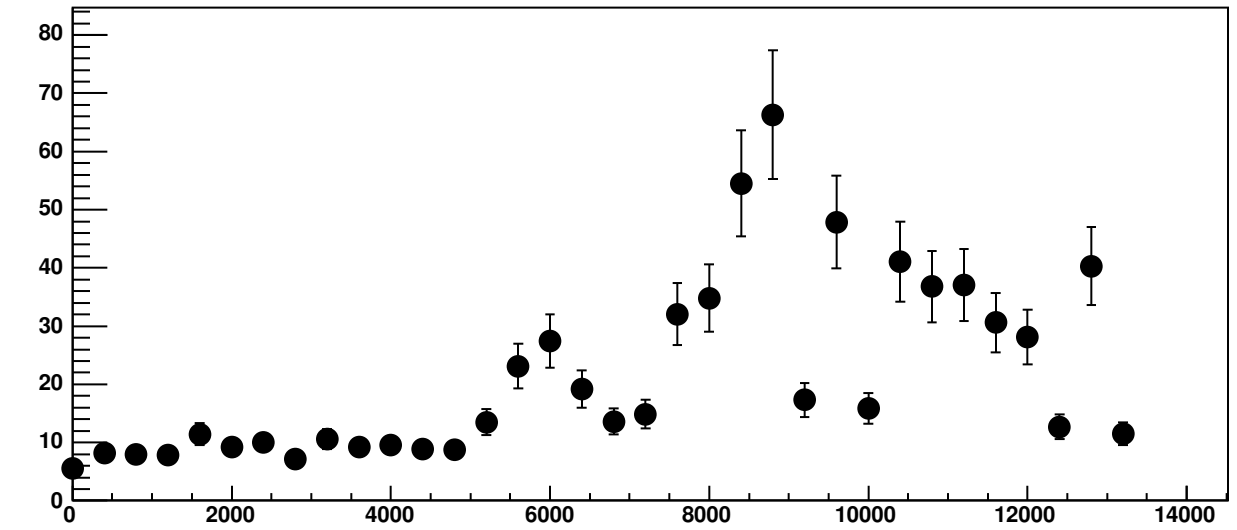

Chip 1, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

Chip 1, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

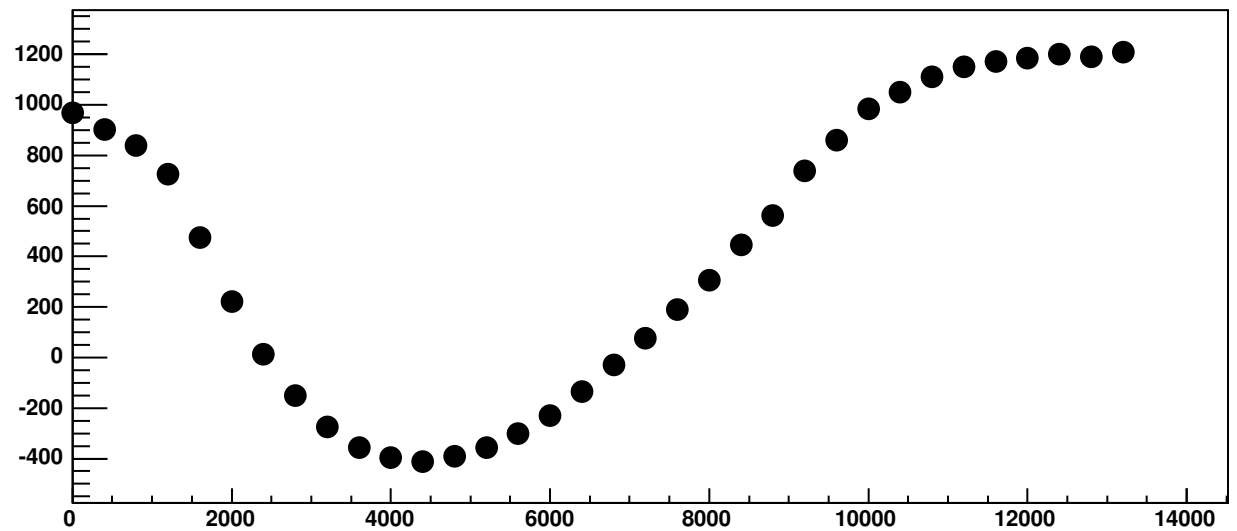
χ^2 / ndf	5.378e+05 / 23
p0	-1458 ± 1.184
p1	0.1657 ± 0.0002638

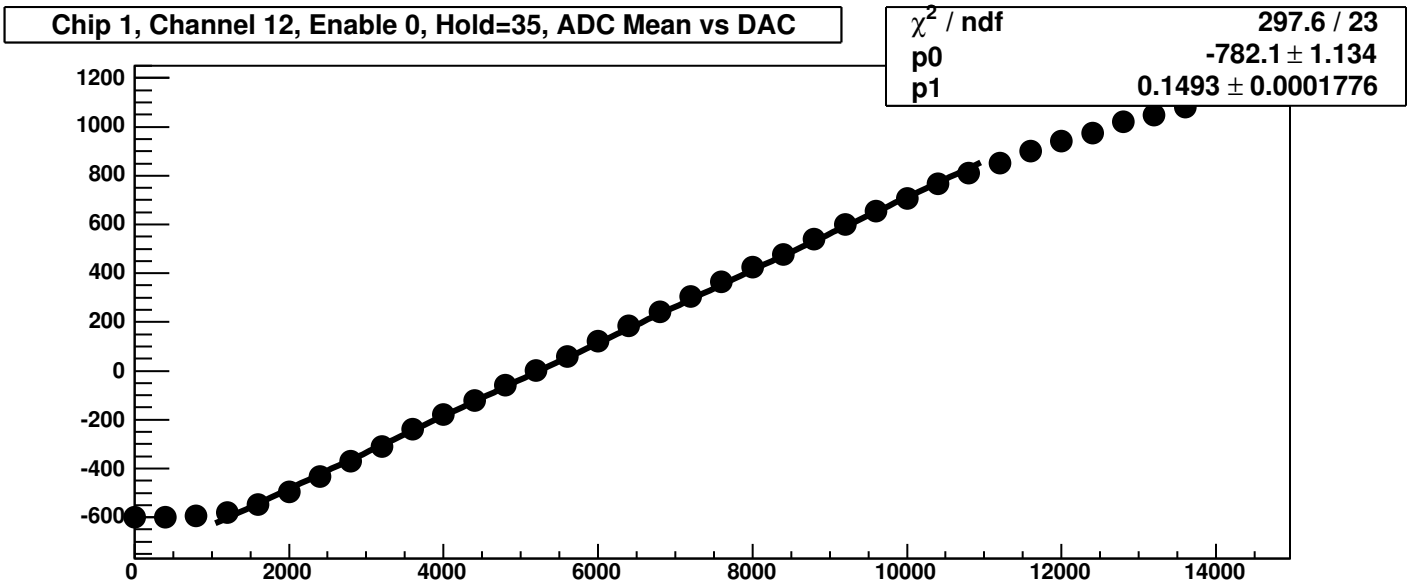


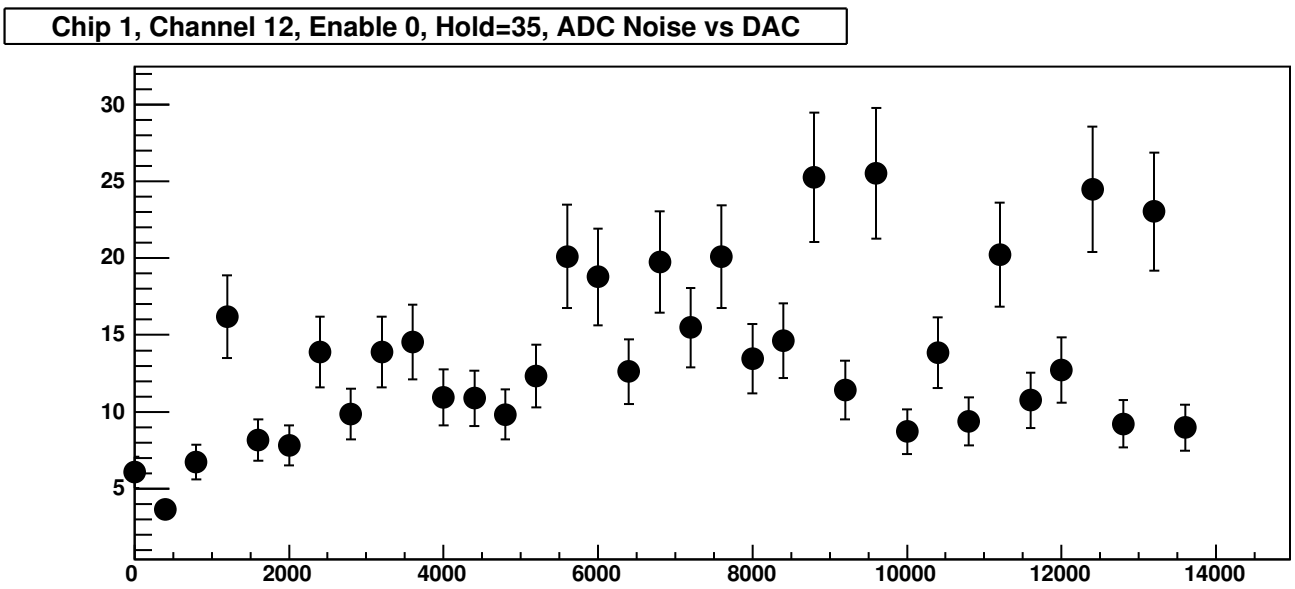
Chip 1, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

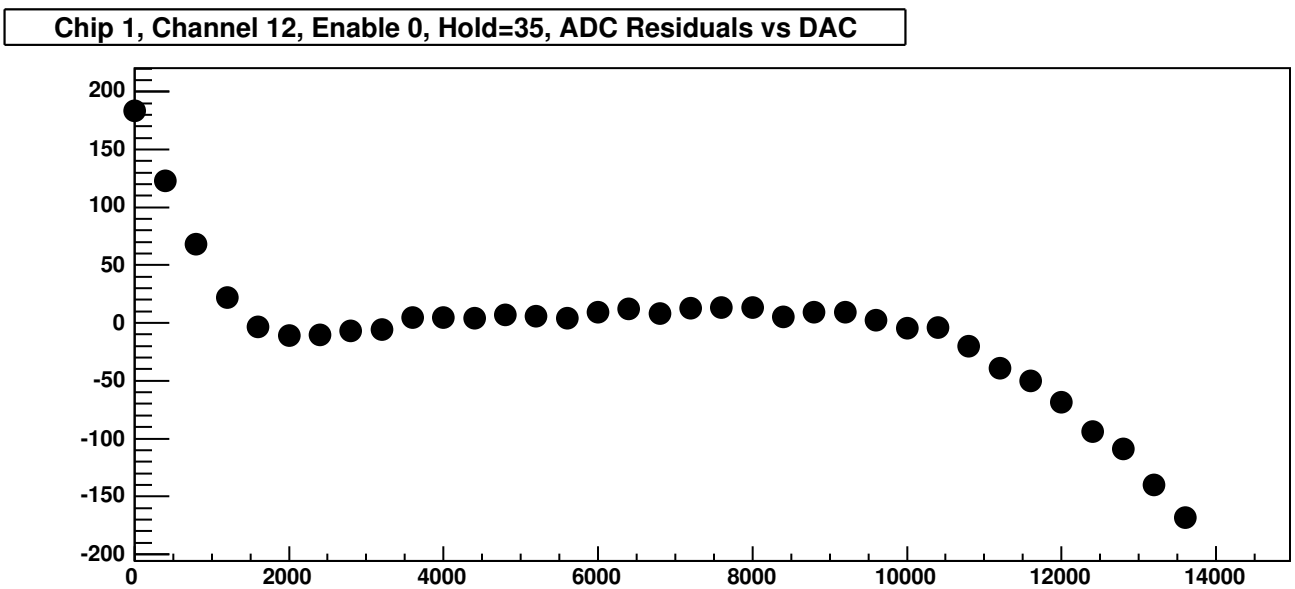


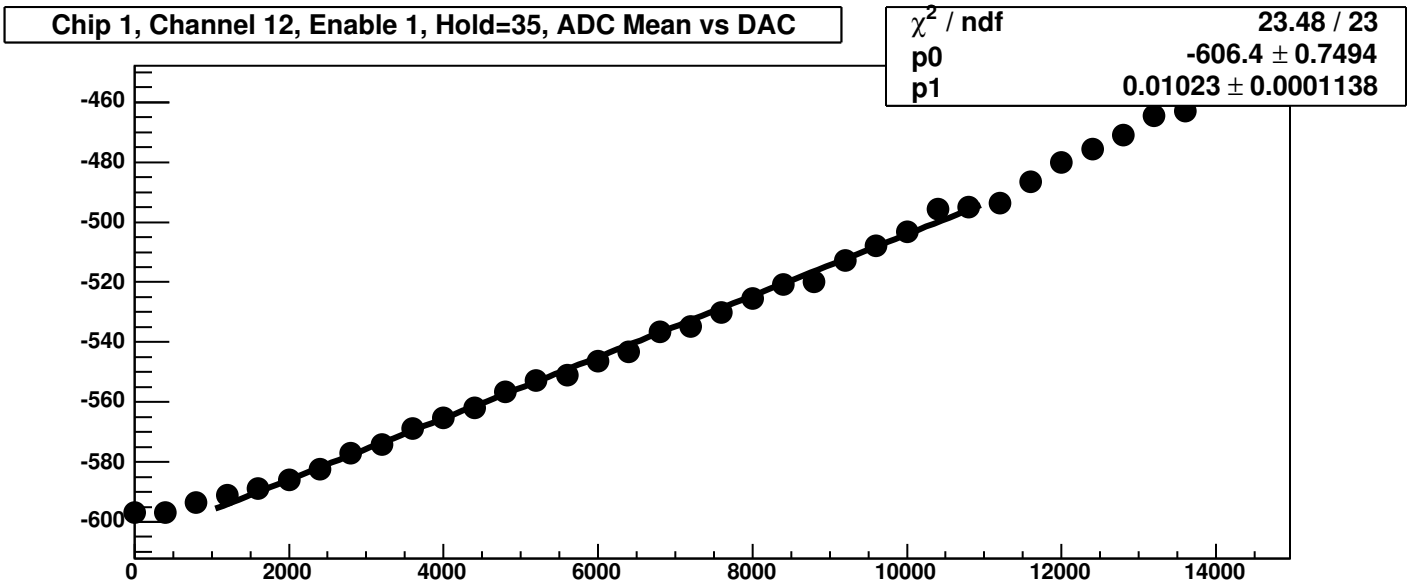
Chip 1, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

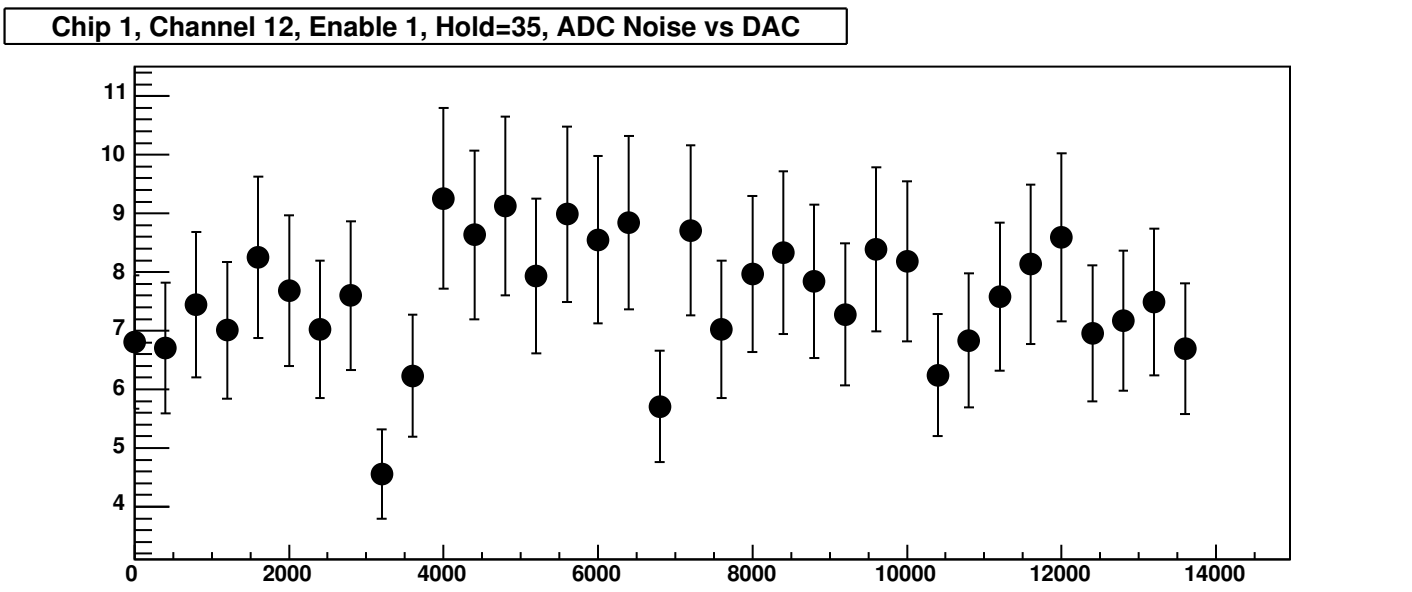


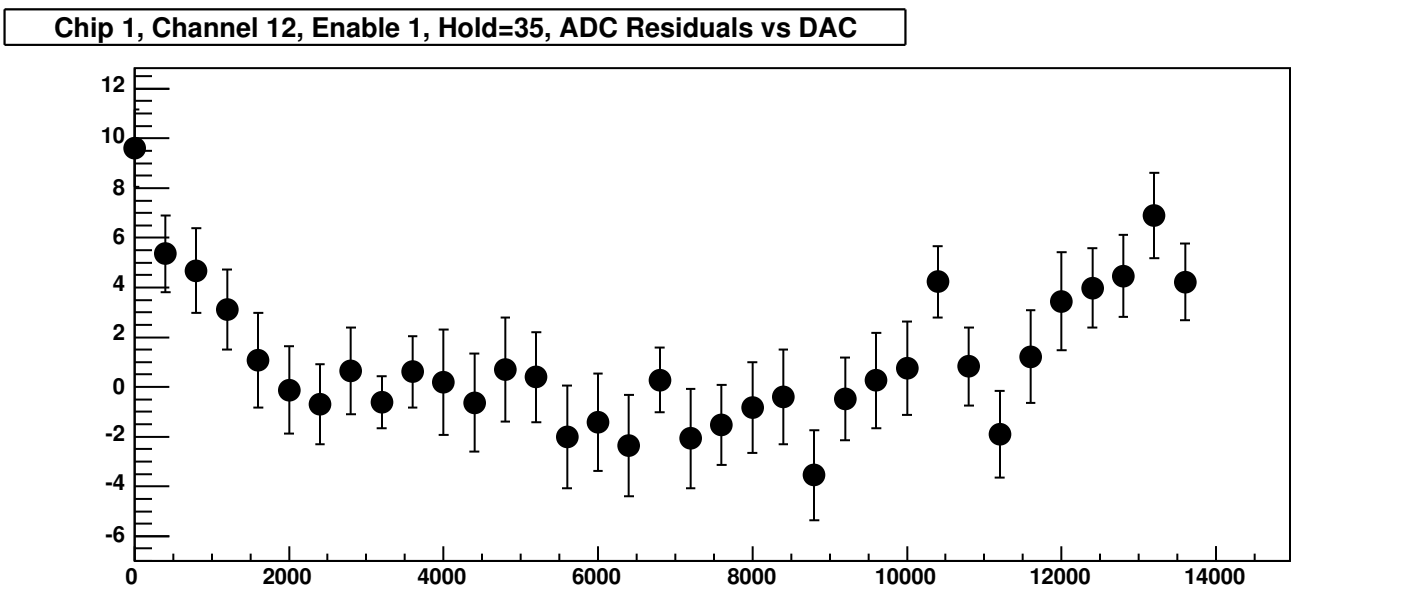

Chip 1, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 297.6 / 23
p0 -782.1 ± 1.134
p1 0.1493 ± 0.0001776
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

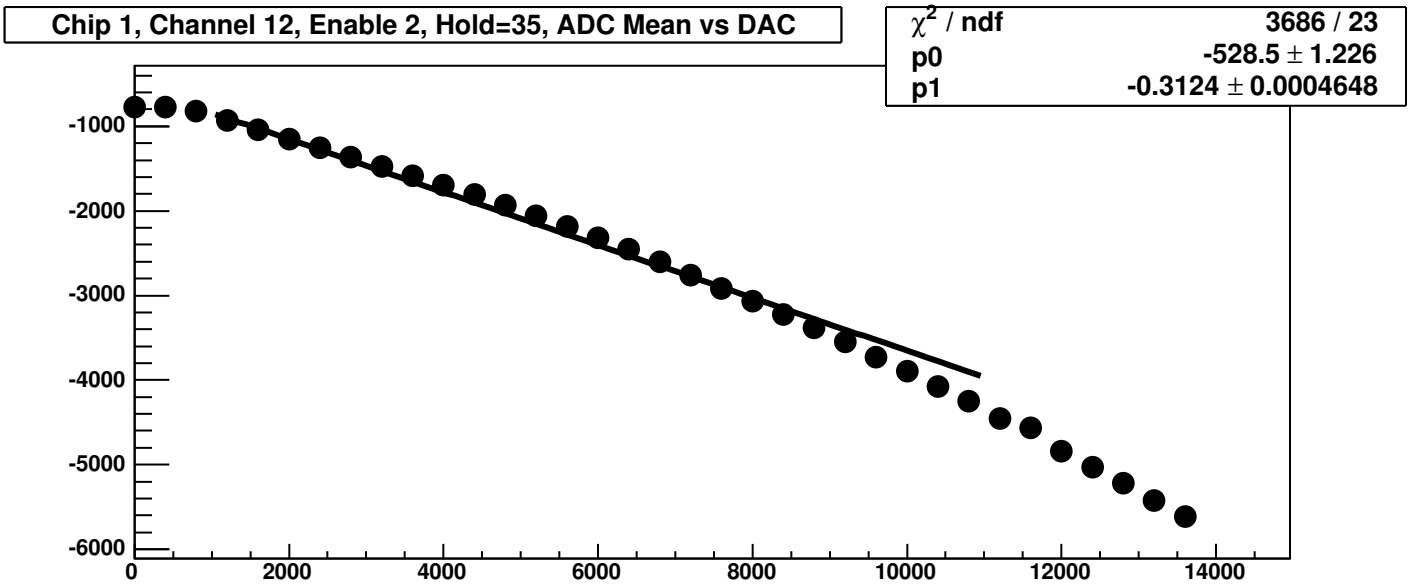

Chip 1, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

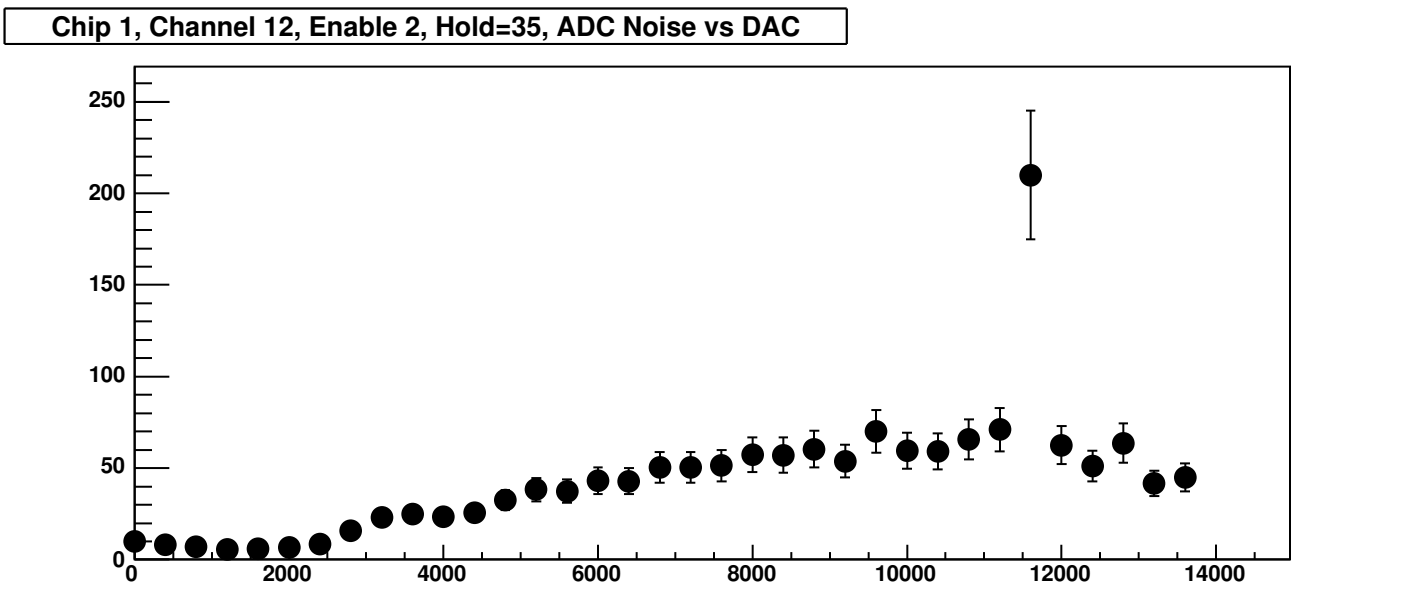

Chip 1, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

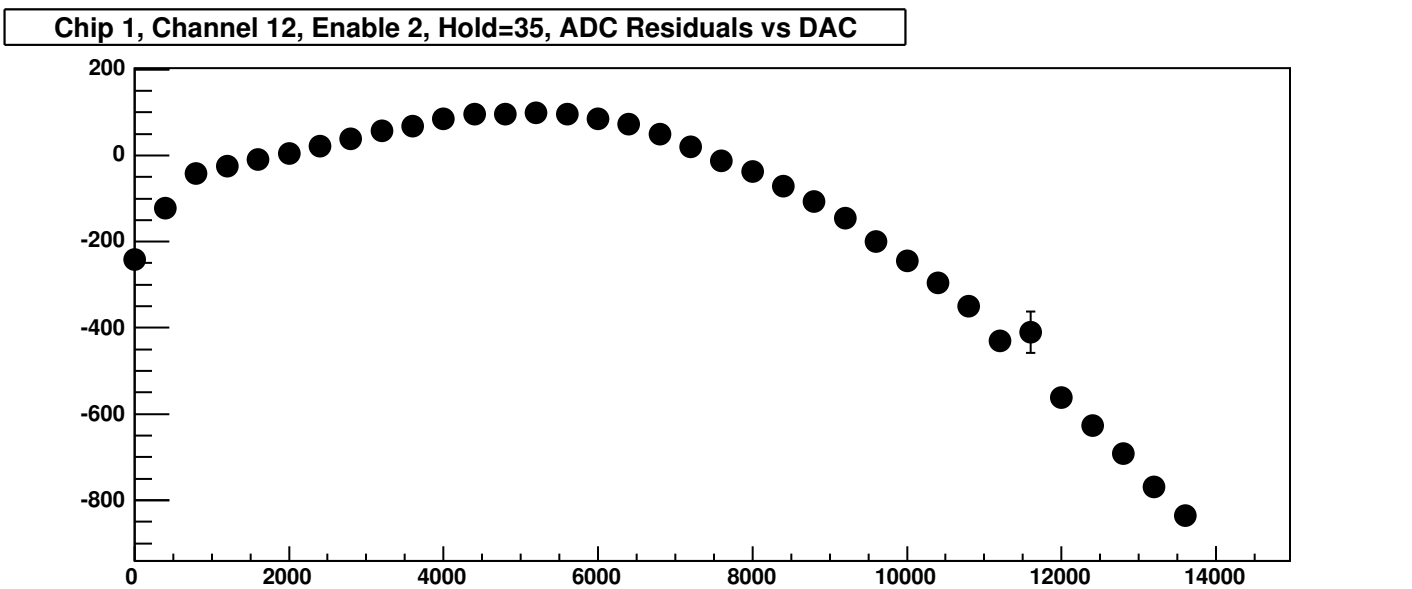

Chip 1, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 23.48 / 23
p0 -606.4 ± 0.7494
p1 0.01023 ± 0.0001138
-460
-480
-500
-520
-540
-560
-580
-600
0
2000
4000
6000
8000
10000
12000
14000

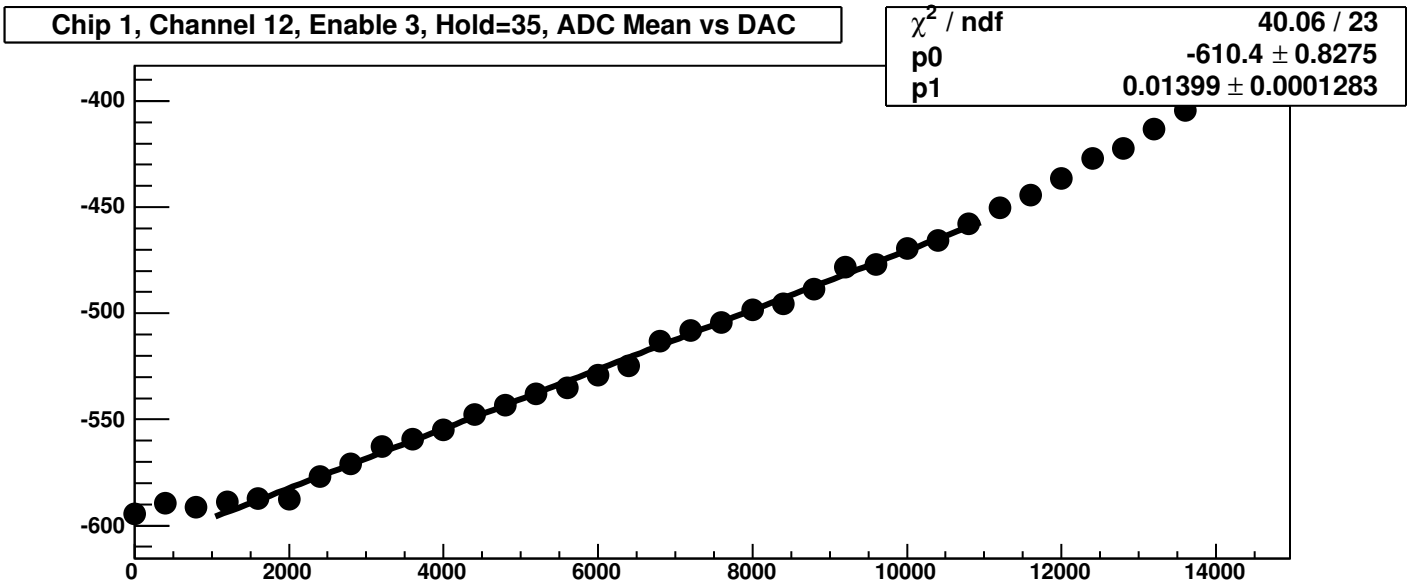

Chip 1, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

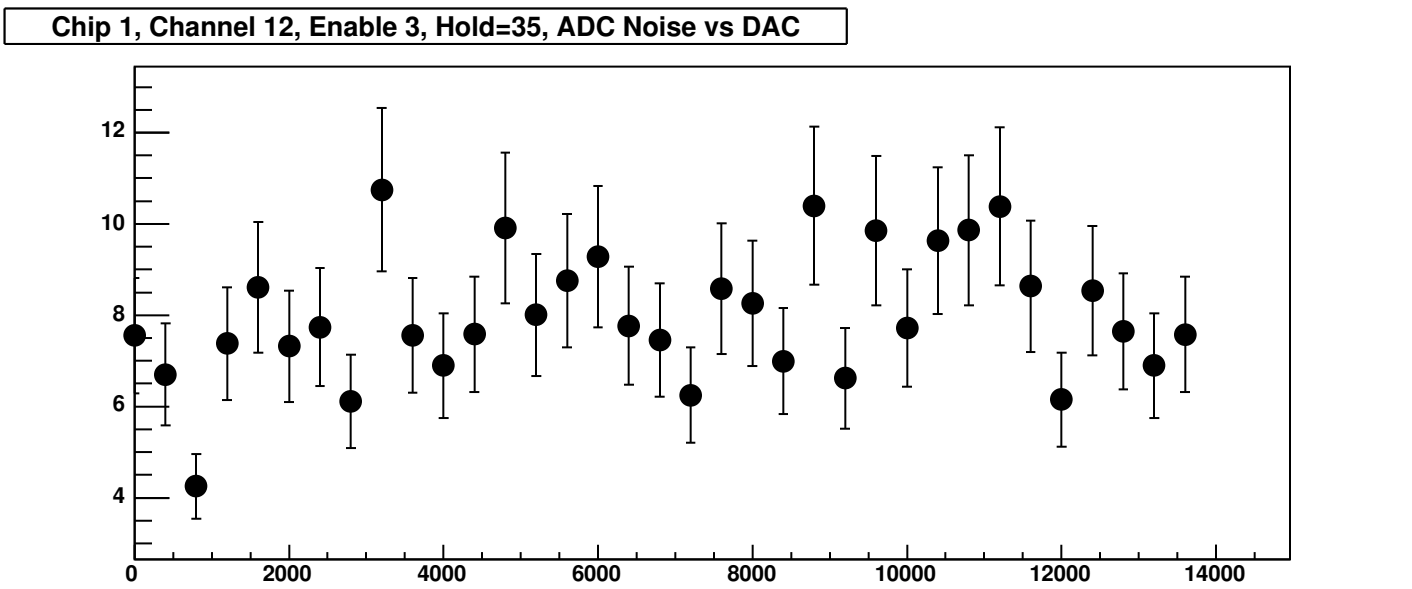

Chip 1, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000

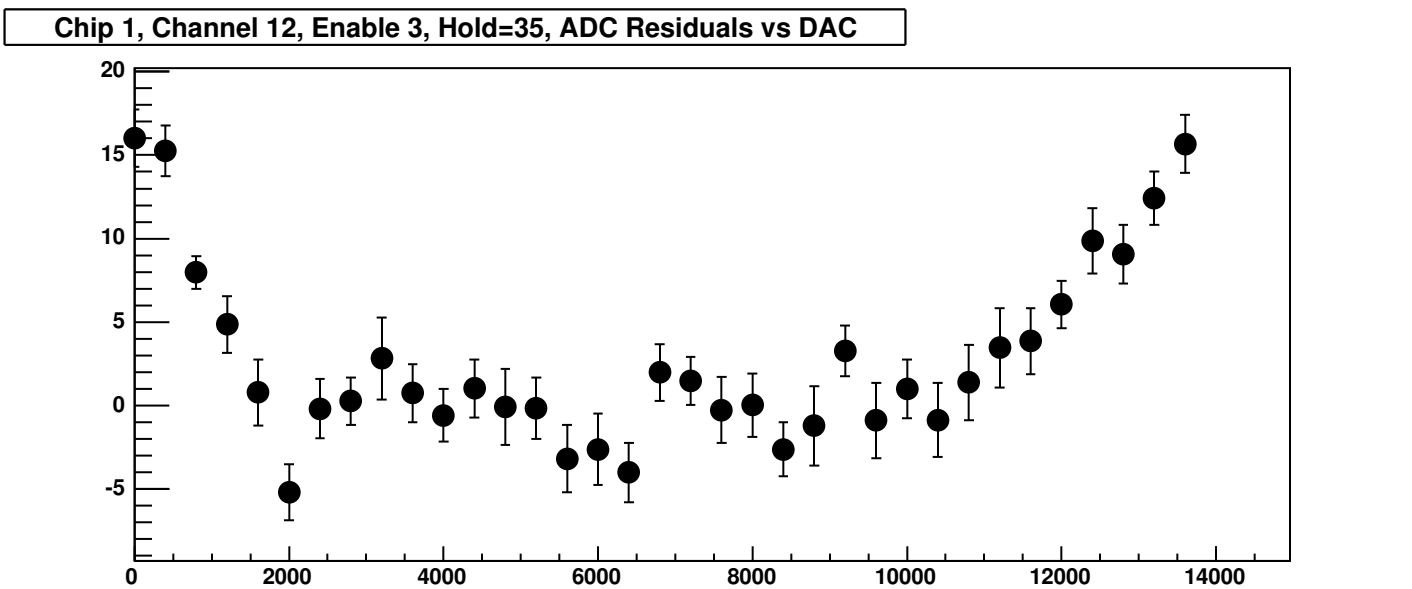

Chip 1, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 3686 / 23
p0 -528.5 ± 1.226
p1 -0.3124 ± 0.0004648
-1000
-2000
-3000
-4000
-5000
-6000
0
2000
4000
6000
8000
10000
12000
14000

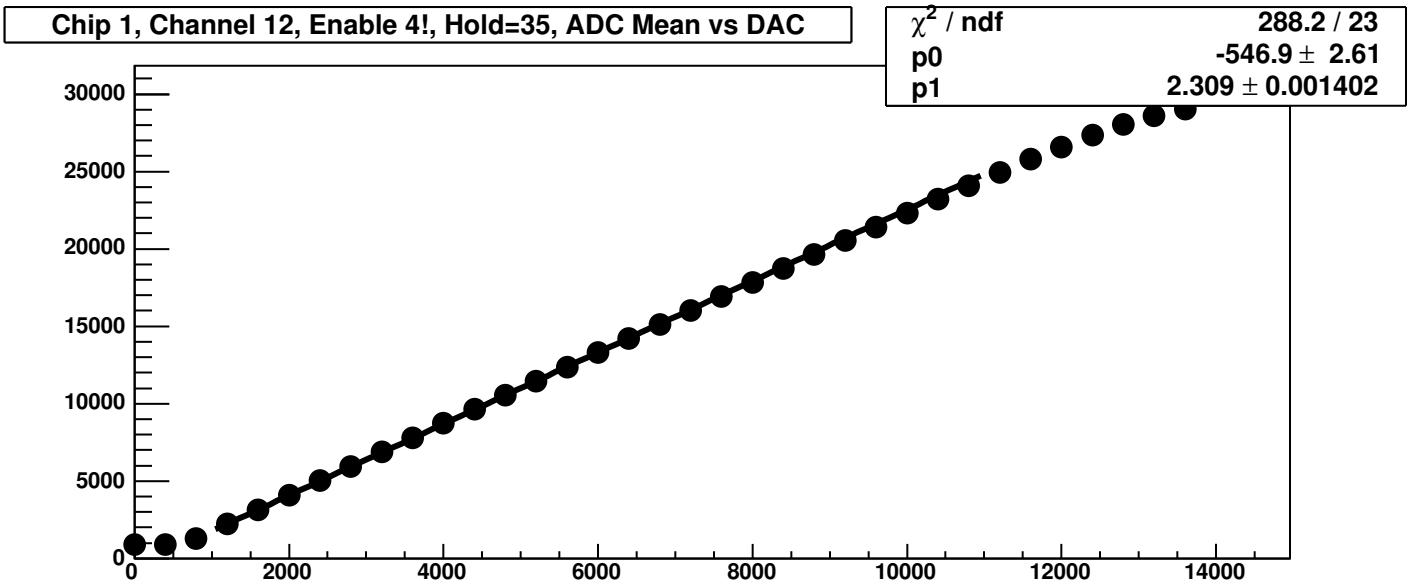

Chip 1, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

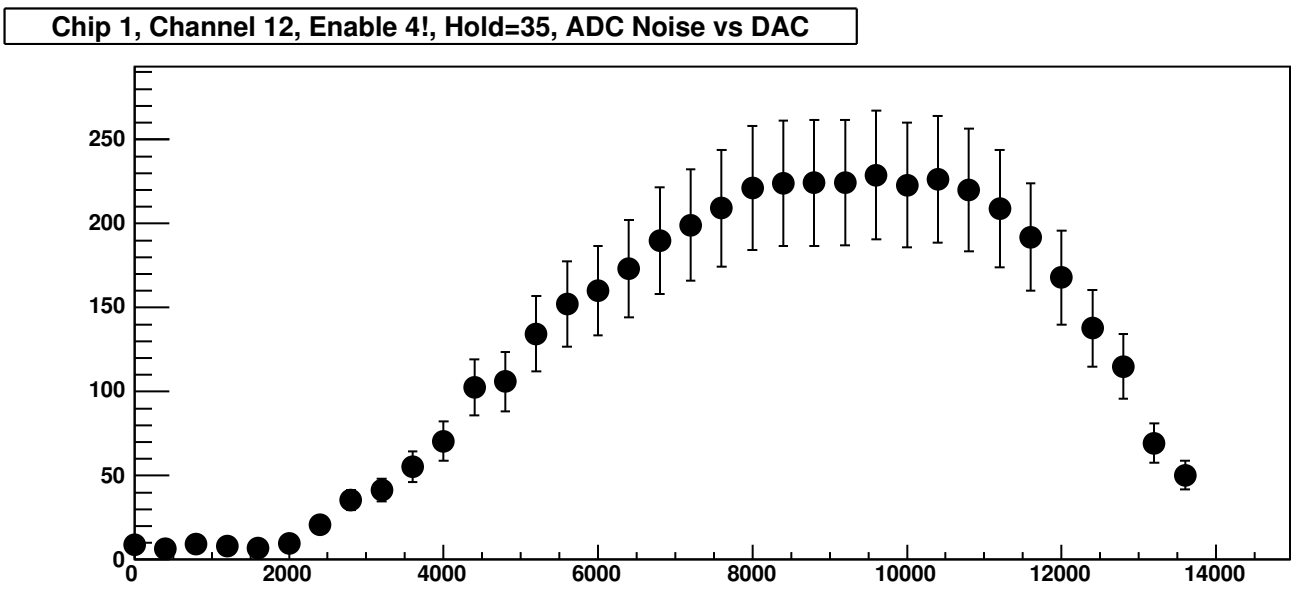

Chip 1, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

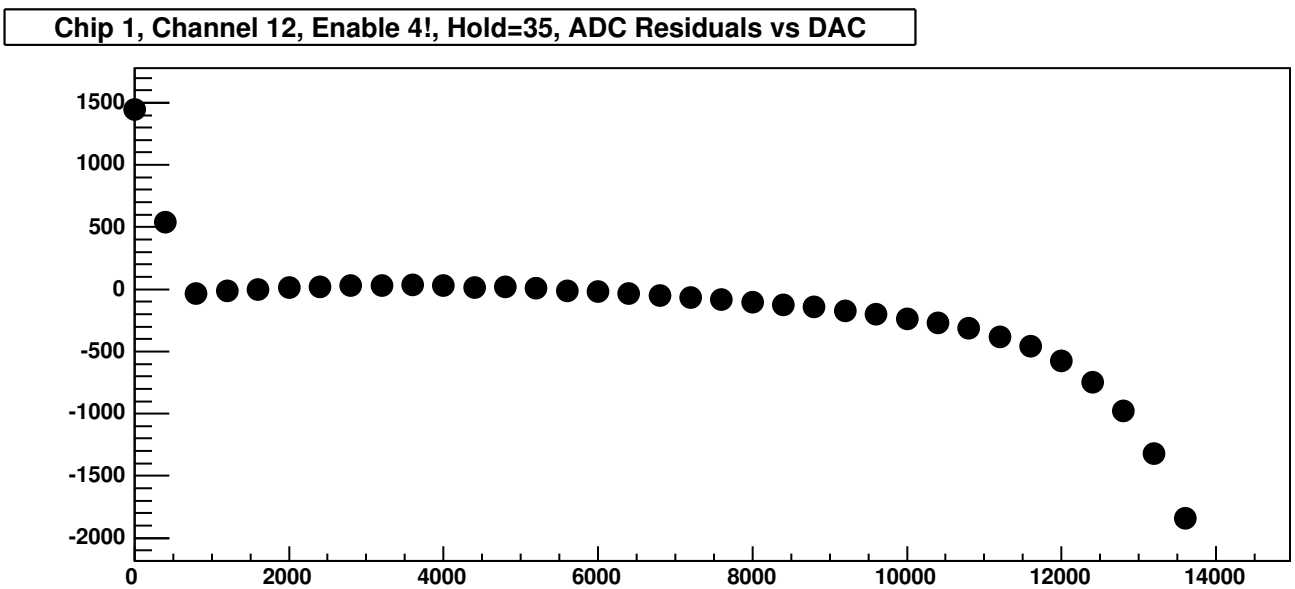

Chip 1, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 40.06 / 23
p0 -610.4 ± 0.8275
p1 0.01399 ± 0.0001283
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

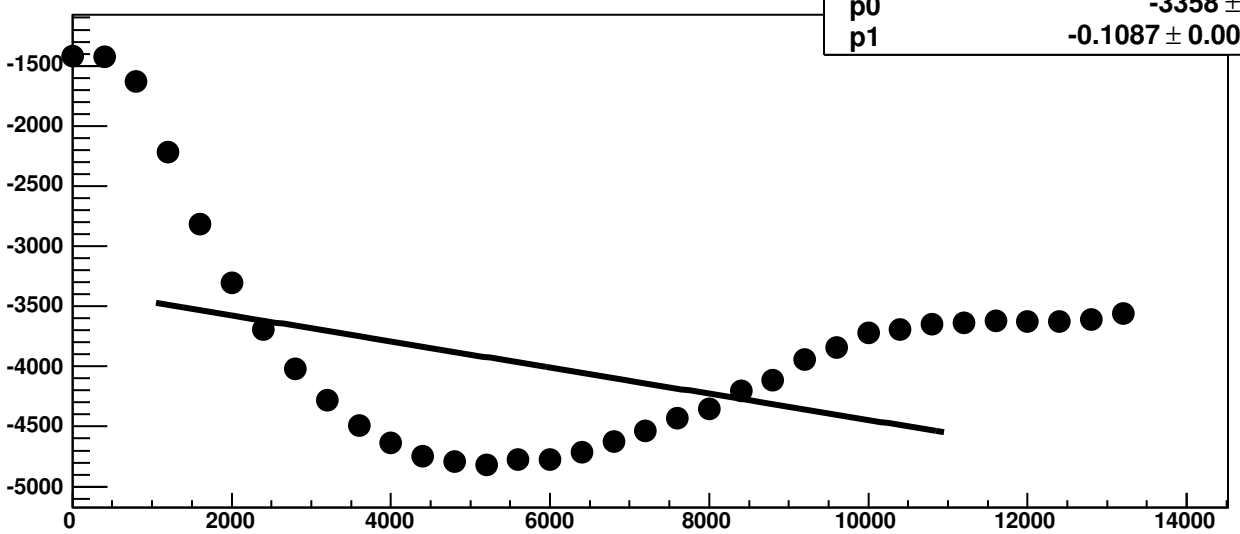

Chip 1, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 288.2 / 23
p0 -546.9 ± 2.61
p1 2.309 ± 0.001402
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

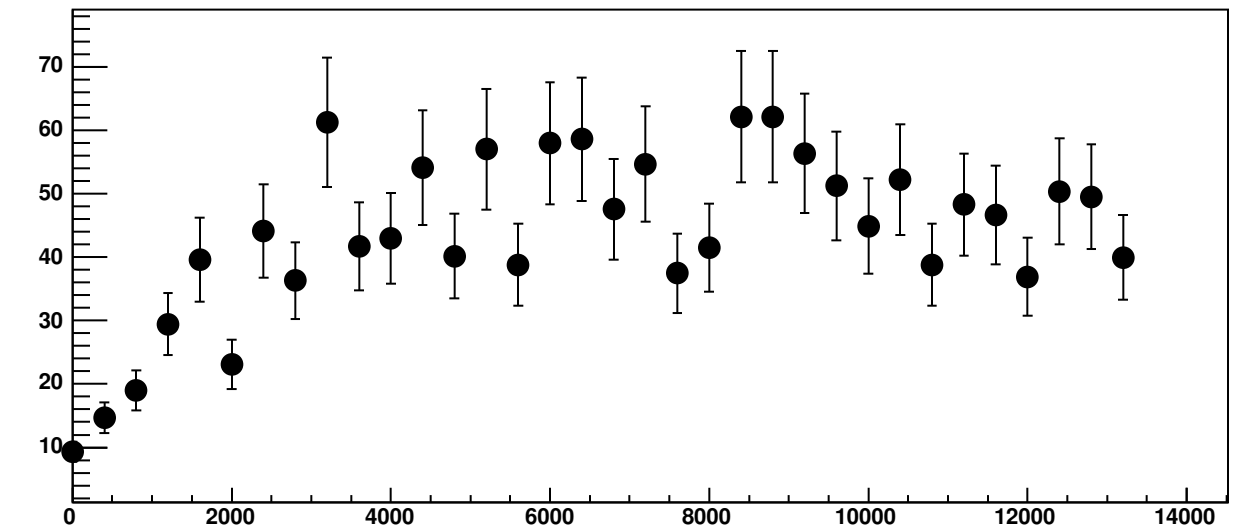

Chip 1, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

Chip 1, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

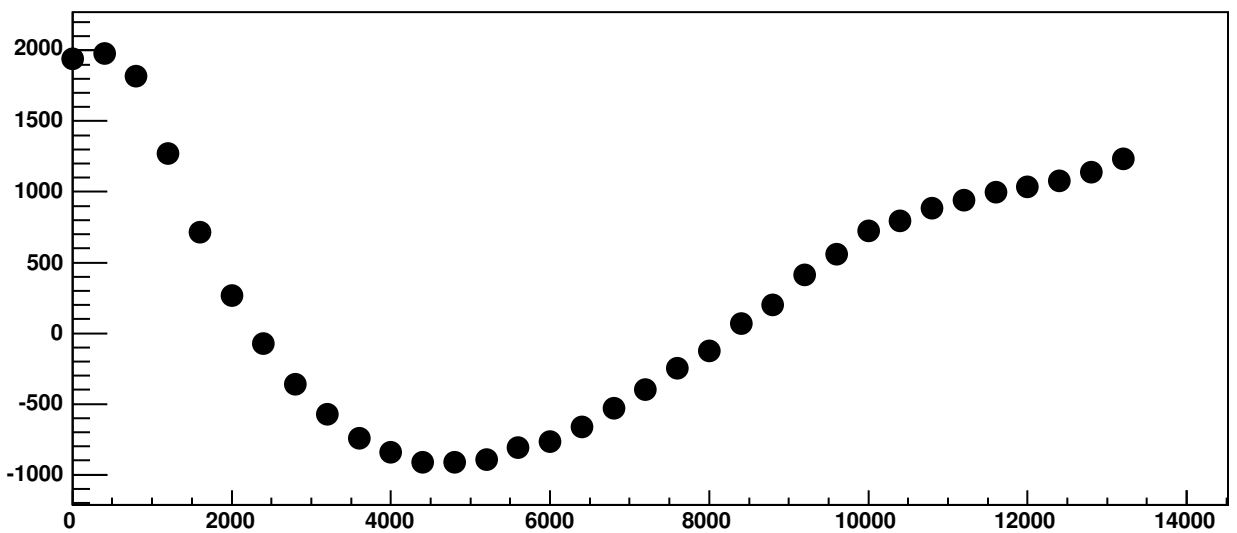
χ^2 / ndf	1.22e+05 / 23
p0	-3358 ± 3.822
p1	-0.1087 ± 0.0006353

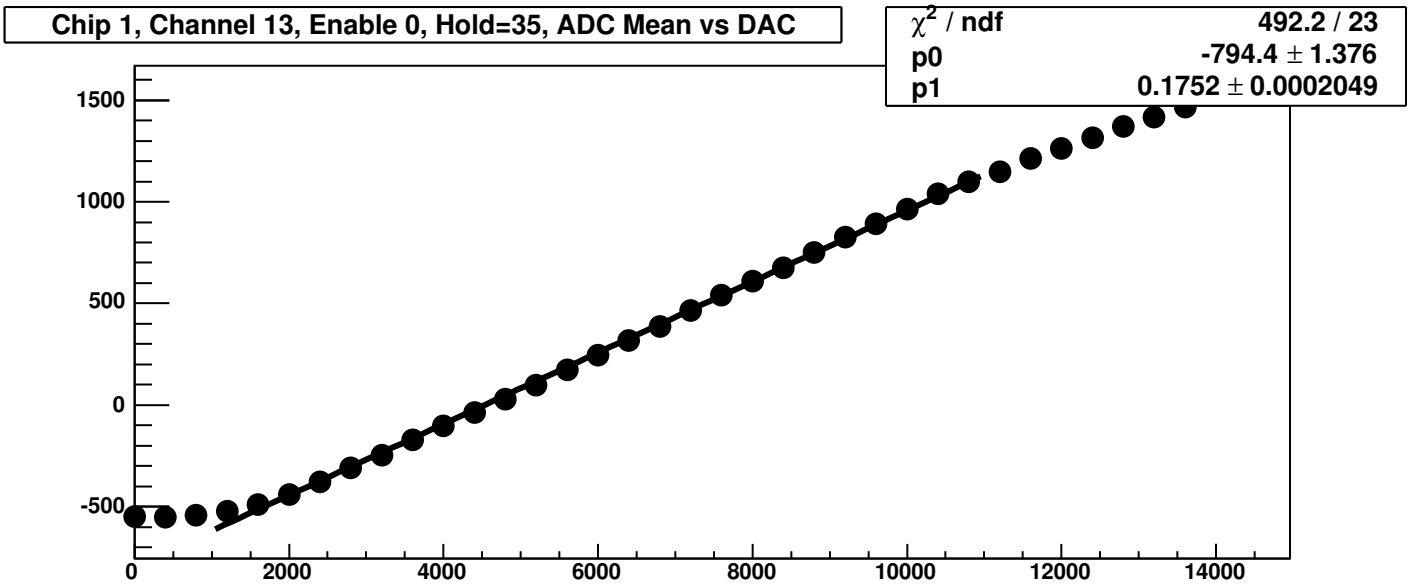


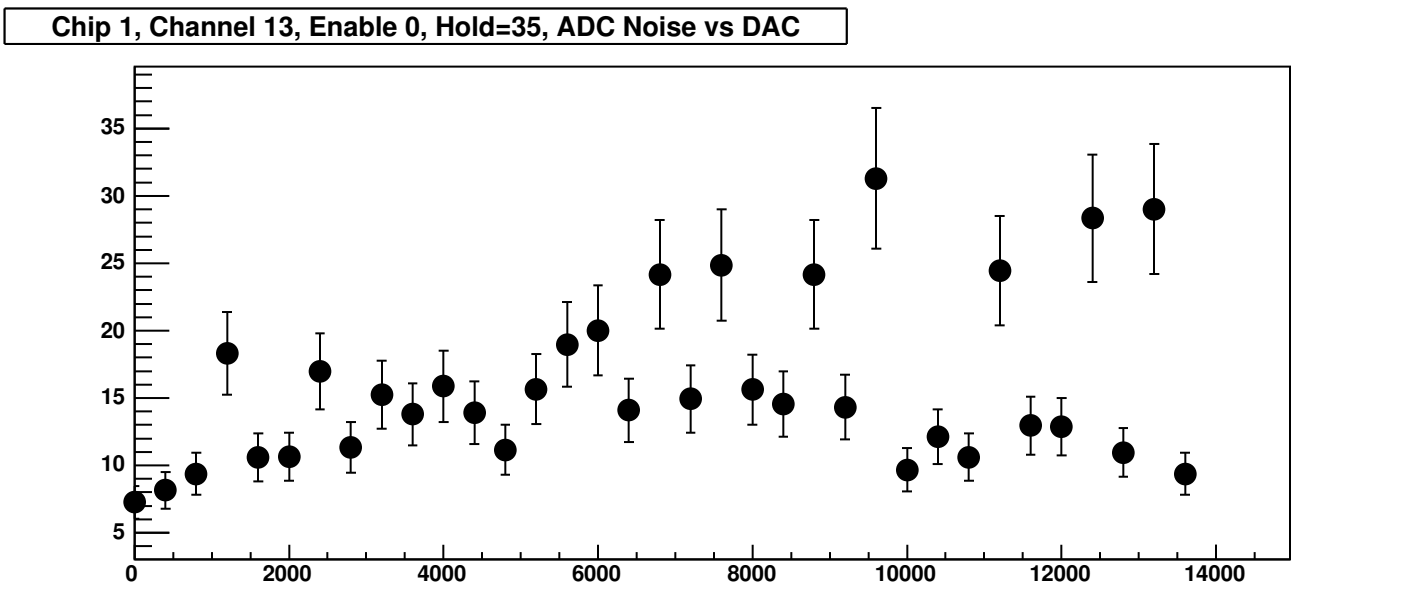
Chip 1, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

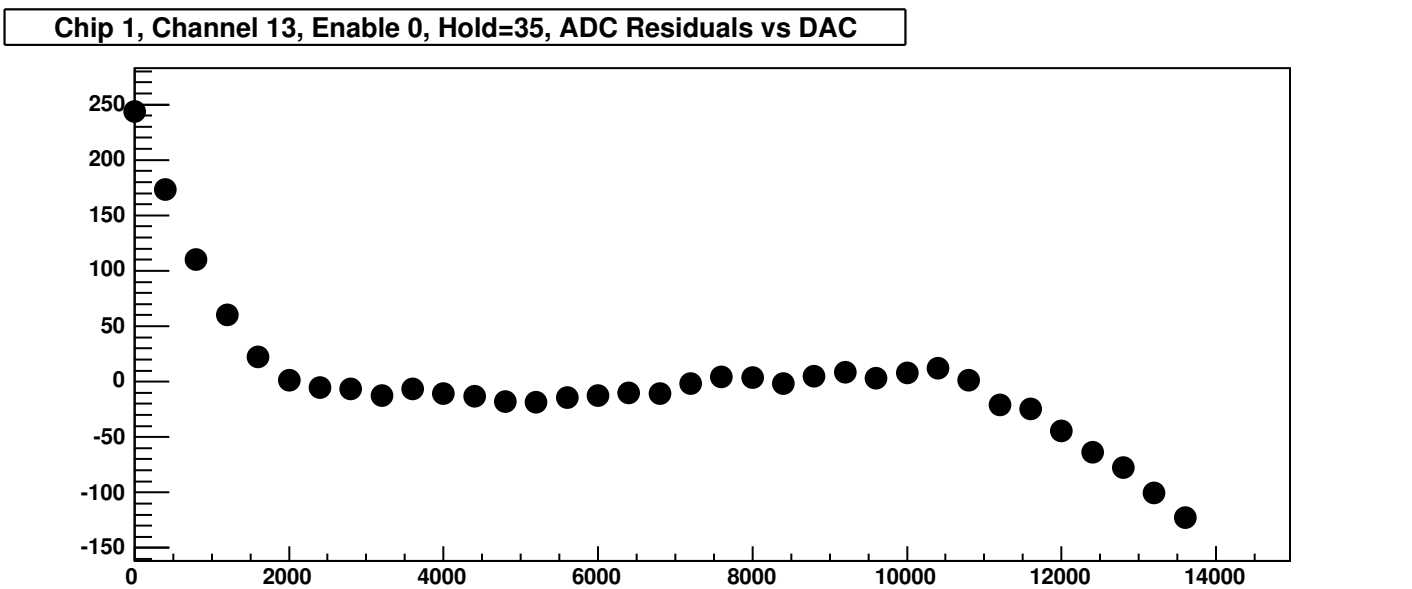


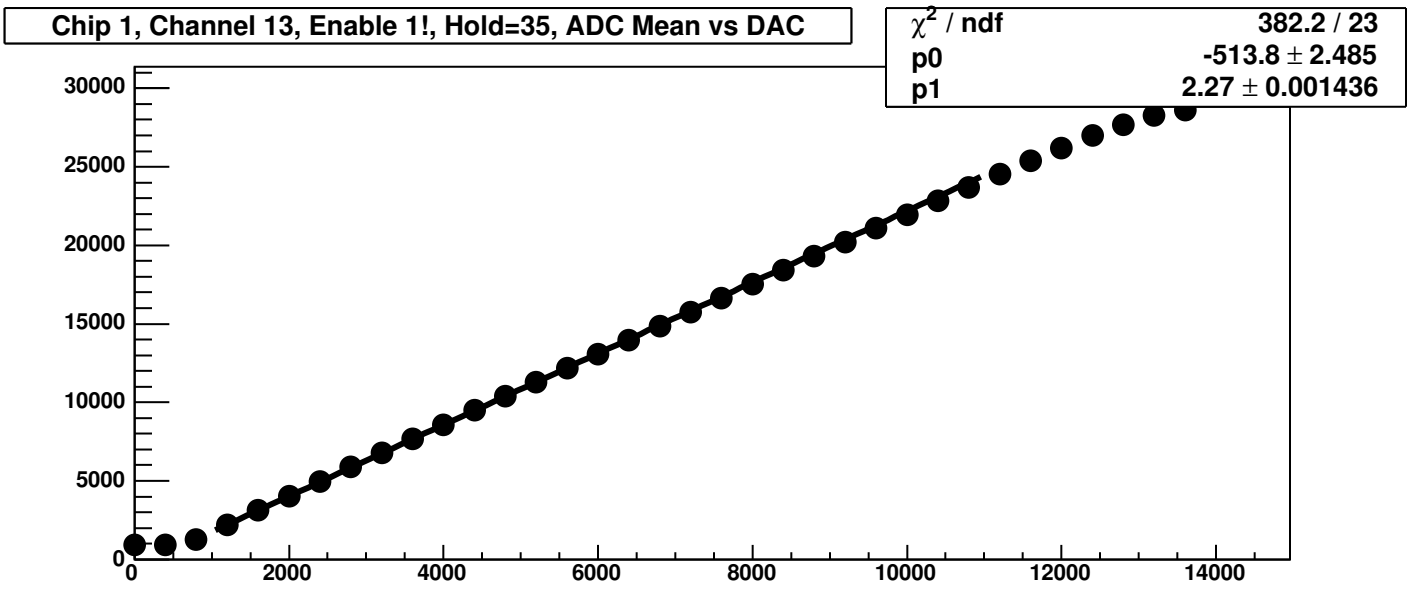
Chip 1, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

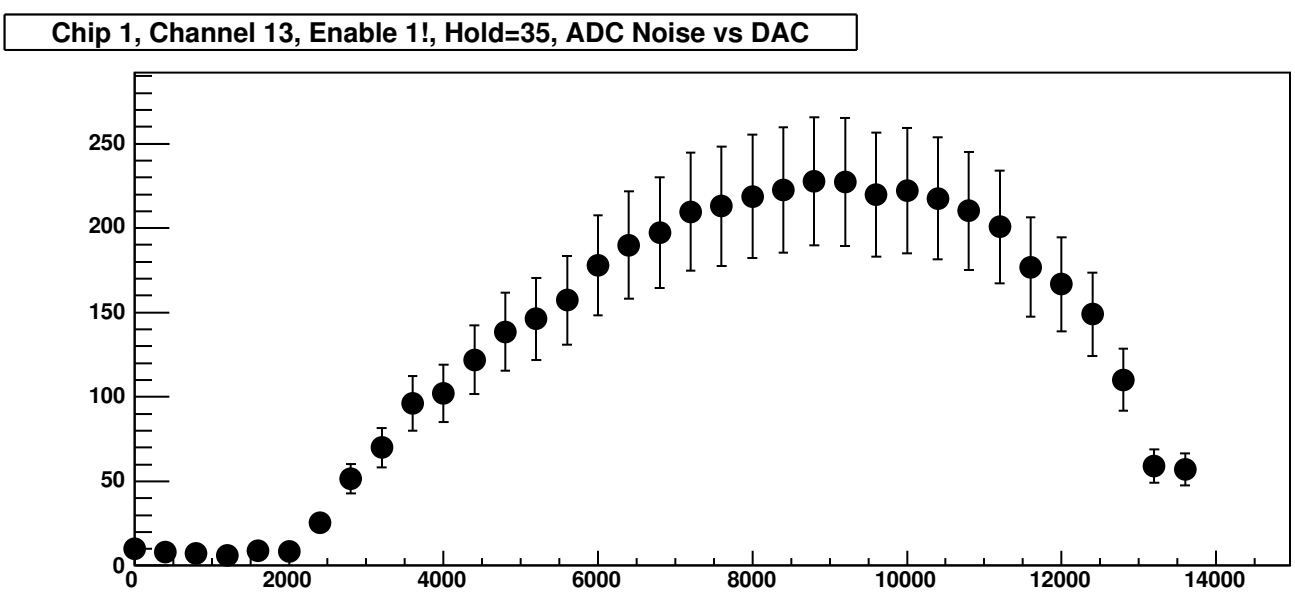


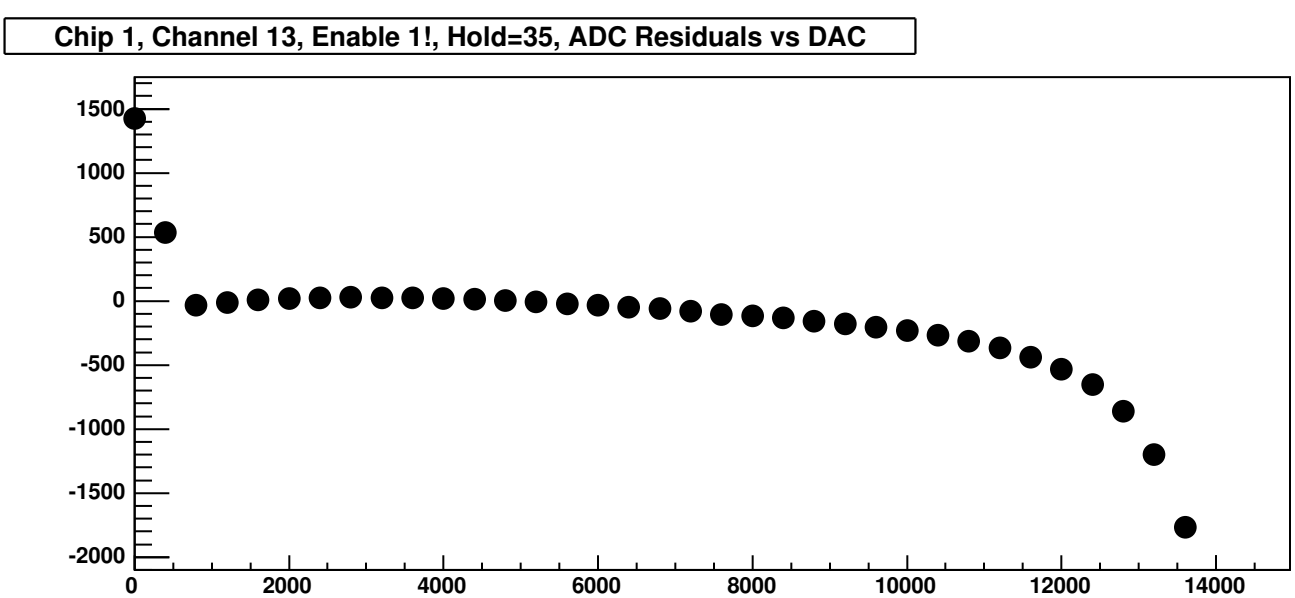

Chip 1, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 492.2 / 23
p0 -794.4 ± 1.376
p1 0.1752 ± 0.0002049
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

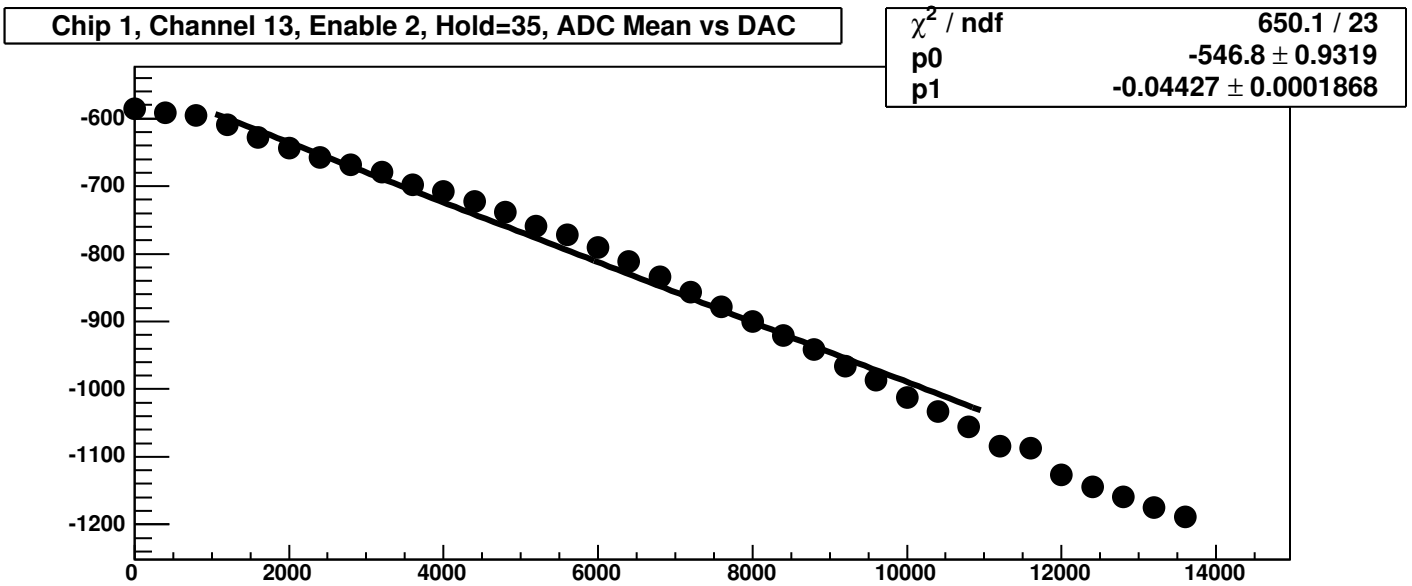

Chip 1, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

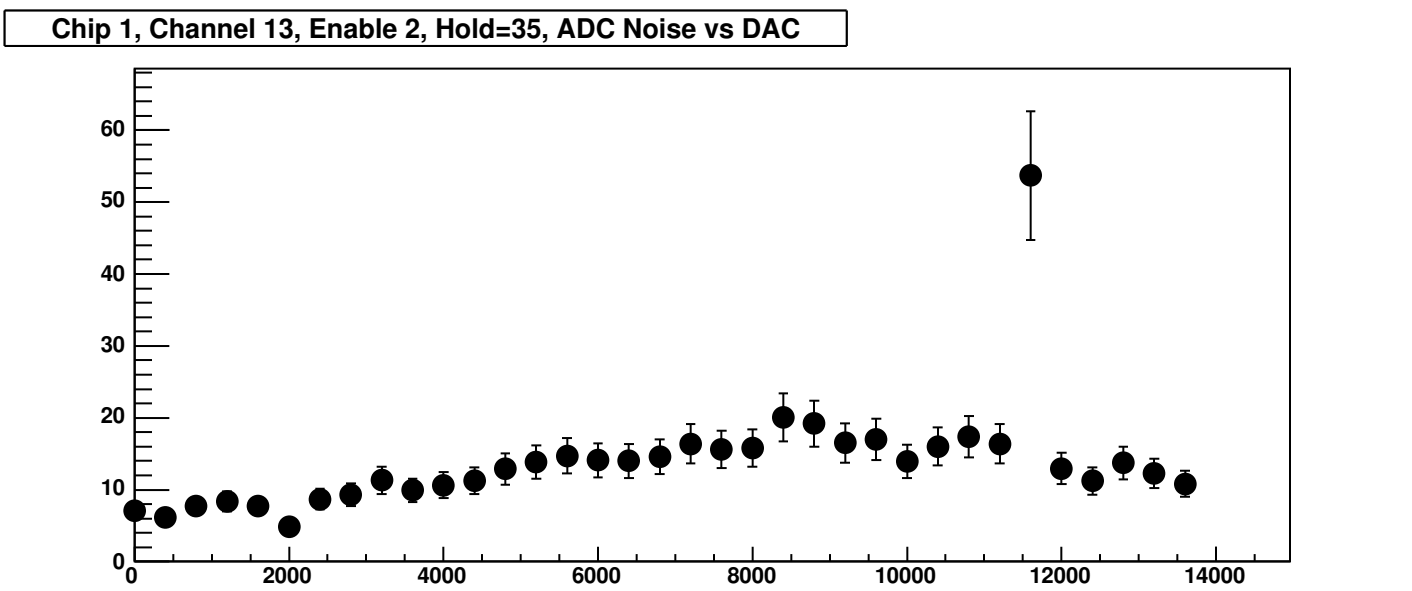

Chip 1, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

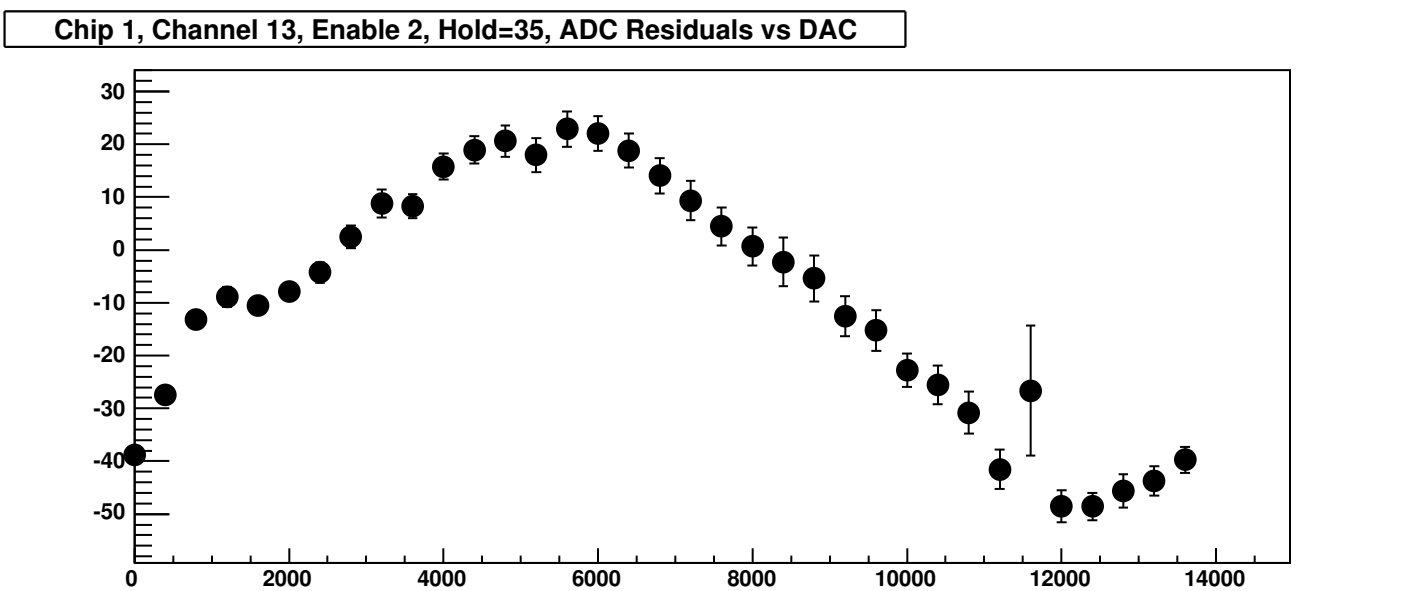

Chip 1, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 382.2 / 23
p0 -513.8 ± 2.485
p1 2.27 ± 0.001436
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

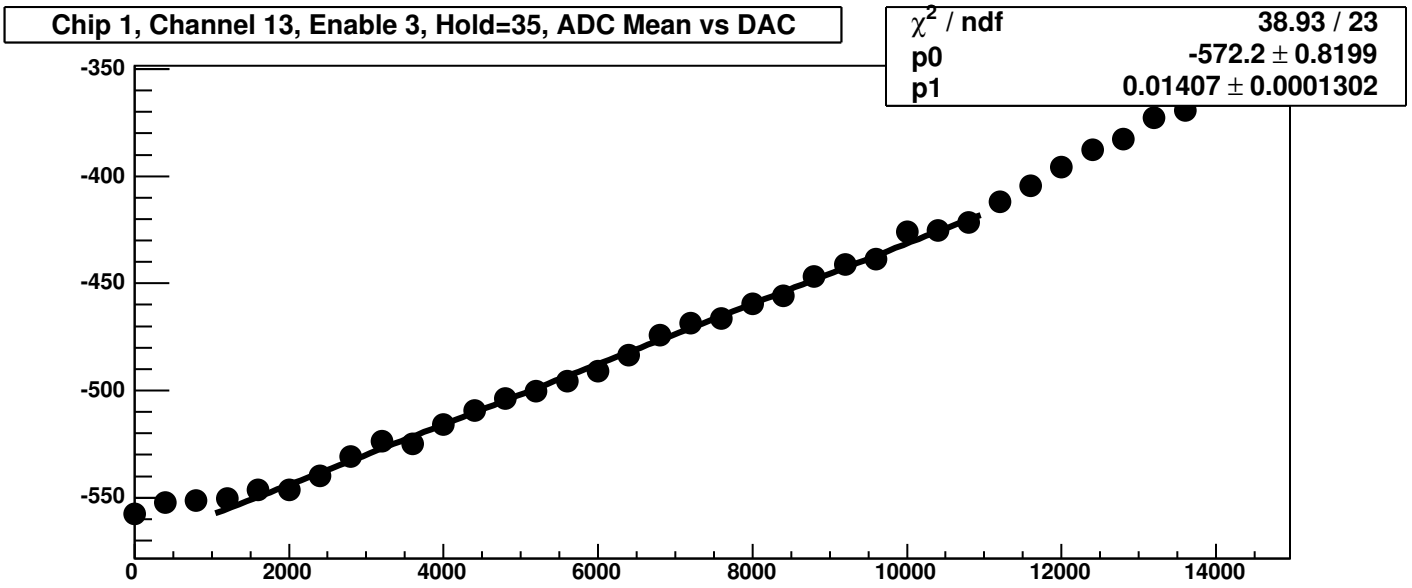

Chip 1, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

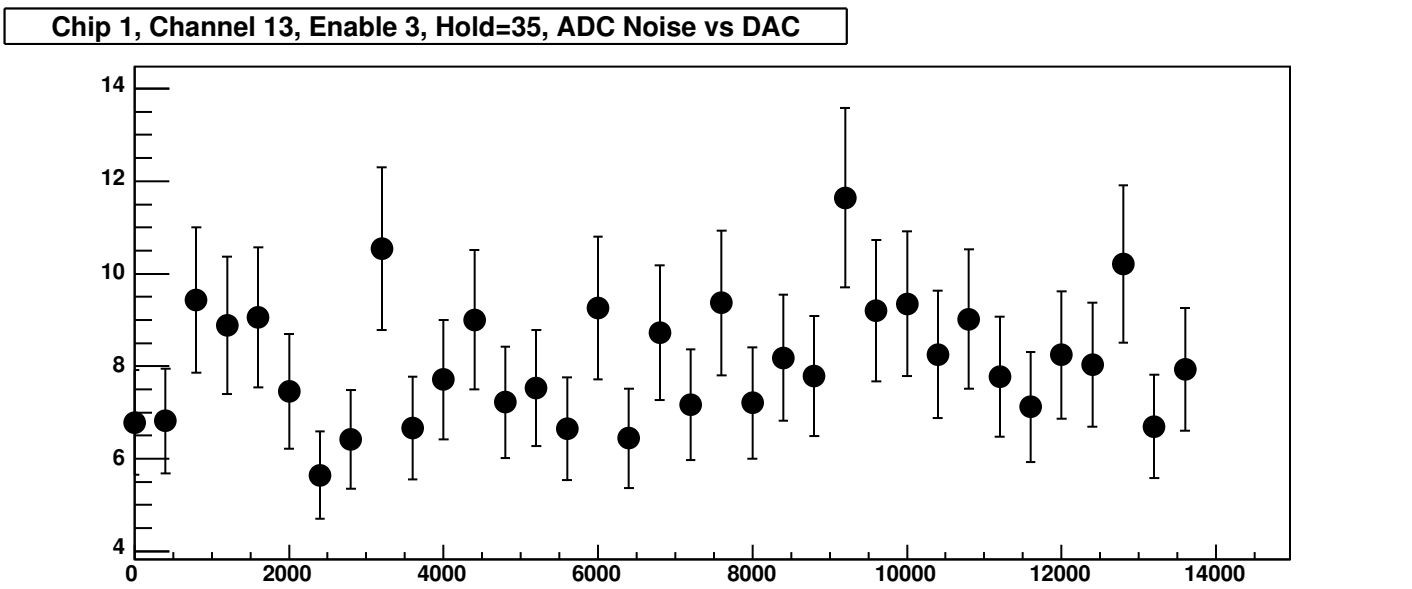

Chip 1, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

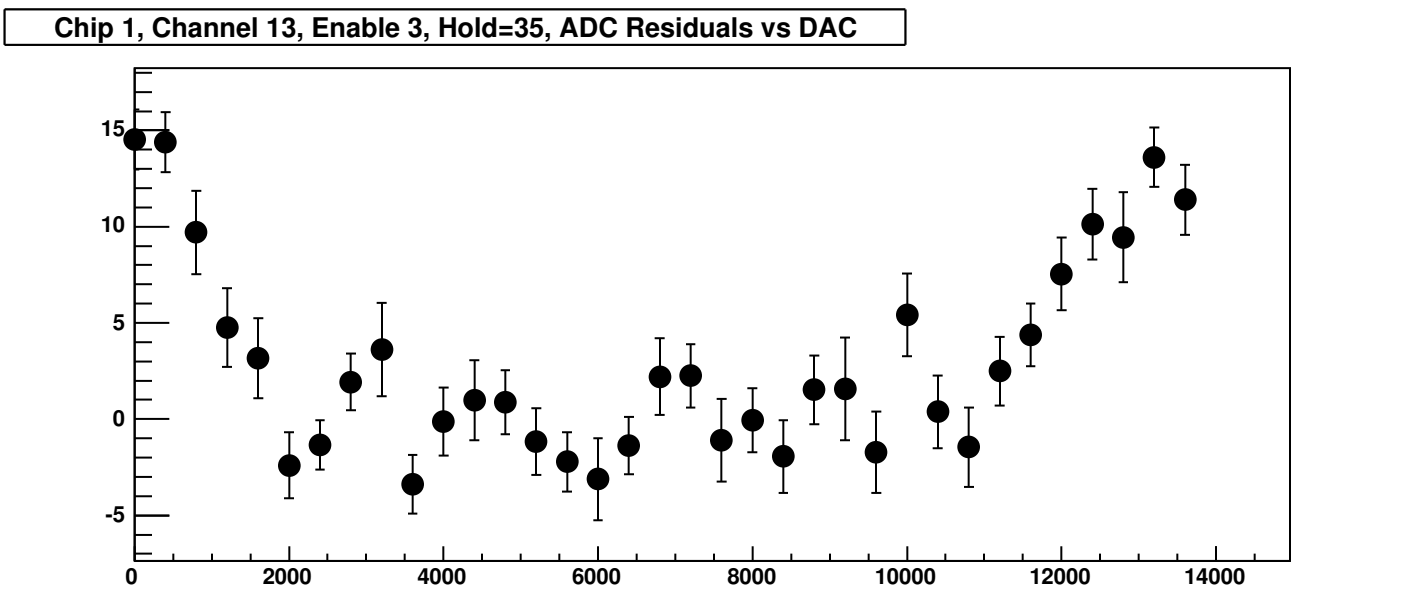

Chip 1, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 650.1 / 23
p0 -546.8 ± 0.9319
p1 -0.04427 ± 0.0001868
-600
-700
-800
-900
-1000
-1100
-1200
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

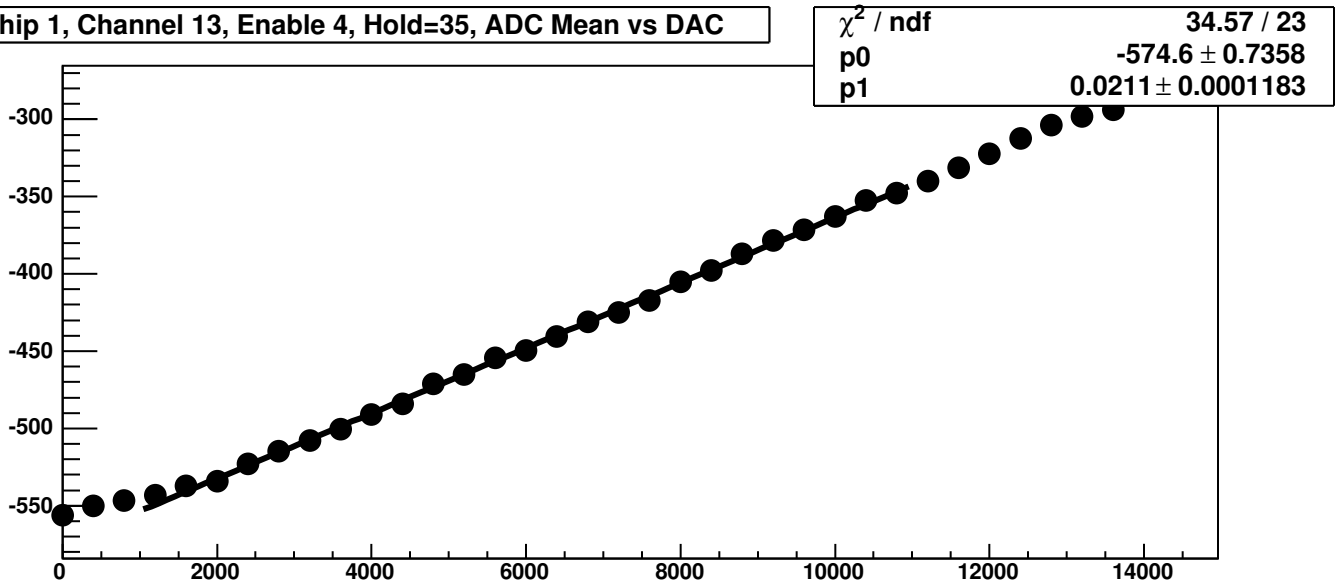

Chip 1, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.93 / 23
p0 -572.2 ± 0.8199
p1 0.01407 ± 0.0001302
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

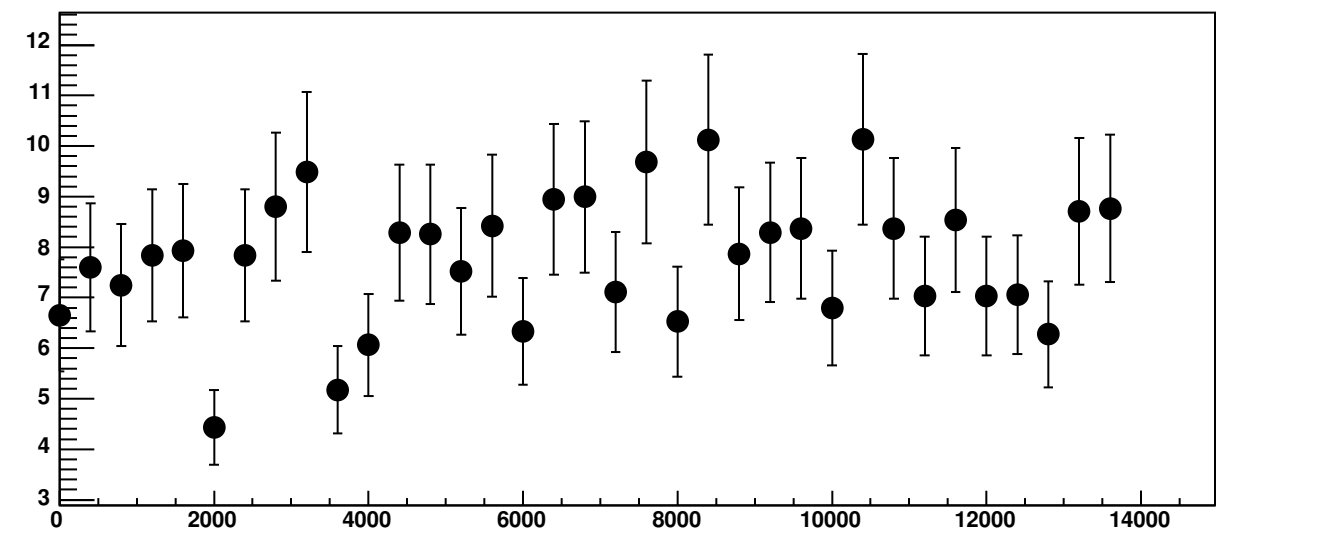

Chip 1, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

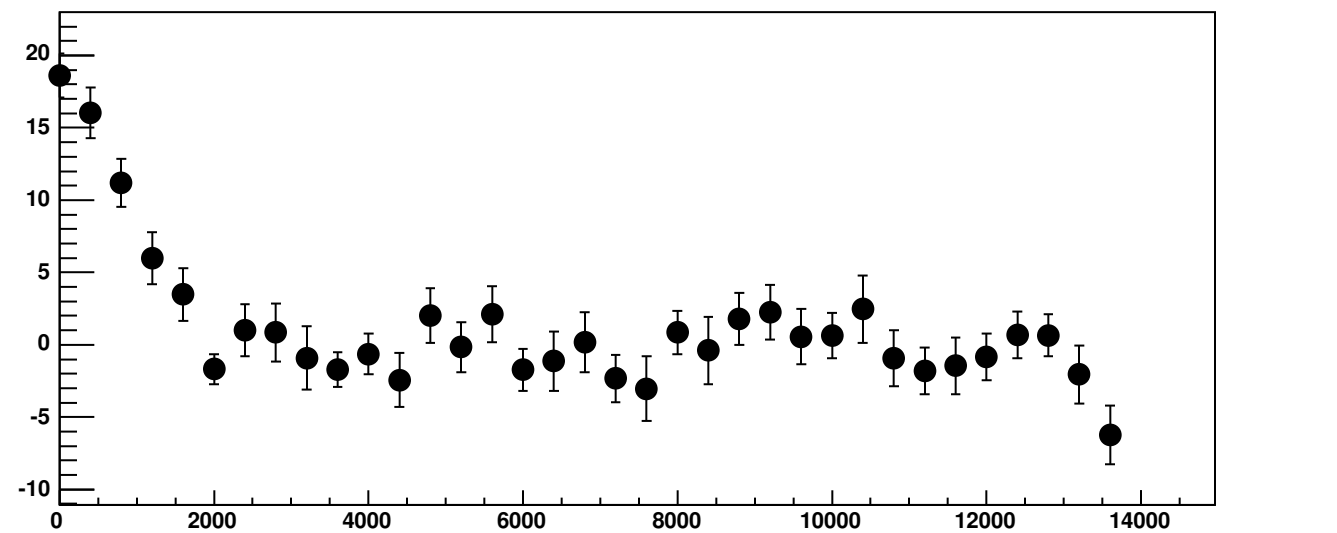
Chip 1, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC



Chip 1, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC

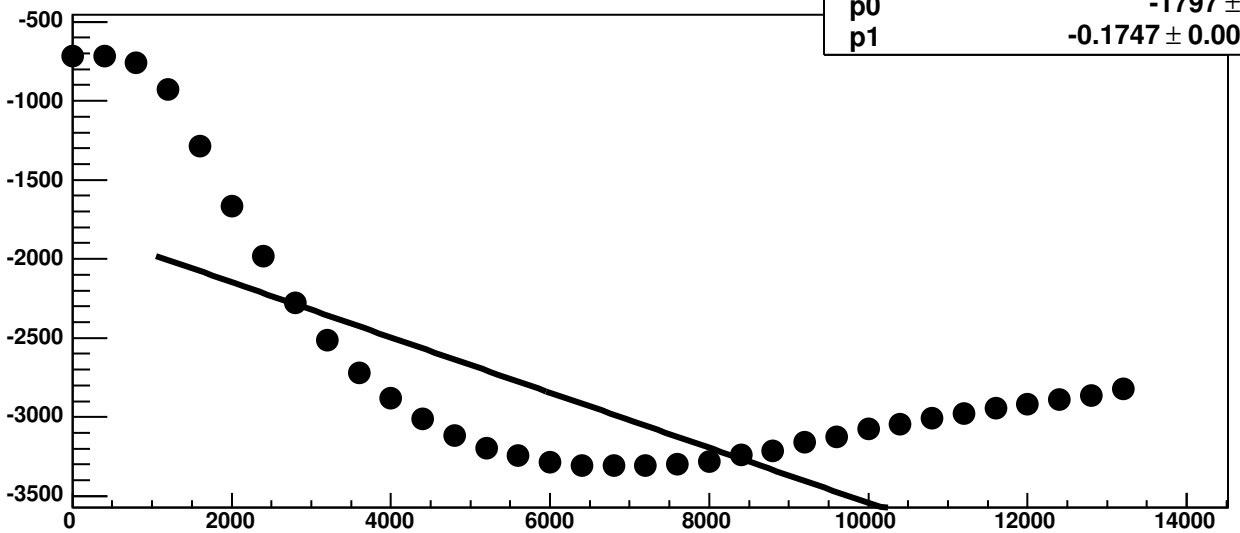


Chip 1, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC

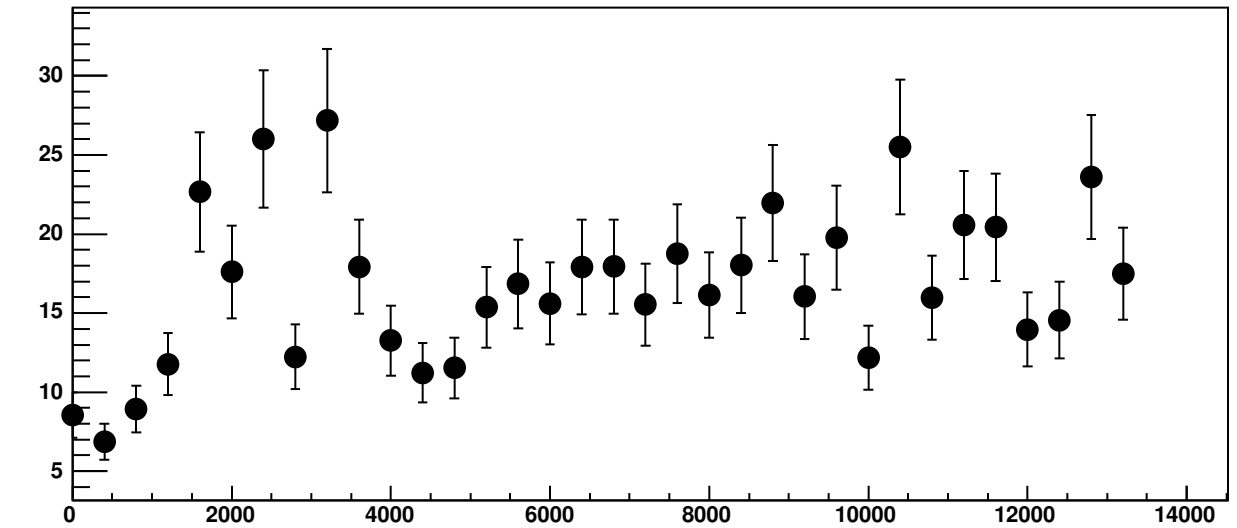


Chip 1, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

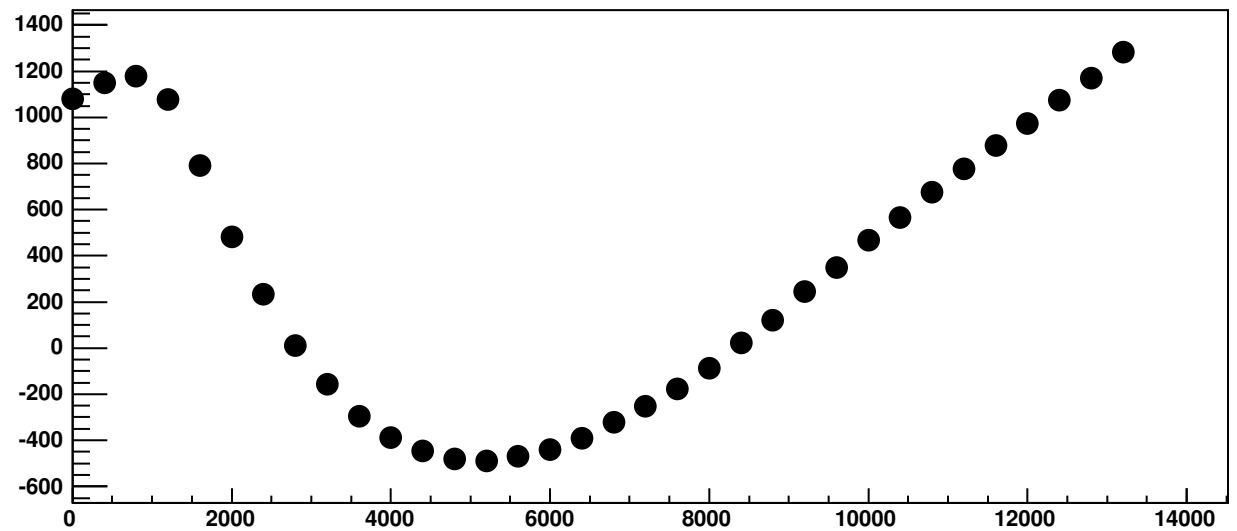
χ^2 / ndf	4.376e+05 / 23
p0	-1797 ± 1.657
p1	-0.1747 ± 0.0002582

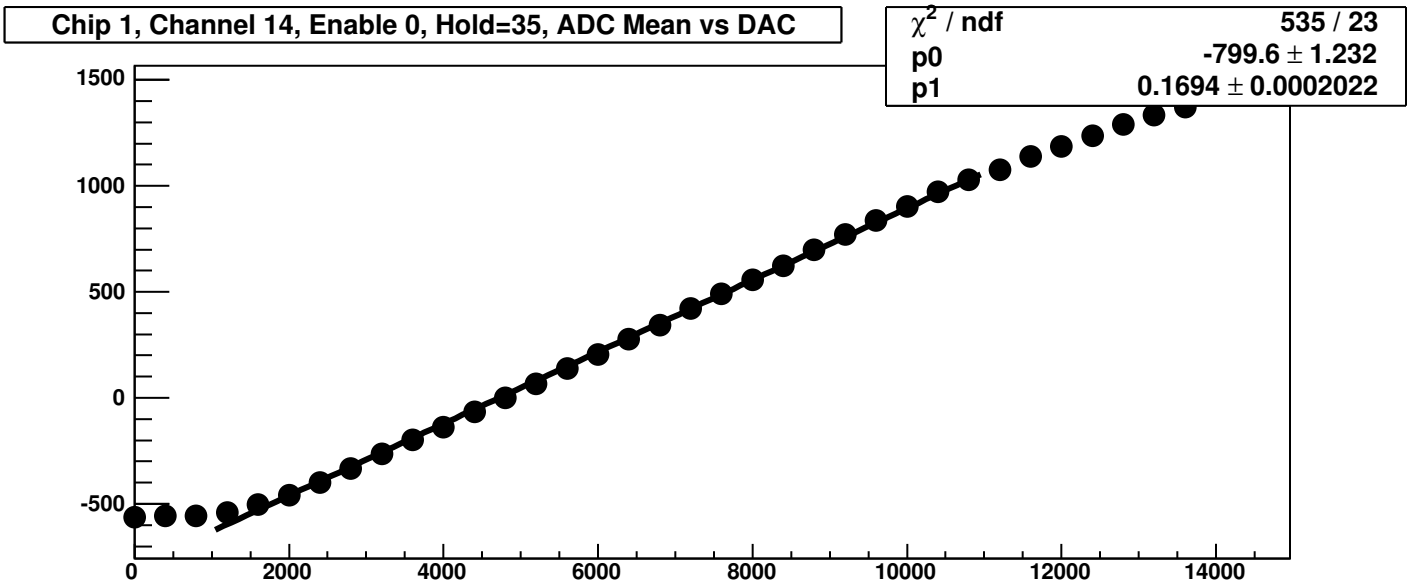


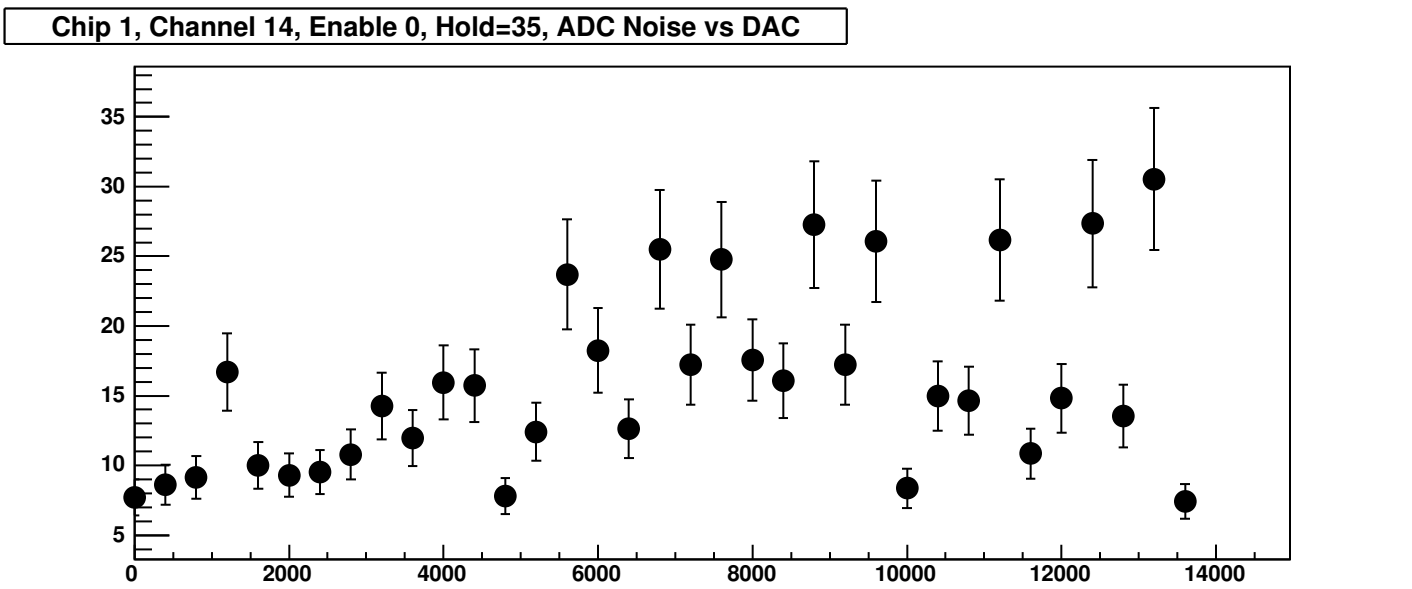
Chip 1, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

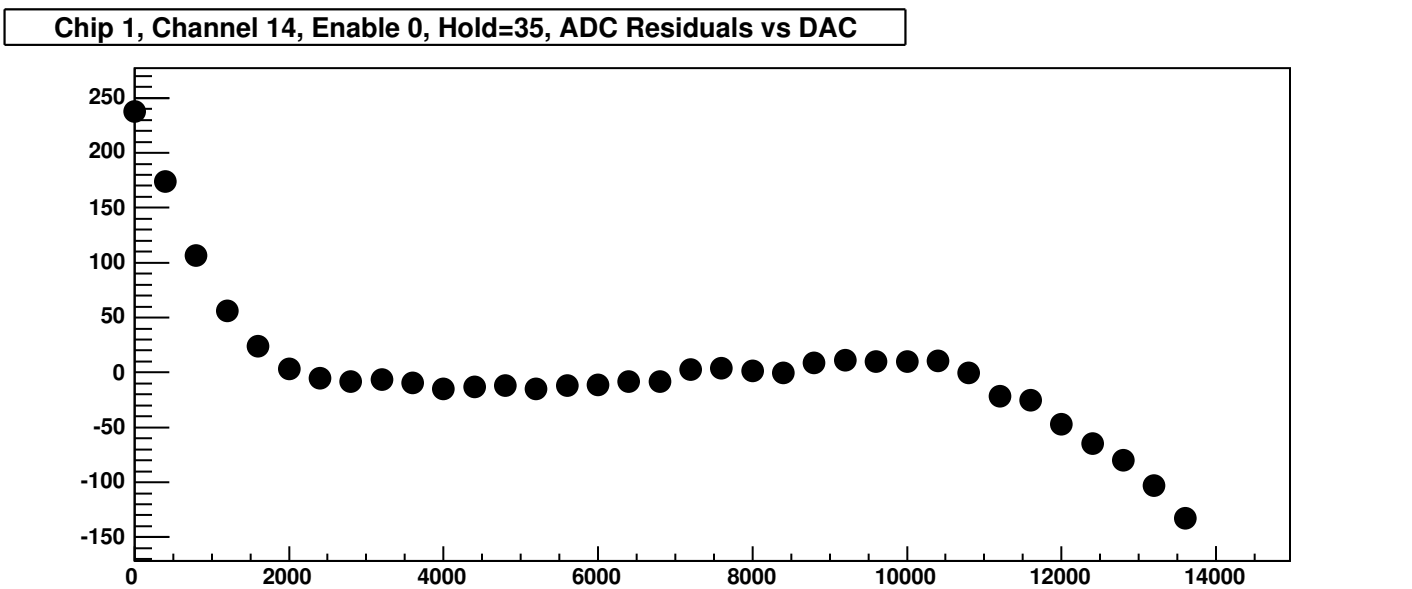


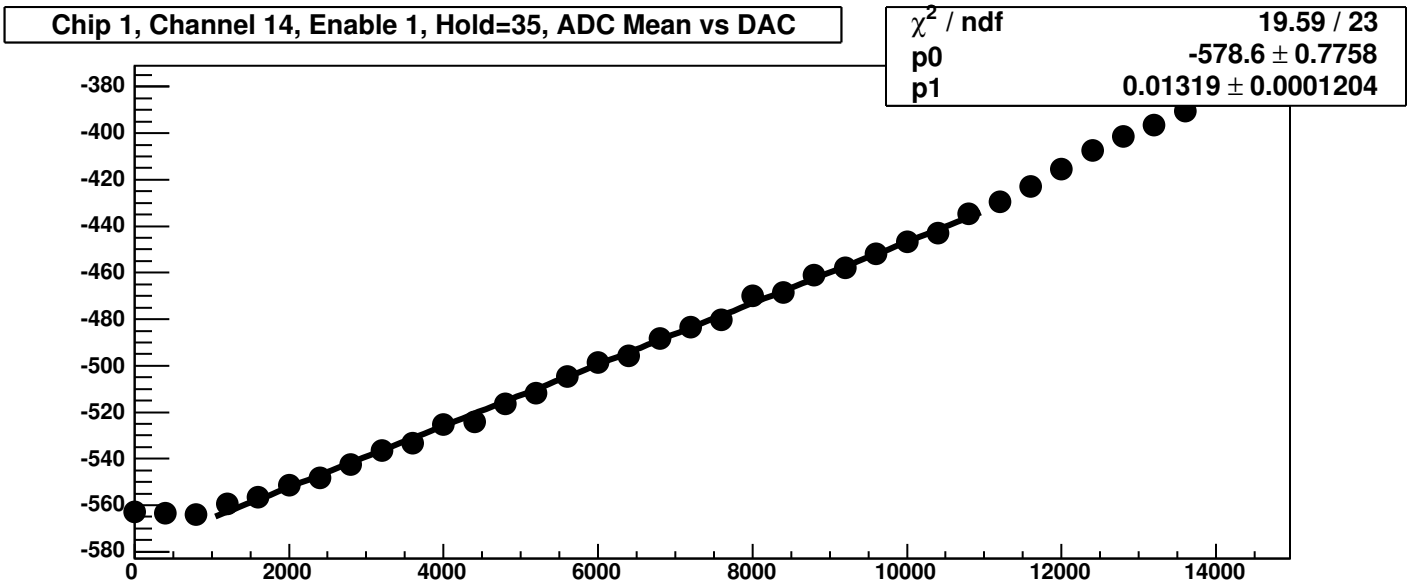
Chip 1, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

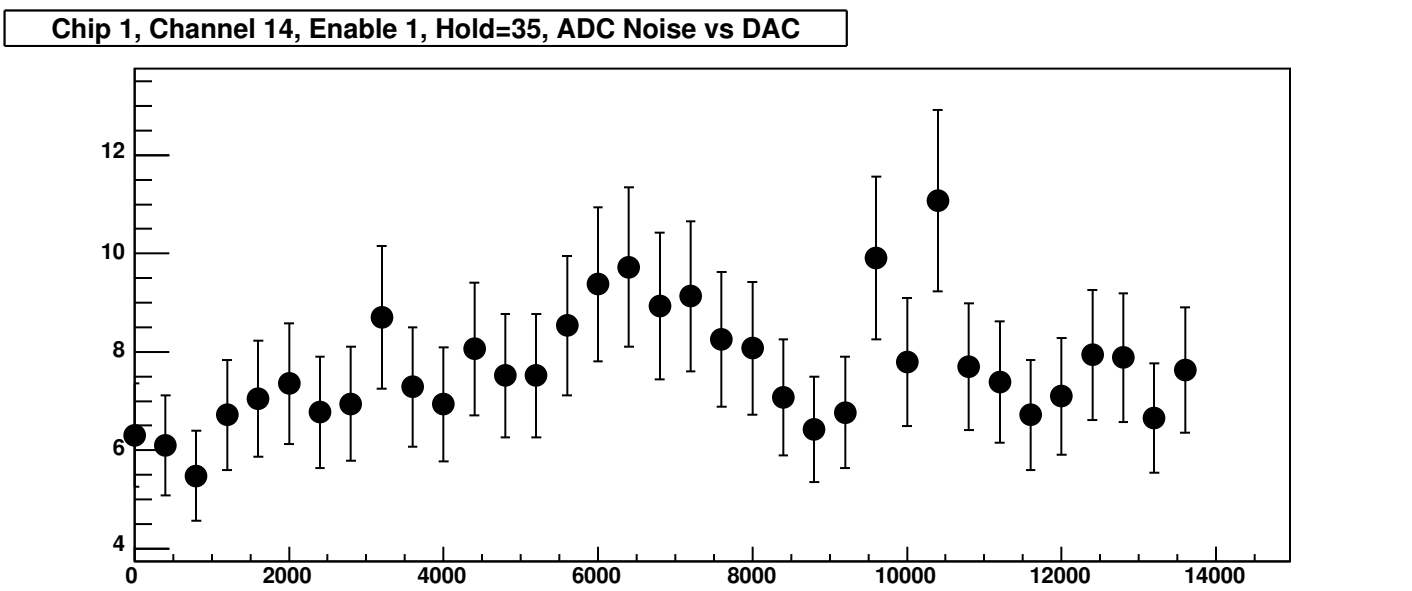


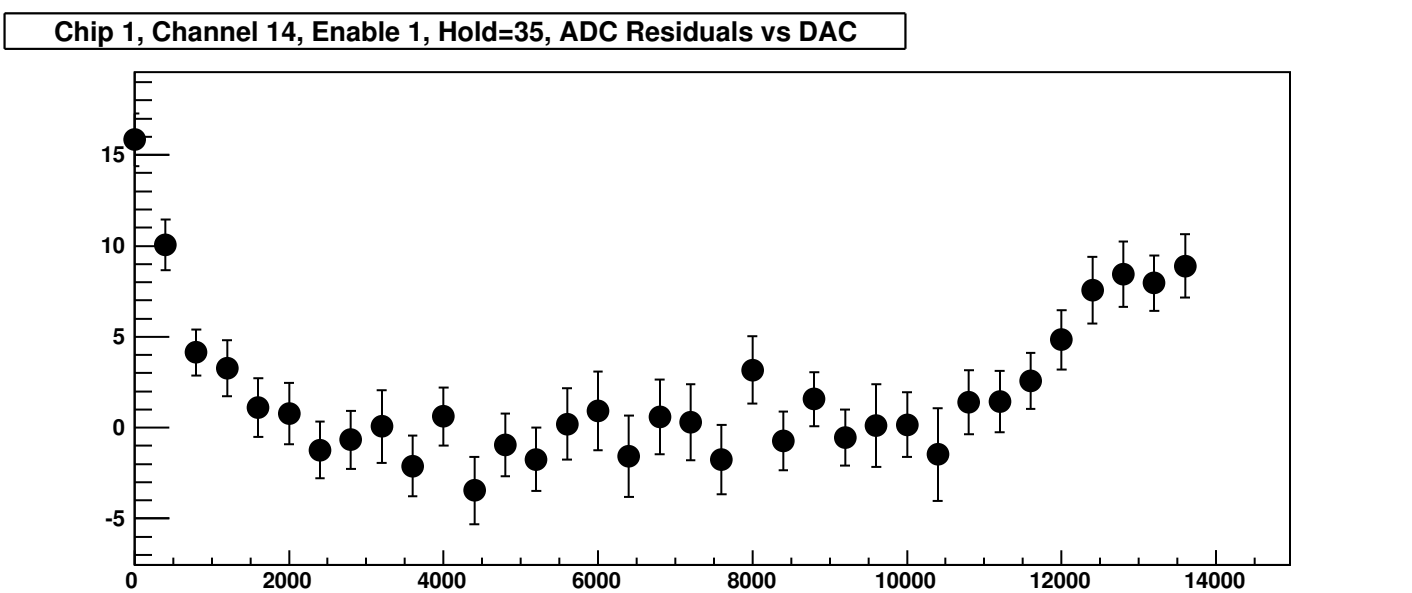

Chip 1, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 535 / 23
p0 -799.6 ± 1.232
p1 0.1694 ± 0.0002022
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

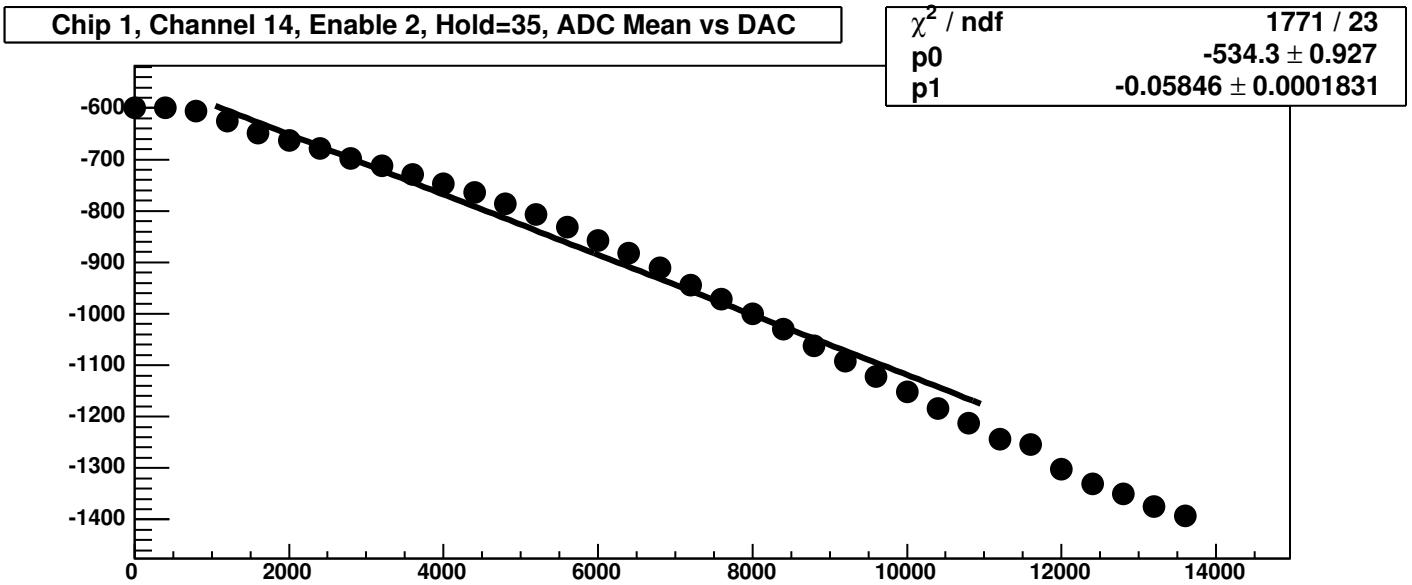

Chip 1, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

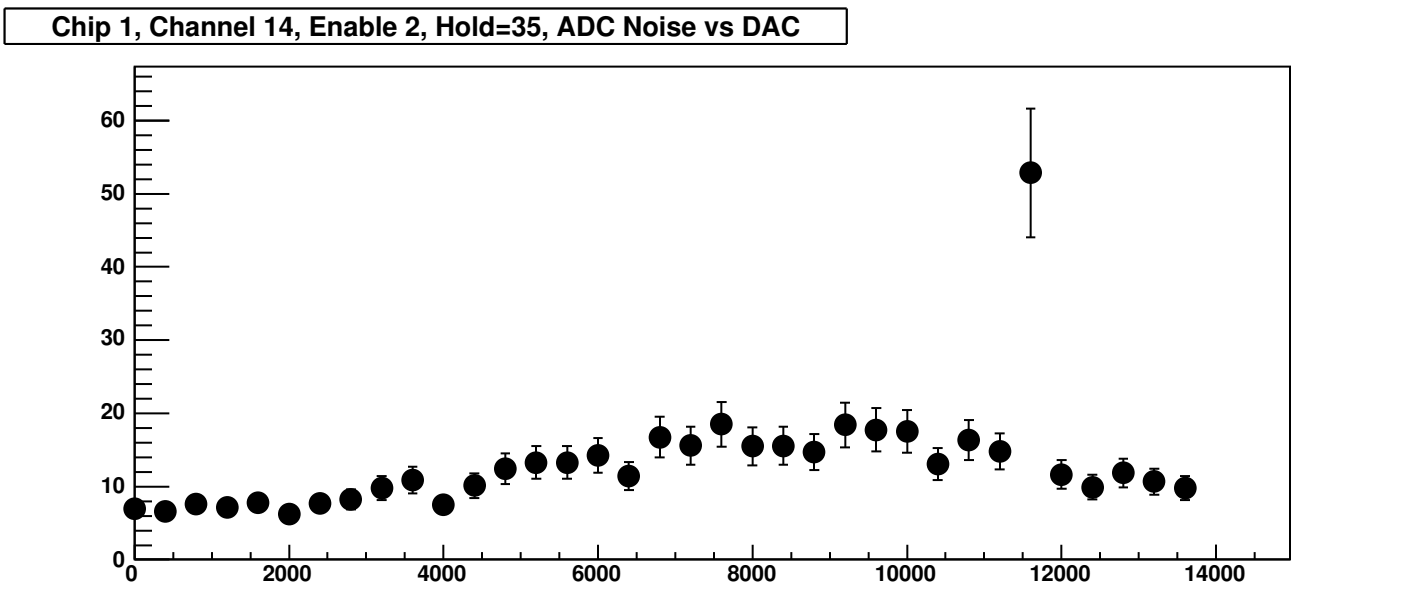

Chip 1, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

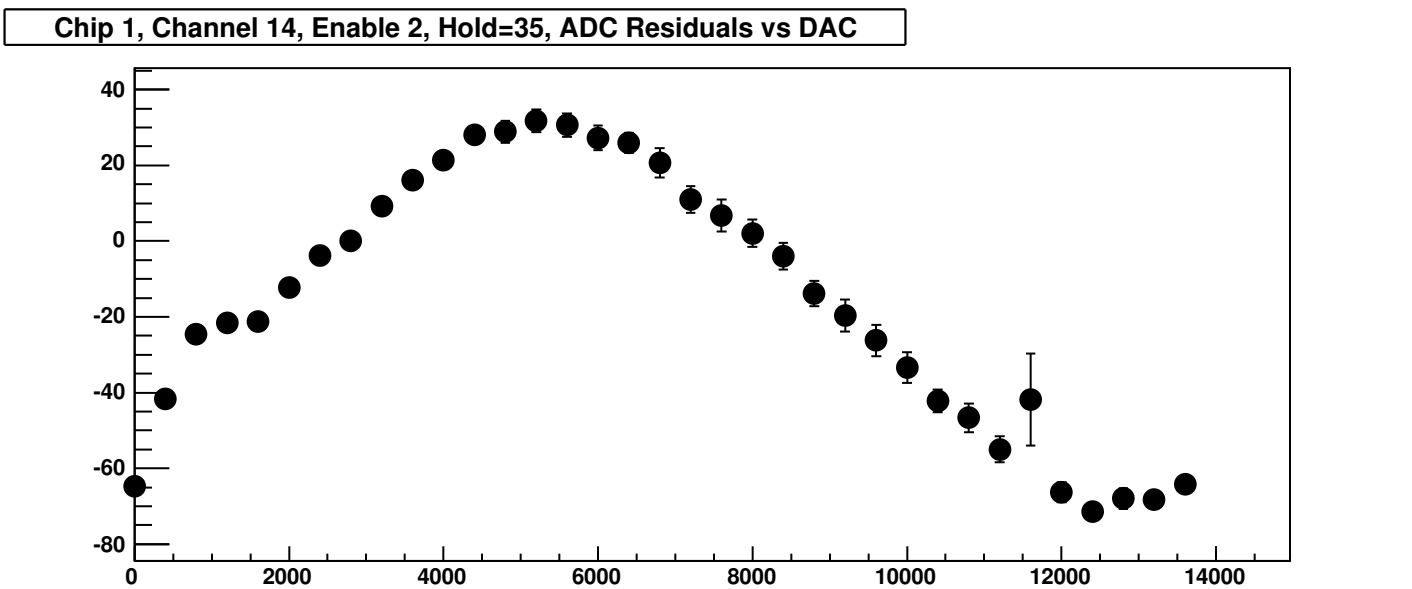

Chip 1, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 19.59 / 23
p0 -578.6 ± 0.7758
p1 0.01319 ± 0.0001204
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000

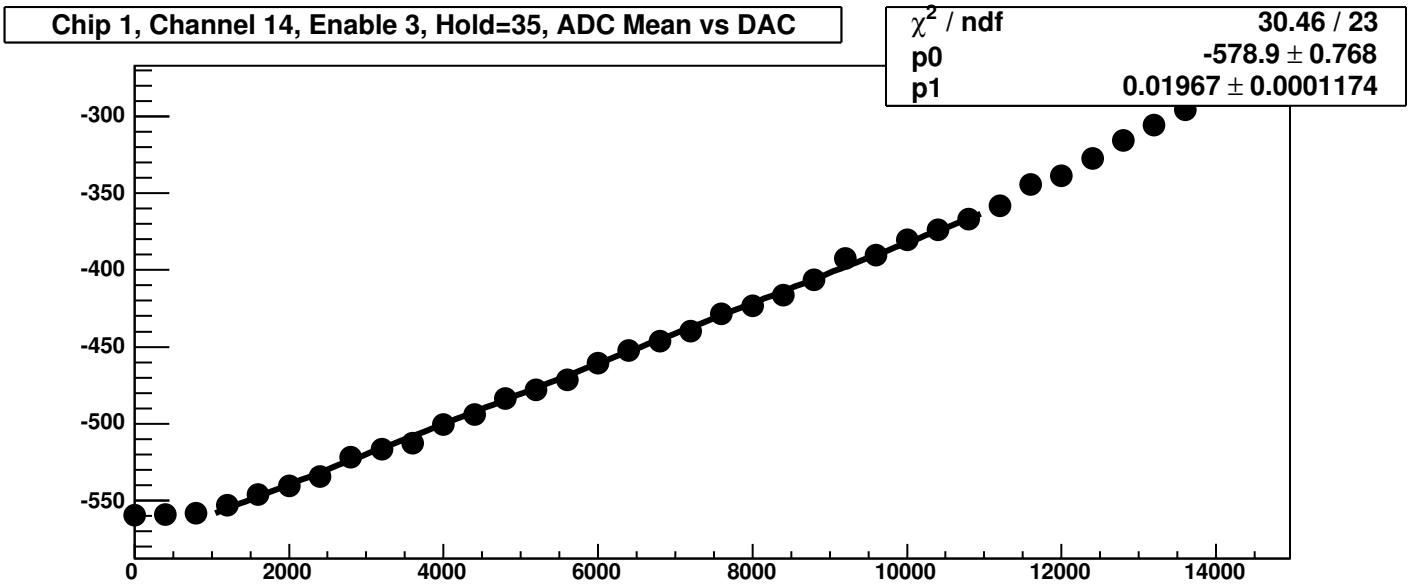

Chip 1, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

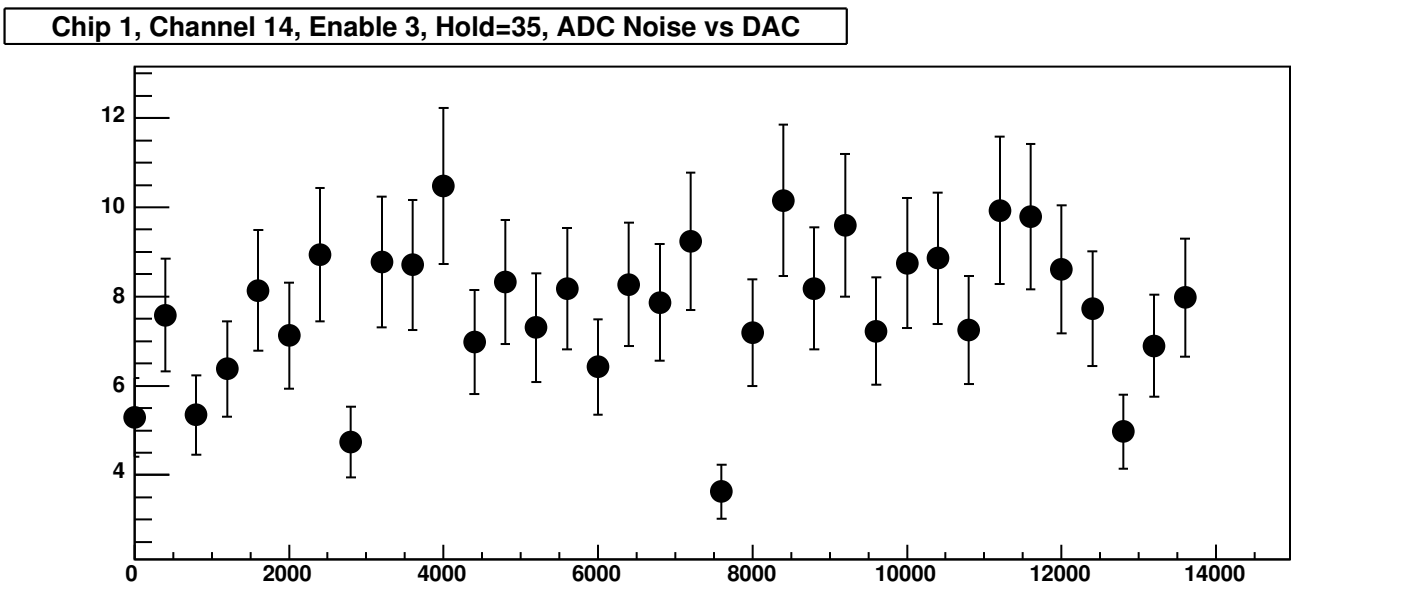

Chip 1, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

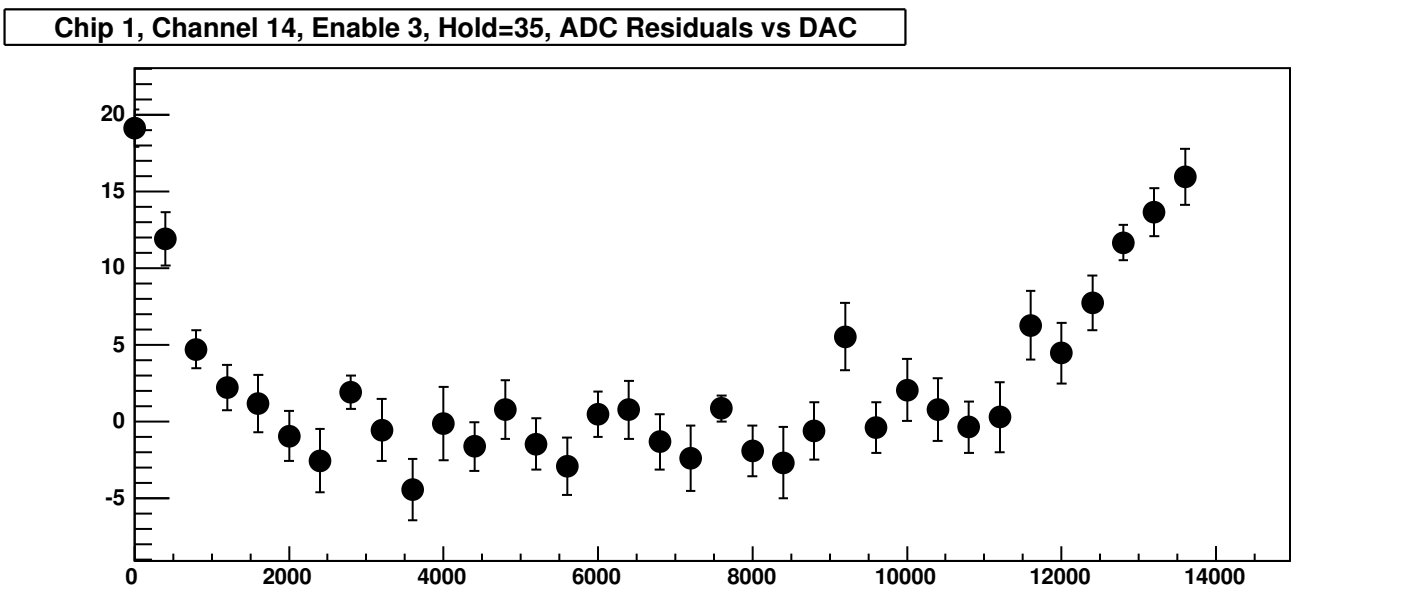

Chip 1, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1771 / 23
p0 -534.3 ± 0.927
p1 -0.05846 ± 0.0001831
-600
-700
-800
-900
-1000
-1100
-1200
-1300
-1400
0
2000
4000
6000
8000
10000
12000
14000

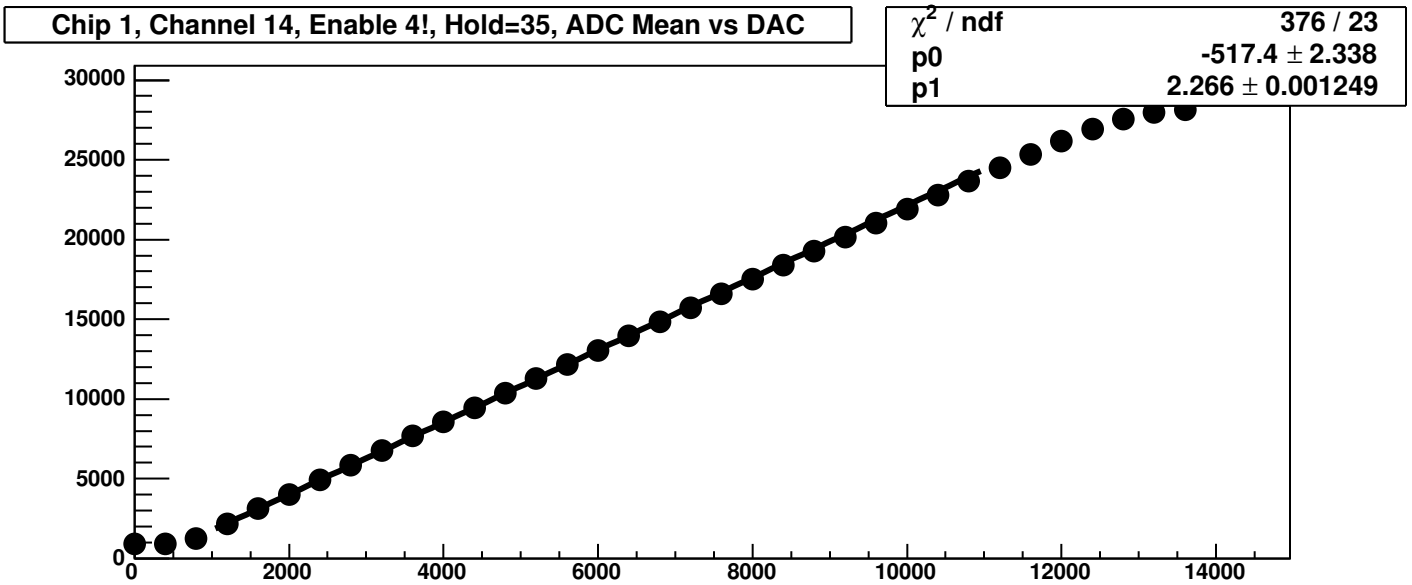

Chip 1, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

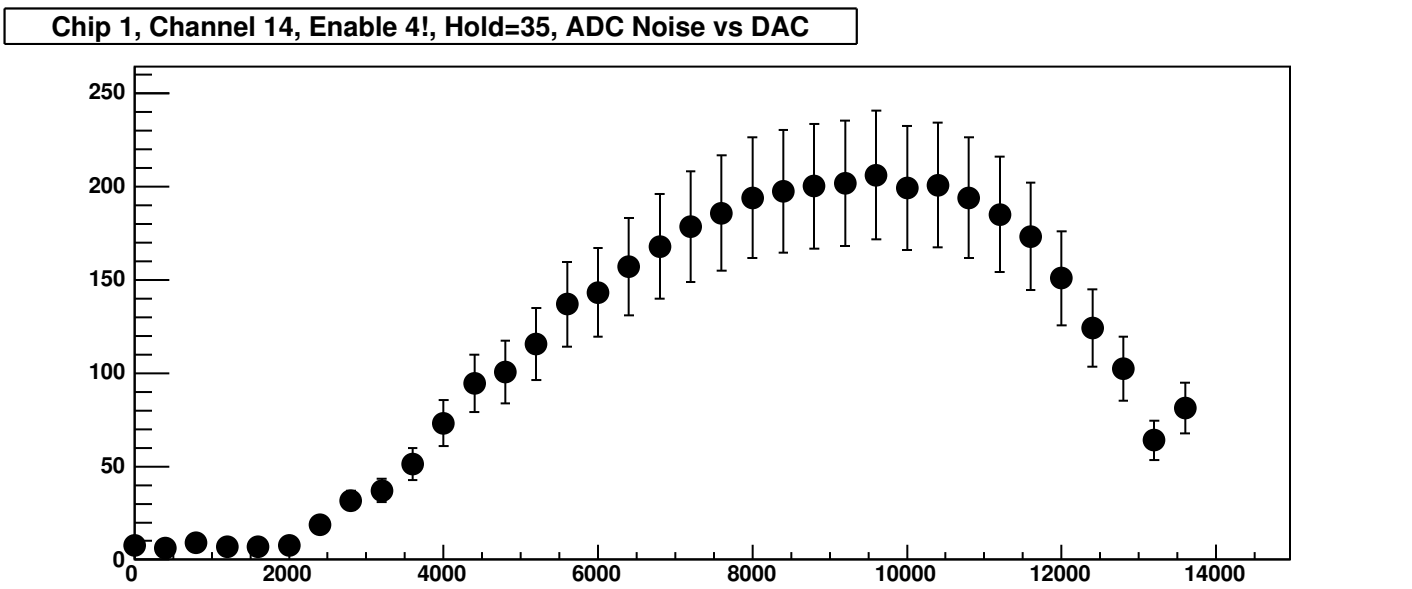

Chip 1, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
40
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000

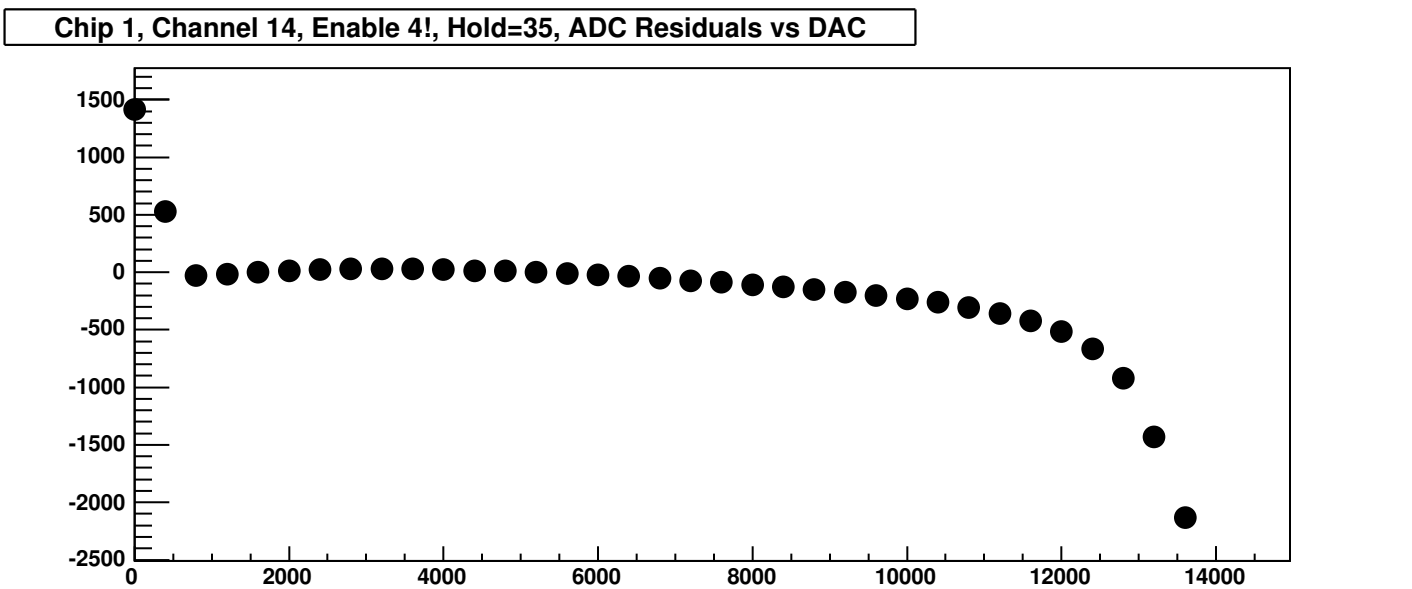

Chip 1, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 30.46 / 23
p0 -578.9 ± 0.768
p1 0.01967 ± 0.0001174
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

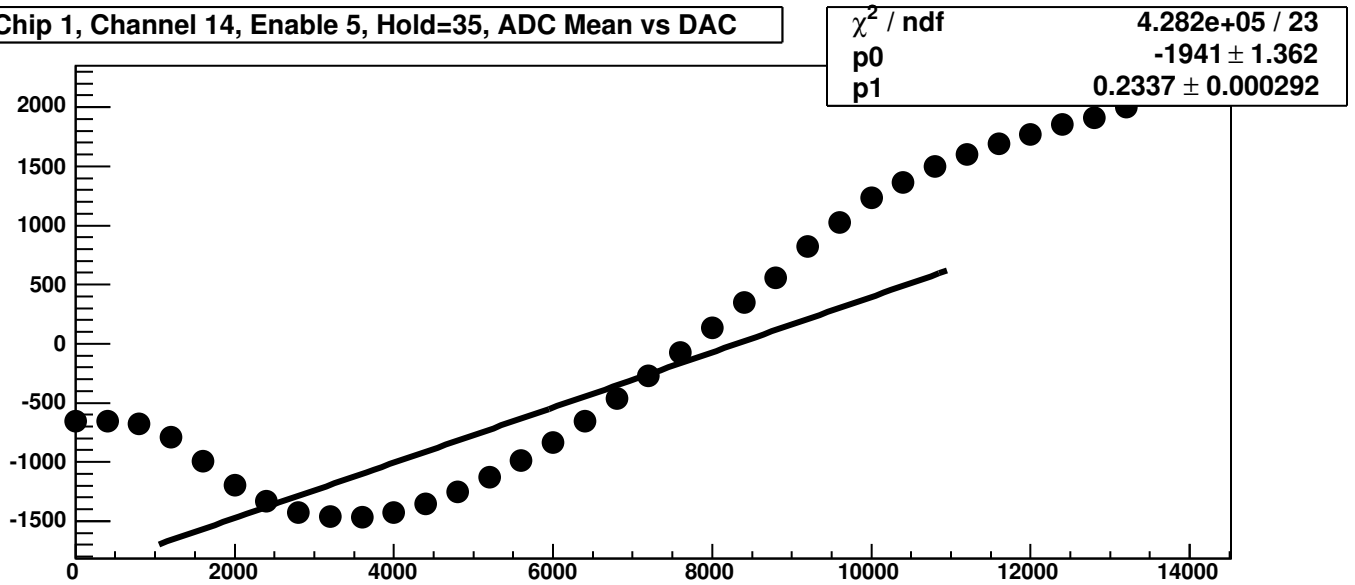

Chip 1, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 376 / 23
p0 -517.4 ± 2.338
p1 2.266 ± 0.001249
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

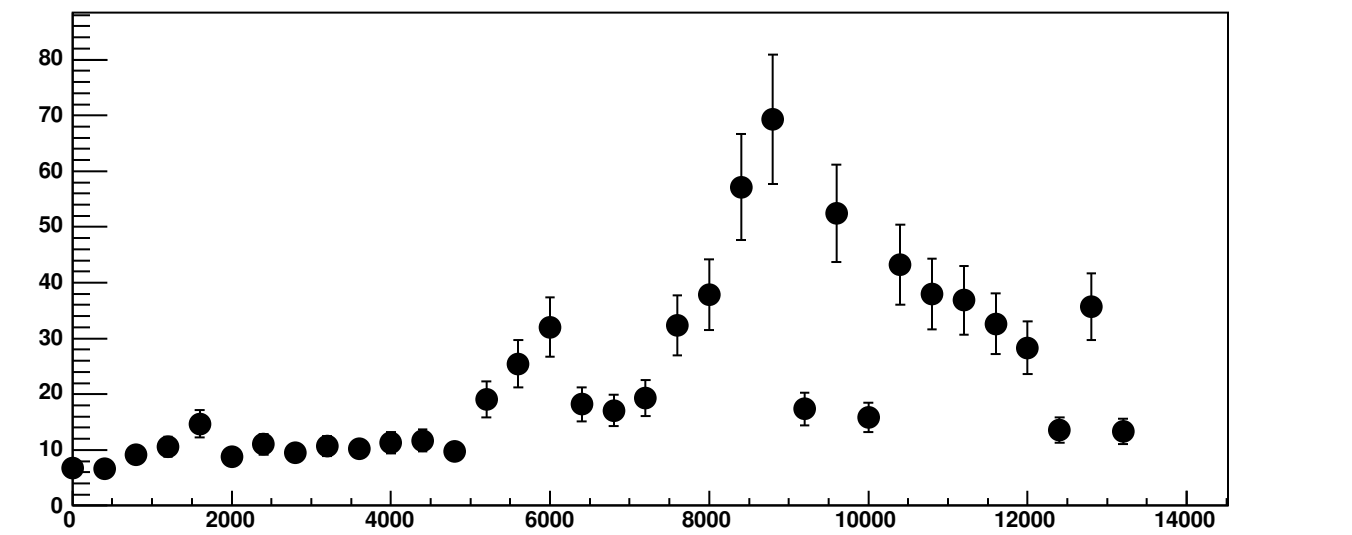

Chip 1, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

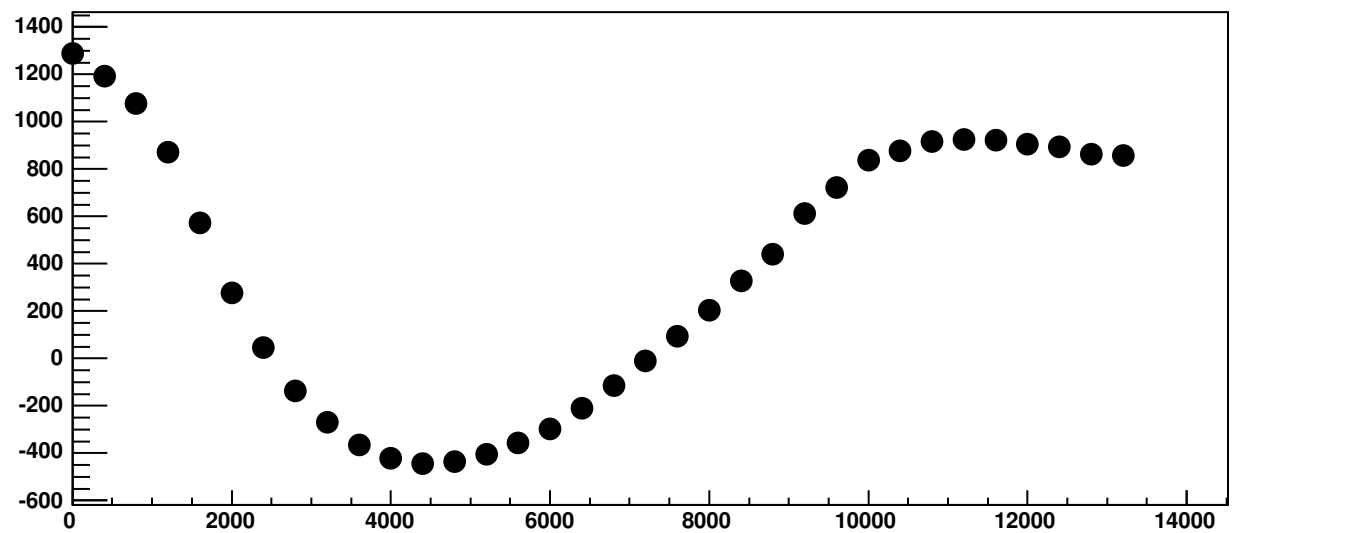
Chip 1, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

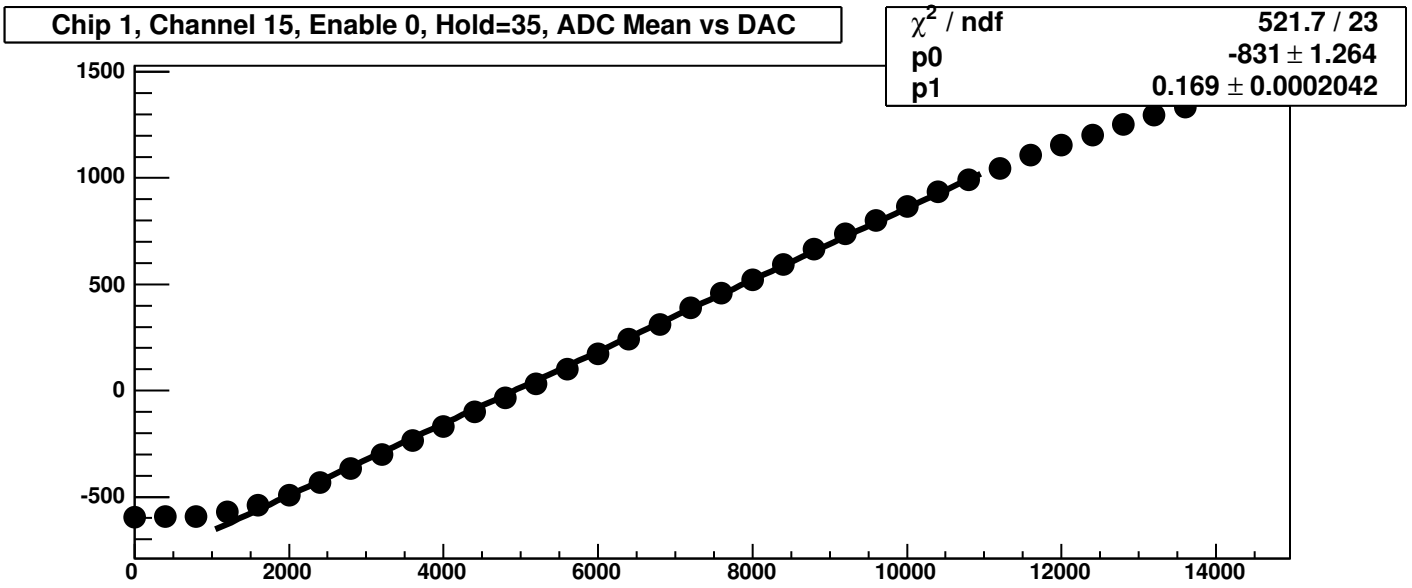


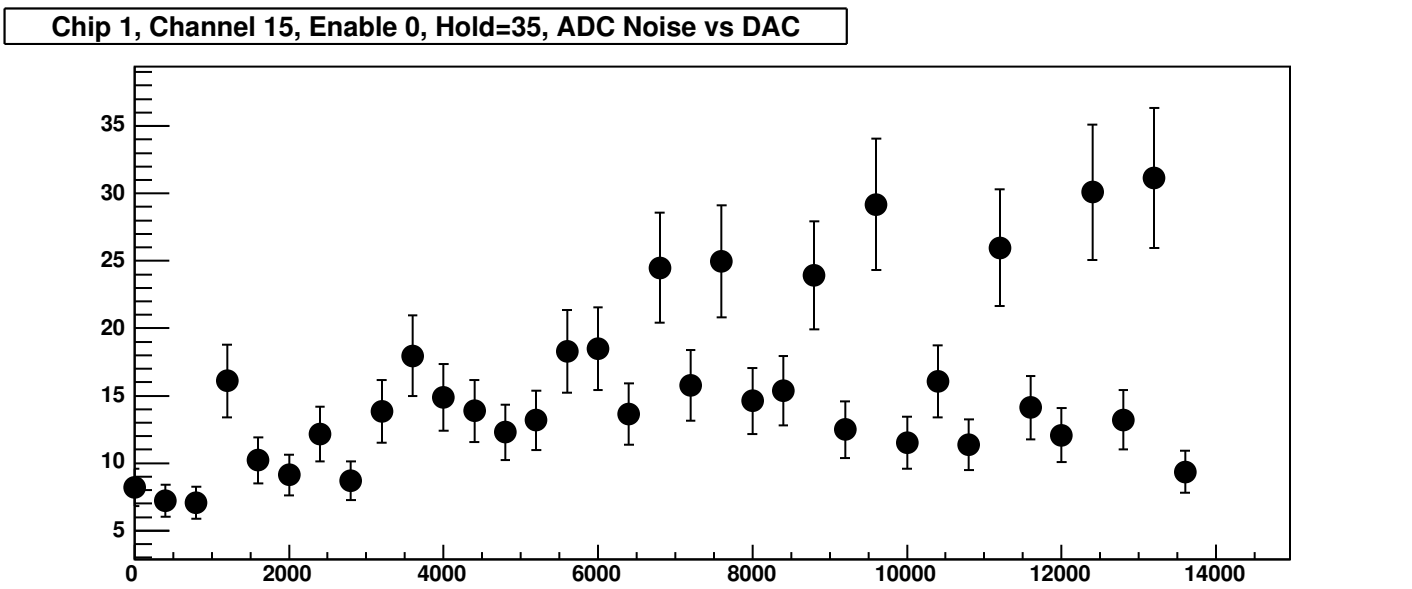
Chip 1, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC

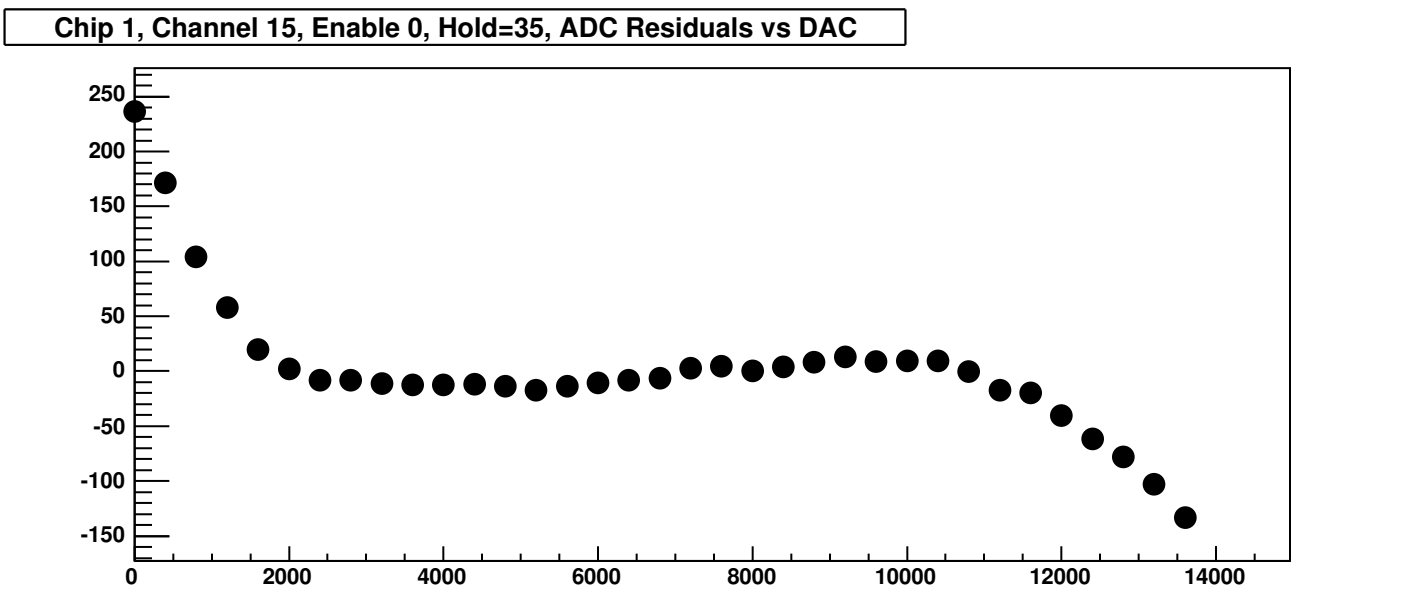


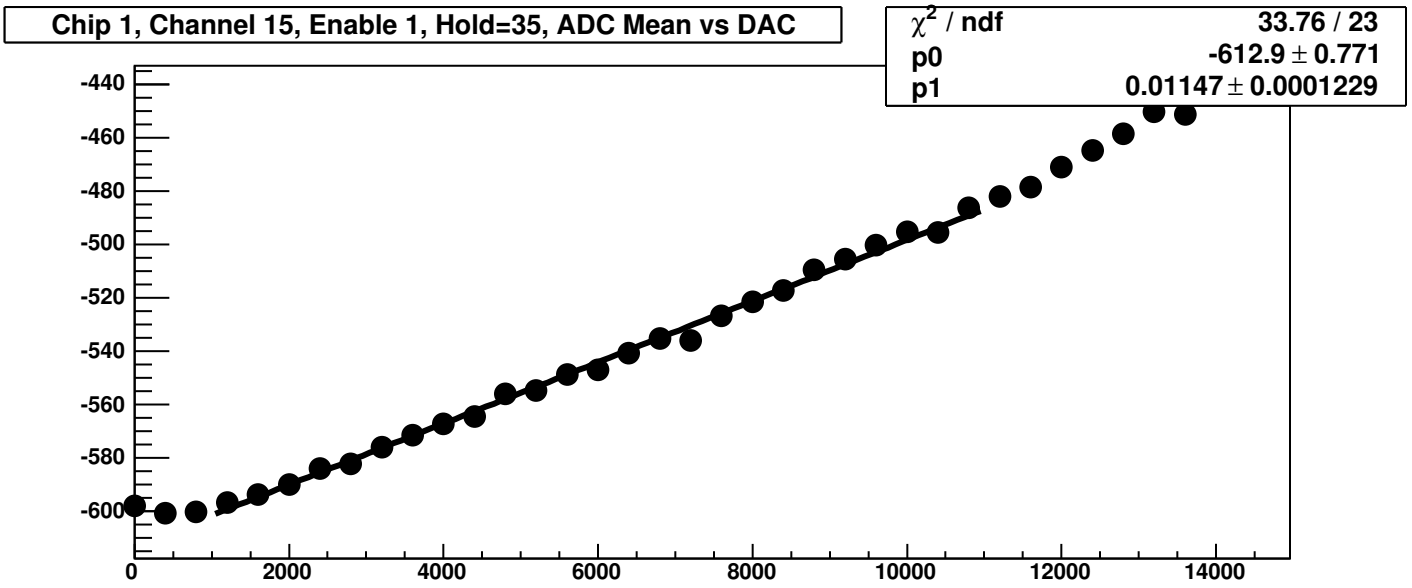
Chip 1, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

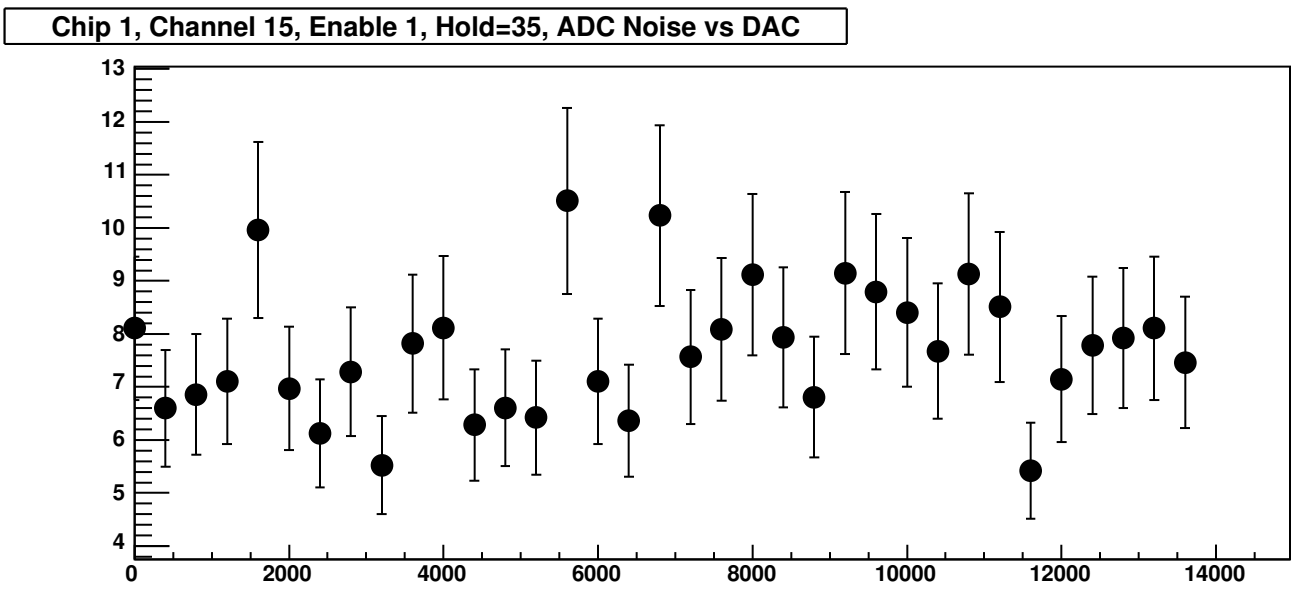


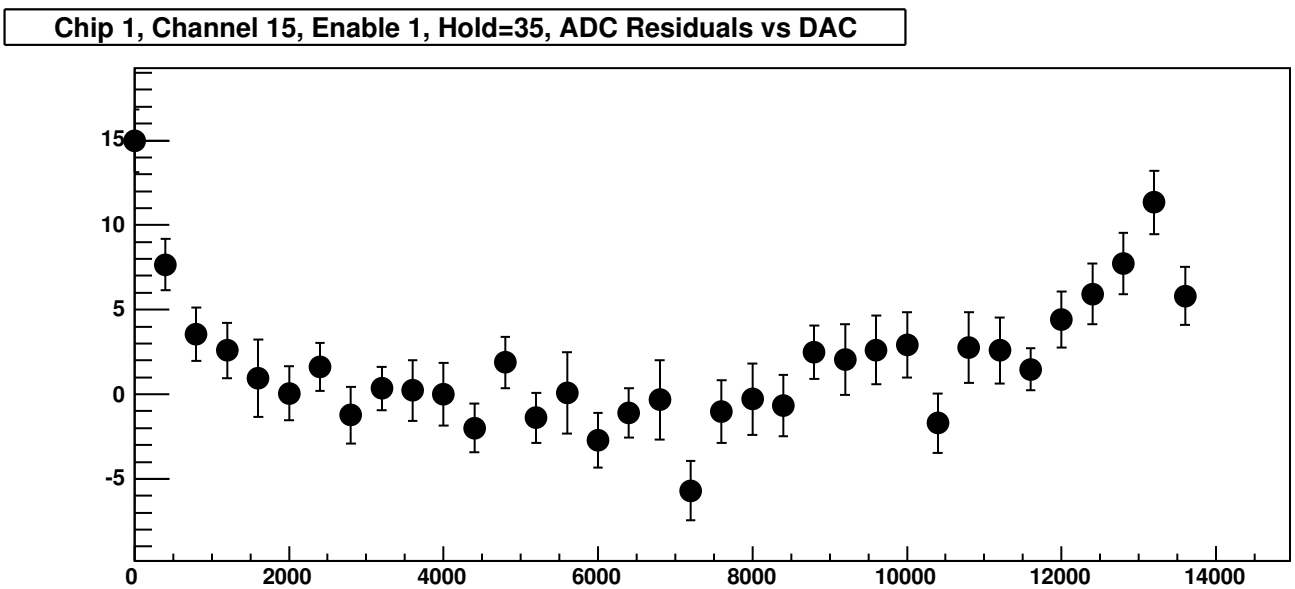

Chip 1, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 521.7 / 23
p0 -831 ± 1.264
p1 0.169 ± 0.0002042
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

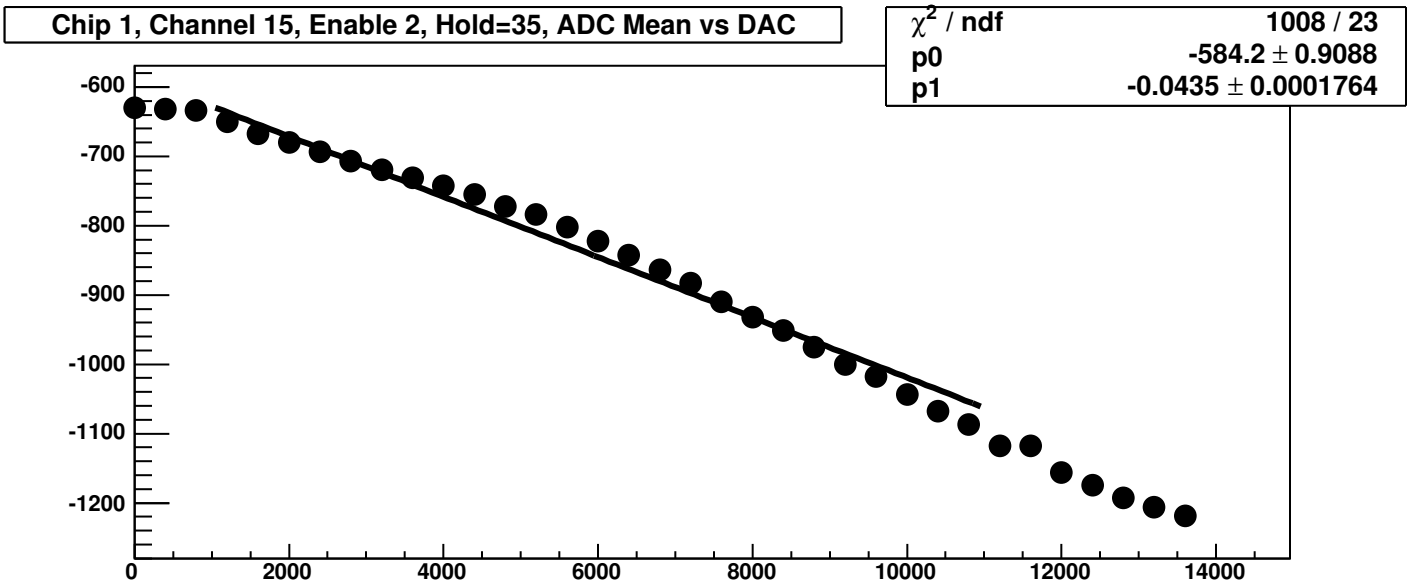

Chip 1, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

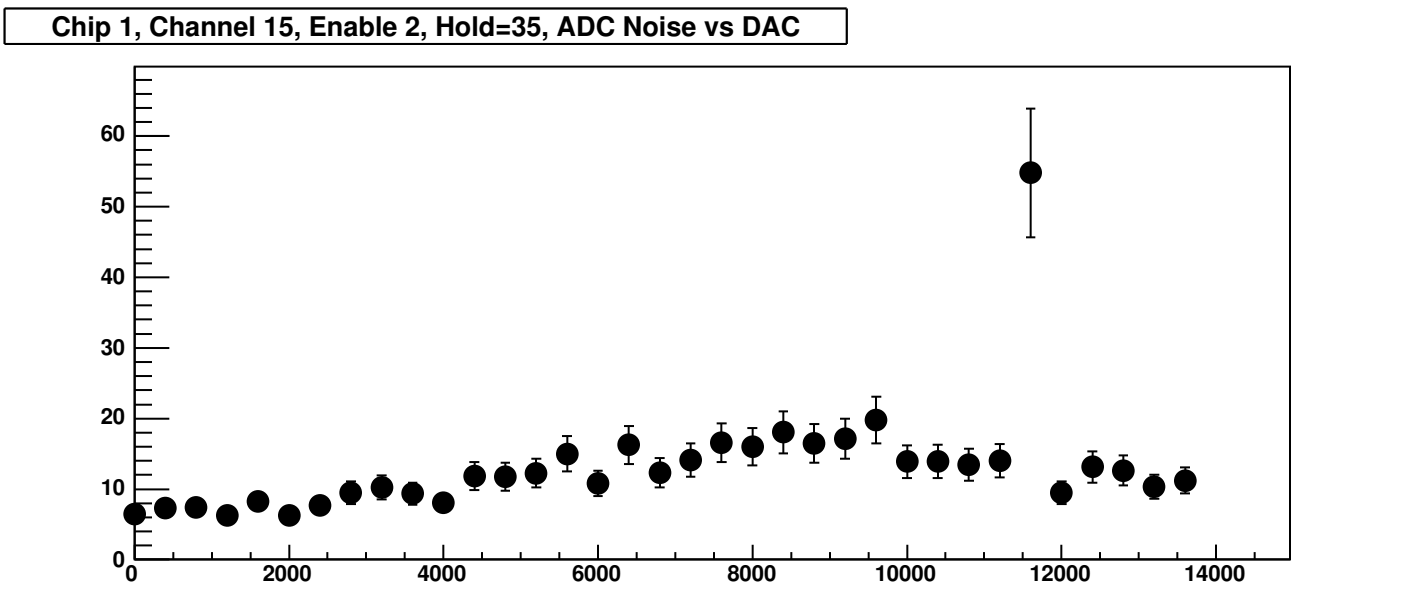

Chip 1, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

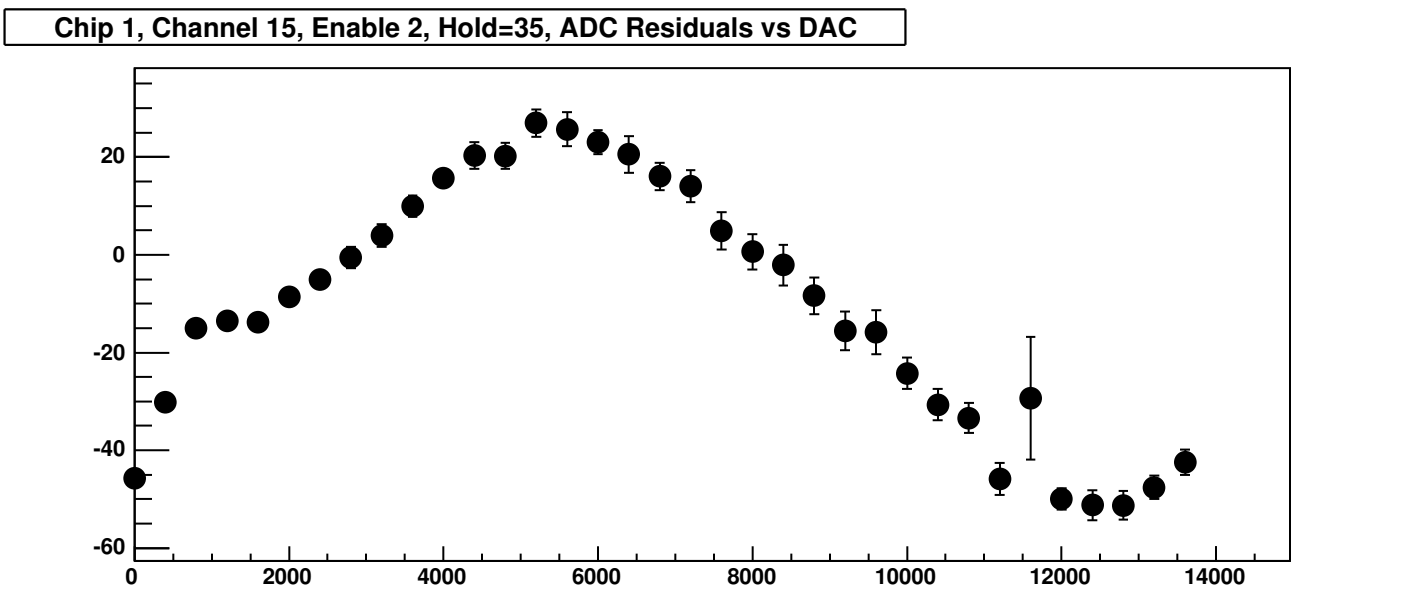

Chip 1, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.76 / 23
p0 -612.9 ± 0.771
p1 0.01147 ± 0.0001229
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
2000
4000
6000
8000
10000
12000
14000

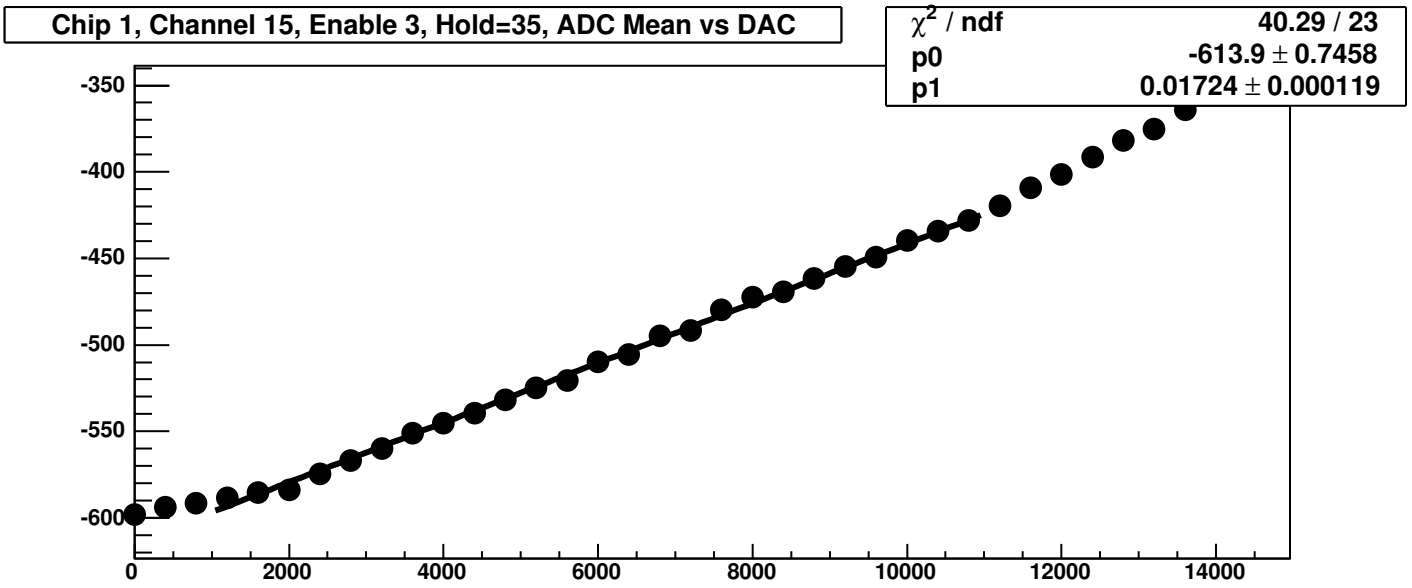

Chip 1, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

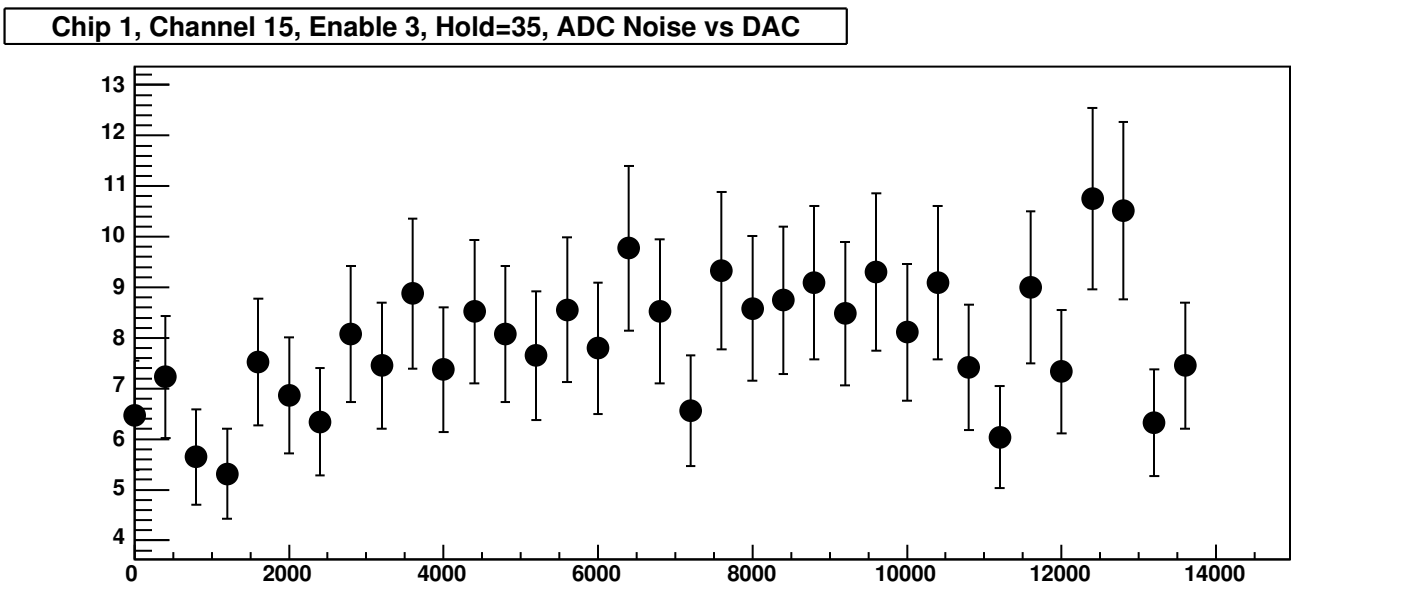

Chip 1, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

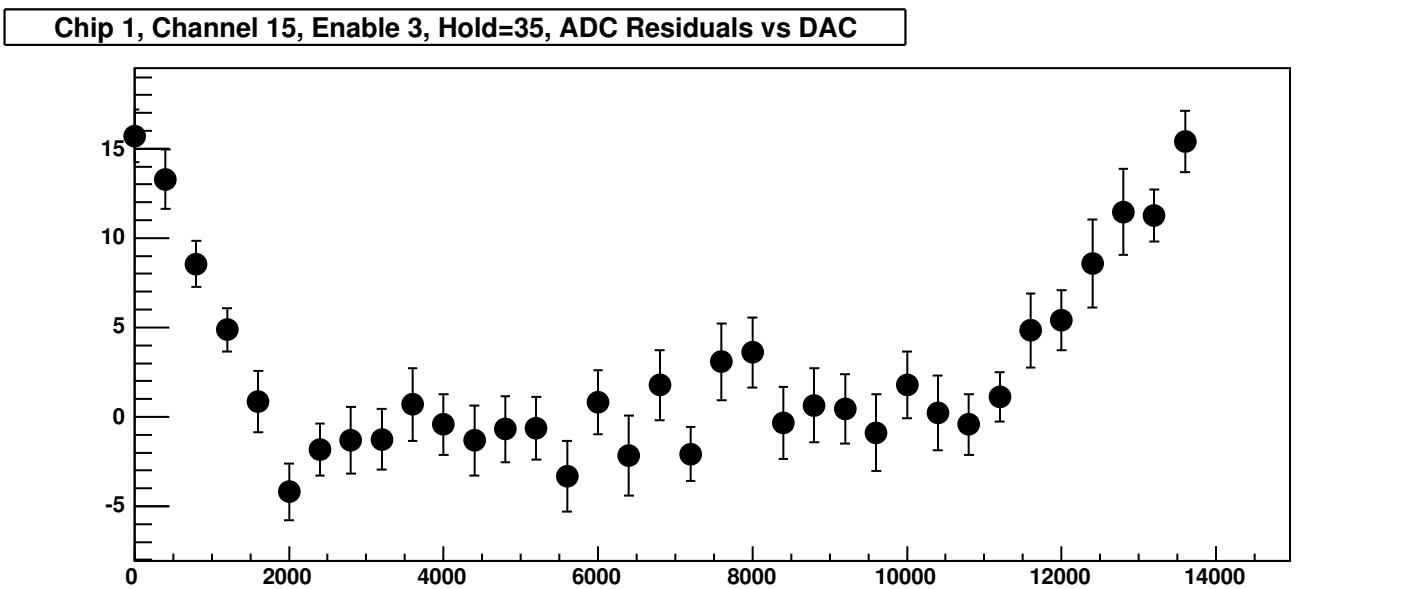

Chip 1, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1008 / 23
p0 -584.2 ± 0.9088
p1 -0.0435 ± 0.0001764
-600
-700
-800
-900
-1000
-1100
-1200
0
2000
4000
6000
8000
10000
12000
14000

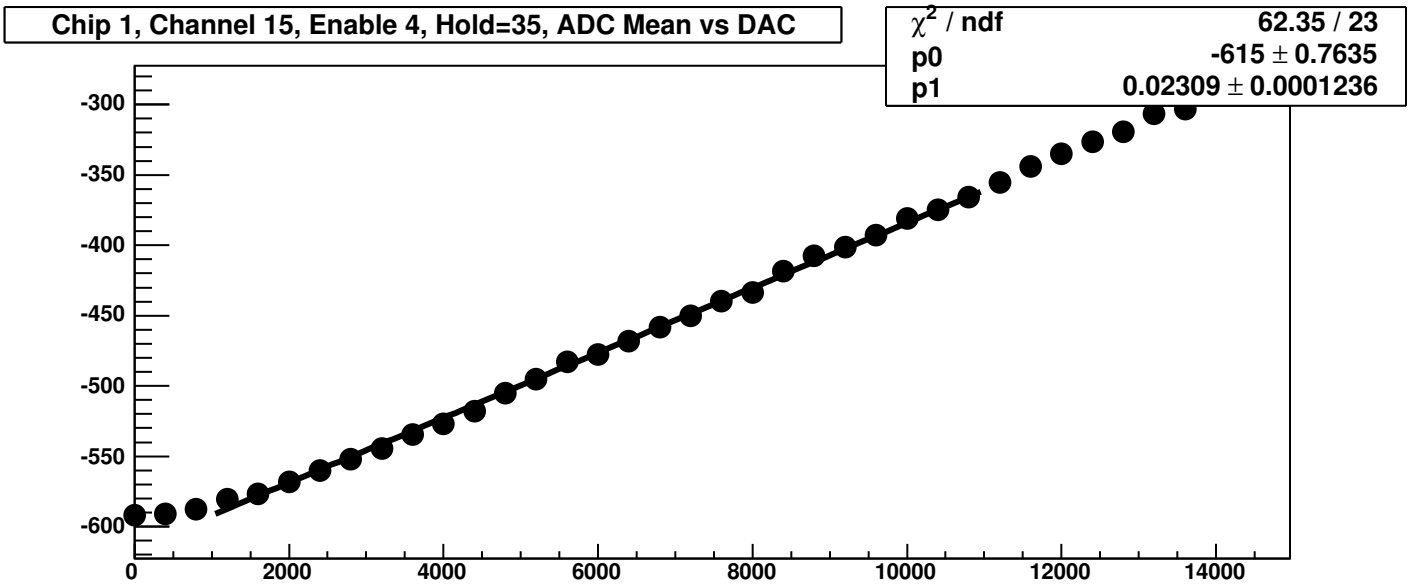

Chip 1, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

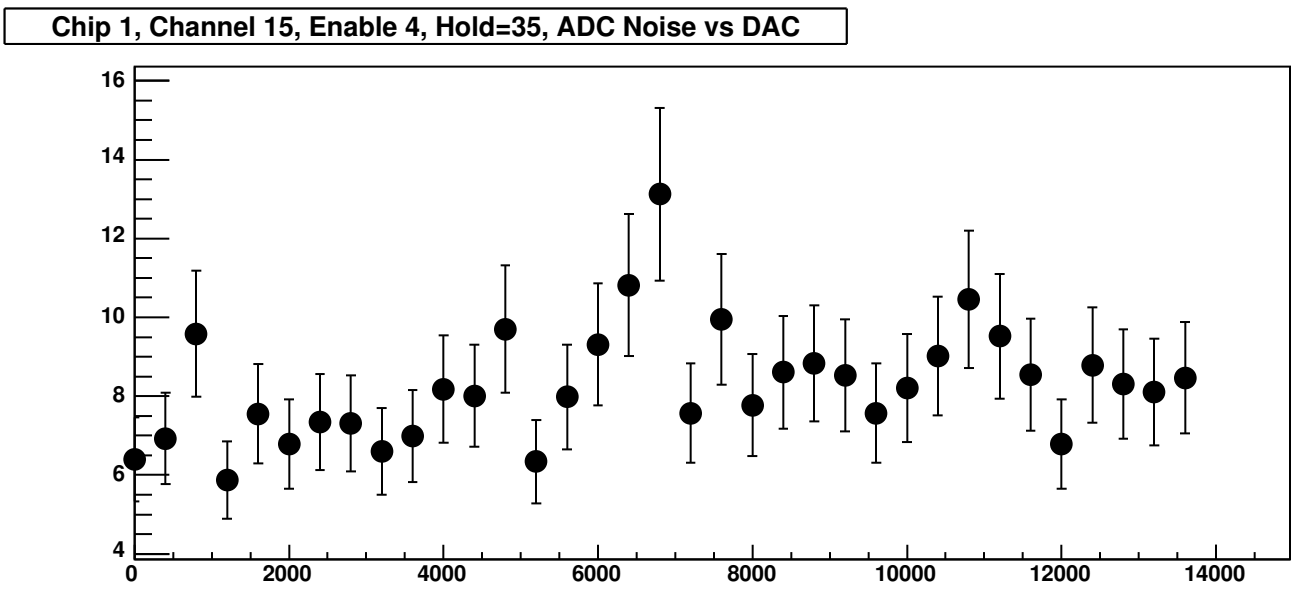

Chip 1, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
0
2000
4000
6000
8000
10000
12000
14000

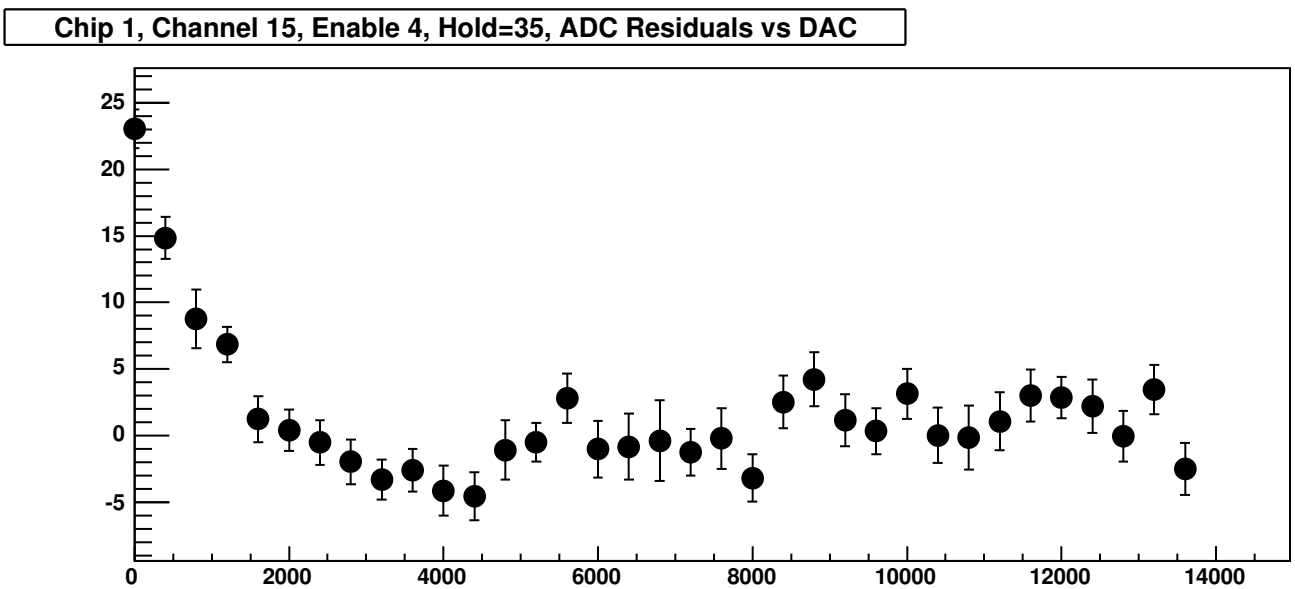

Chip 1, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 40.29 / 23
p0 -613.9 ± 0.7458
p1 0.01724 ± 0.000119
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

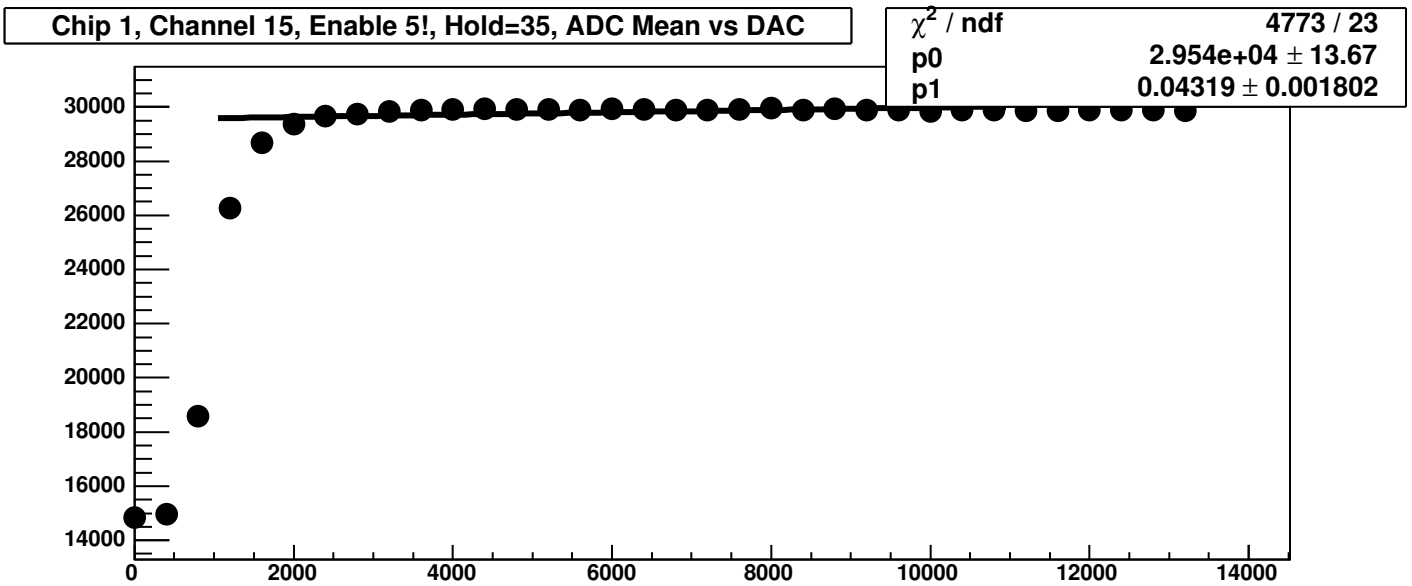

Chip 1, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

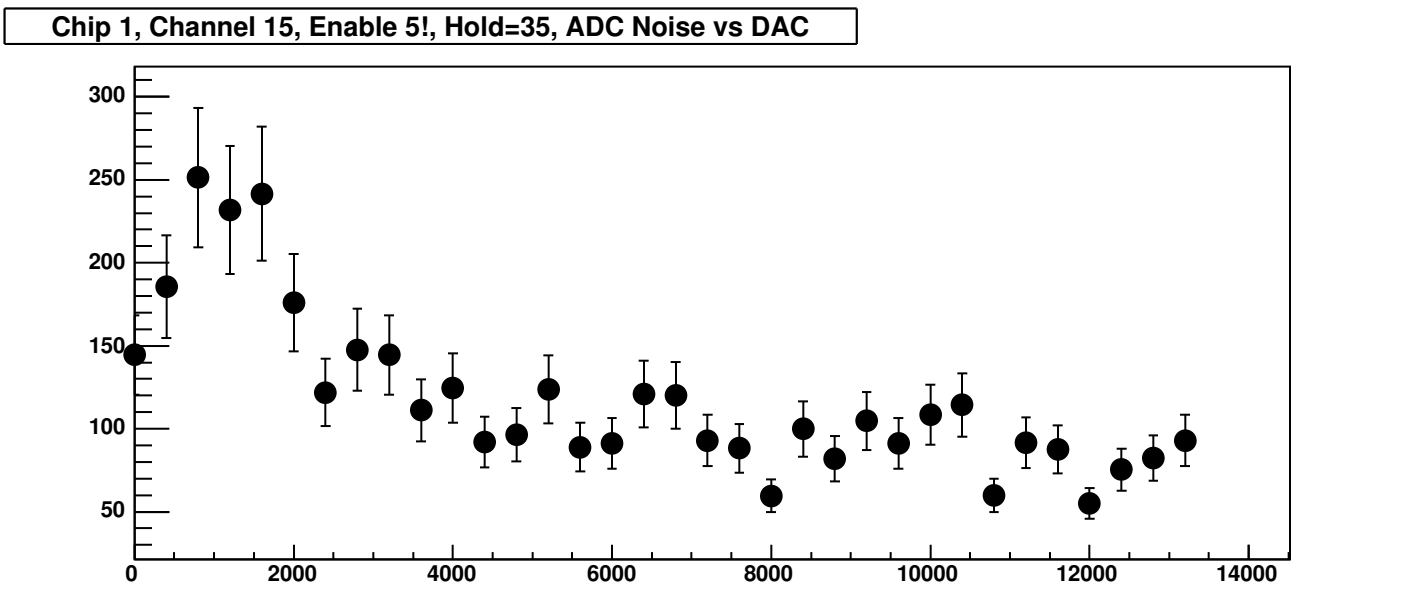

Chip 1, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

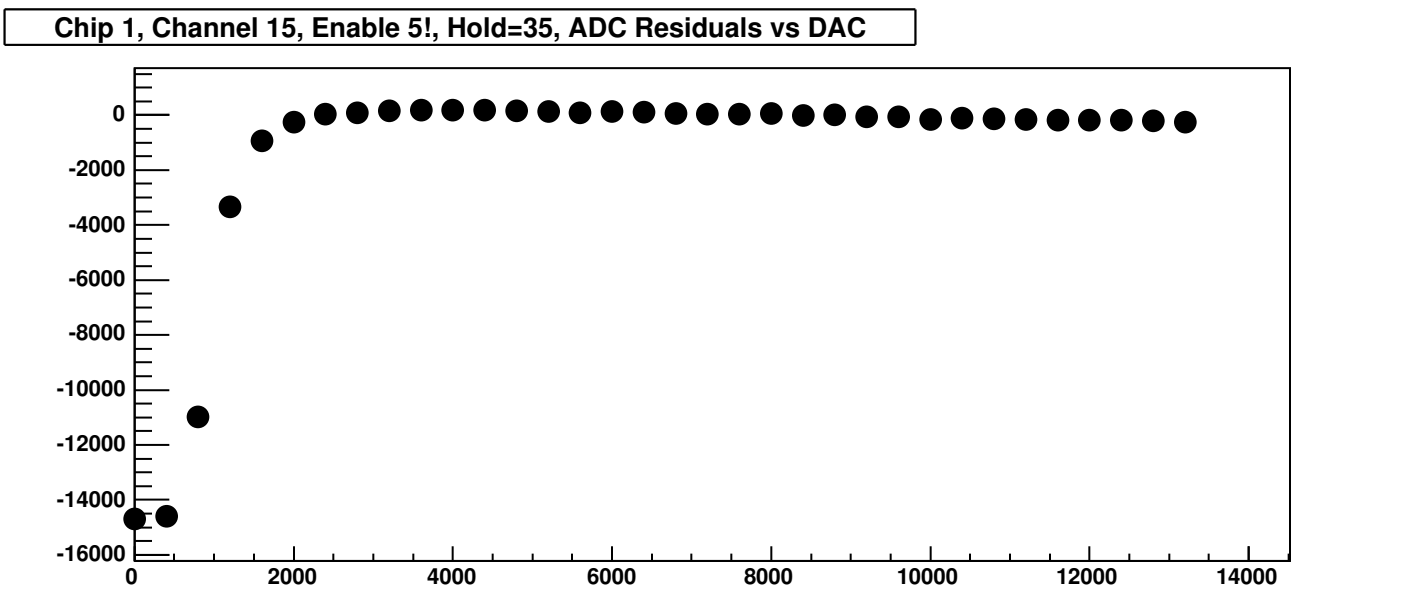

Chip 1, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 62.35 / 23
p0 -615 ± 0.7635
p1 0.02309 ± 0.0001236
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

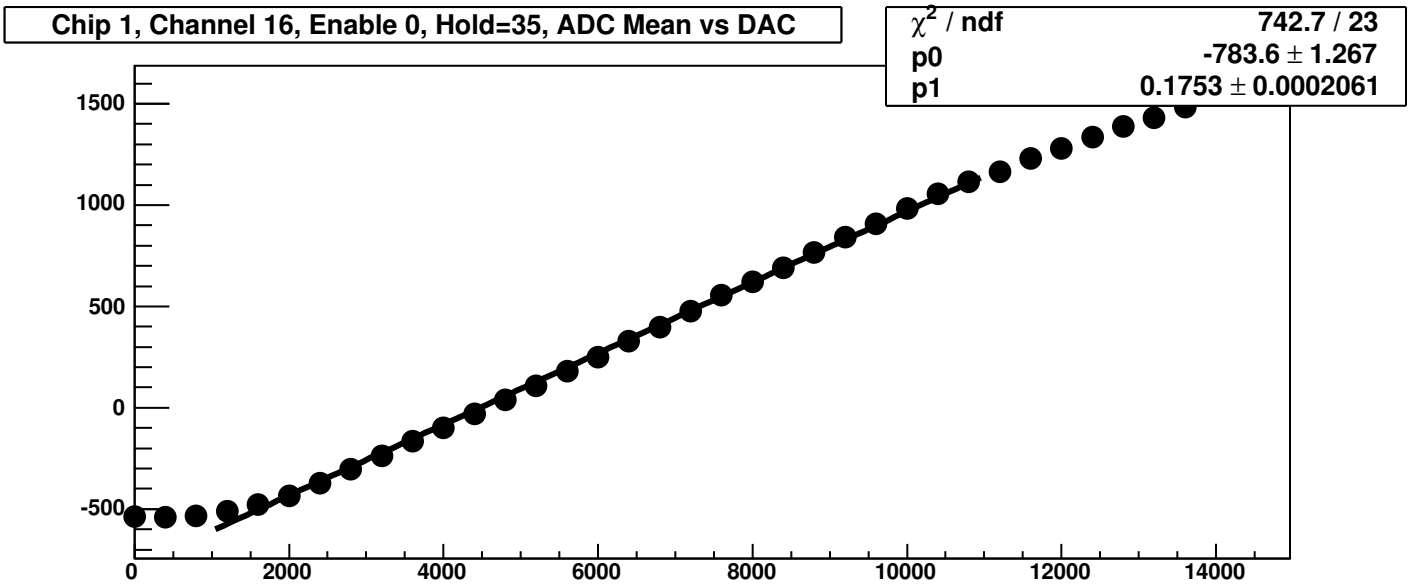

Chip 1, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

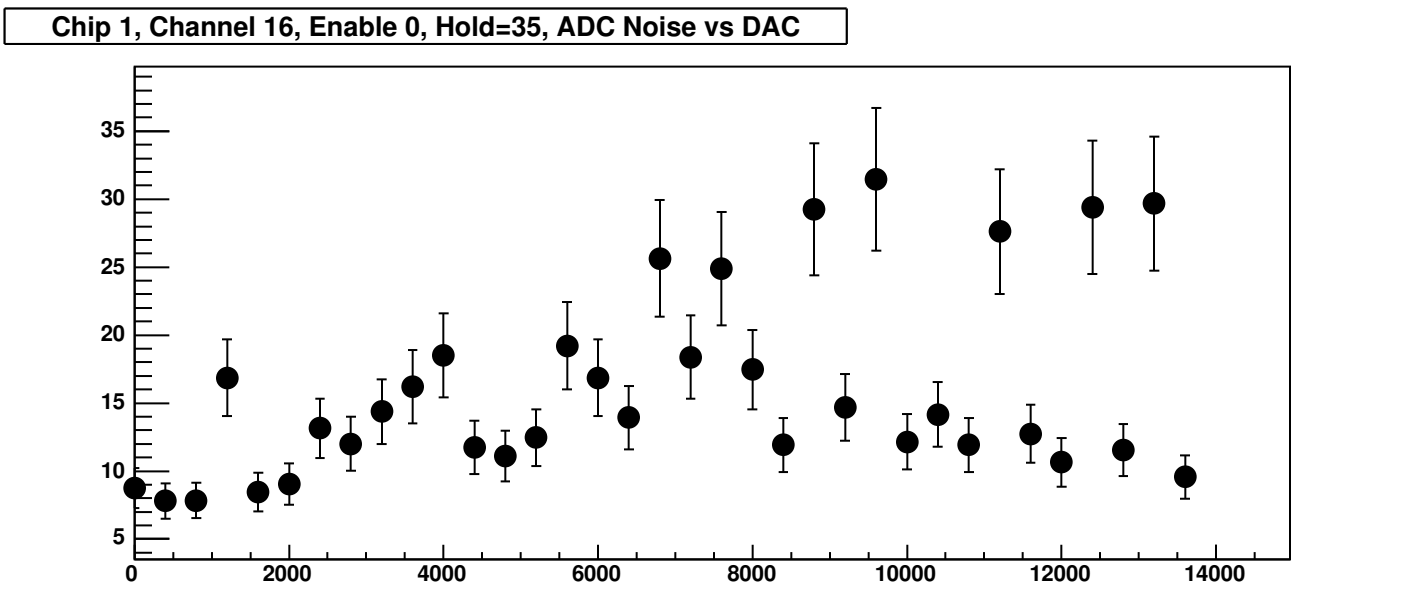

Chip 1, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

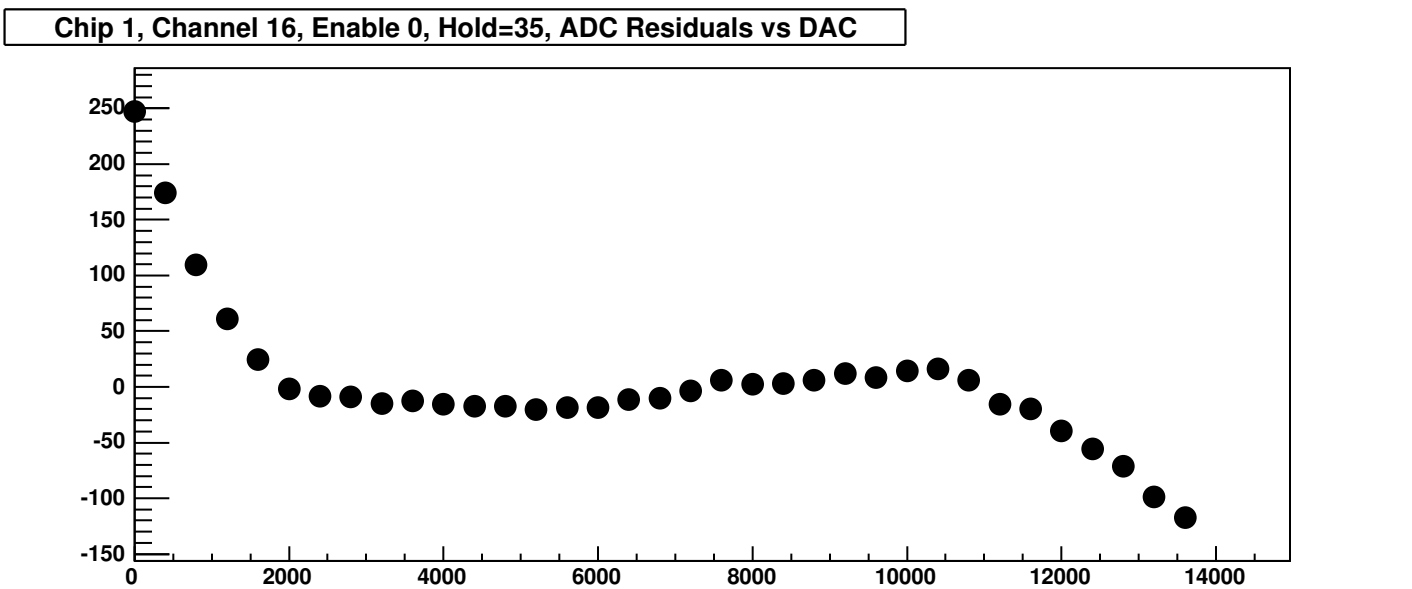

Chip 1, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4773 / 23
p0 2.954e+04 ± 13.67
p1 0.04319 ± 0.001802
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

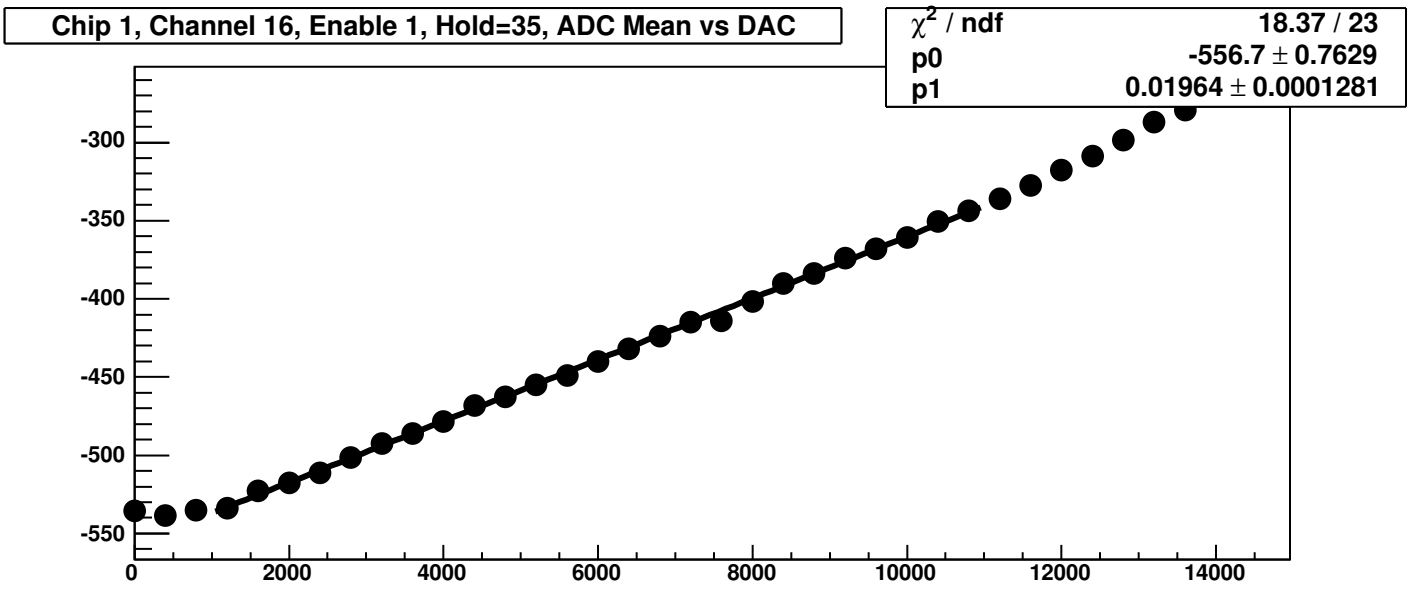

Chip 1, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

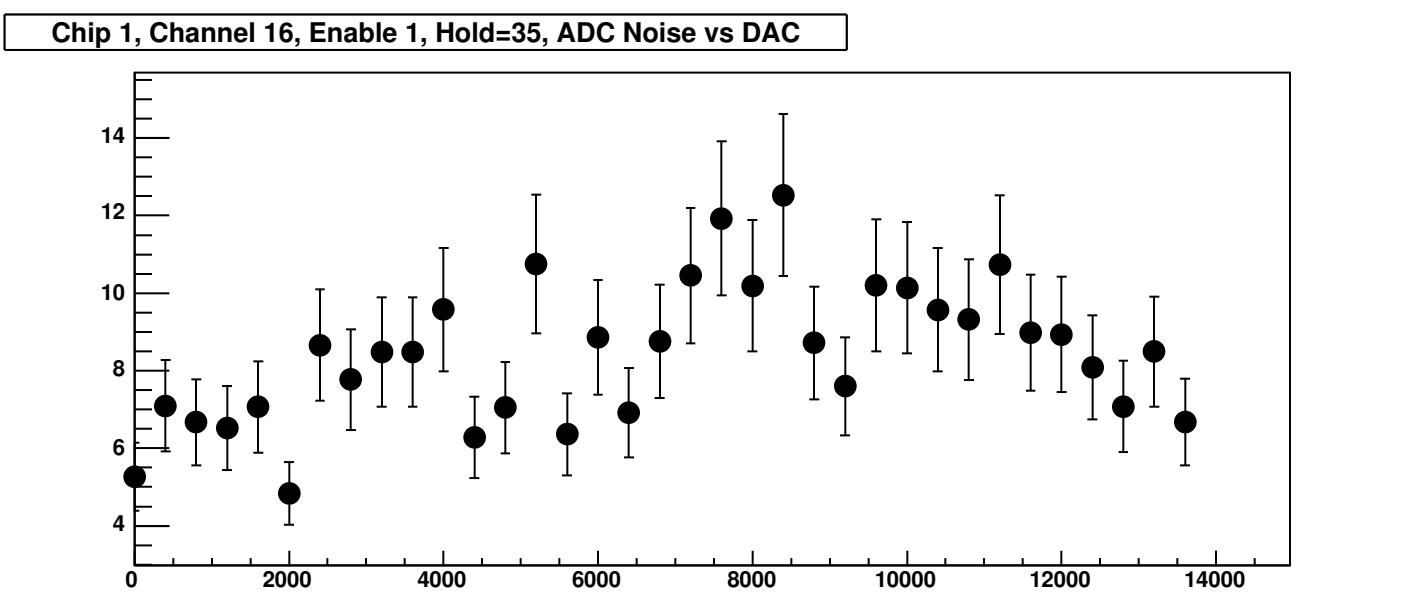

Chip 1, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

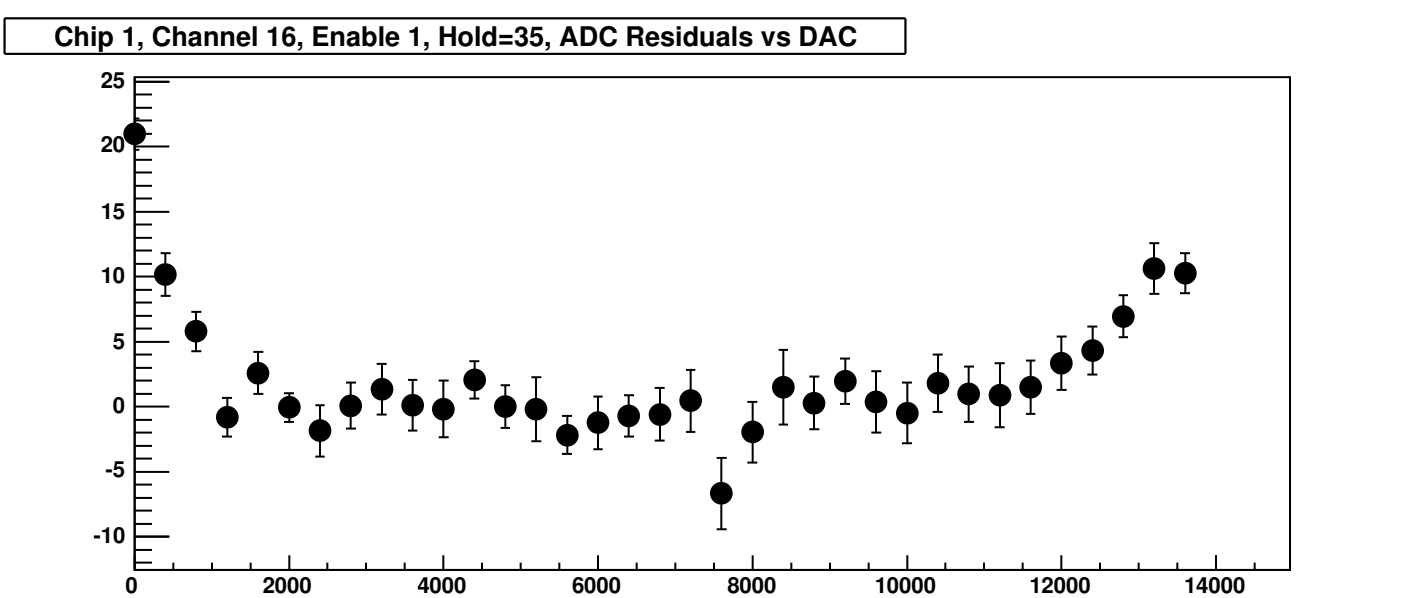

Chip 1, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 742.7 / 23
p0 -783.6 ± 1.267
p1 0.1753 ± 0.0002061
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

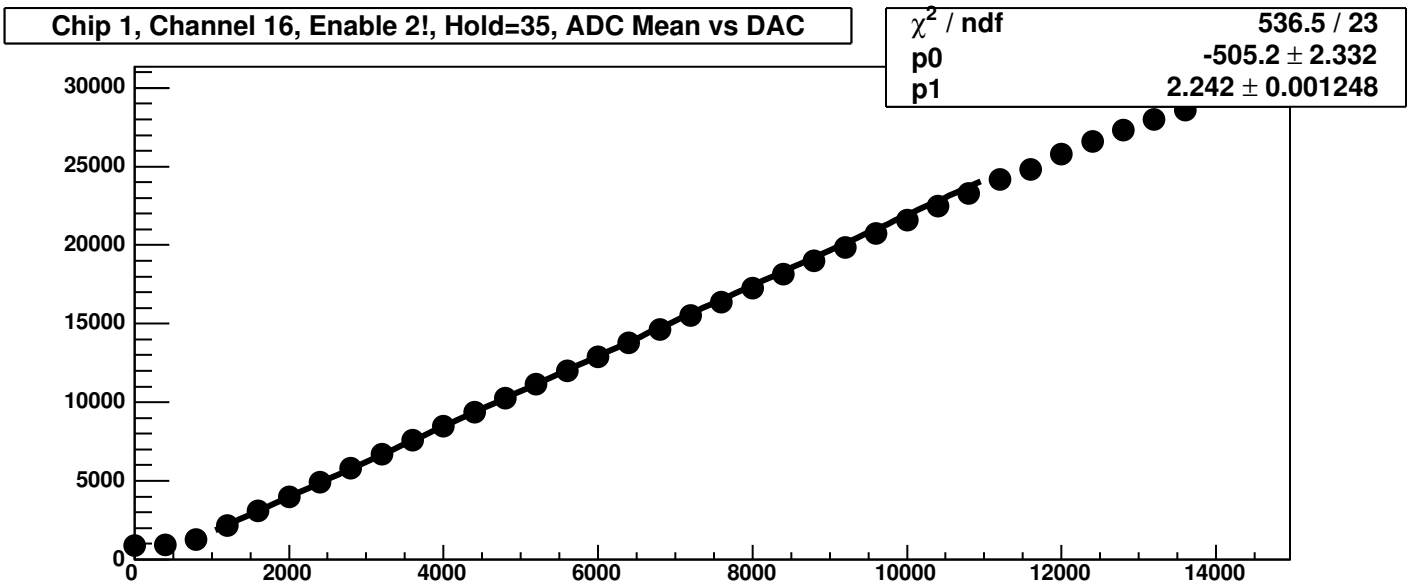

Chip 1, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

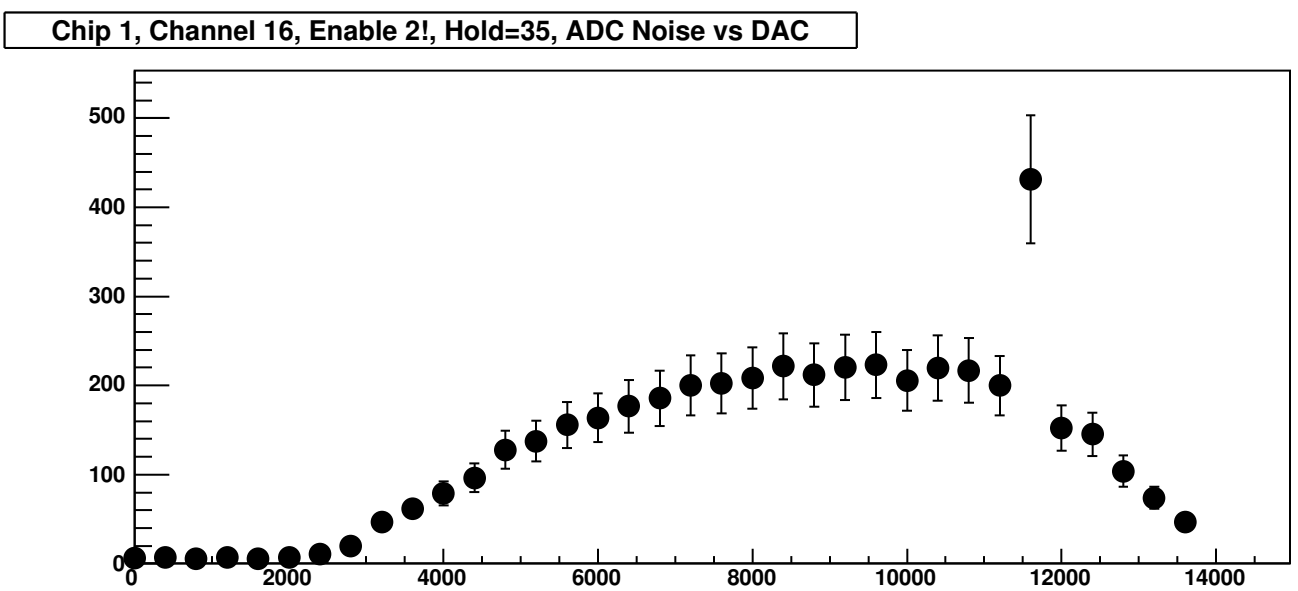

Chip 1, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

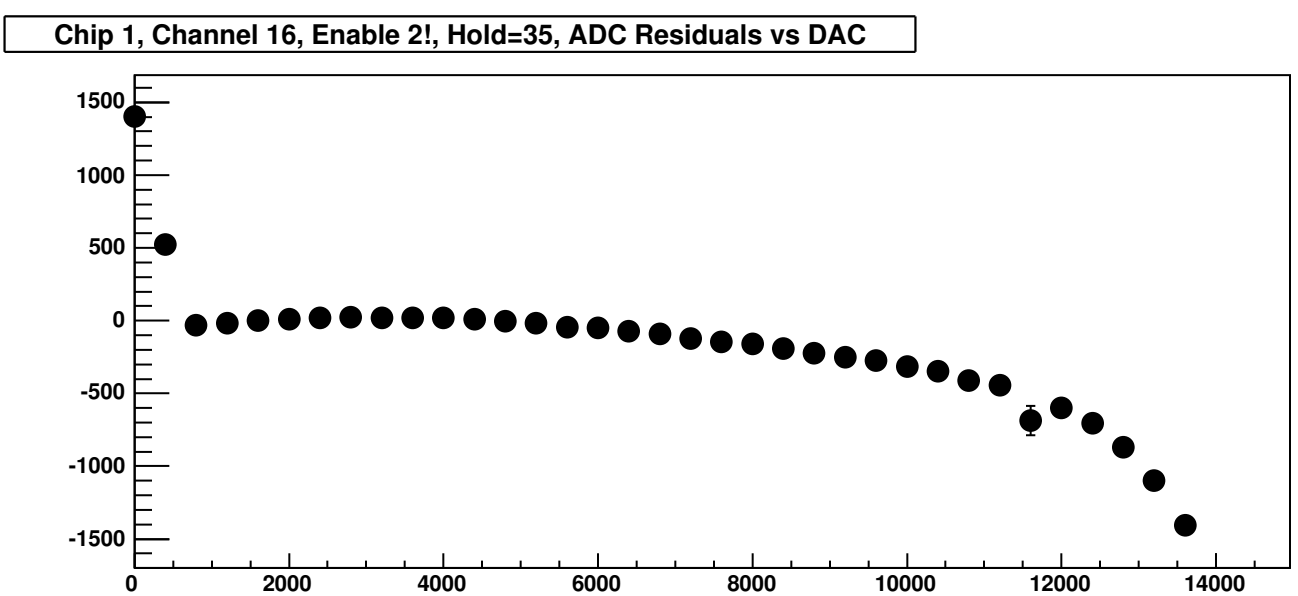

Chip 1, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 18.37 / 23
p0 -556.7 ± 0.7629
p1 0.01964 ± 0.0001281
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

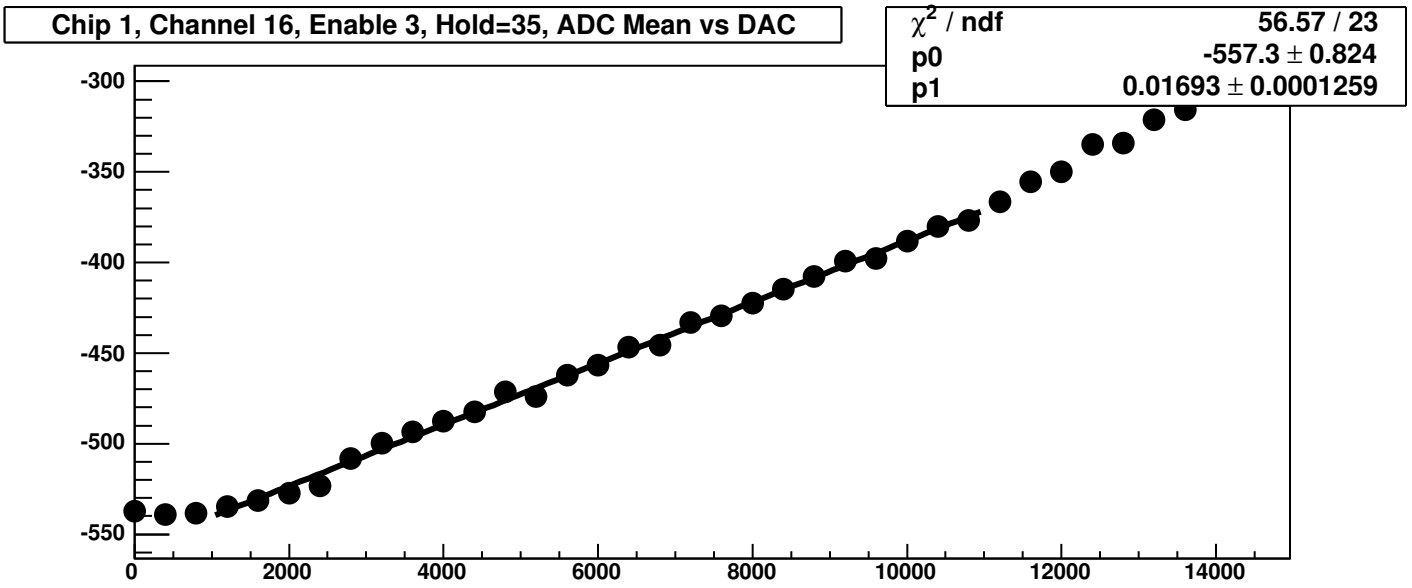

Chip 1, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

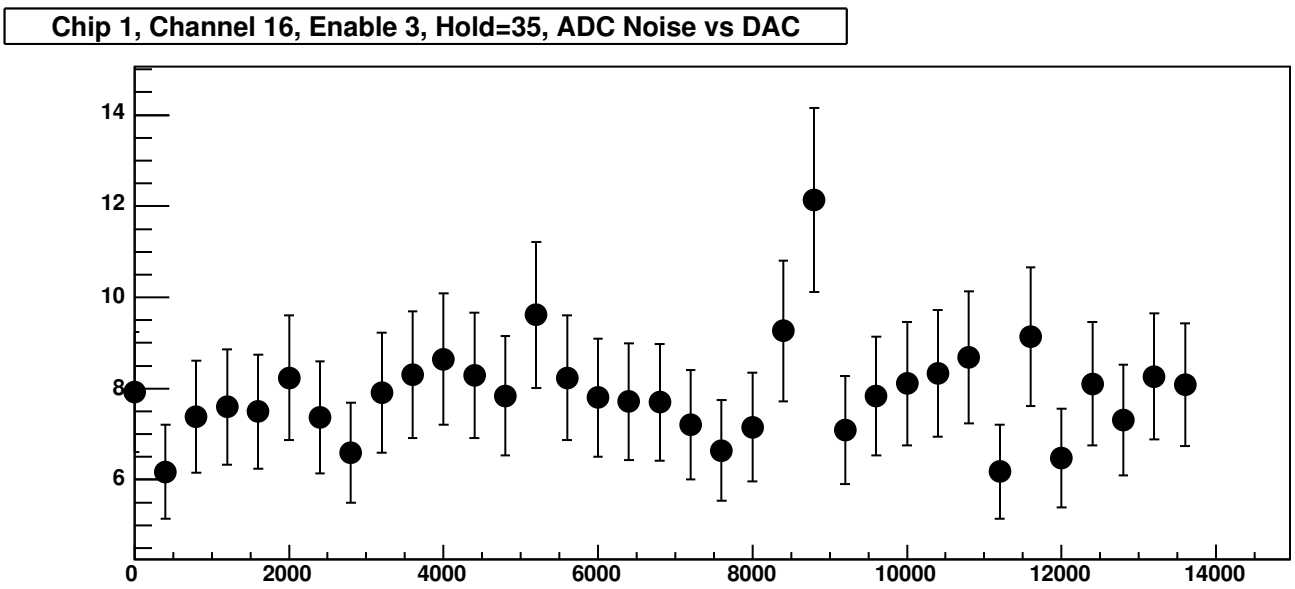

Chip 1, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

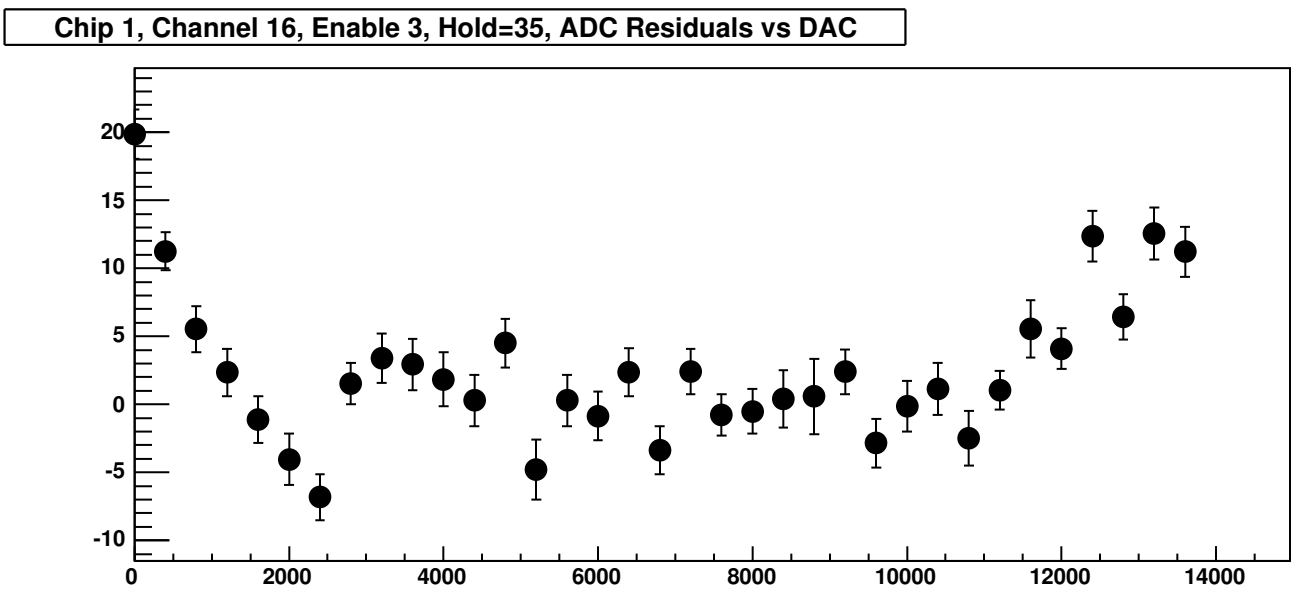

Chip 1, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 536.5 / 23
p0 -505.2 ± 2.332
p1 2.242 ± 0.001248
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

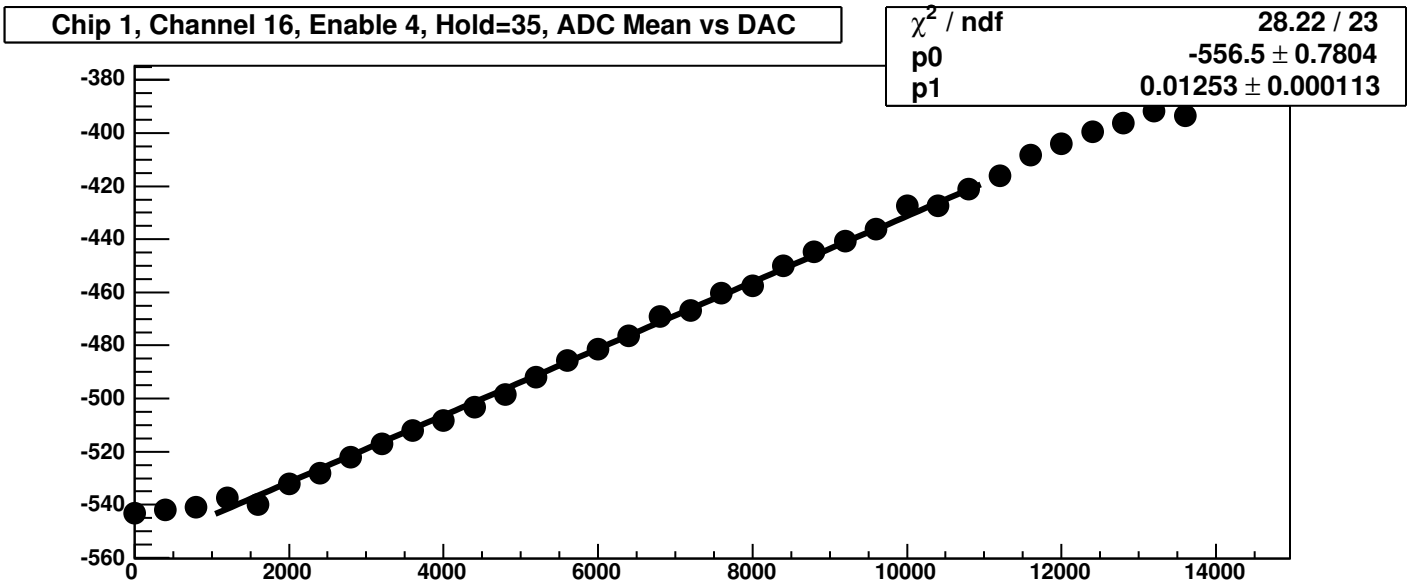

Chip 1, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

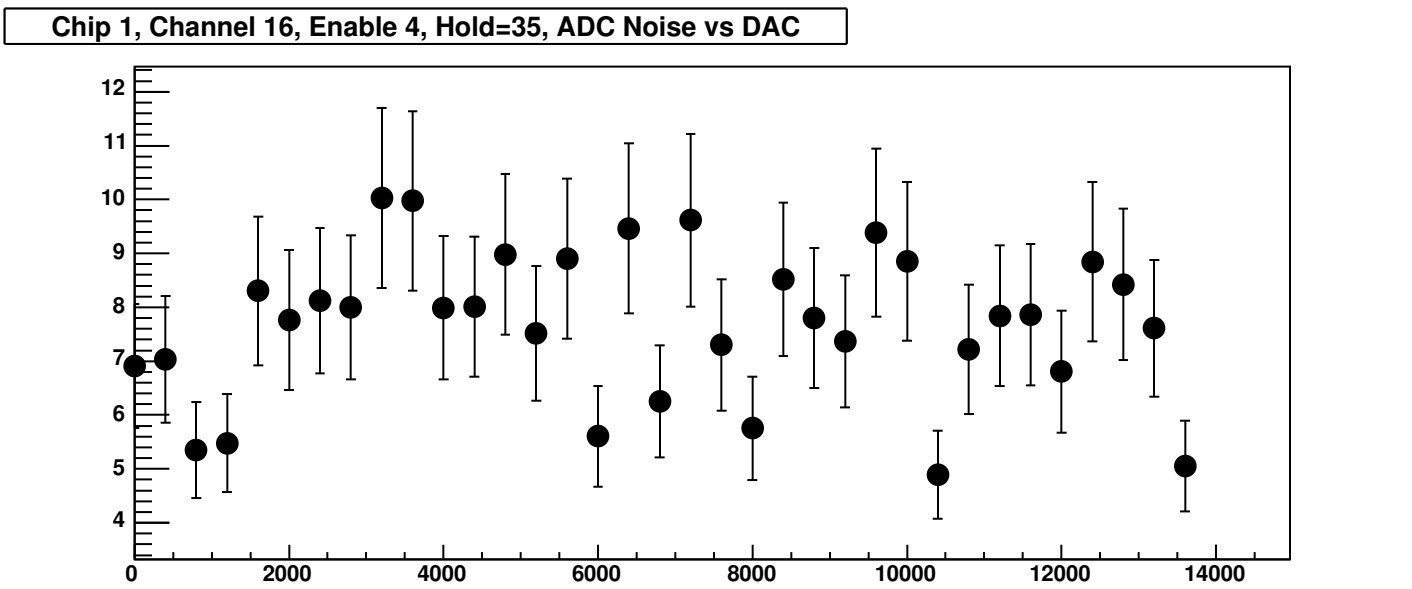

Chip 1, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

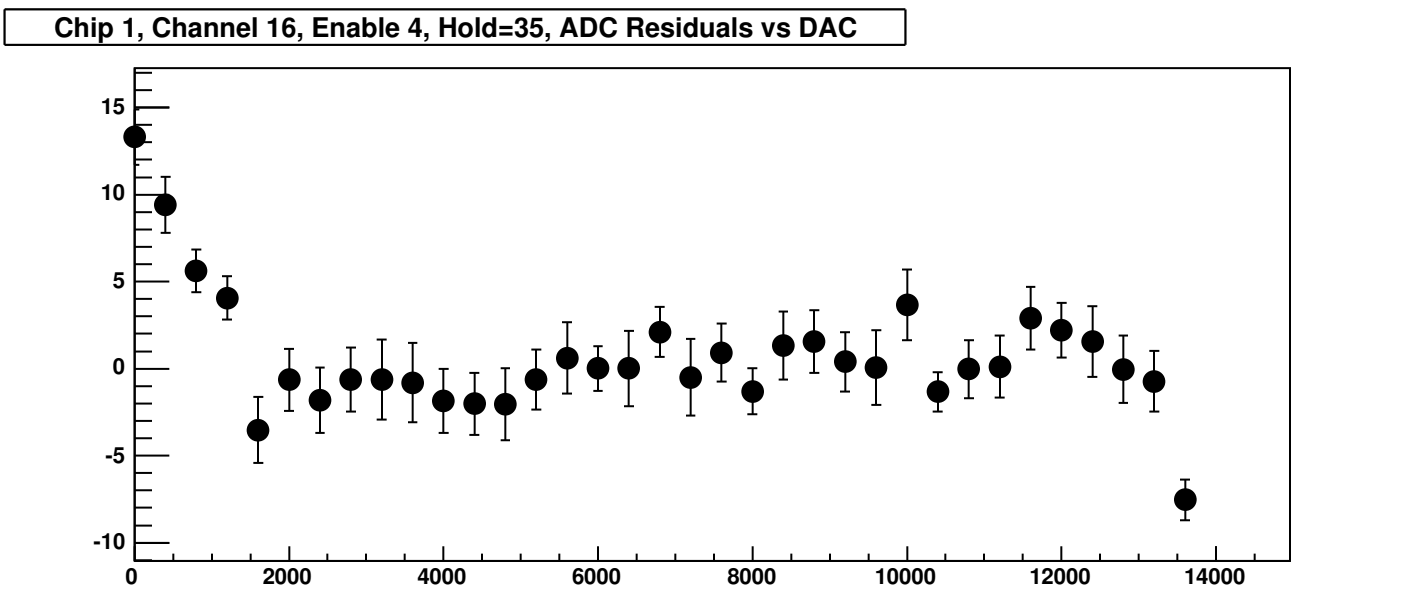

Chip 1, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 56.57 / 23
p0 -557.3 ± 0.824
p1 0.01693 ± 0.0001259
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

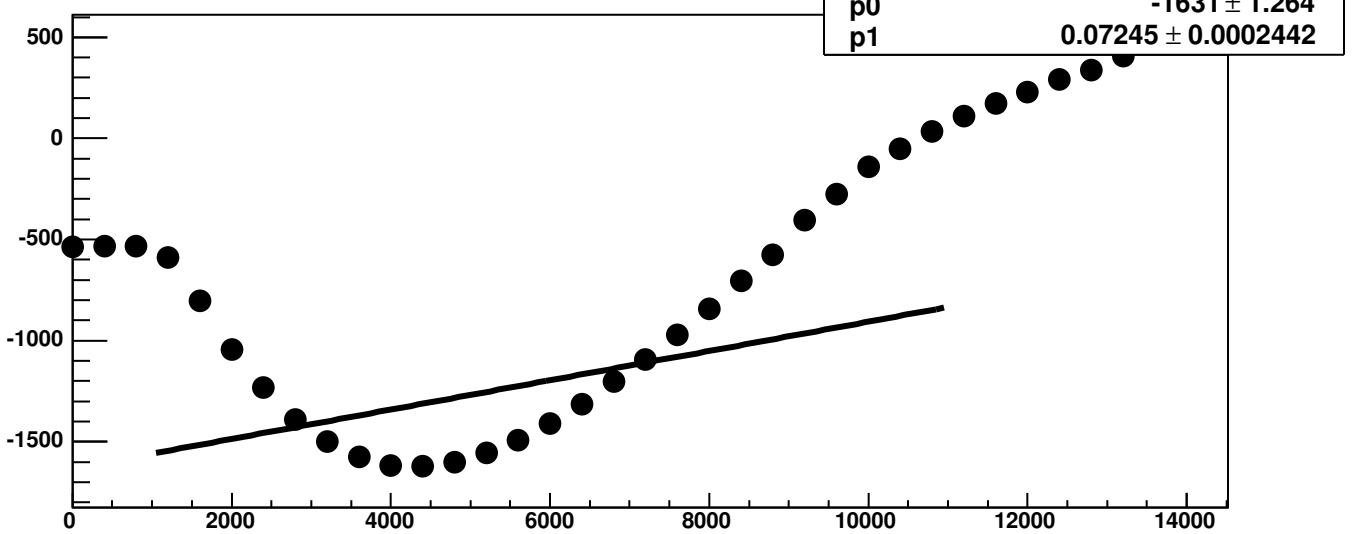

Chip 1, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 28.22 / 23
p0 -556.5 ± 0.7804
p1 0.01253 ± 0.000113
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
0
2000
4000
6000
8000
10000
12000
14000

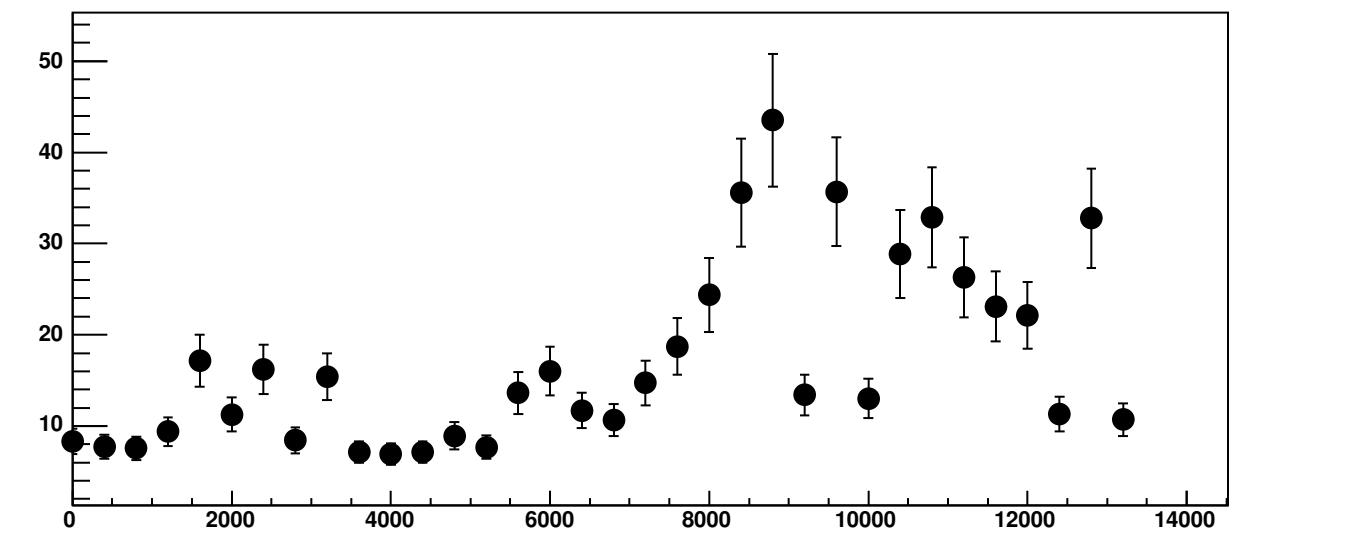

Chip 1, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 1, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

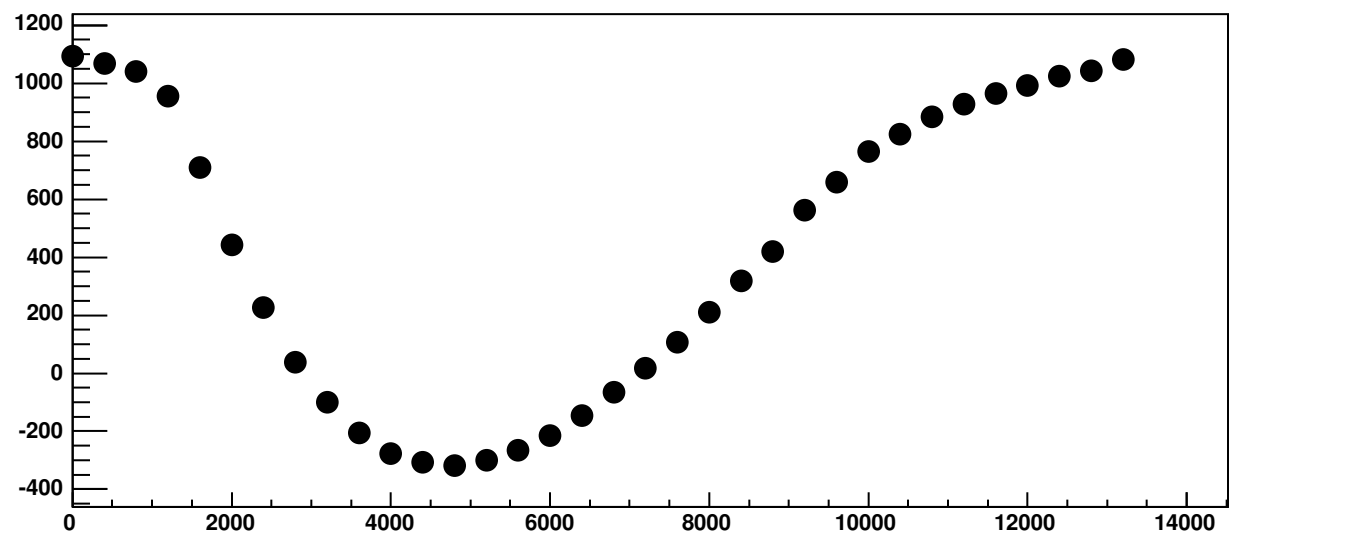
Chip 1, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC

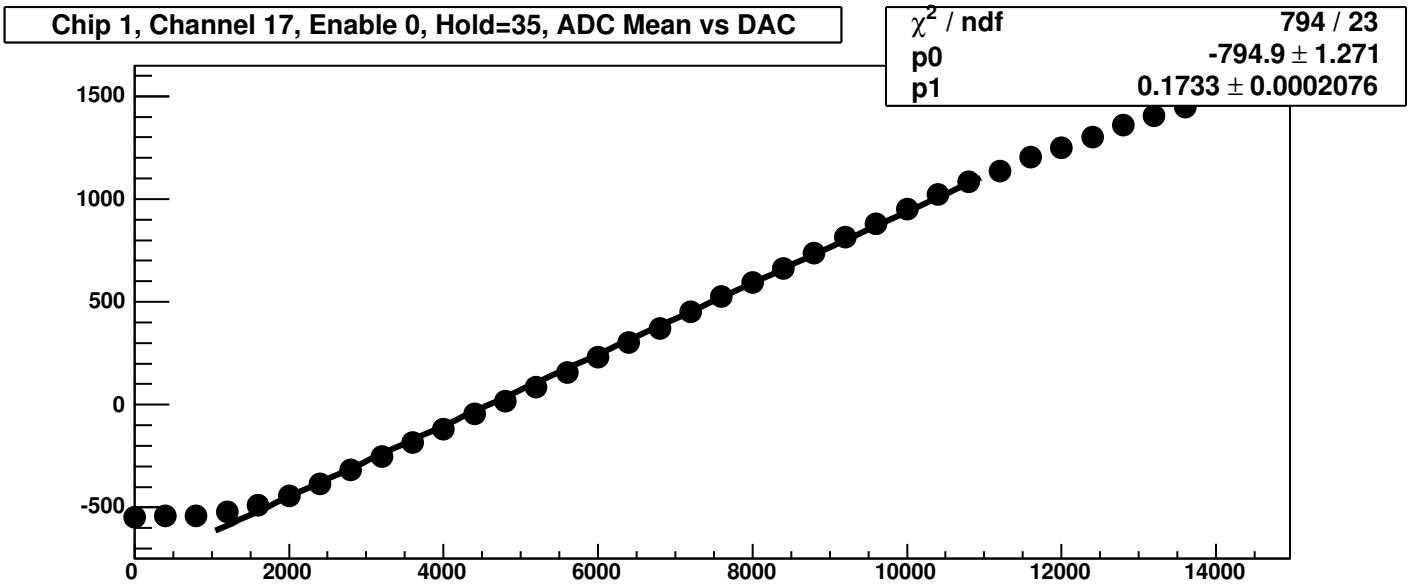


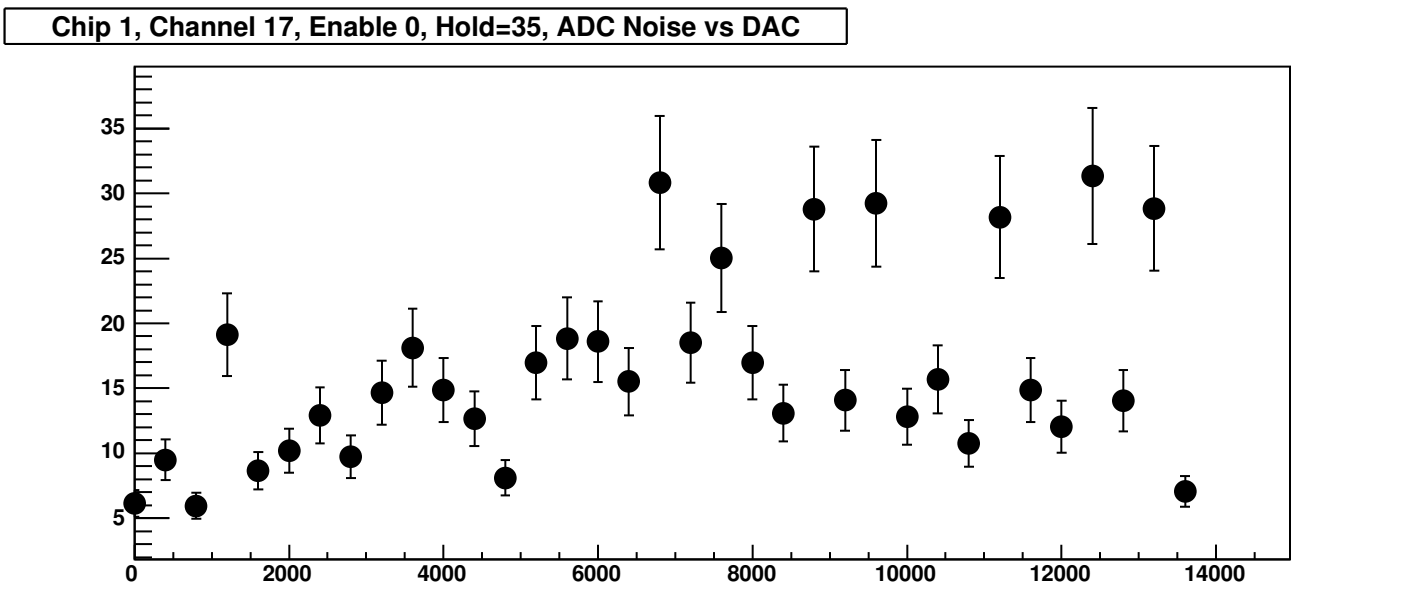
Chip 1, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC

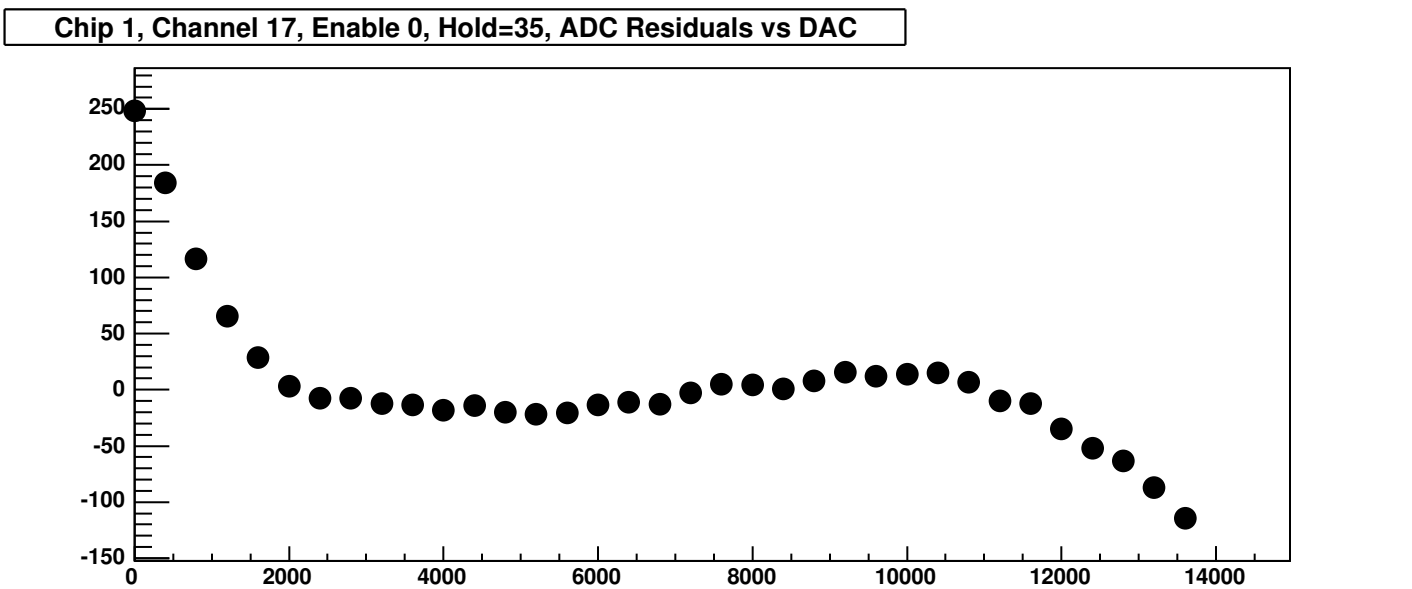


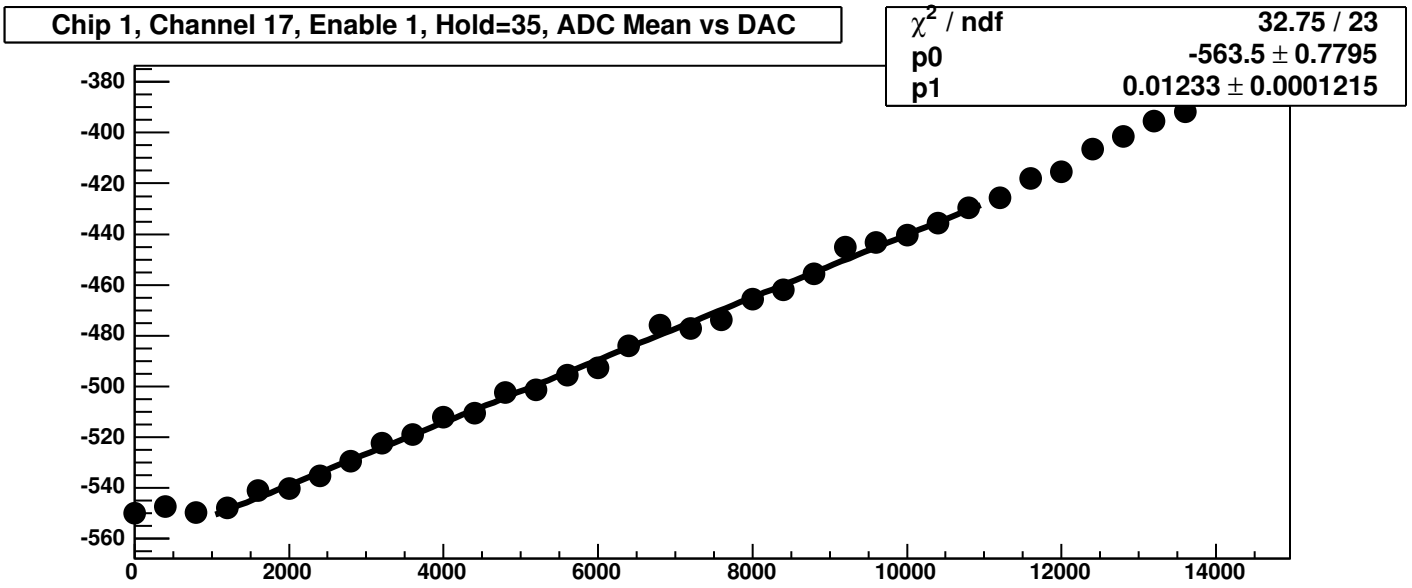
Chip 1, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC

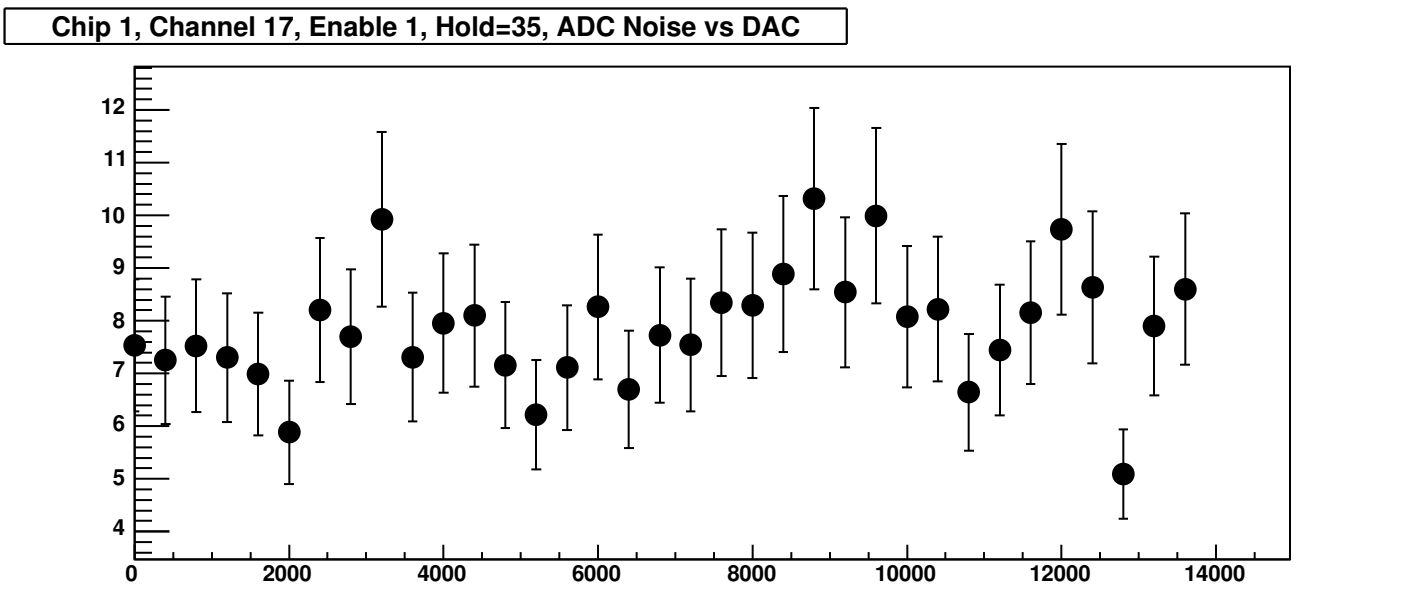


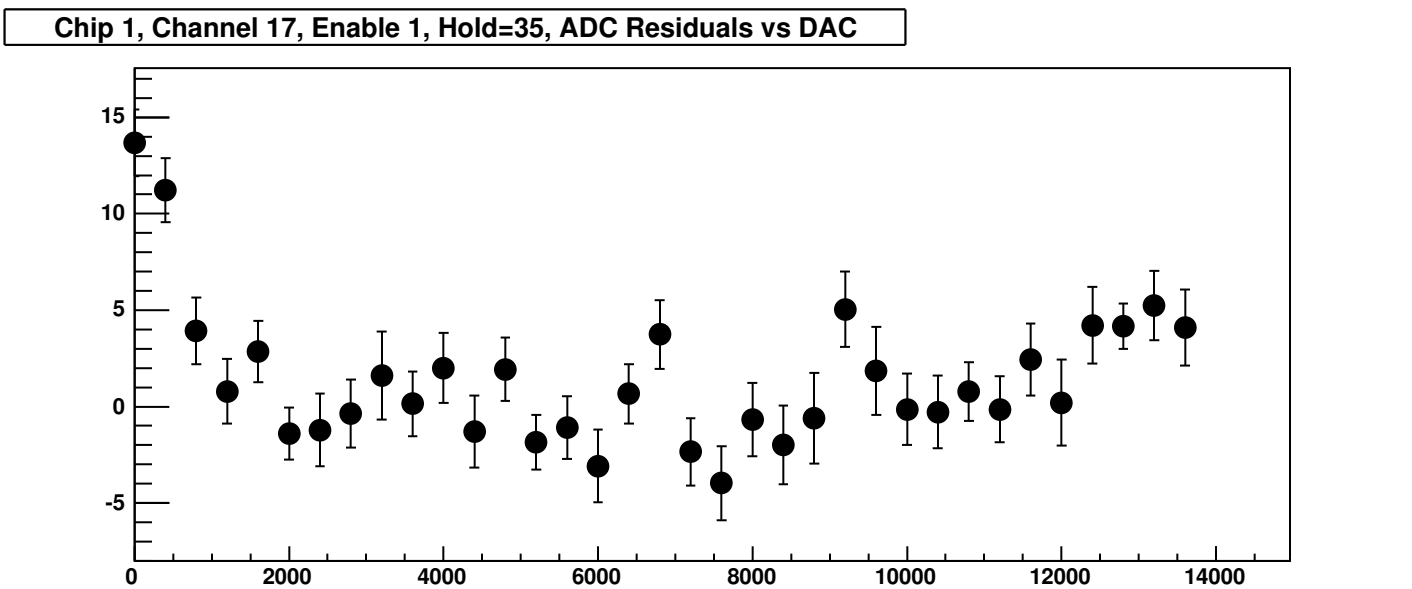

Chip 1, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 794 / 23
p0 -794.9 ± 1.271
p1 0.1733 ± 0.0002076
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

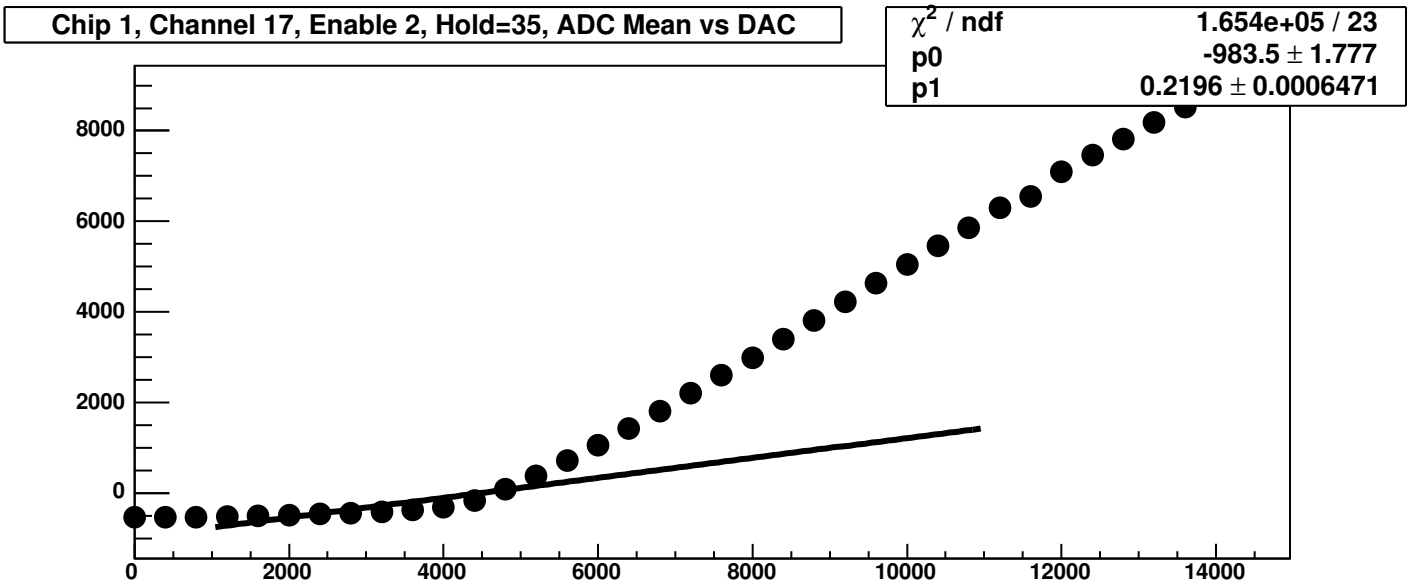

Chip 1, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

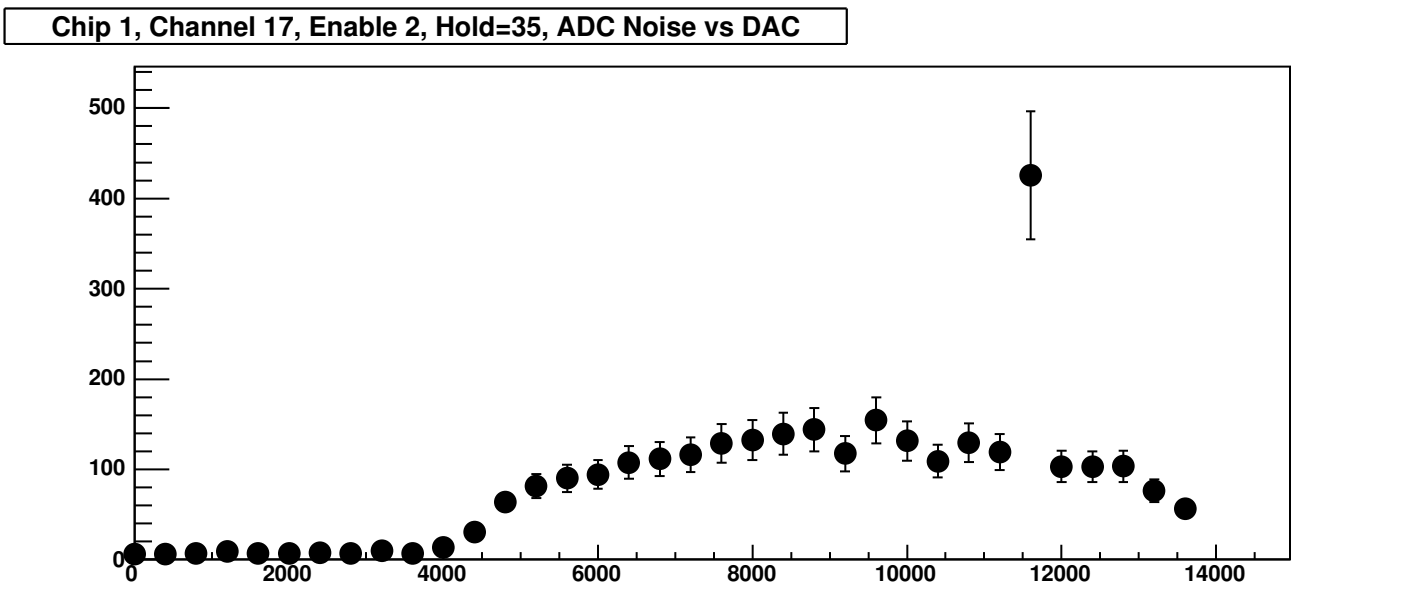

Chip 1, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

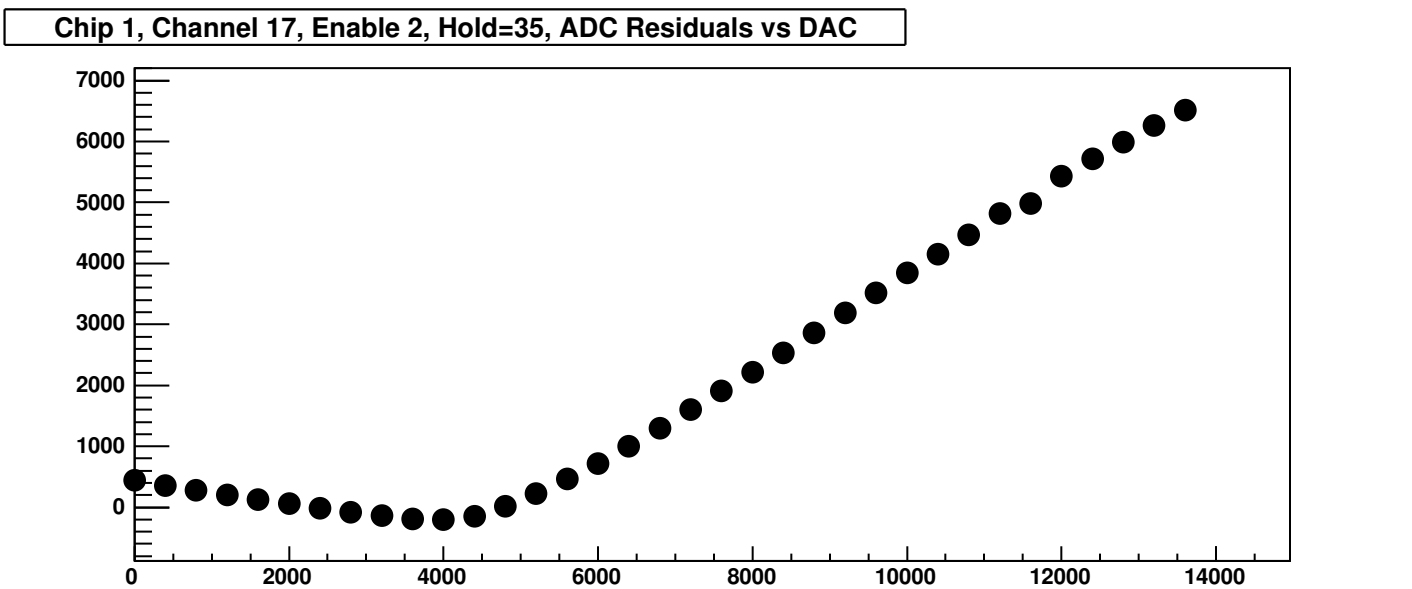

Chip 1, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.75 / 23
p0 -563.5 ± 0.7795
p1 0.01233 ± 0.0001215
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
0
2000
4000
6000
8000
10000
12000
14000

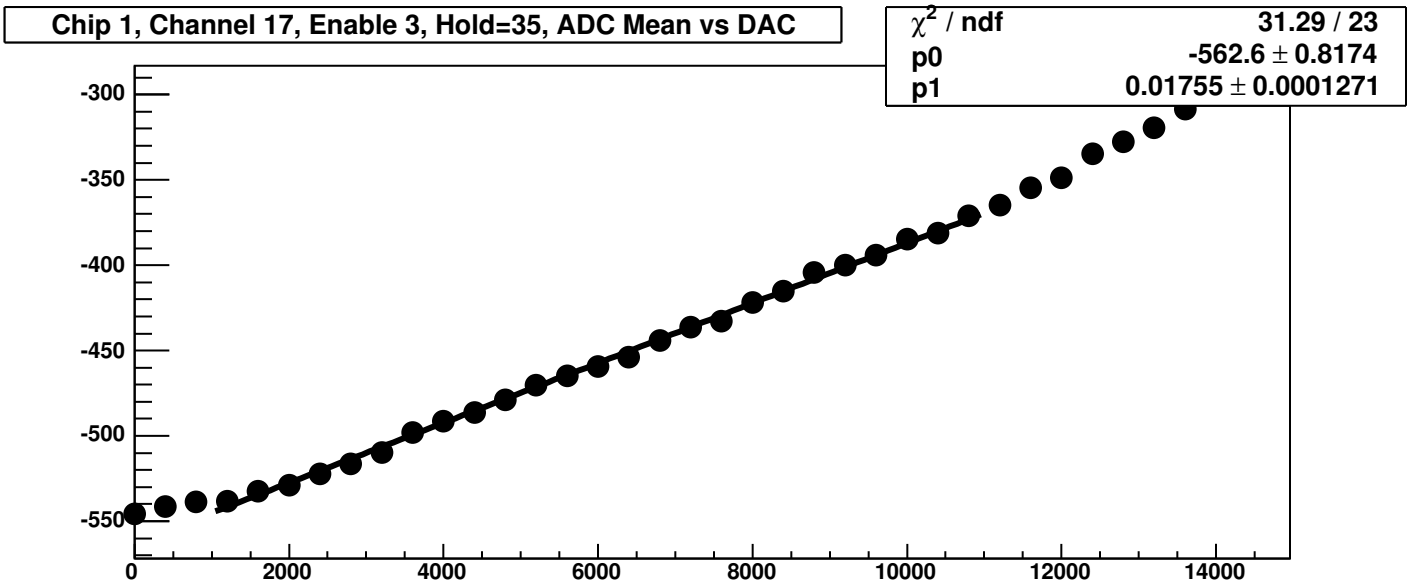

Chip 1, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

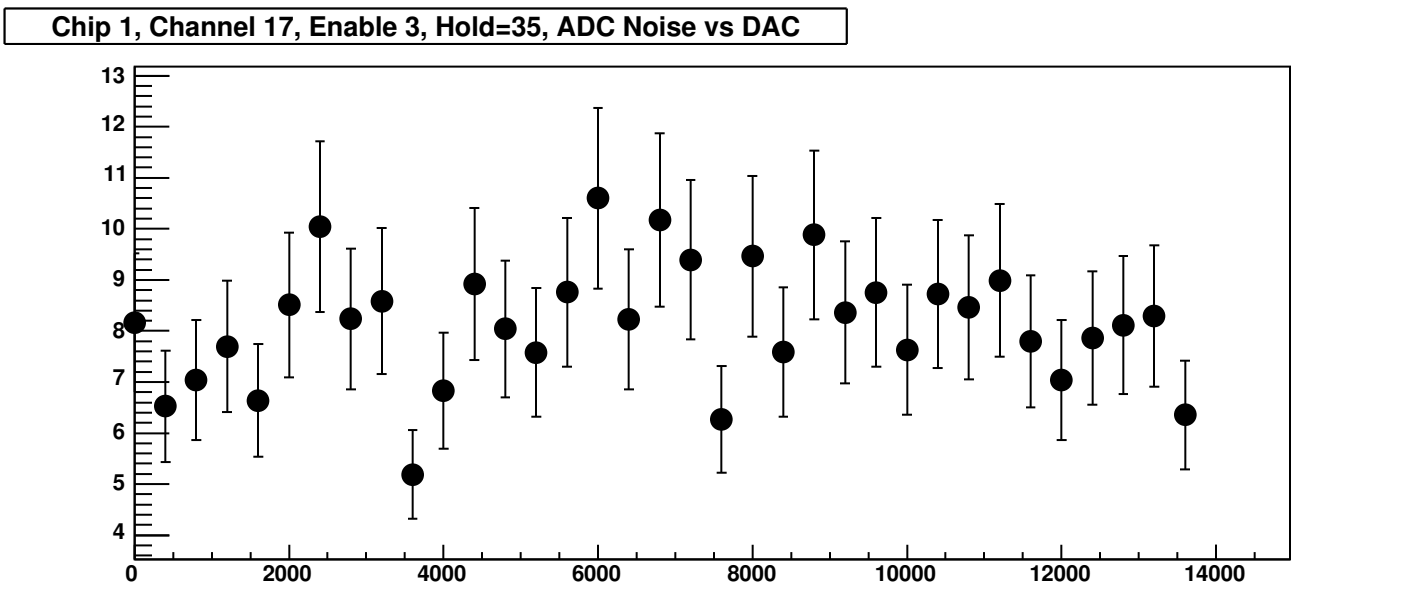

Chip 1, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

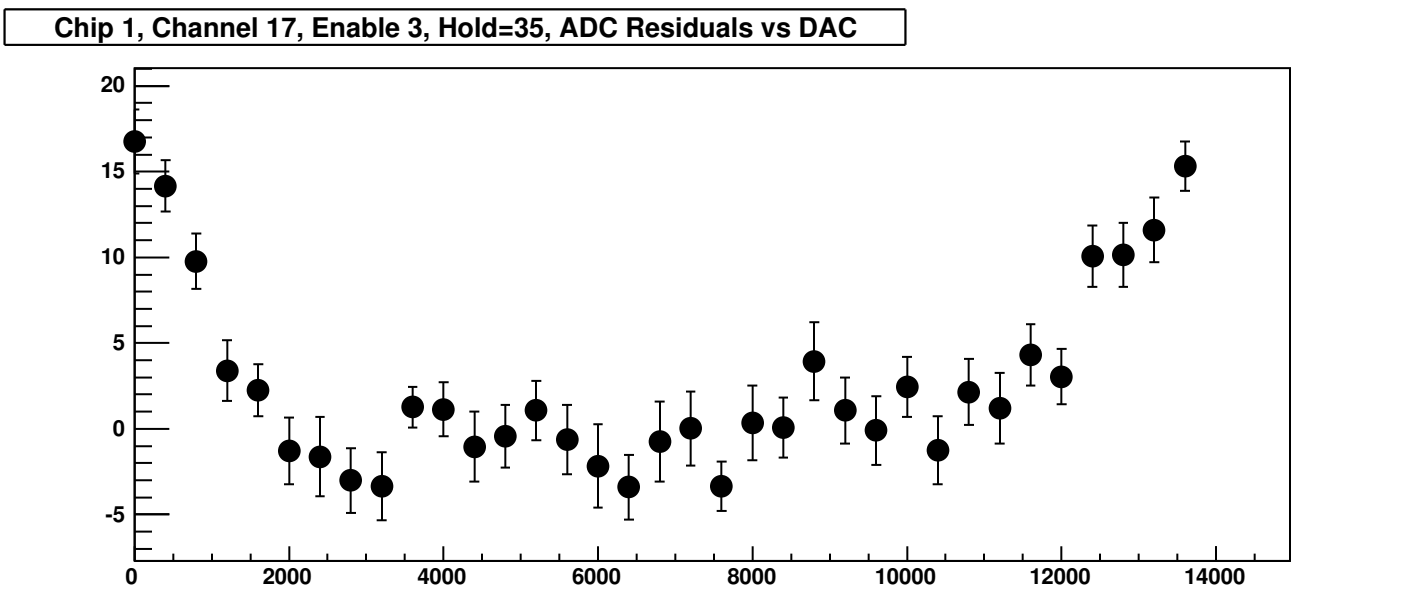

Chip 1, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1.654e+05 / 23
p0 -983.5 ± 1.777
p1 0.2196 ± 0.0006471
8000
6000
4000
2000
0
0
2000
4000
6000
8000
10000
12000
14000

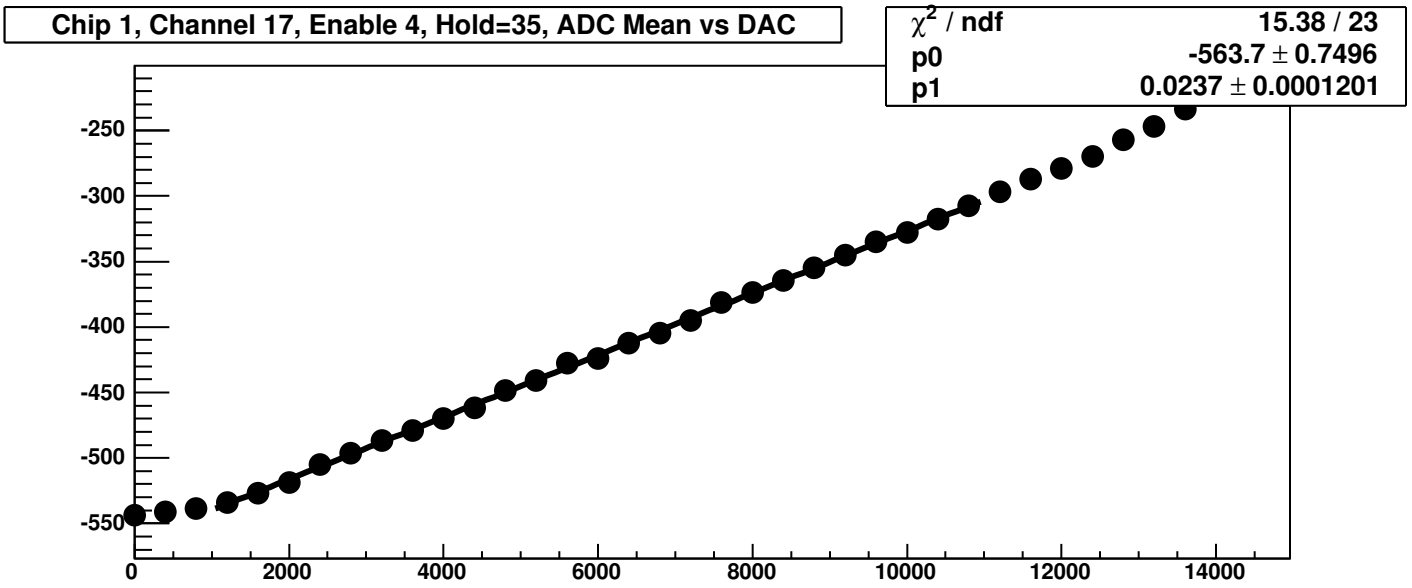

Chip 1, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

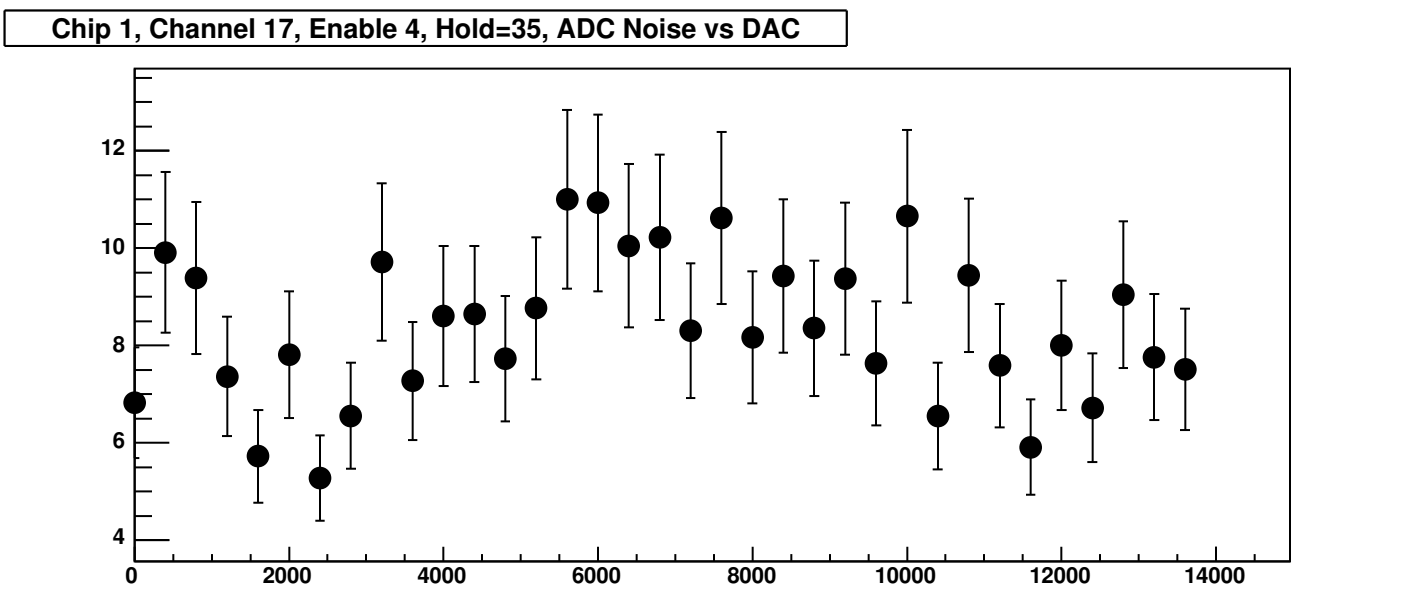

Chip 1, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
7000
6000
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

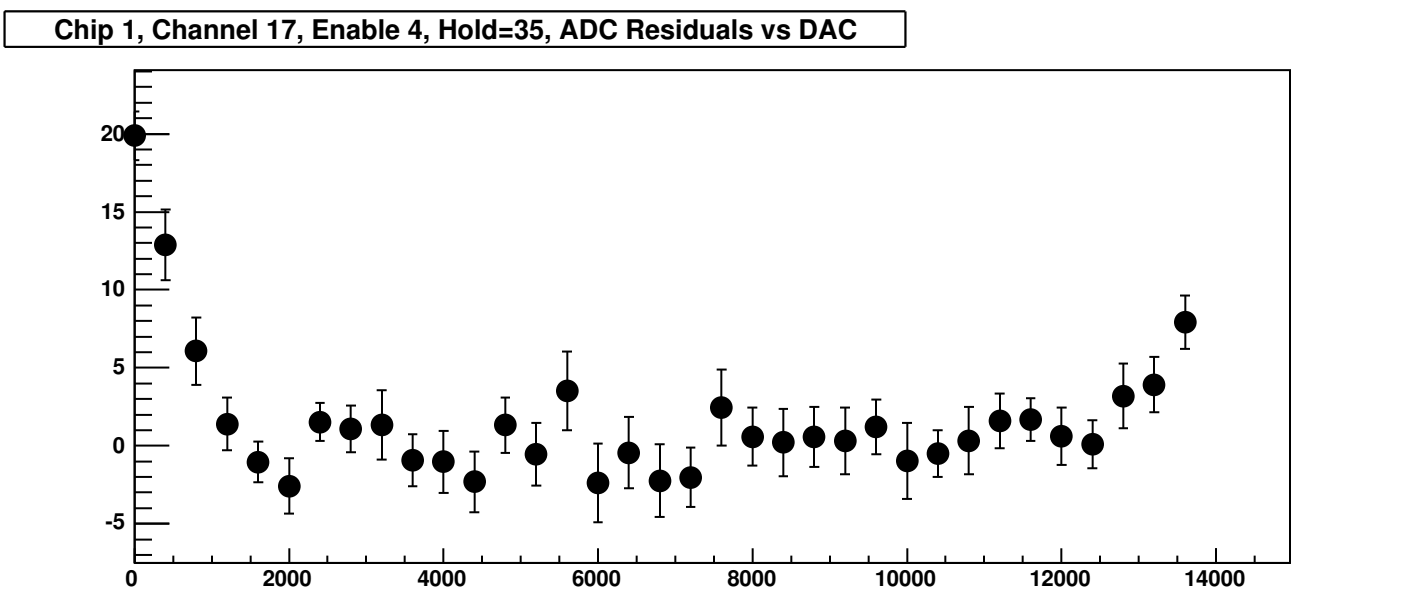

Chip 1, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.29 / 23
p0 -562.6 ± 0.8174
p1 0.01755 ± 0.0001271
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

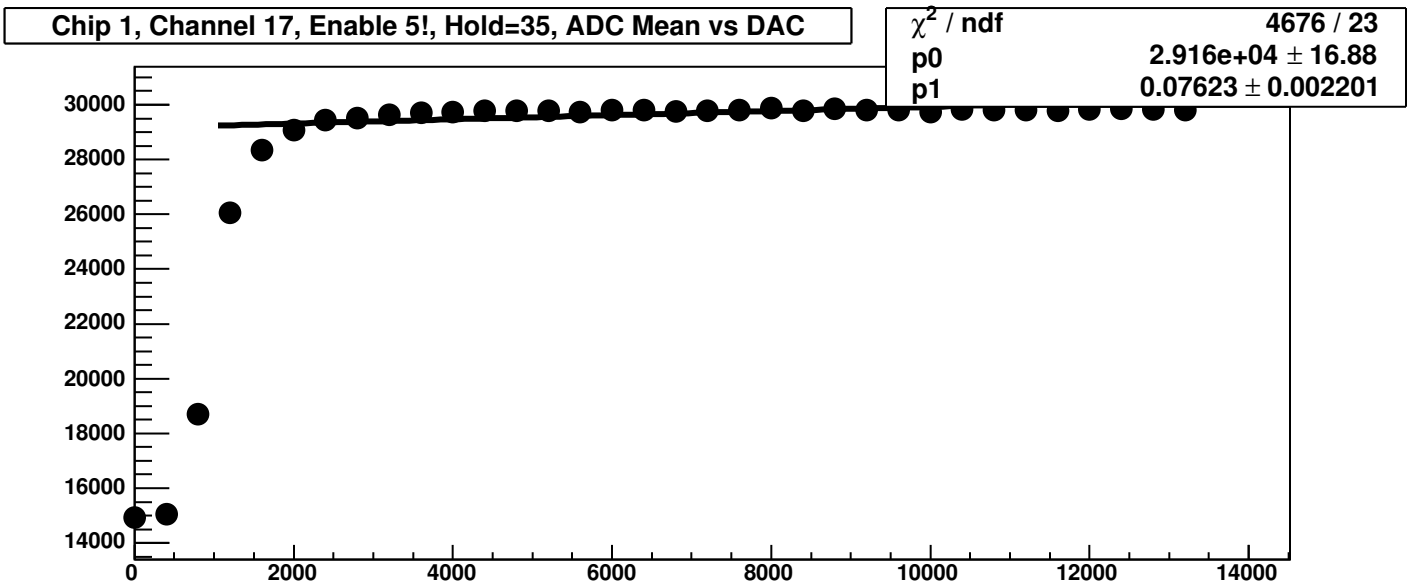

Chip 1, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

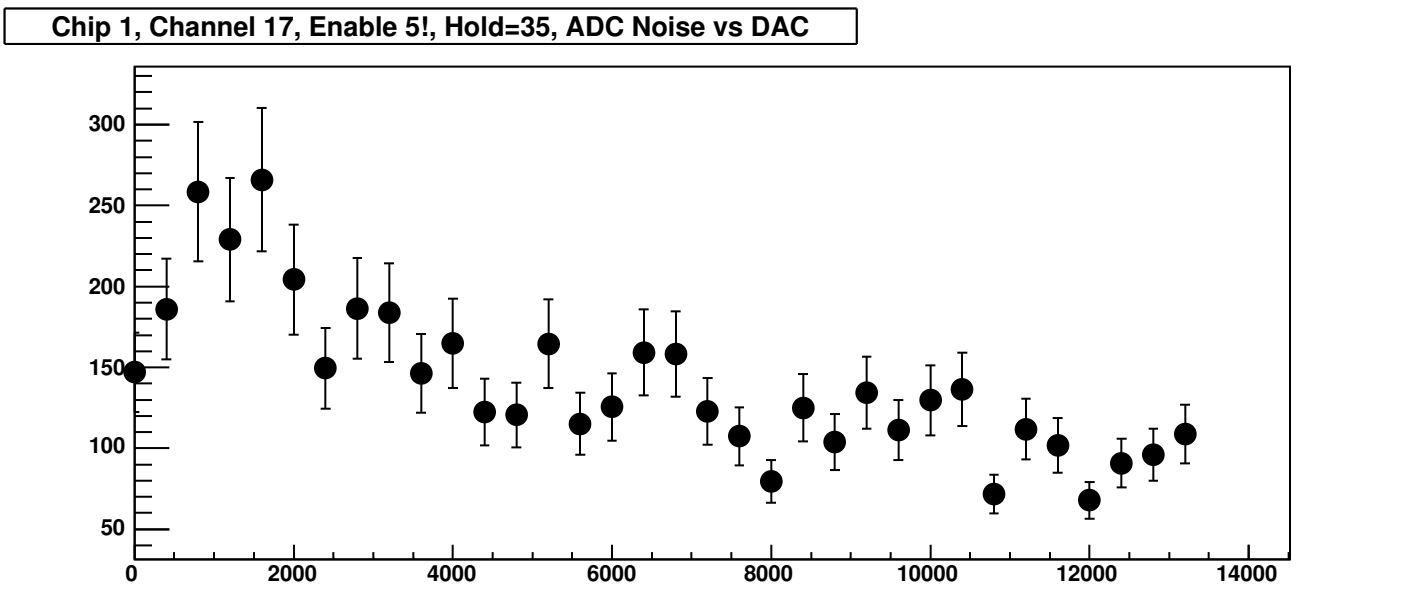

Chip 1, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

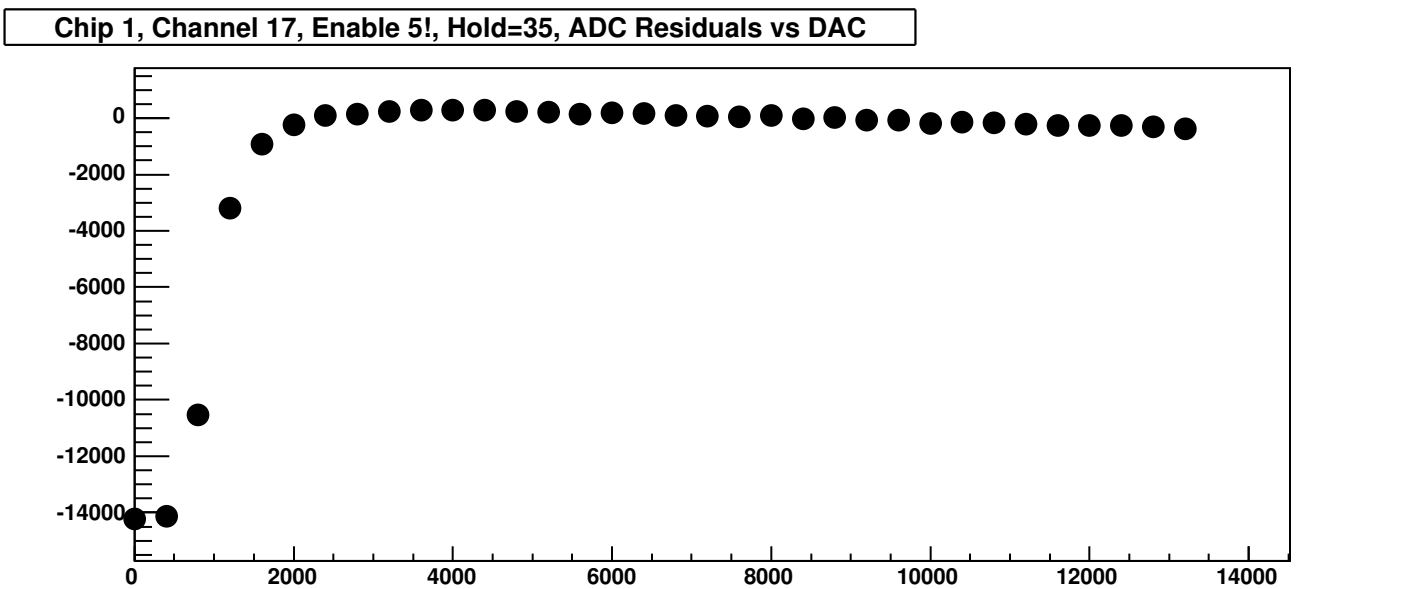

Chip 1, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 15.38 / 23
p0 -563.7 ± 0.7496
p1 0.0237 ± 0.0001201
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

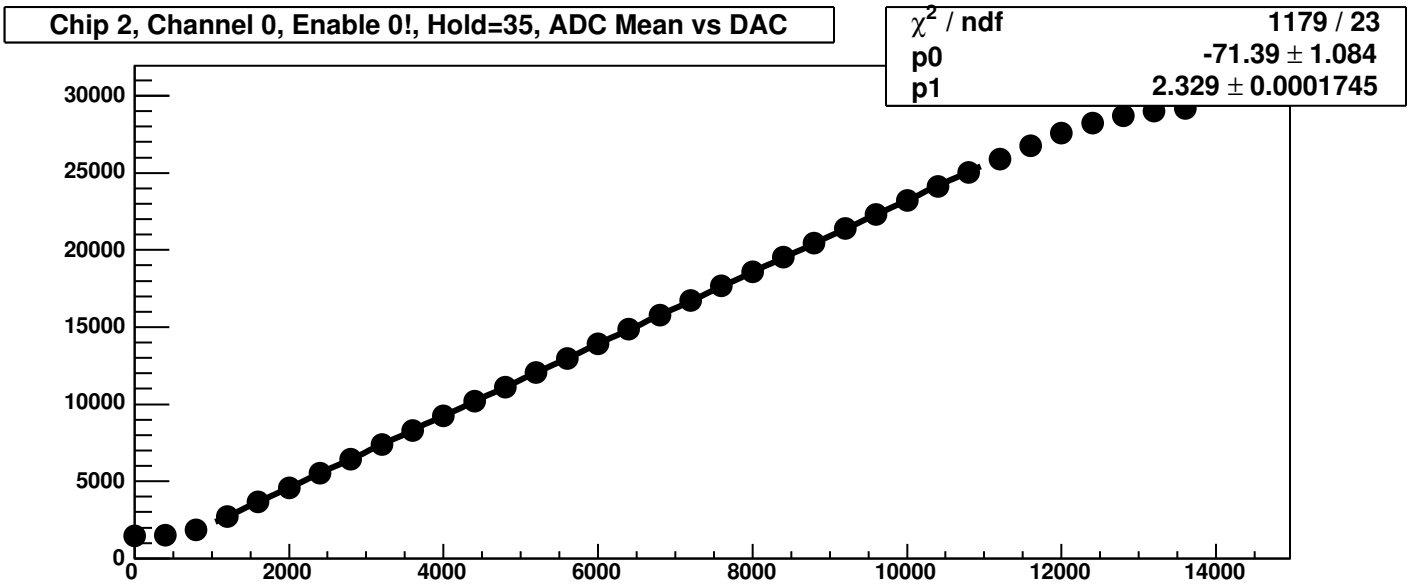

Chip 1, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

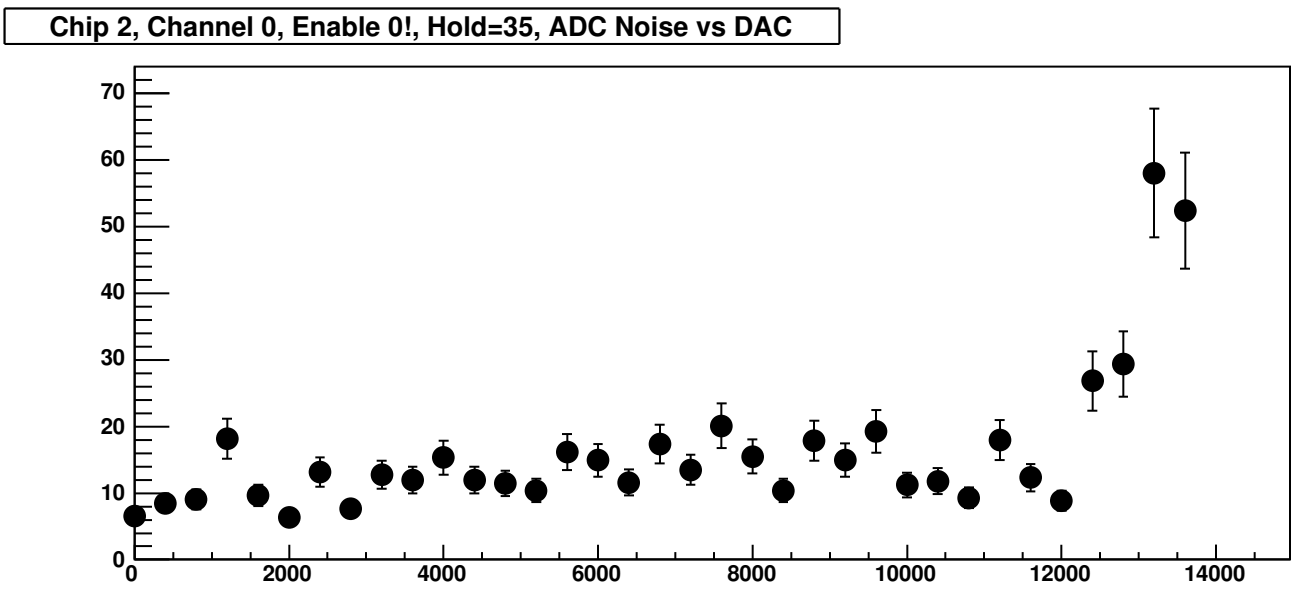

Chip 1, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

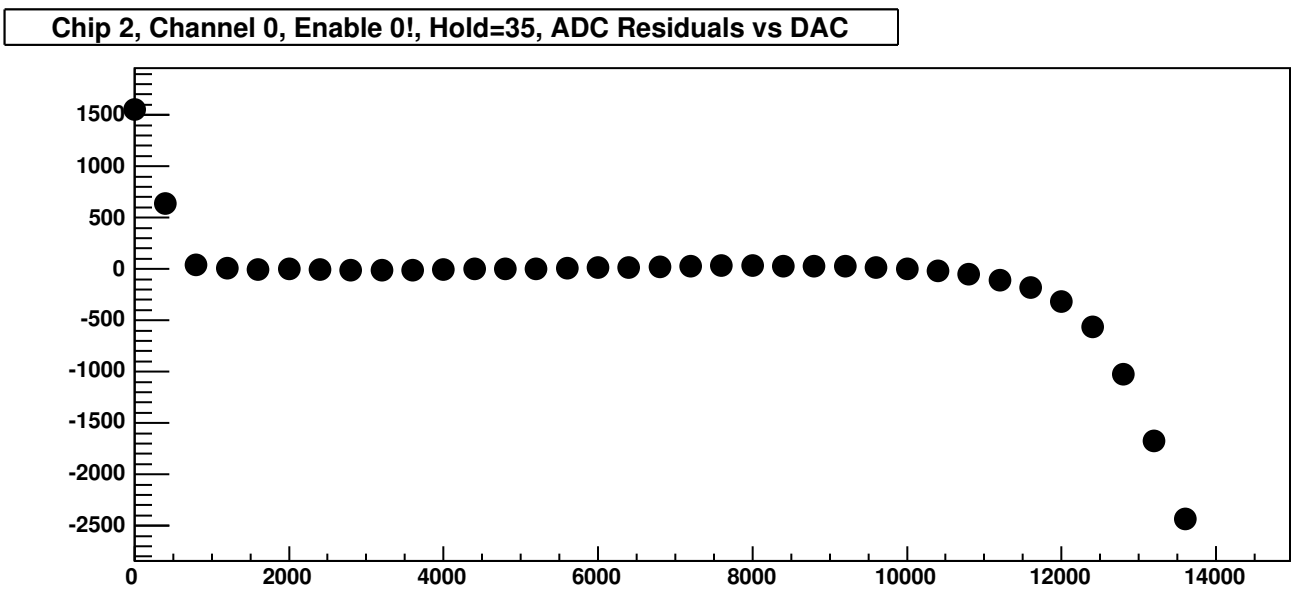

Chip 1, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4676 / 23
p0 2.916e+04 ± 16.88
p1 0.07623 ± 0.002201
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

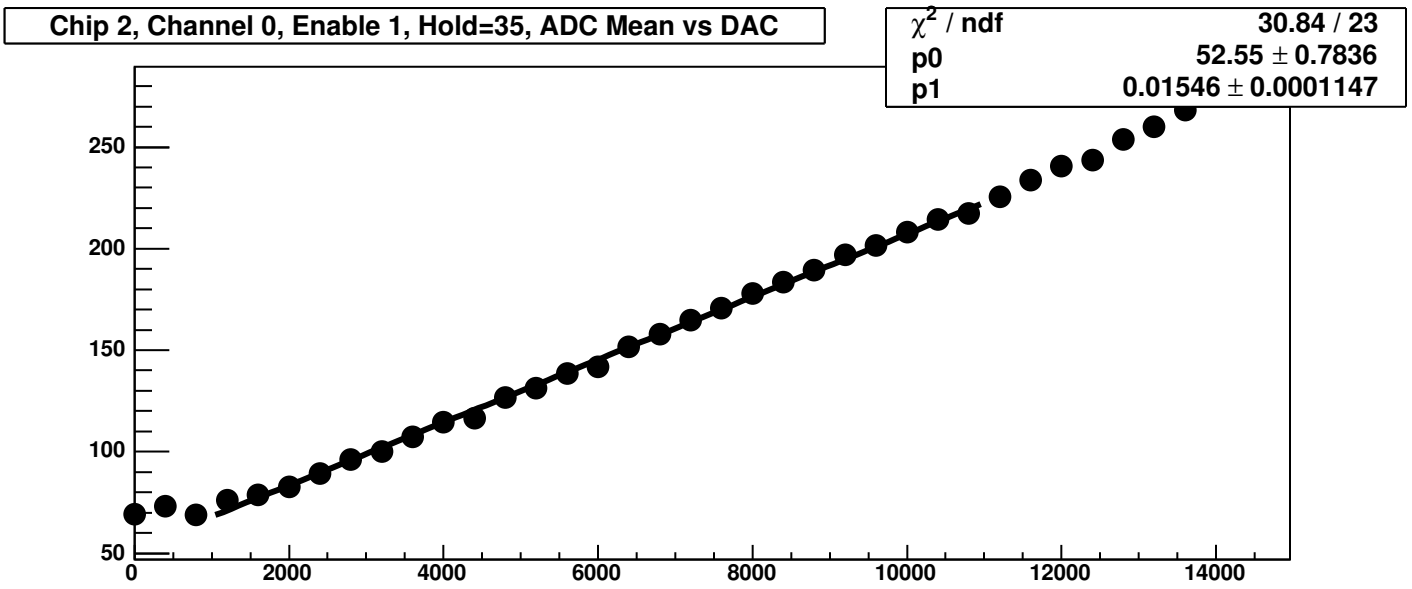

Chip 1, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

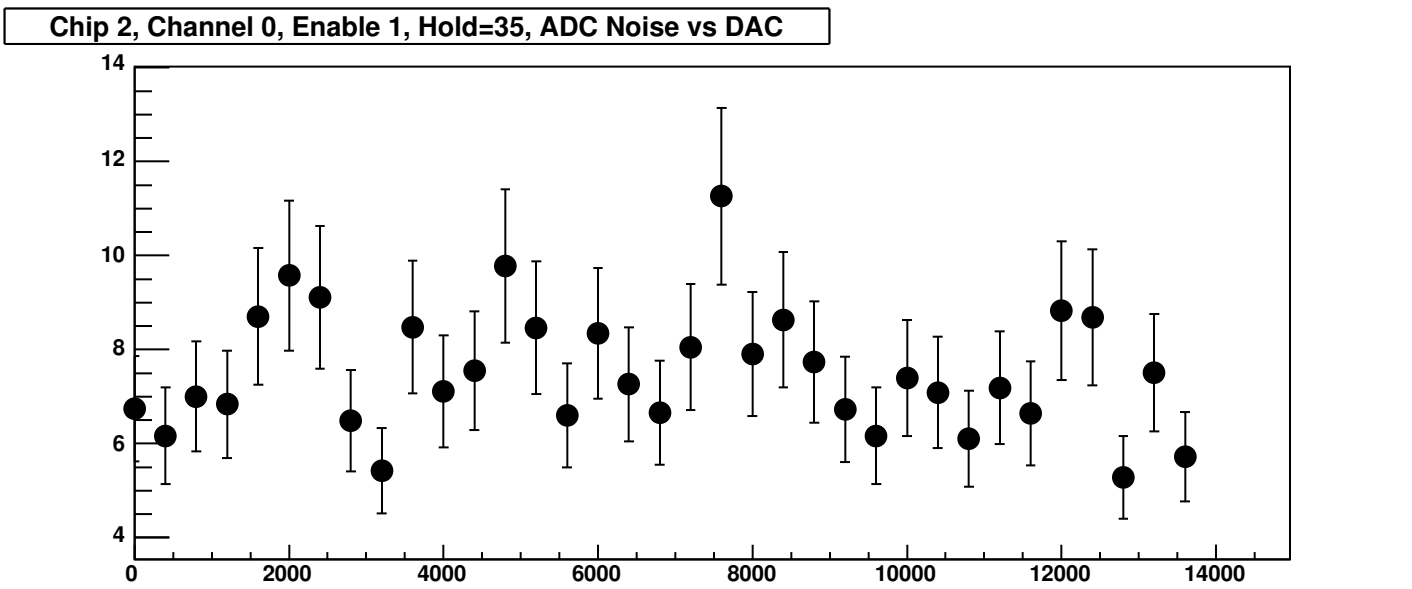

Chip 1, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

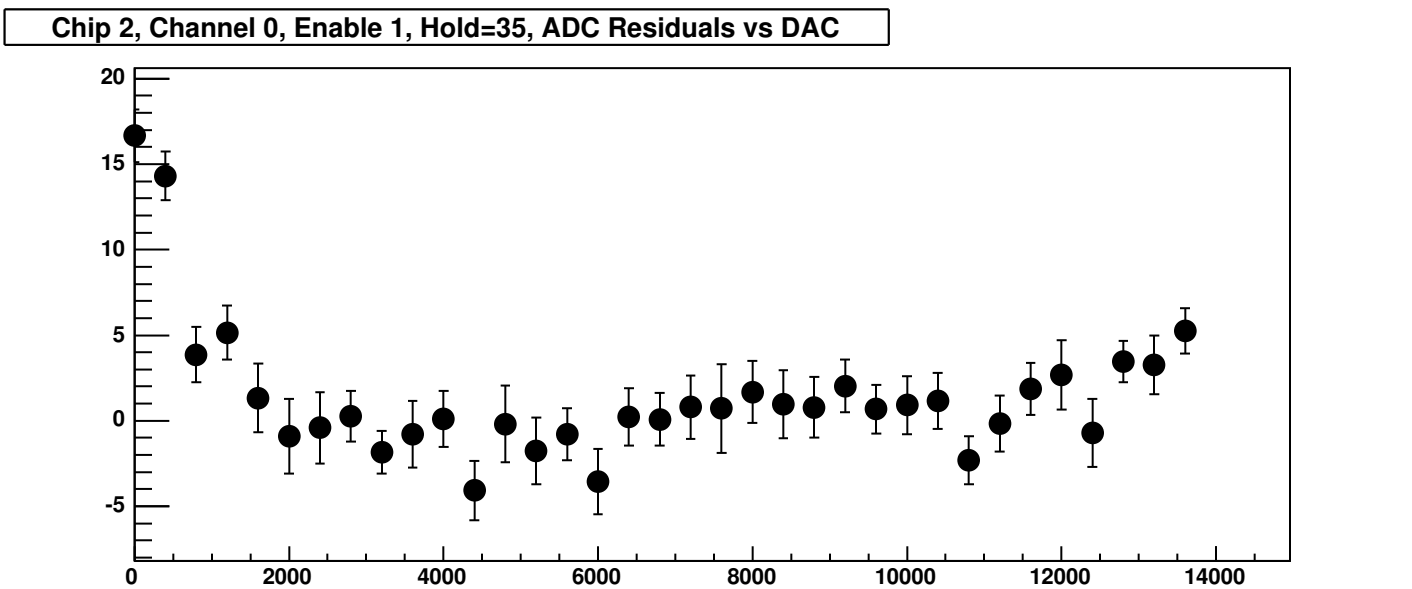

Chip 2, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1179 / 23
p0 -71.39 ± 1.084
p1 2.329 ± 0.0001745
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

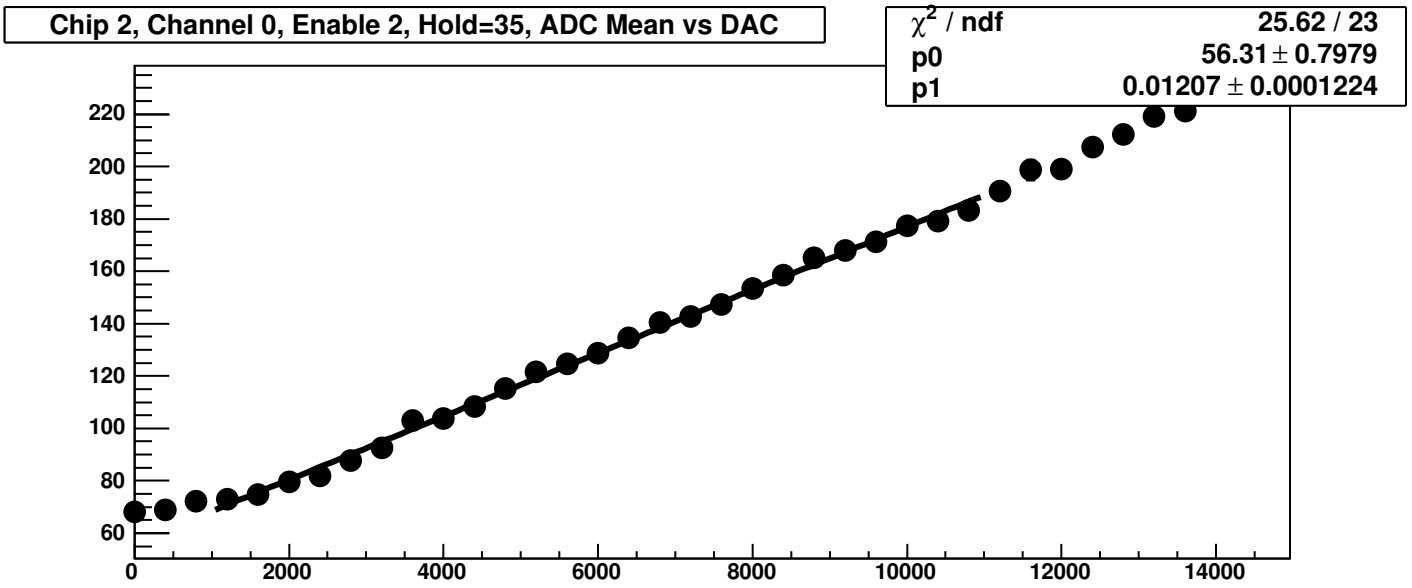

Chip 2, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

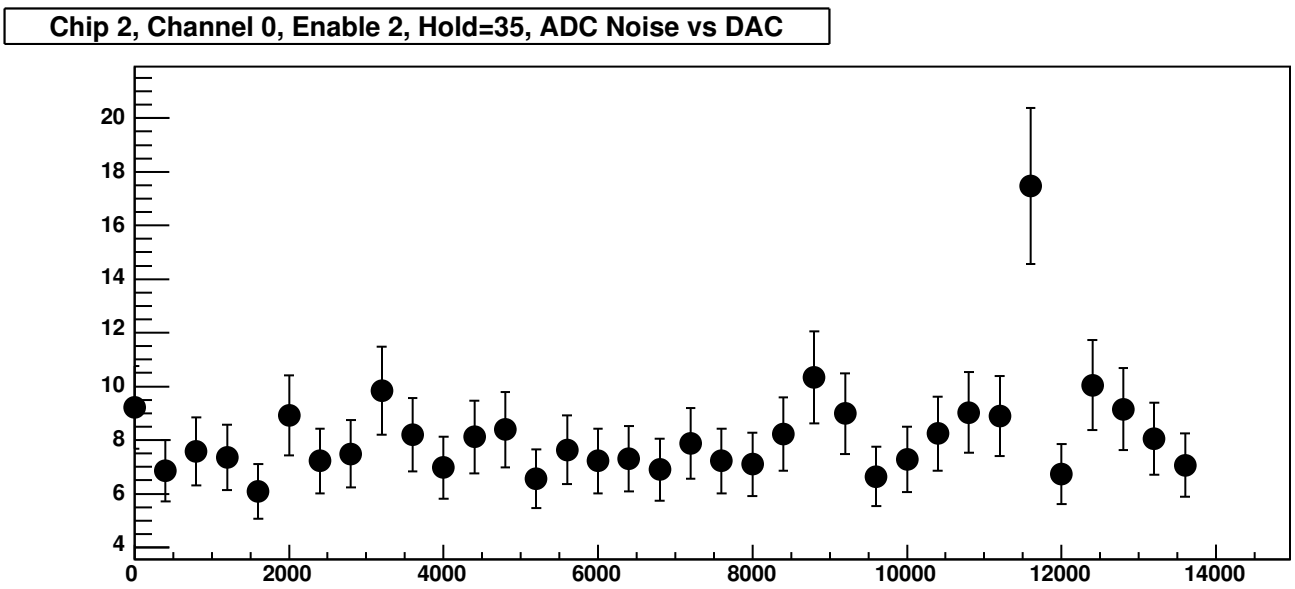

Chip 2, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

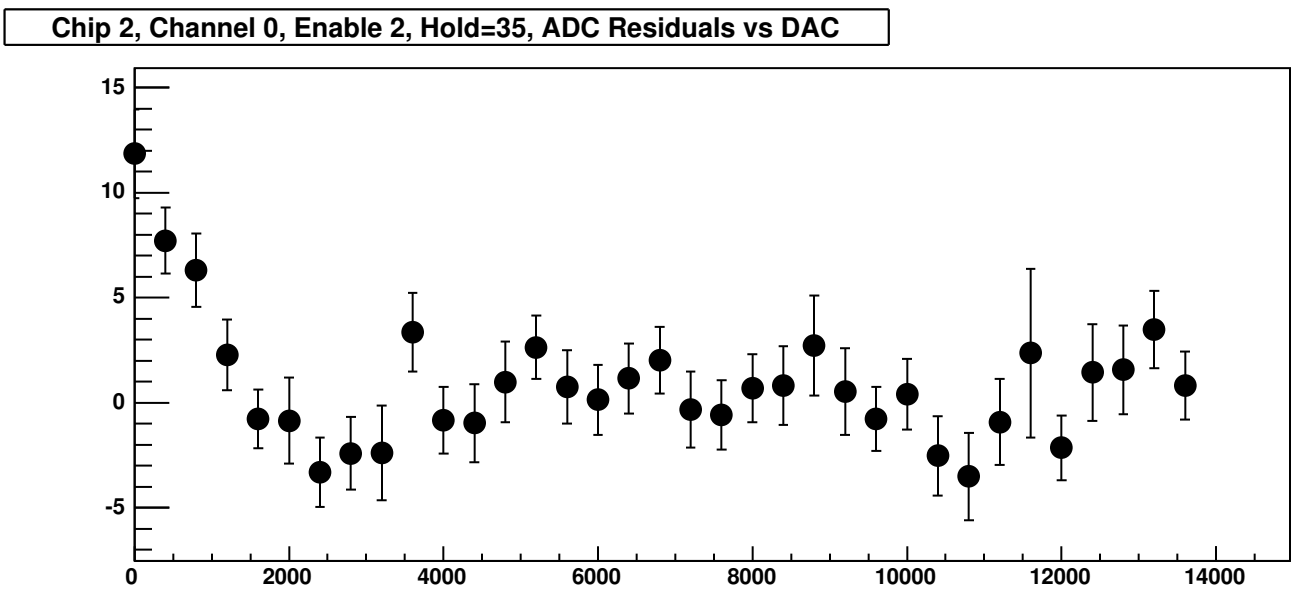

Chip 2, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 30.84 / 23
p0 52.55 ± 0.7836
p1 0.01546 ± 0.0001147
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

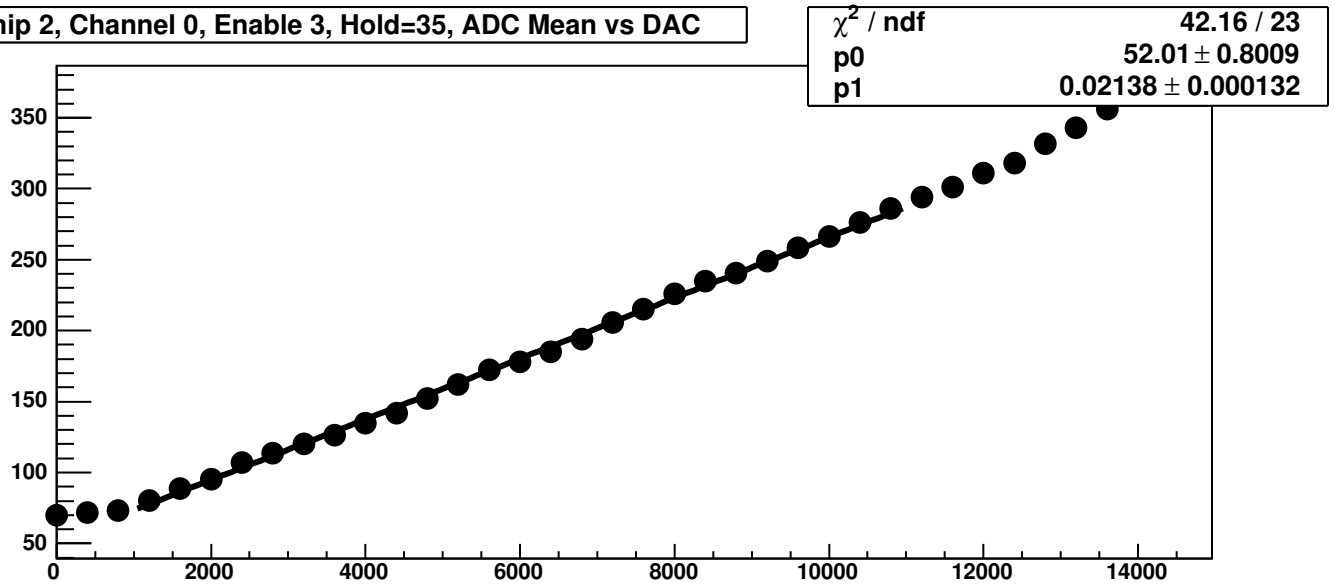

Chip 2, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 25.62 / 23
p0 56.31 ± 0.7979
p1 0.01207 ± 0.0001224
220
200
180
160
140
120
100
80
60
0
2000
4000
6000
8000
10000
12000
14000

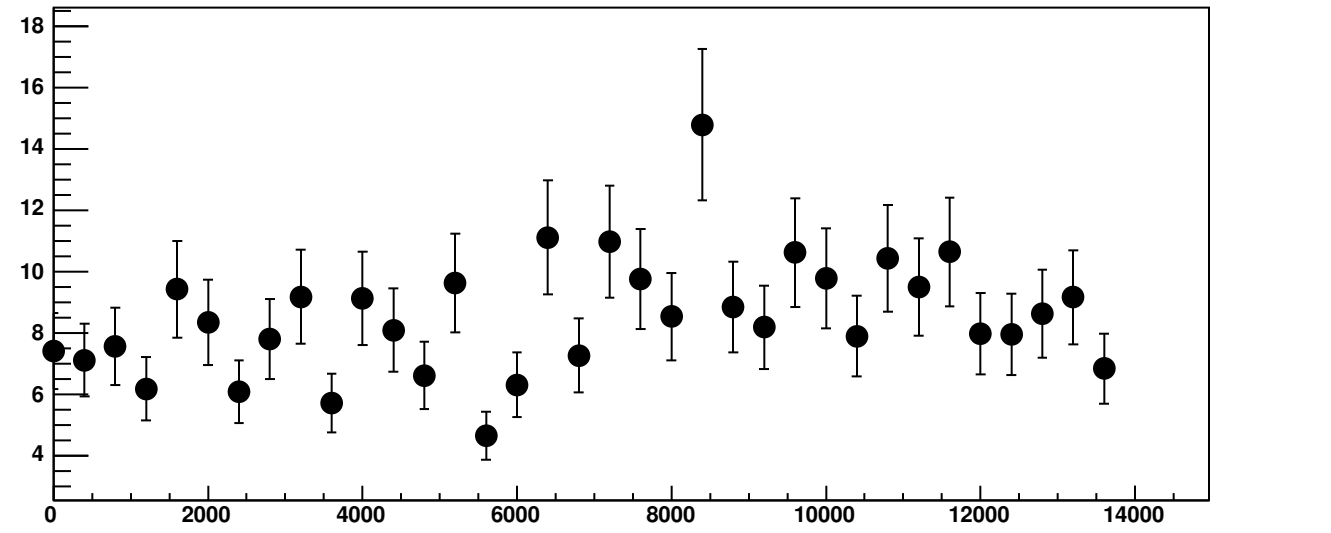

Chip 2, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

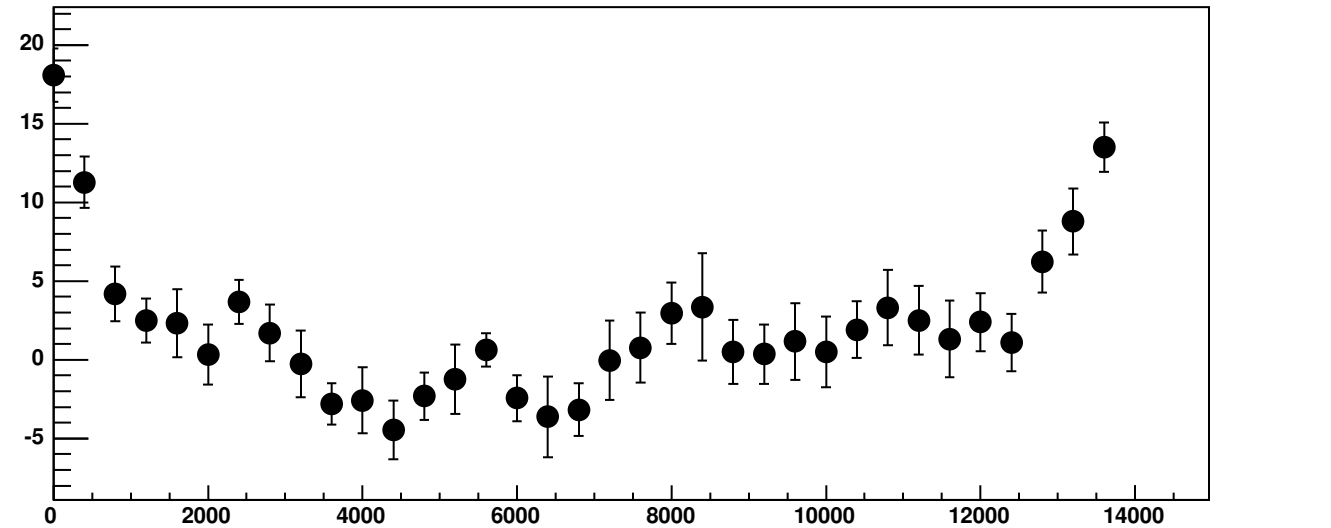
Chip 2, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC

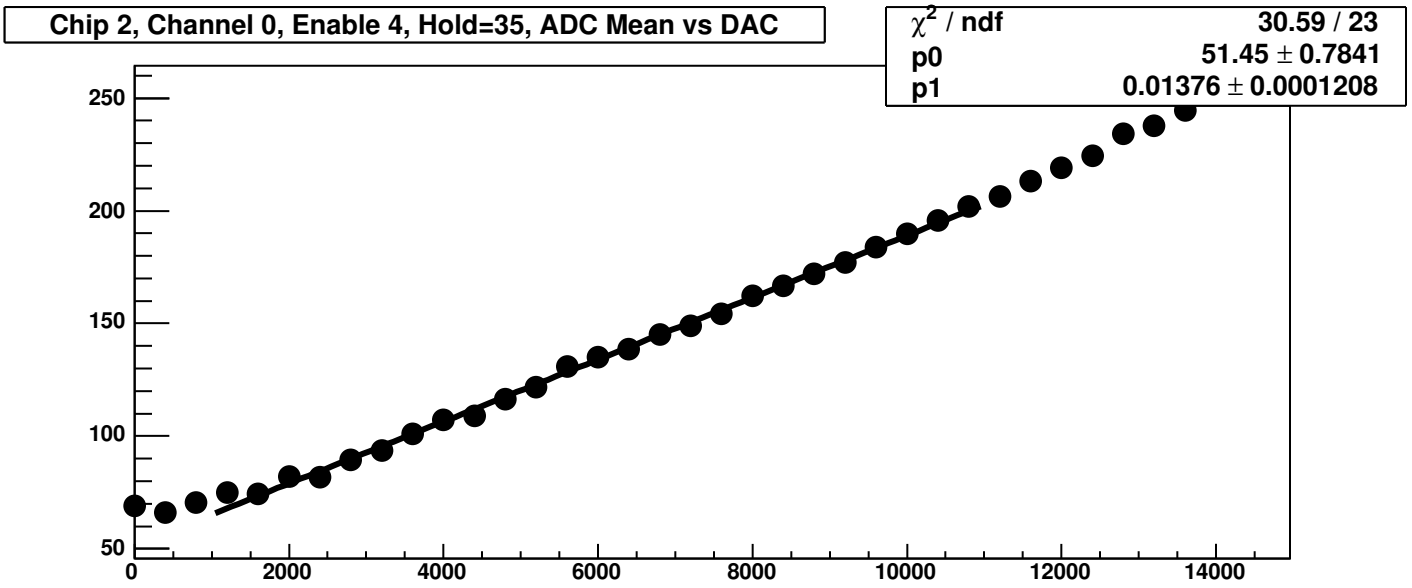


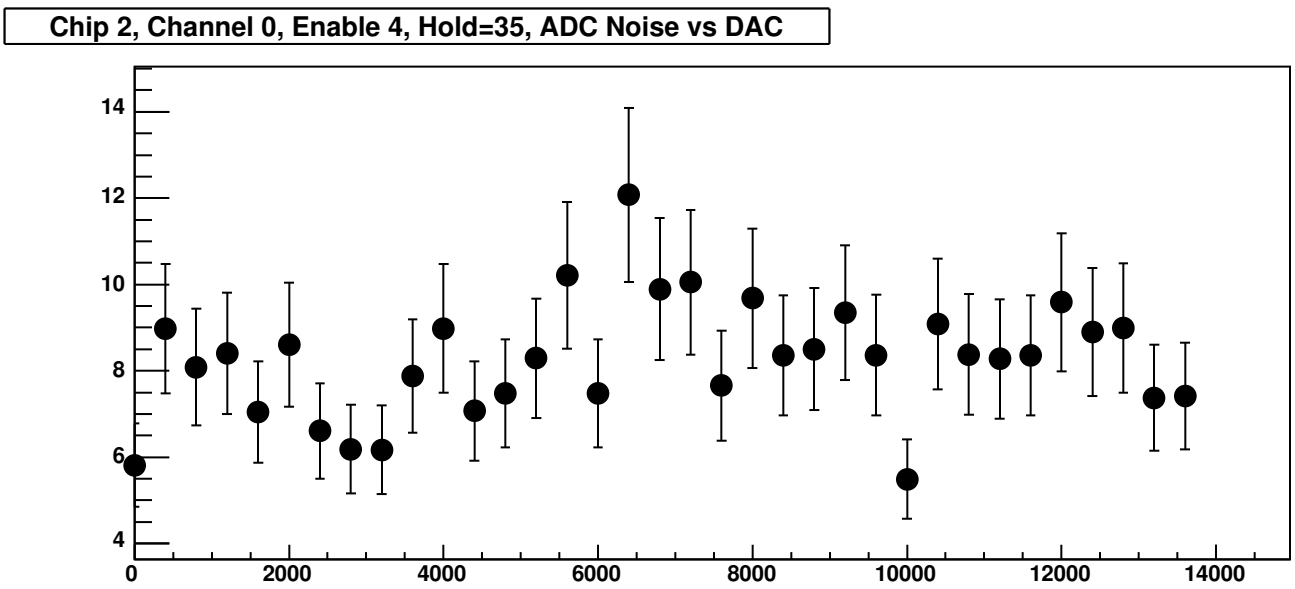
Chip 2, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC

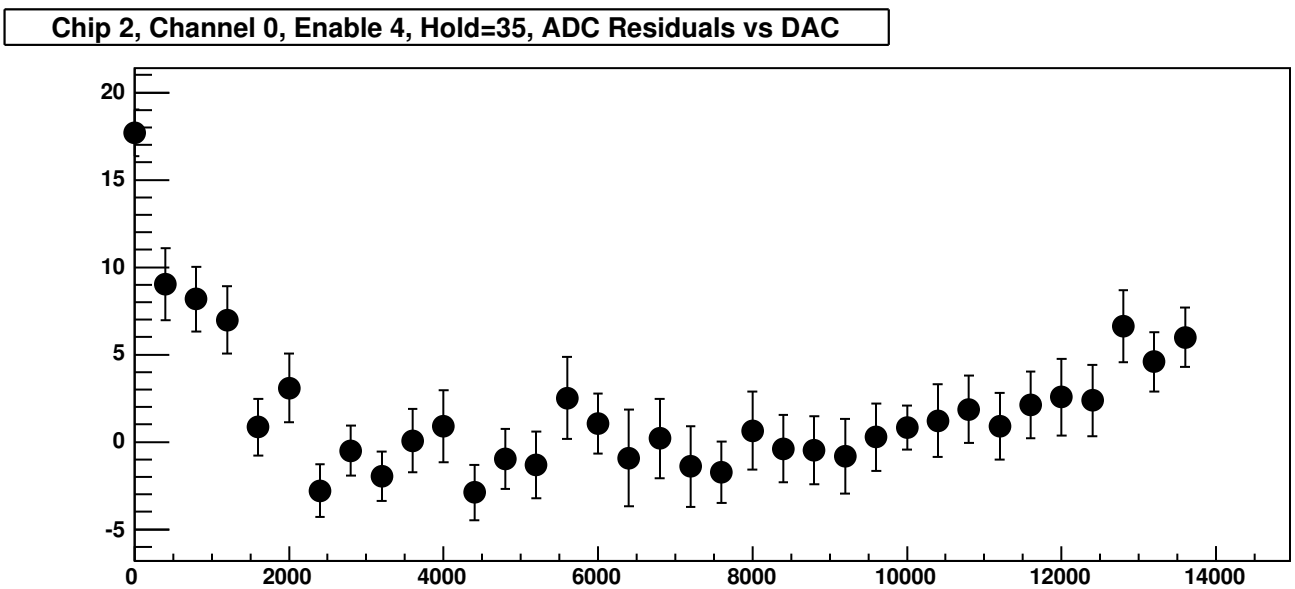


Chip 2, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC

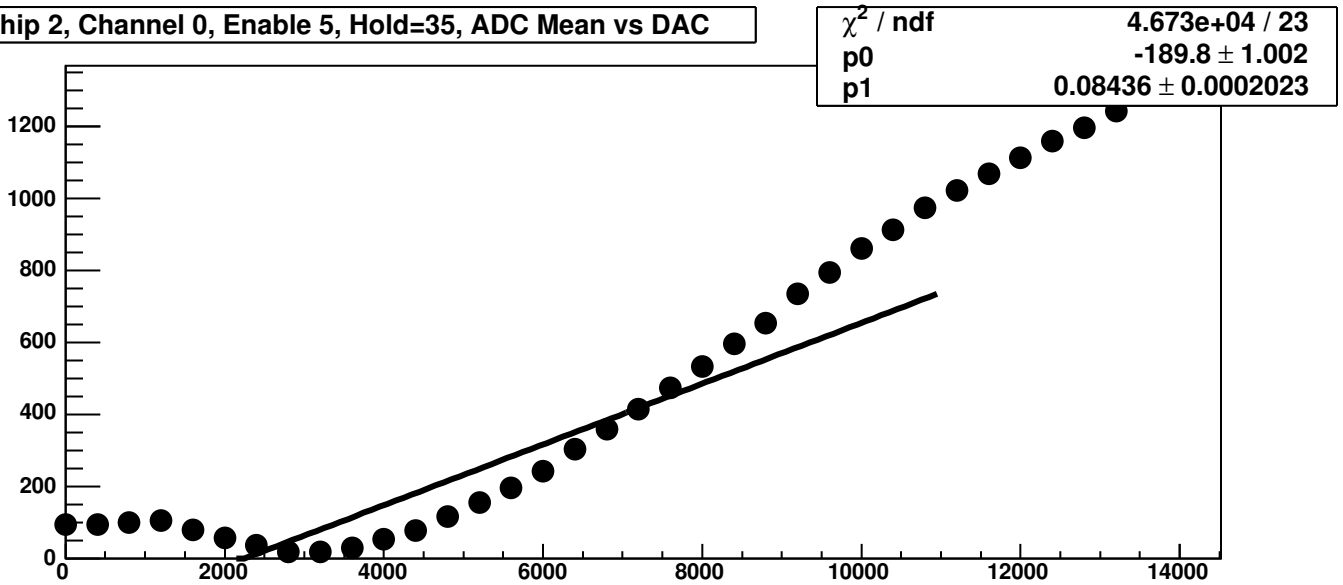



Chip 2, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.59 / 23
p0 51.45 ± 0.7841
p1 0.01376 ± 0.0001208
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

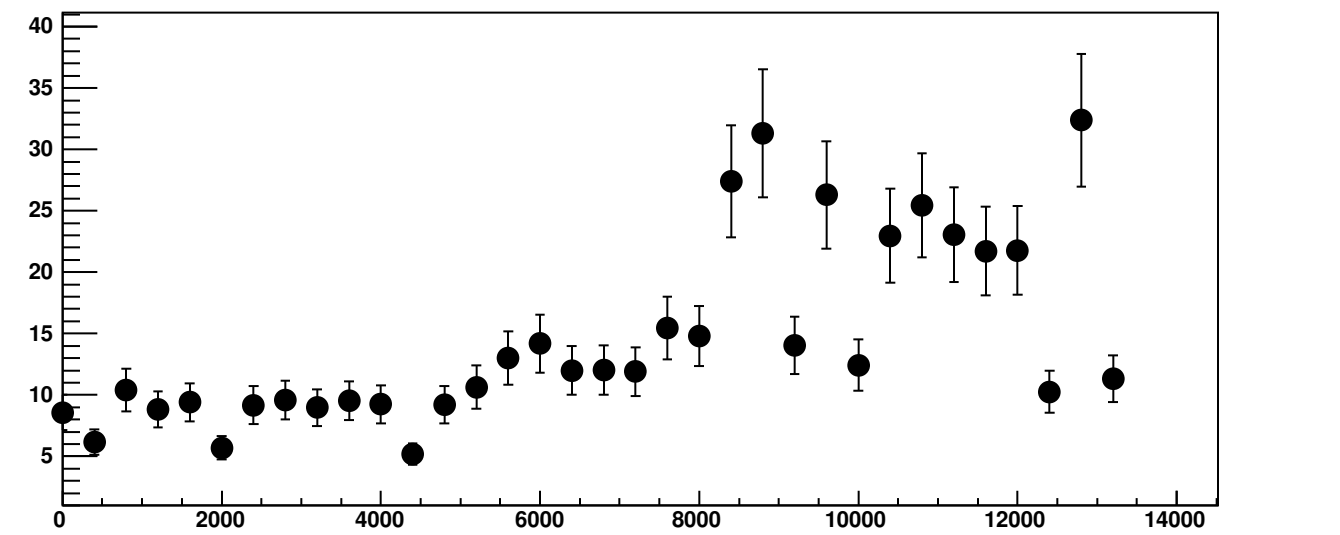

Chip 2, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

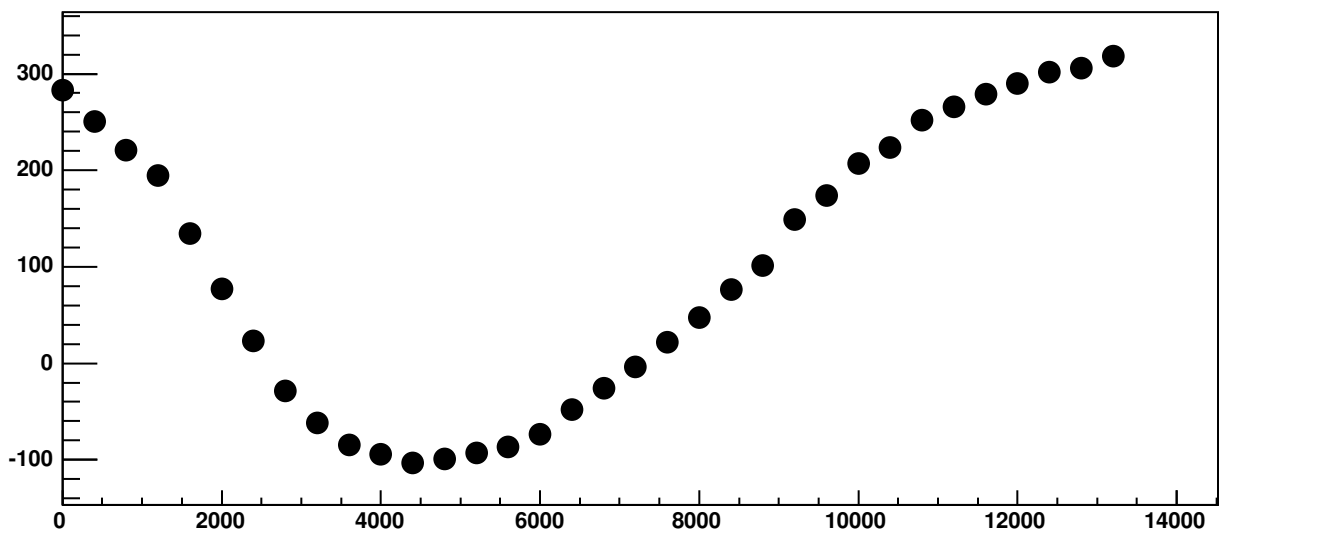
Chip 2, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

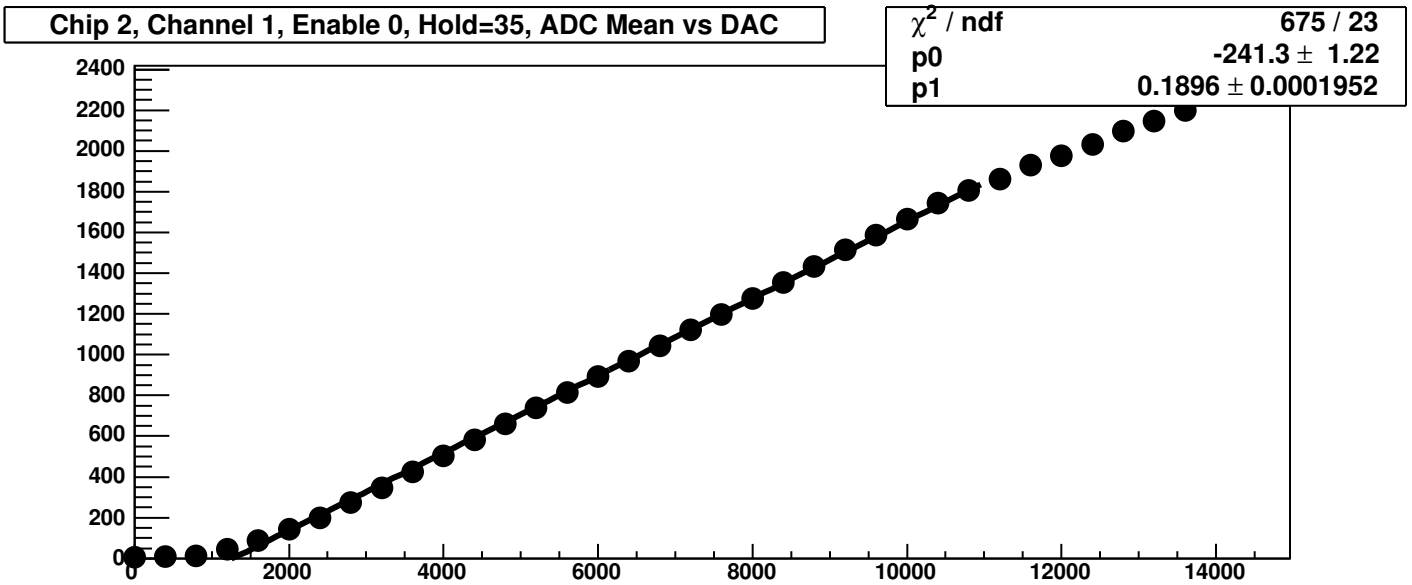


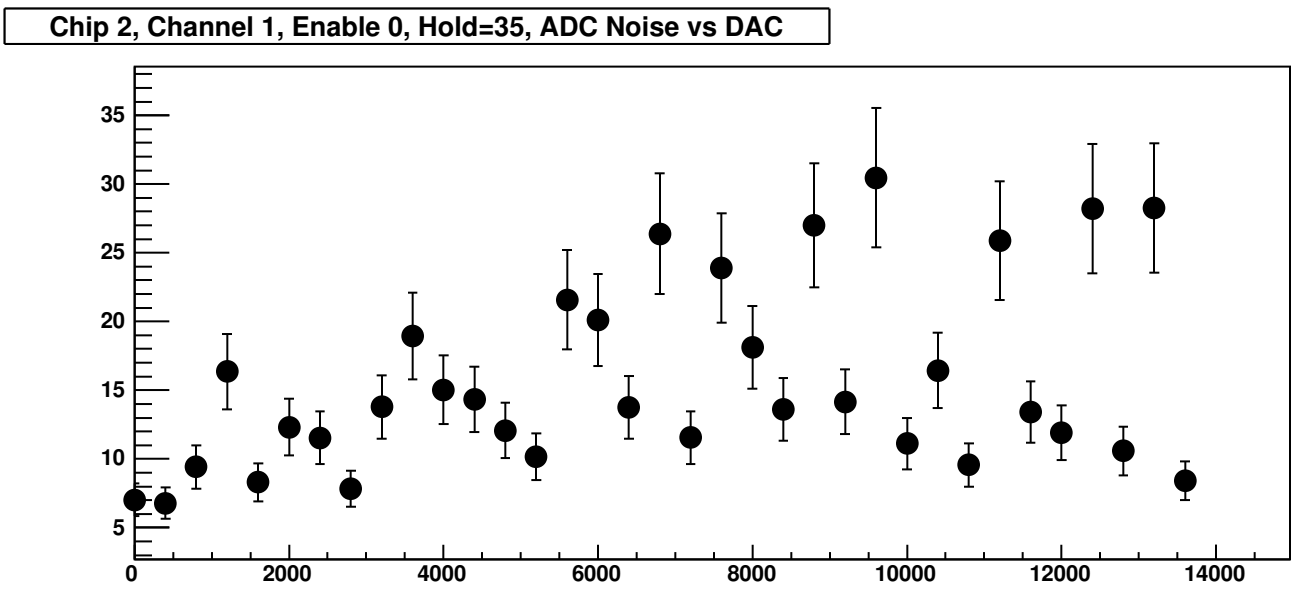
Chip 2, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC

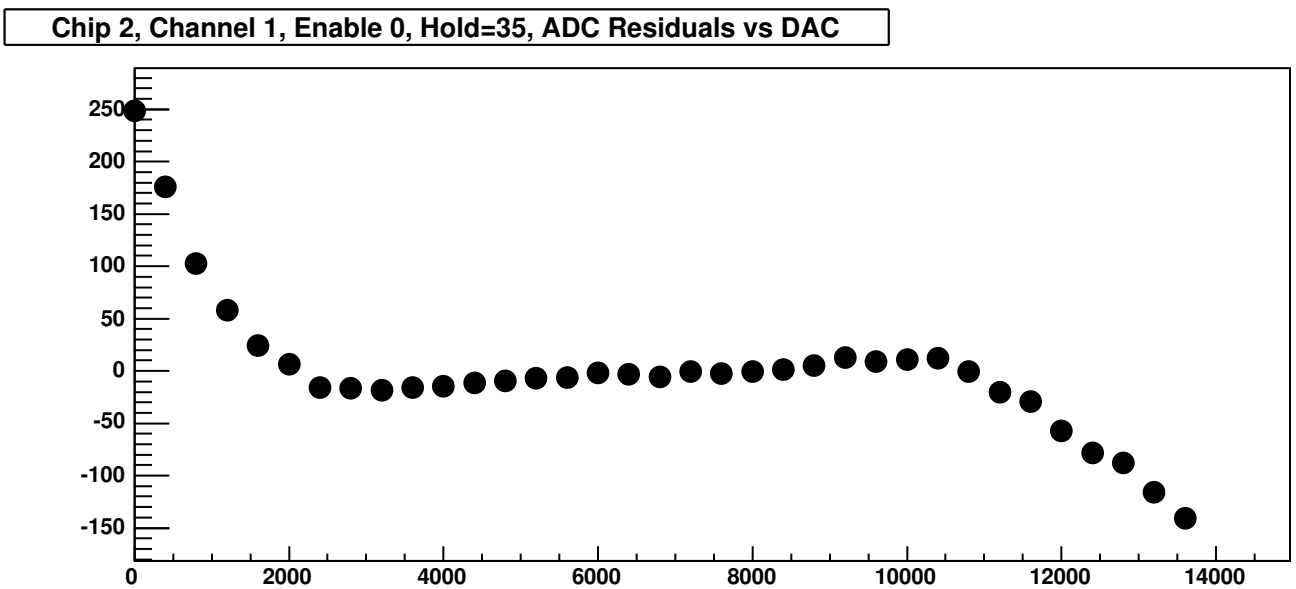


Chip 2, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC

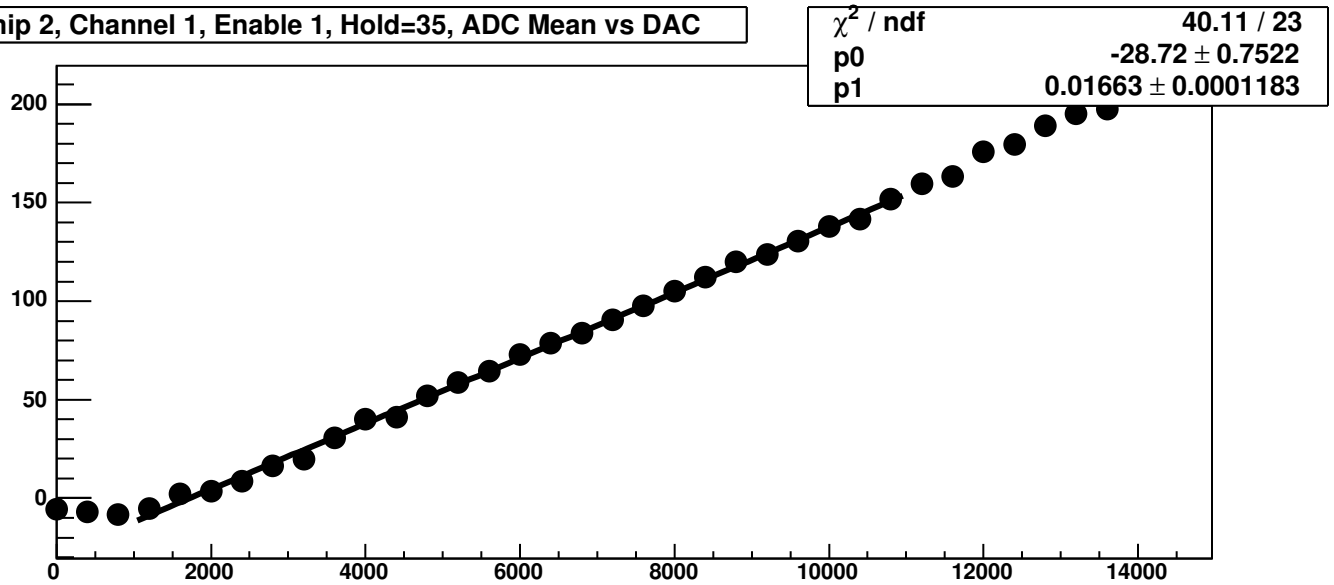



Chip 2, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 675 / 23
p0 -241.3 ± 1.22
p1 0.1896 ± 0.0001952
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

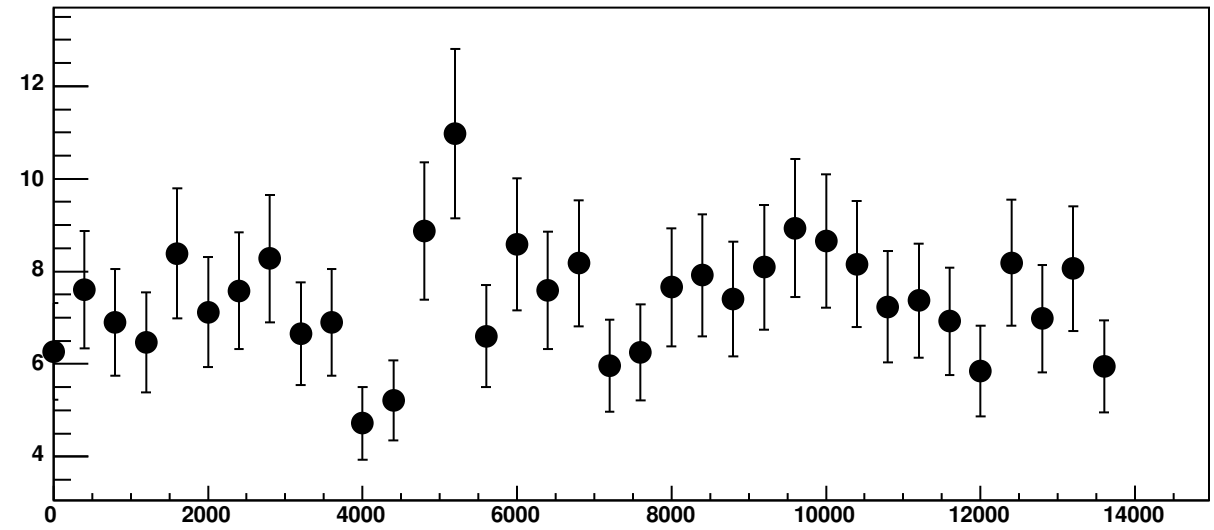

Chip 2, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

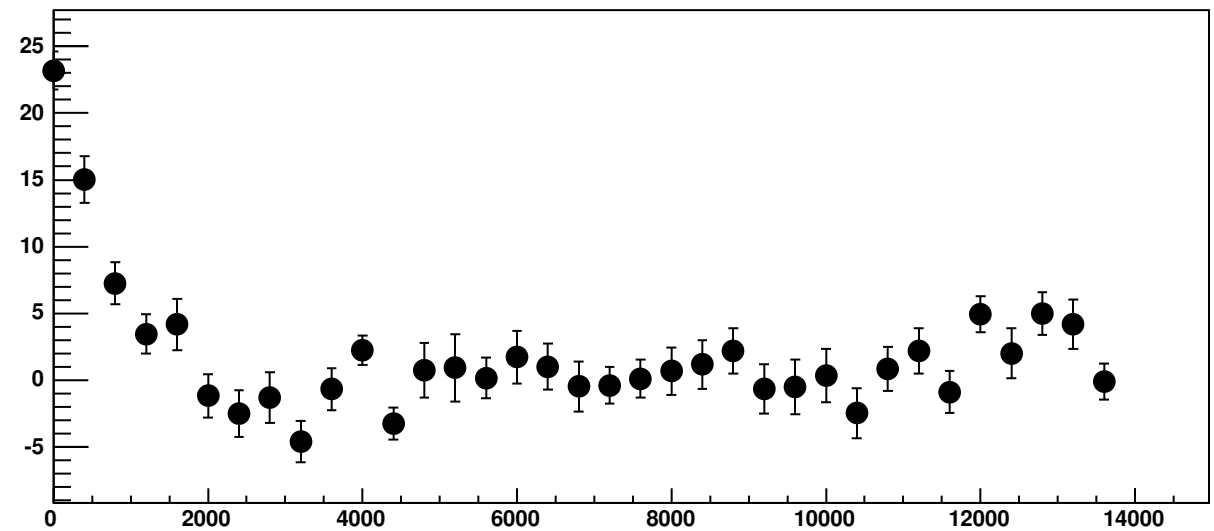
Chip 2, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

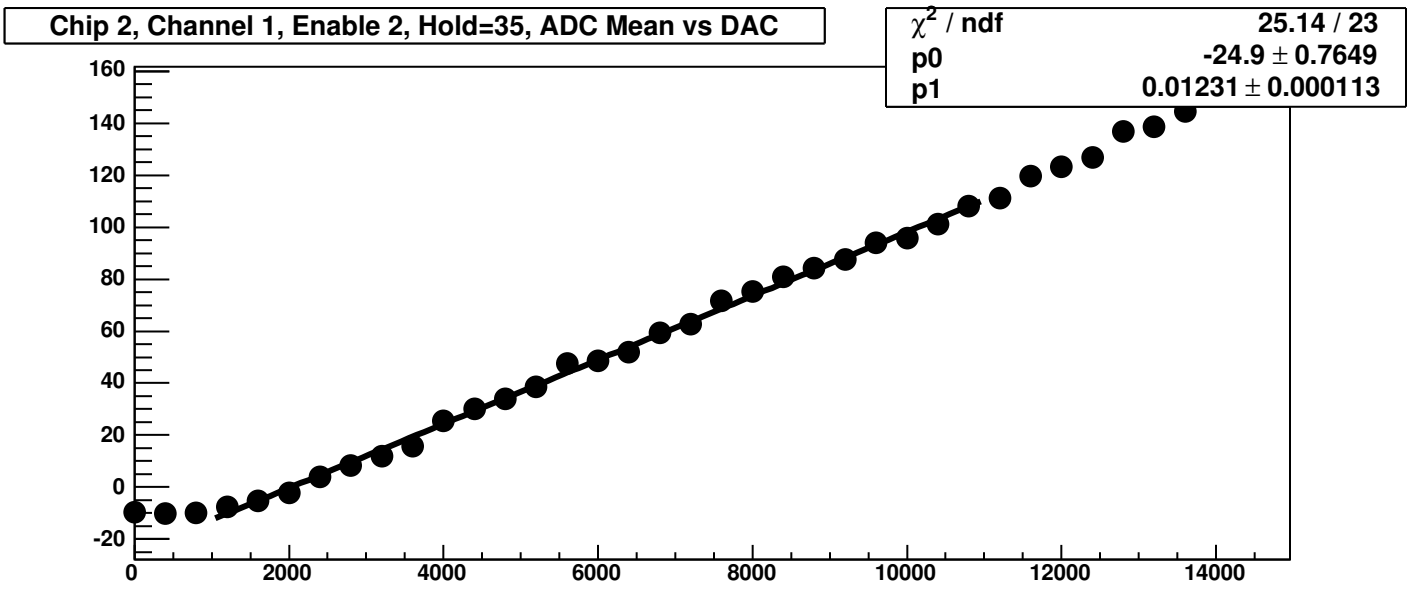


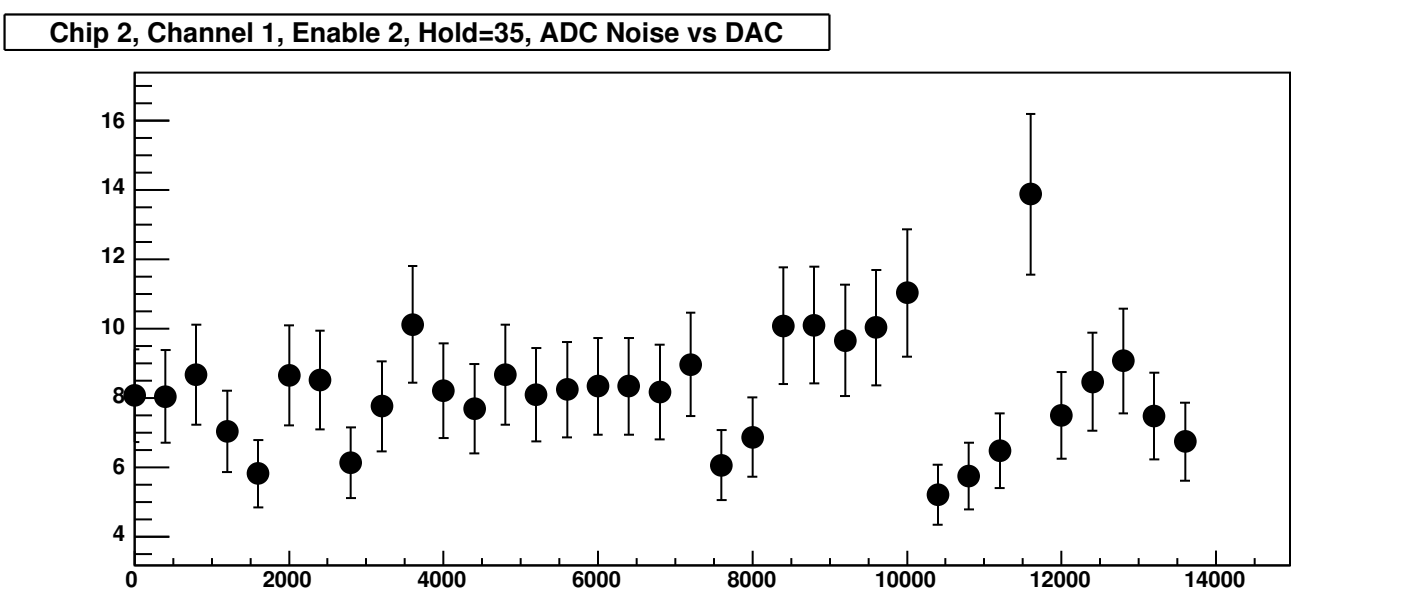
Chip 2, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC

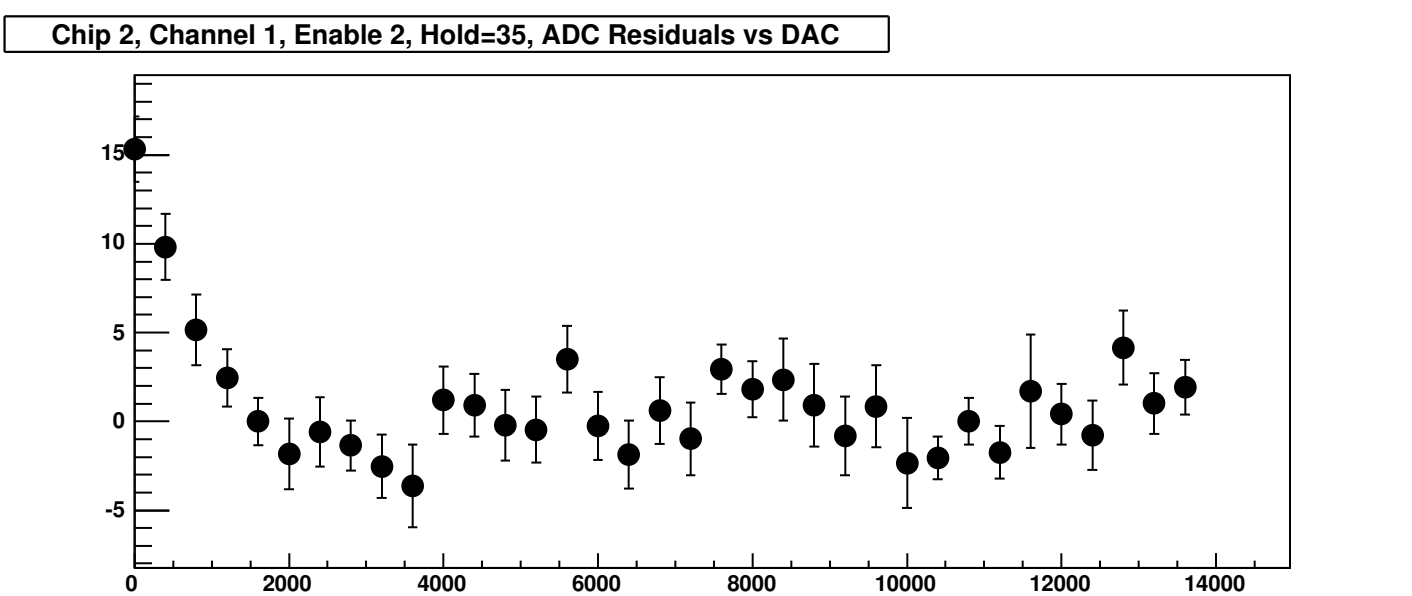


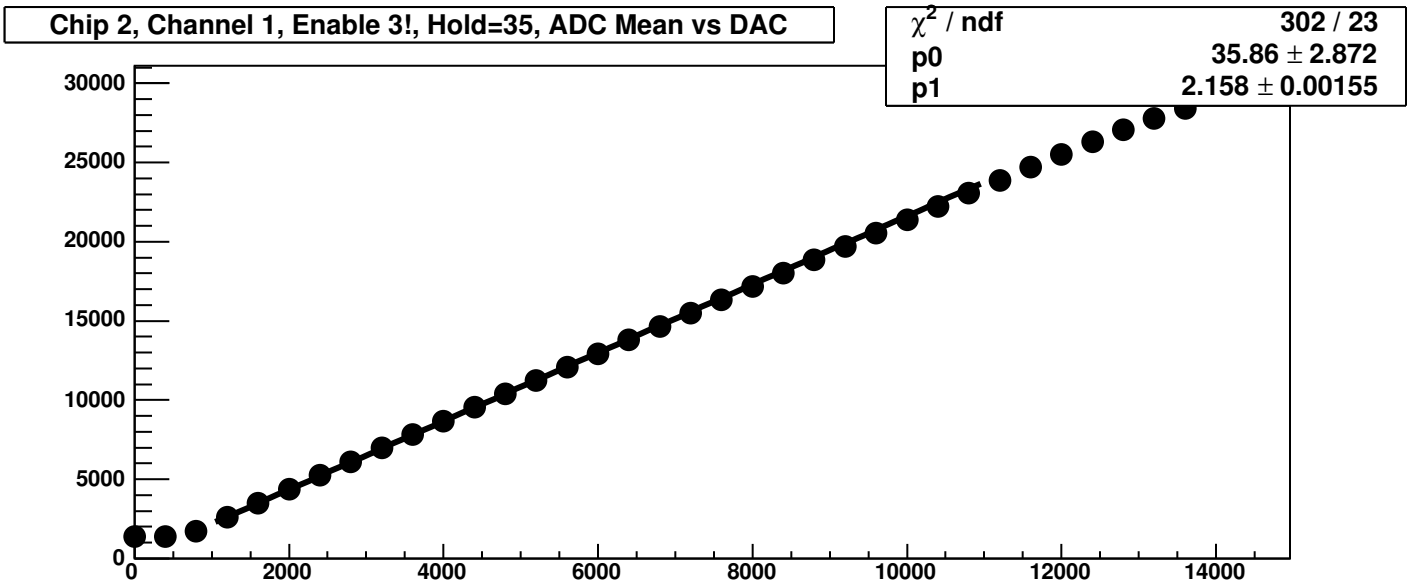
Chip 2, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

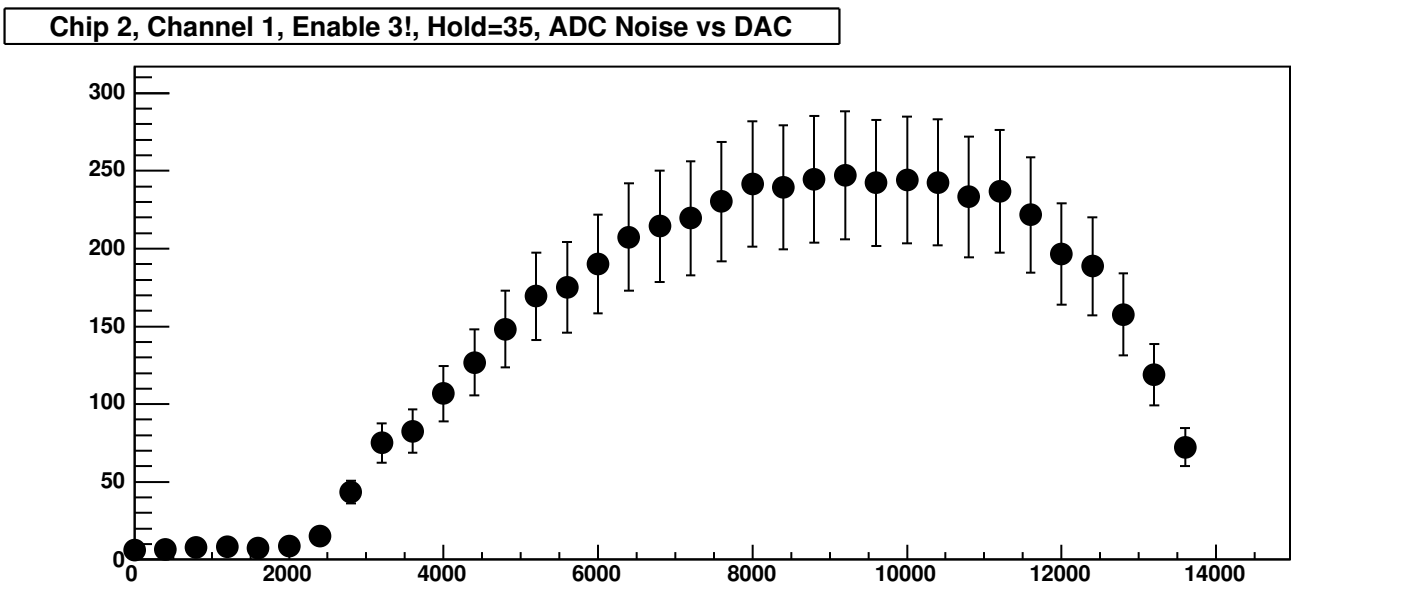


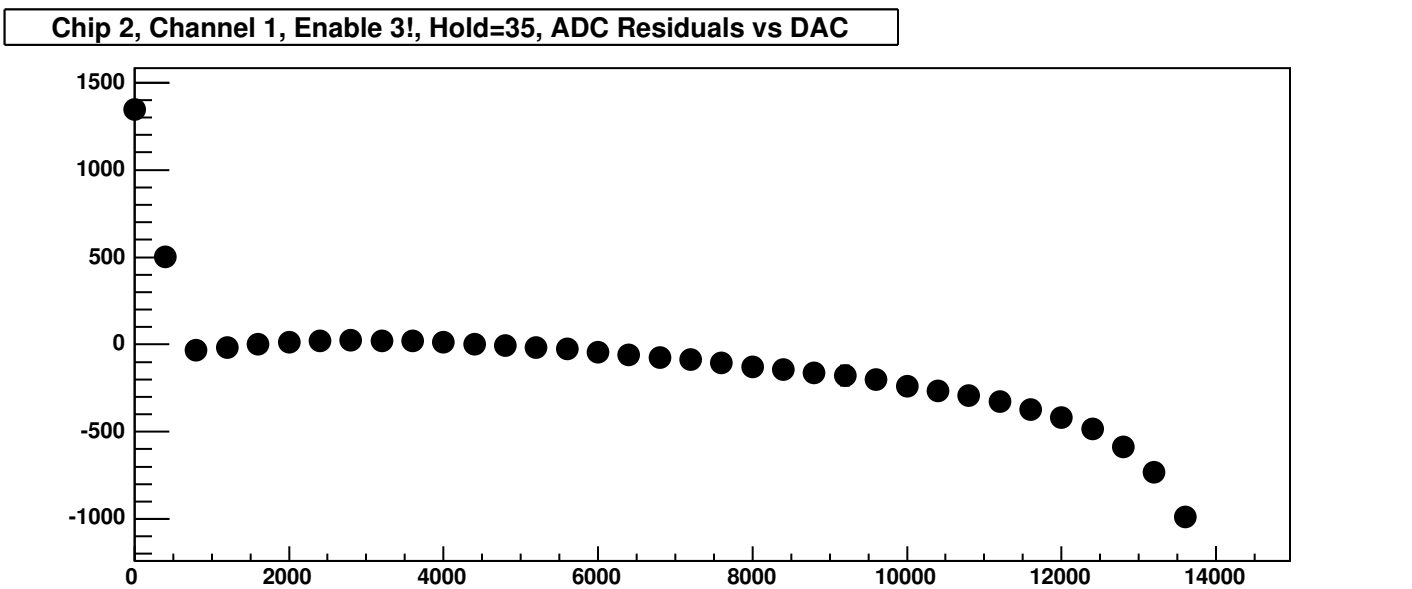

Chip 2, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 25.14 / 23
p0 -24.9 ± 0.7649
p1 0.01231 ± 0.000113
160
140
120
100
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000

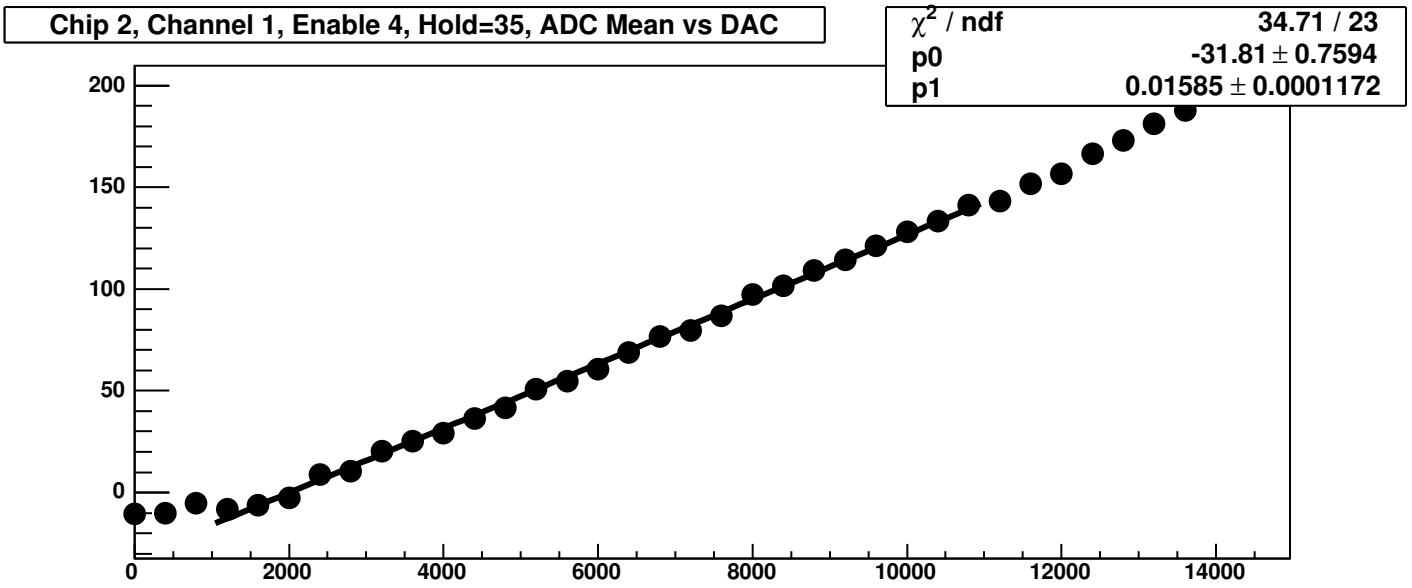

Chip 2, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

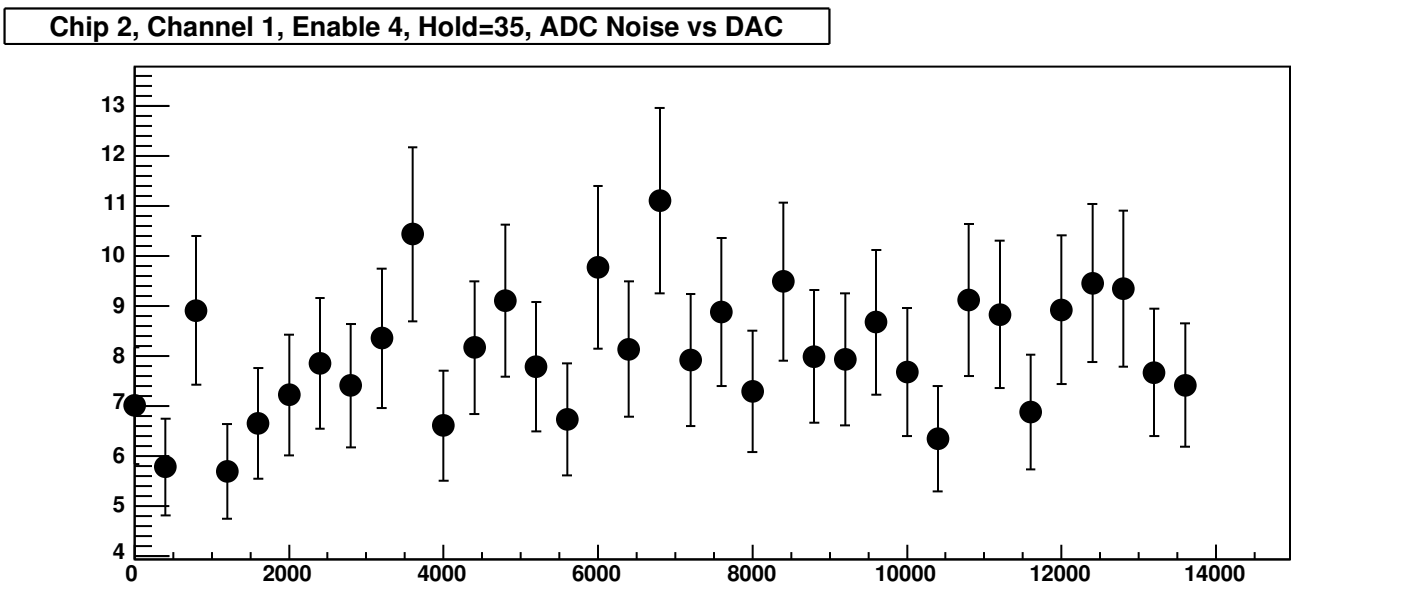

Chip 2, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

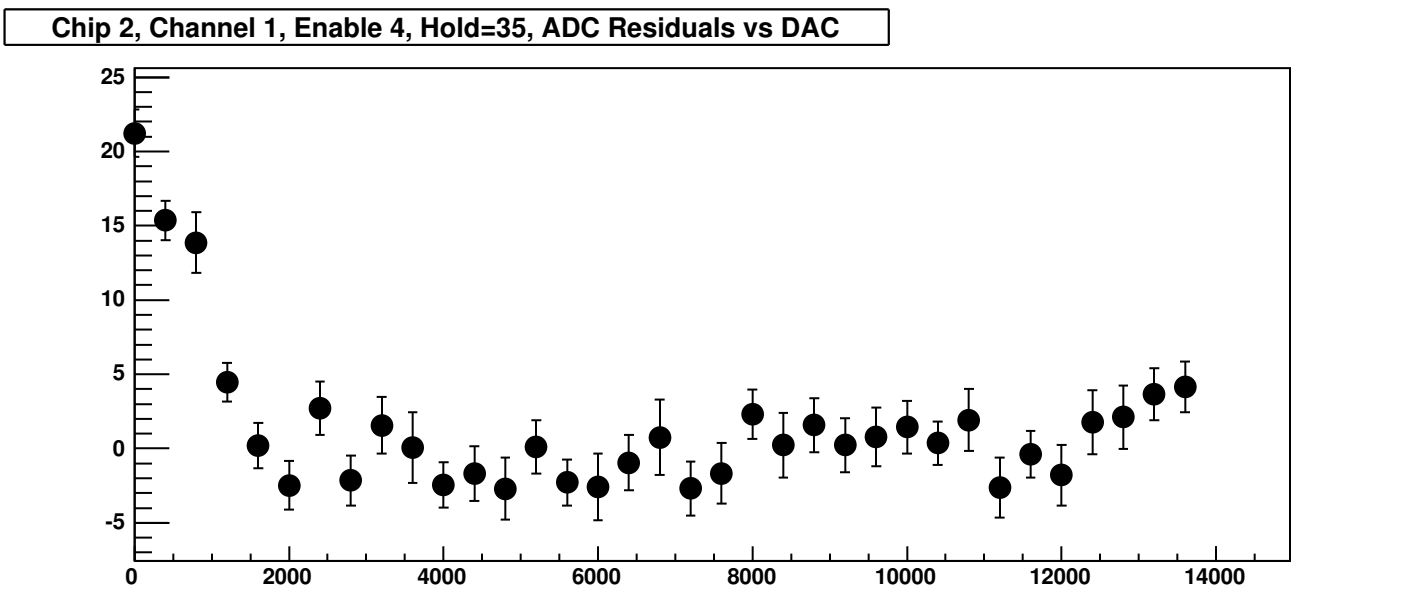

Chip 2, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 302 / 23
p0 35.86 ± 2.872
p1 2.158 ± 0.00155
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

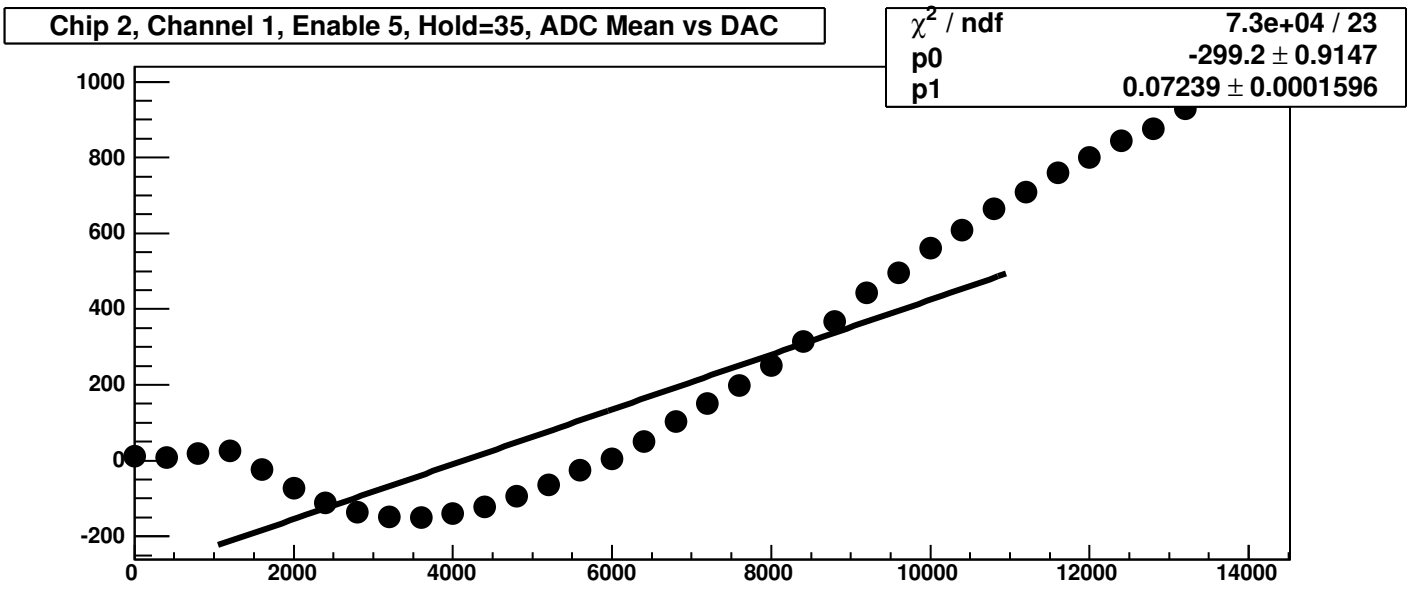

Chip 2, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

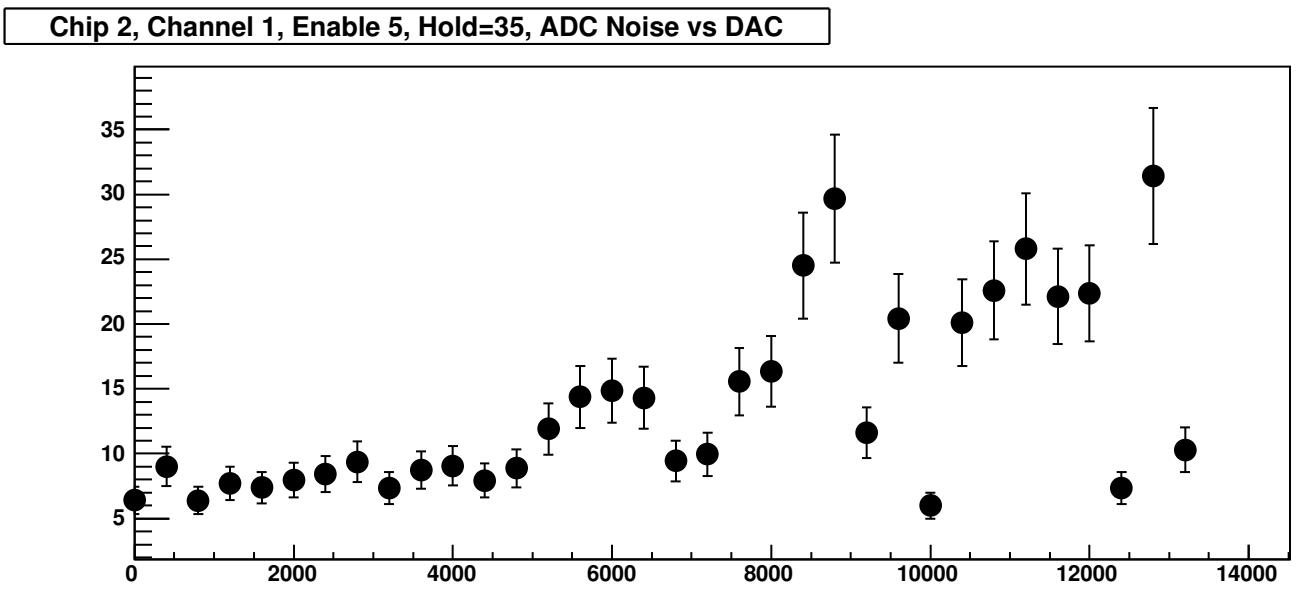

Chip 2, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

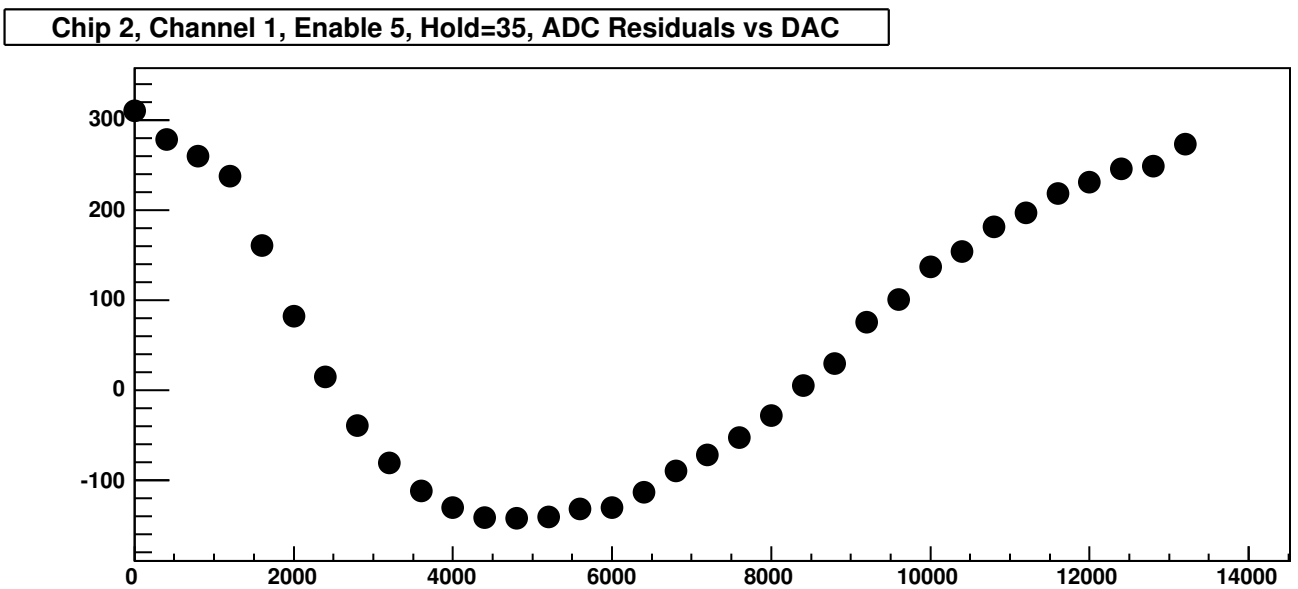

Chip 2, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 34.71 / 23
p0 -31.81 ± 0.7594
p1 0.01585 ± 0.0001172
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

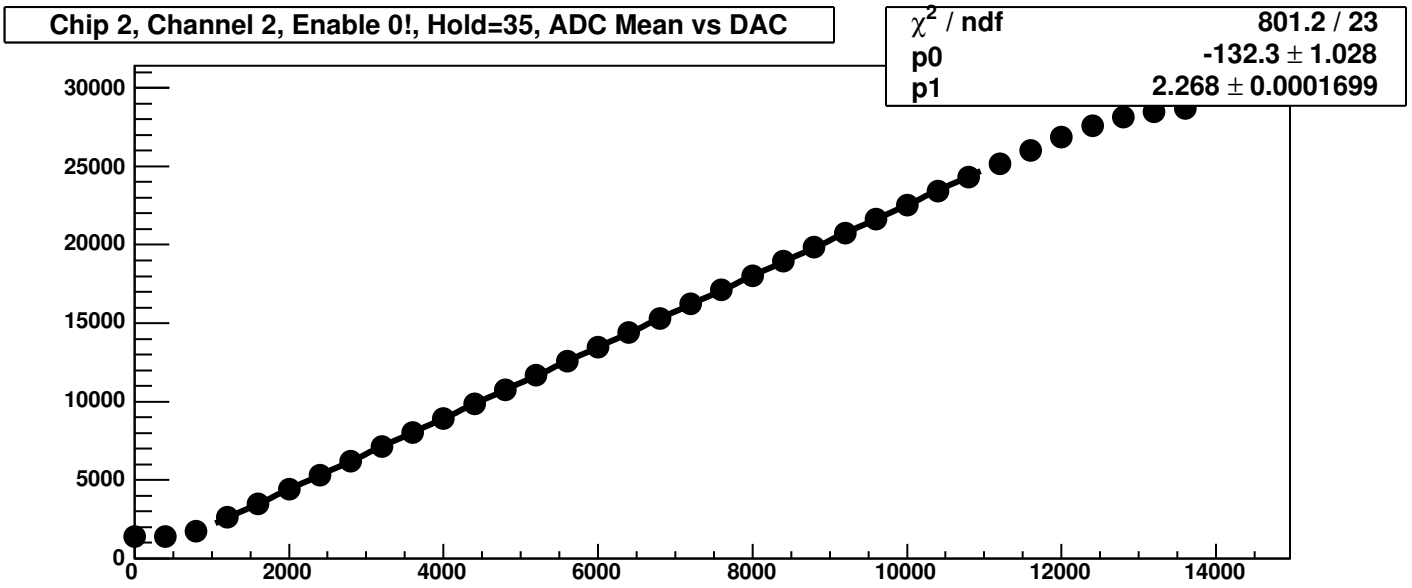

Chip 2, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

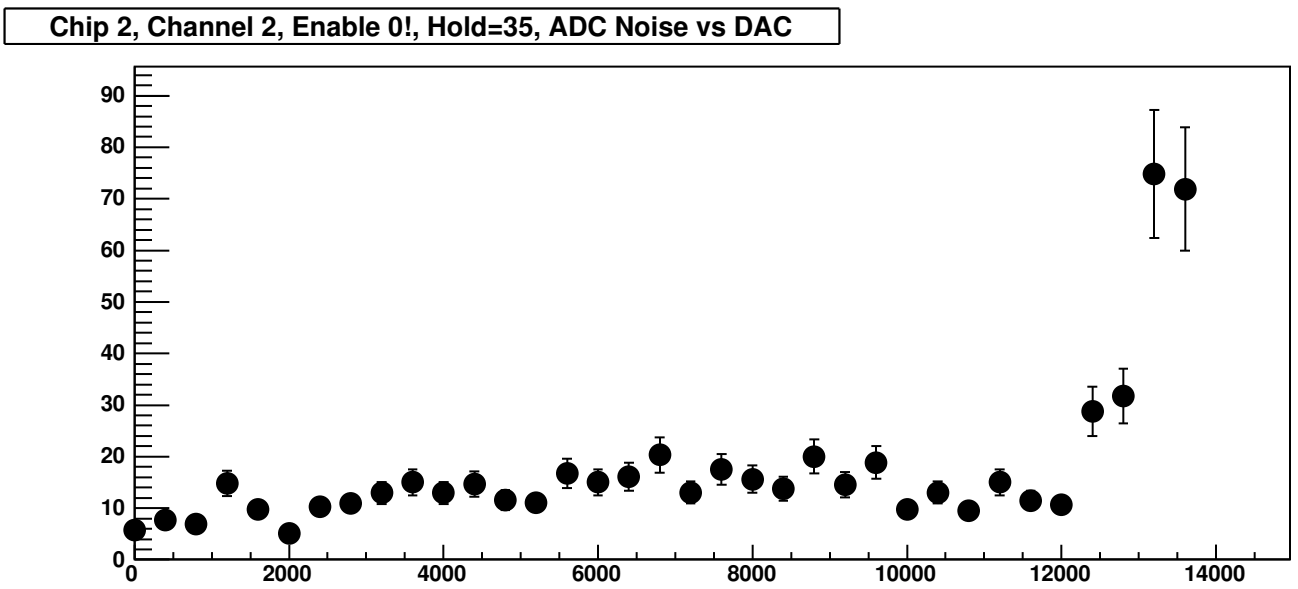

Chip 2, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

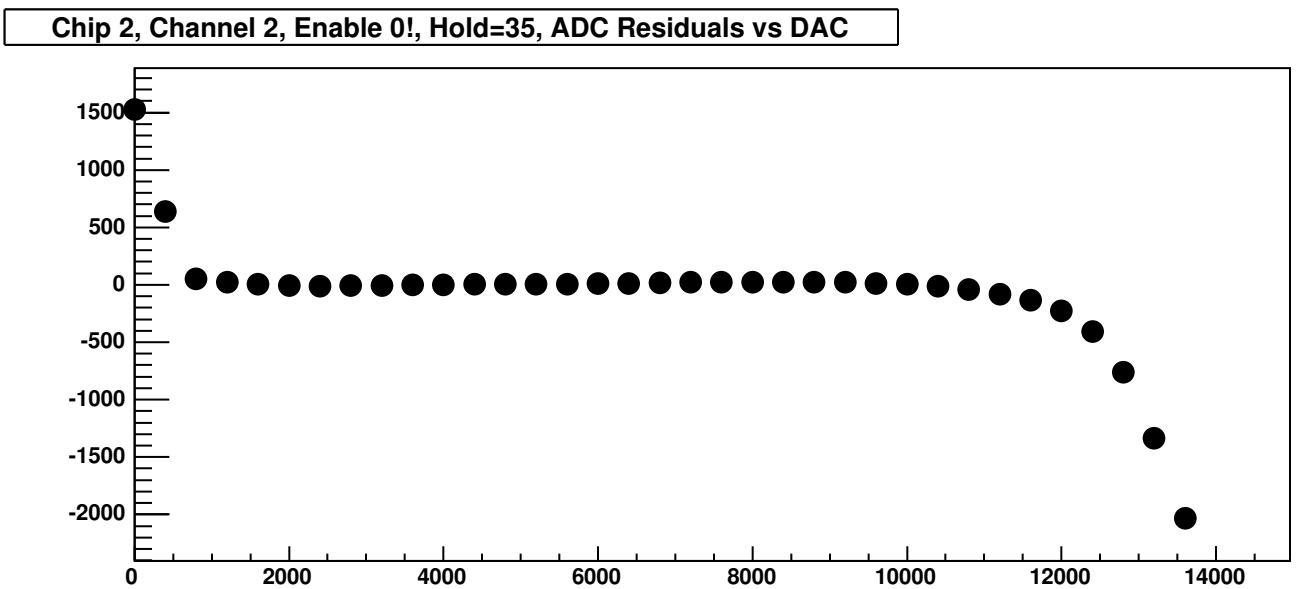

Chip 2, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 7.3e+04 / 23
p0 -299.2 ± 0.9147
p1 0.07239 ± 0.0001596
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

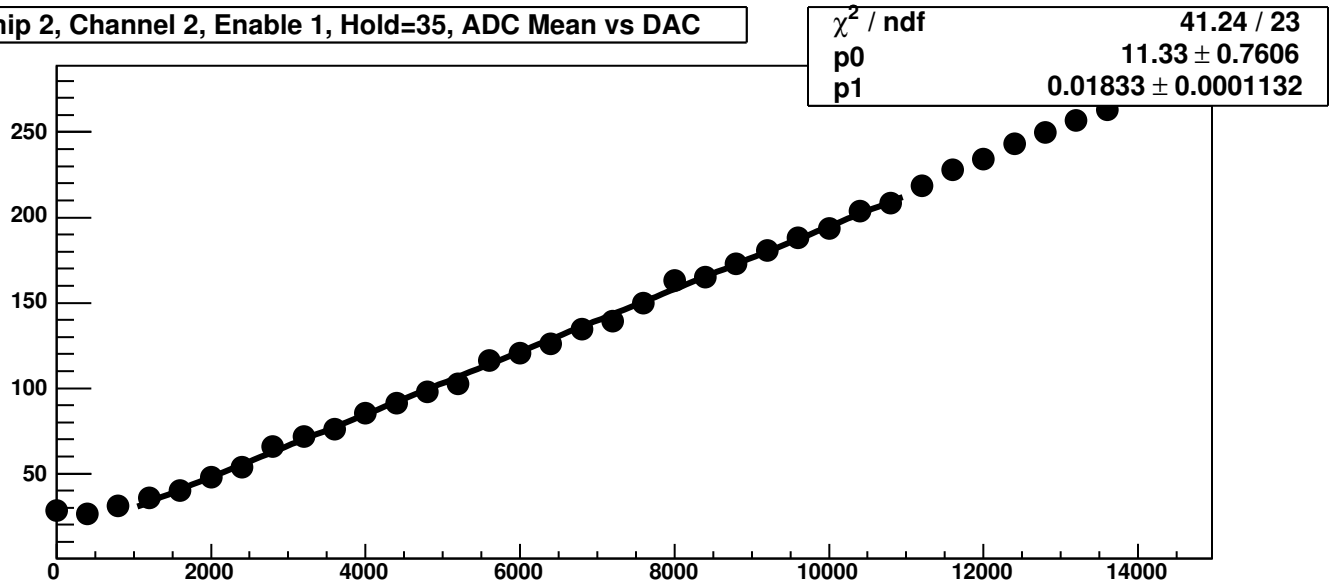

Chip 2, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 801.2 / 23
p0 -132.3 ± 1.028
p1 2.268 ± 0.0001699
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

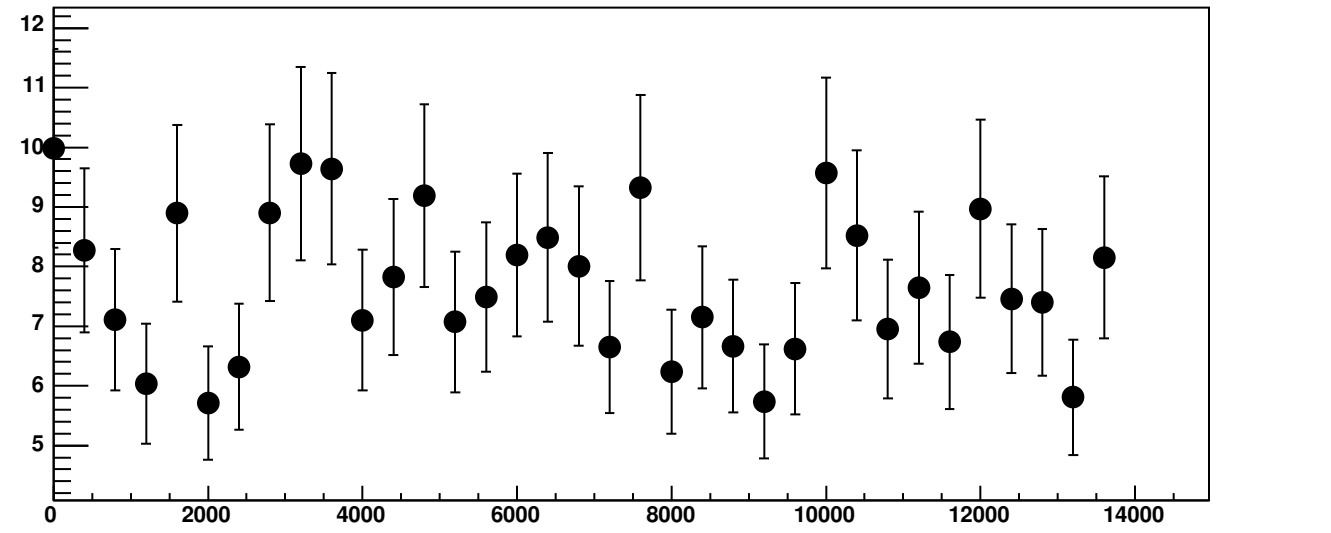

Chip 2, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

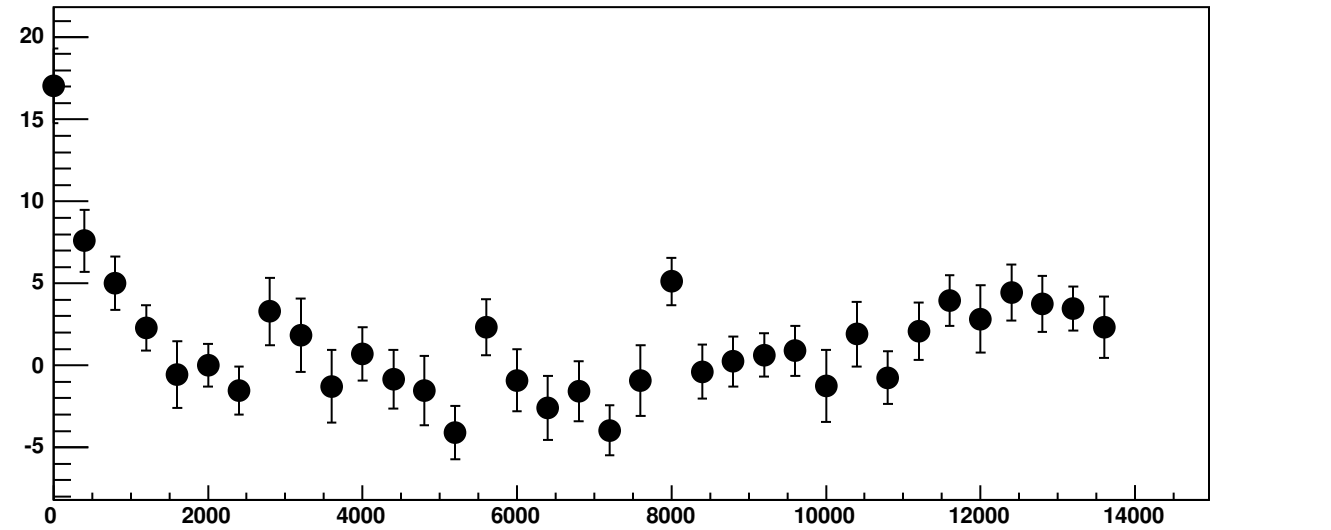
Chip 2, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



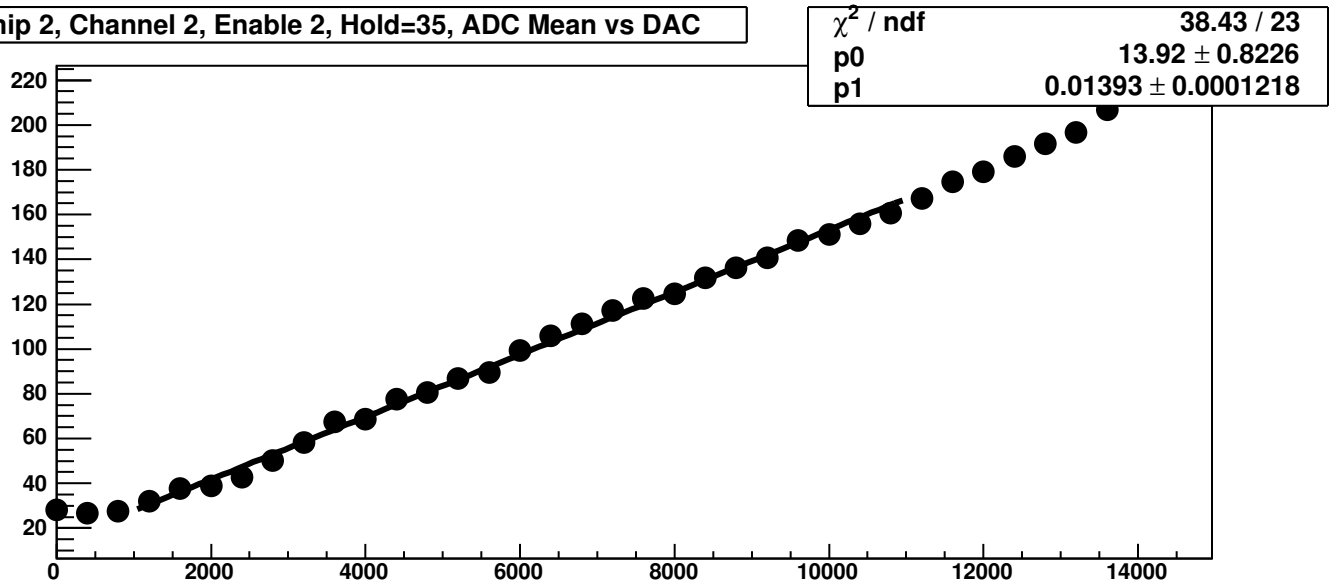
Chip 2, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



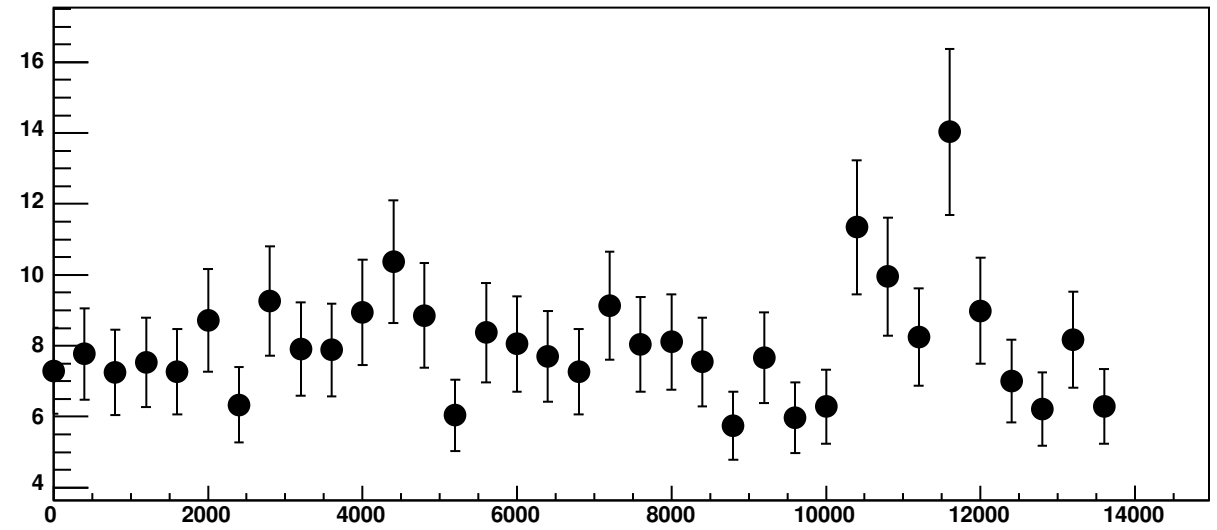
Chip 2, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



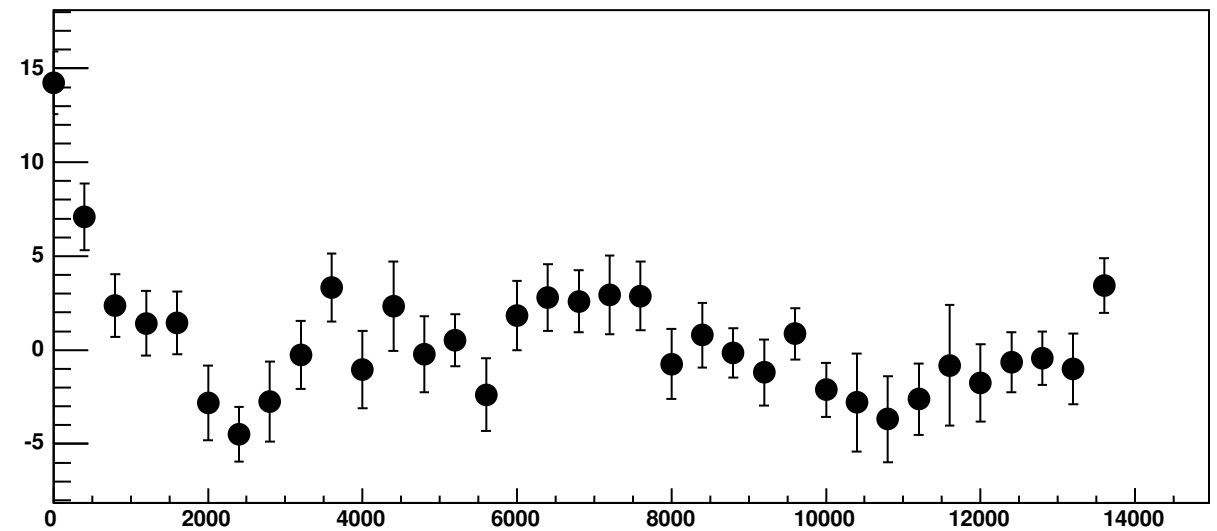
Chip 2, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



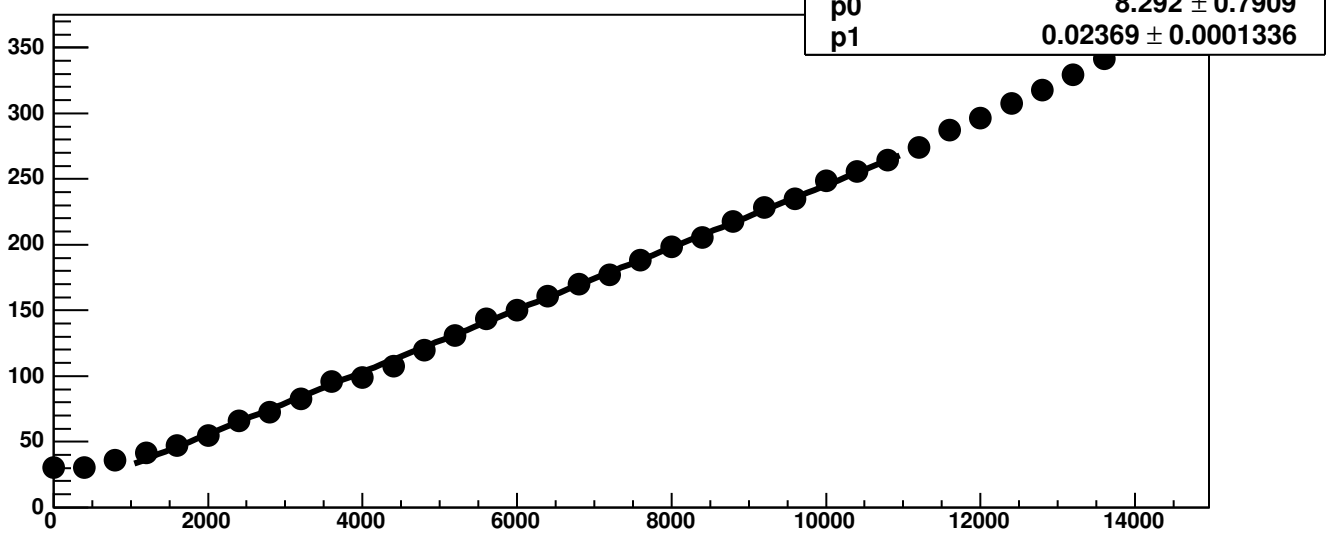
Chip 2, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



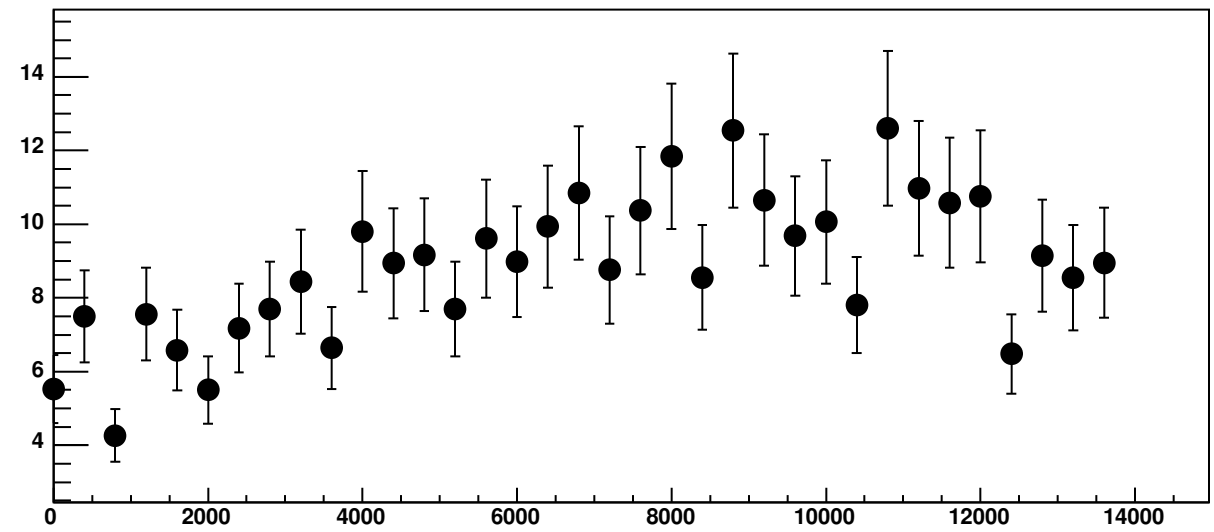
Chip 2, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



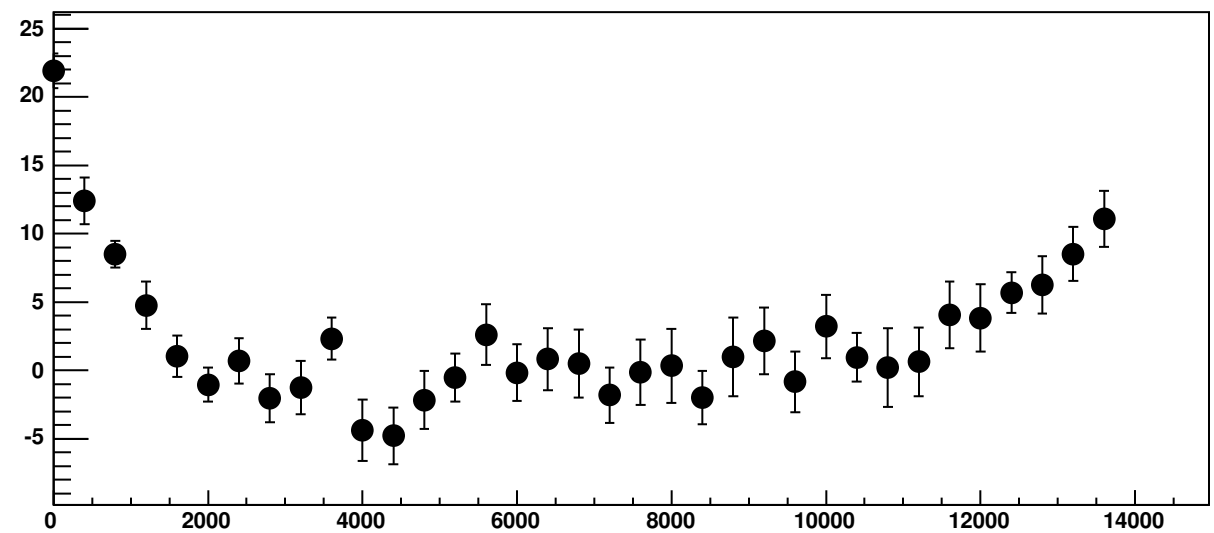
Chip 2, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC



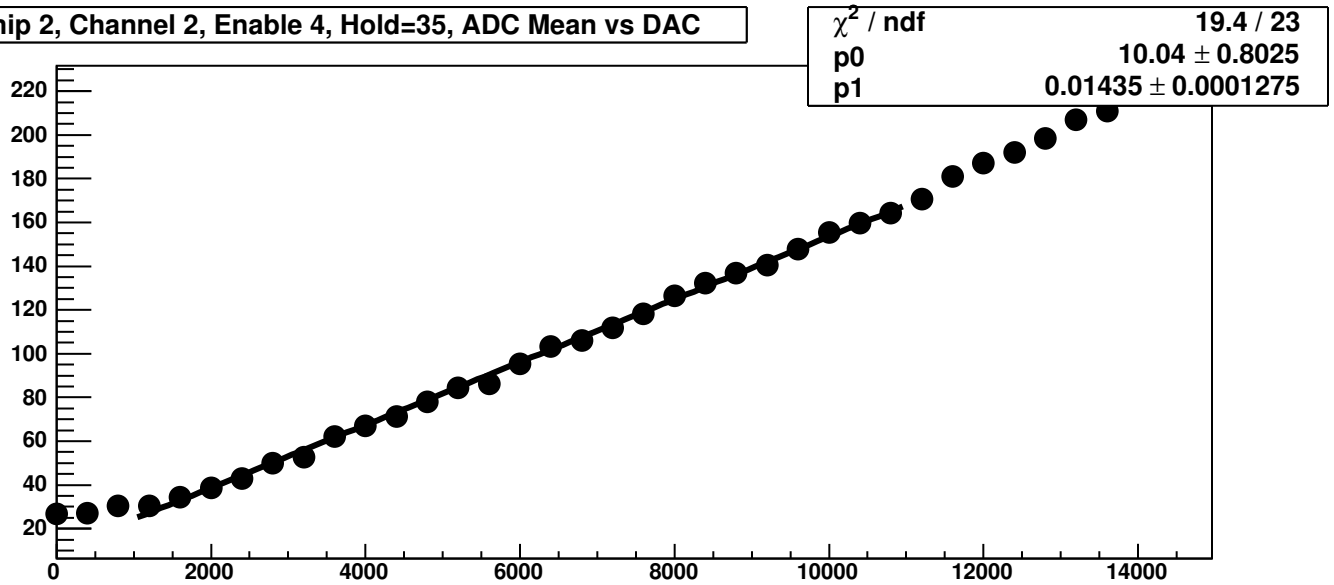
Chip 2, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



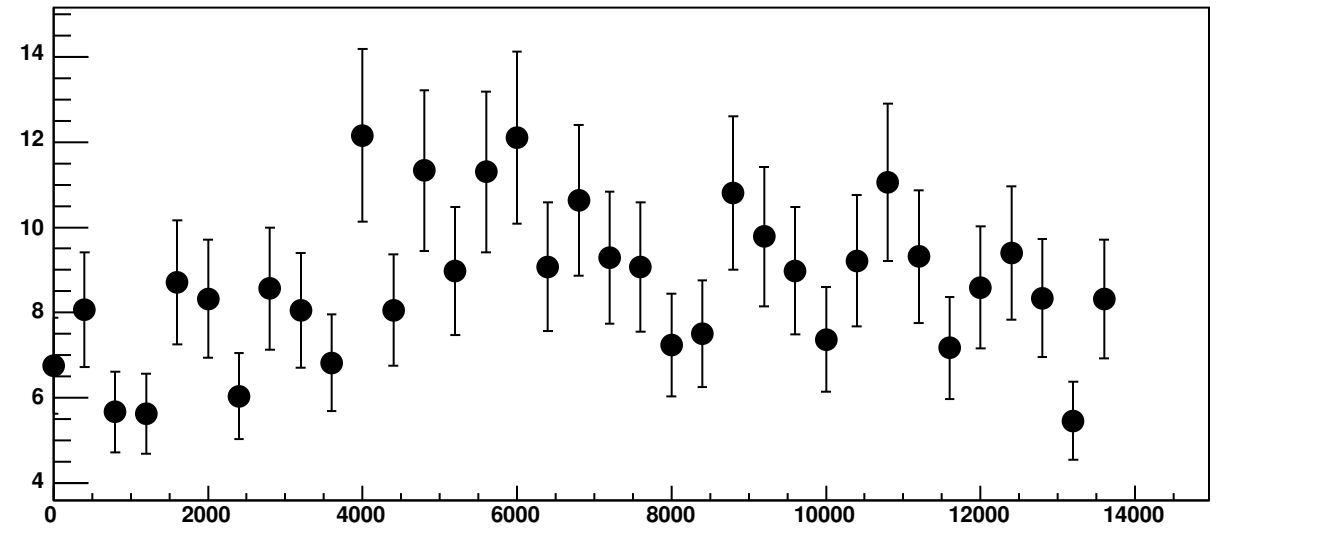
Chip 2, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC



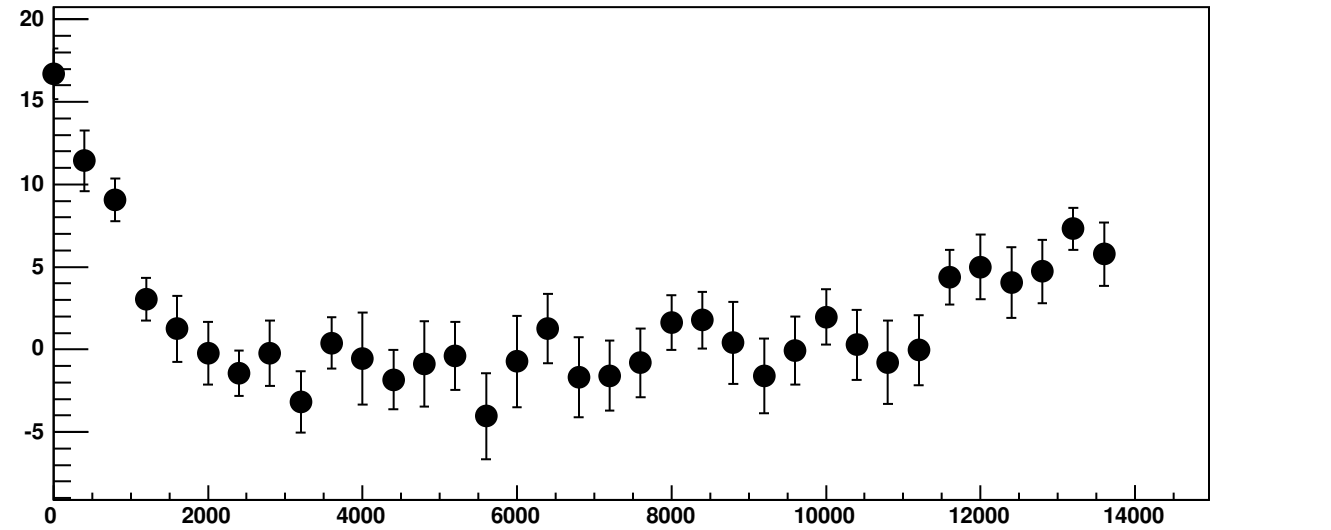
Chip 2, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

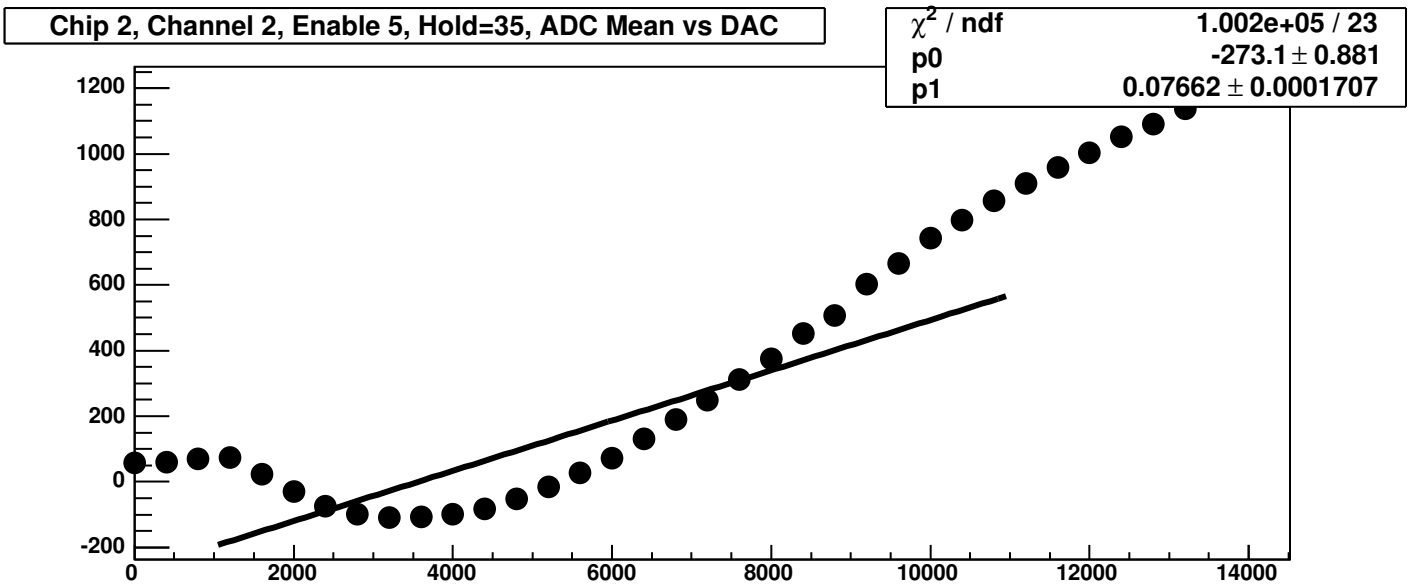


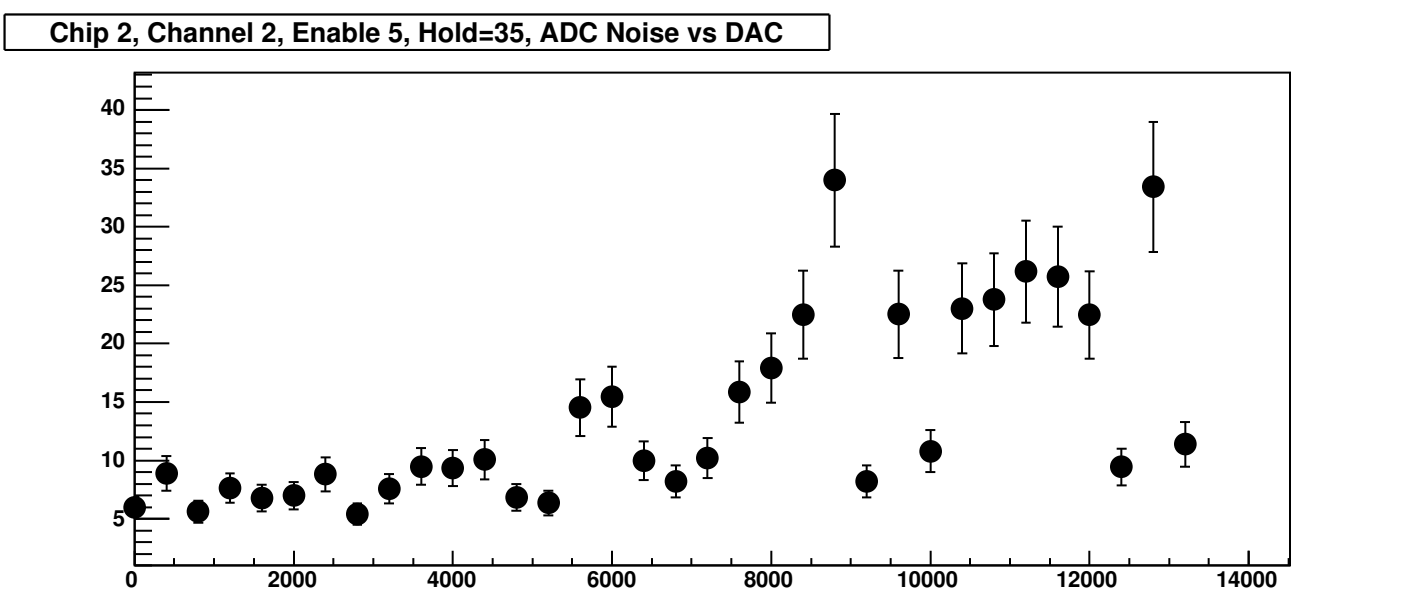
Chip 2, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

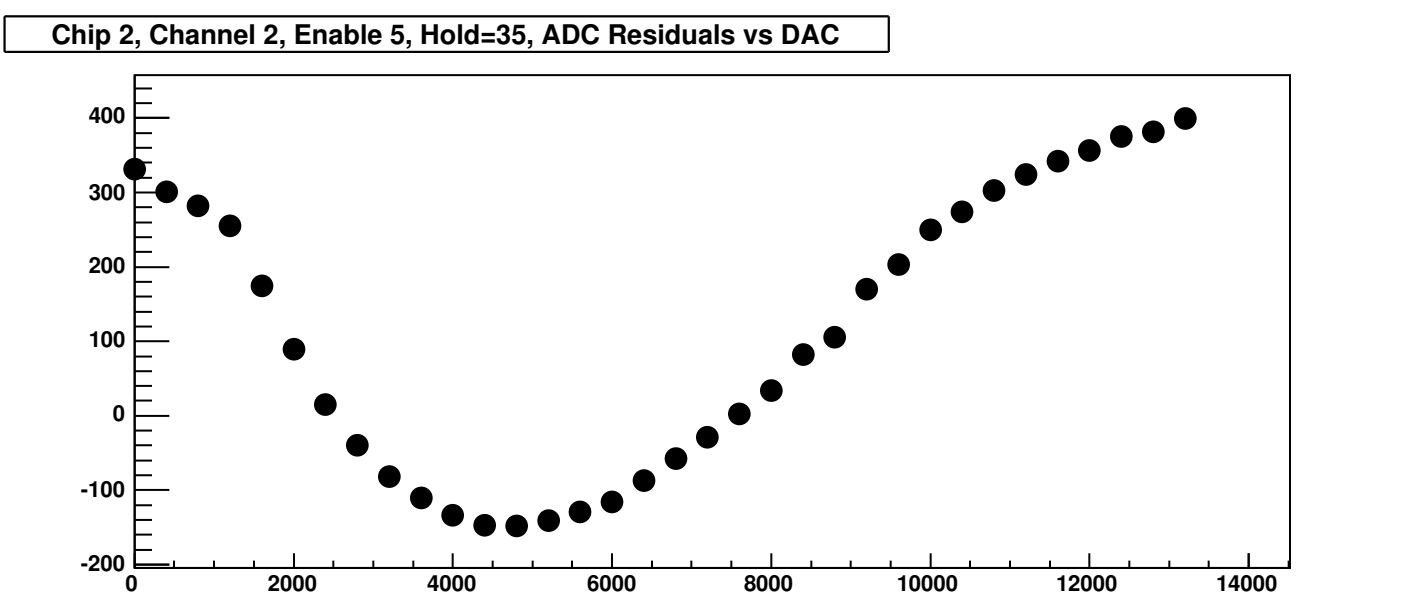


Chip 2, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

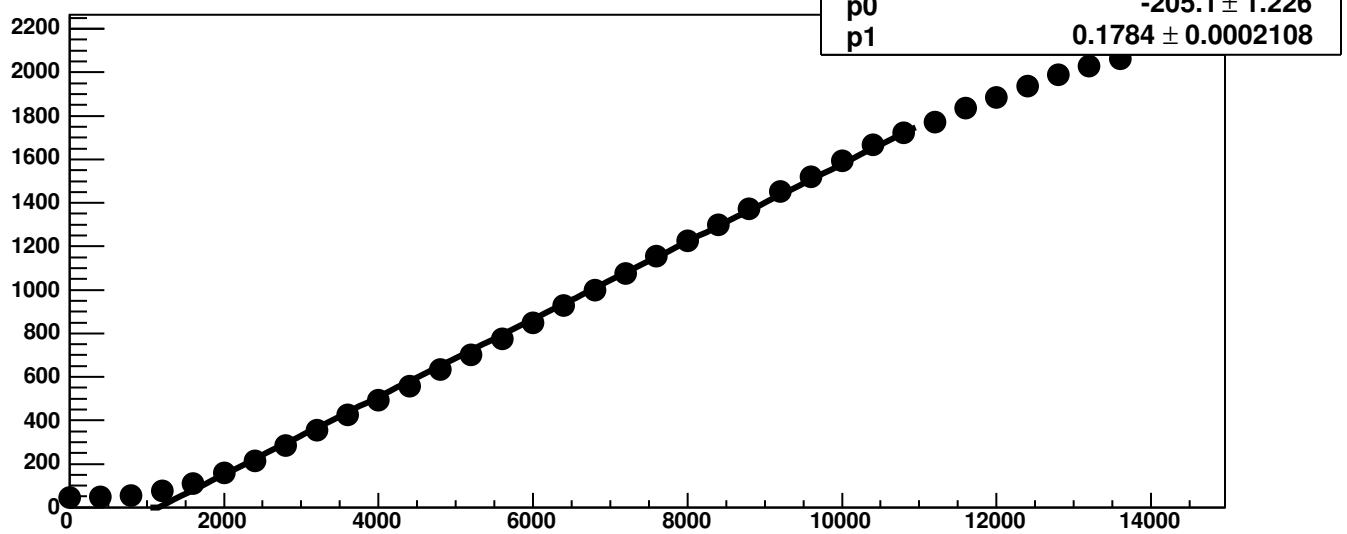



Chip 2, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.002e+05 / 23
p0 -273.1 ± 0.881
p1 0.07662 ± 0.0001707
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

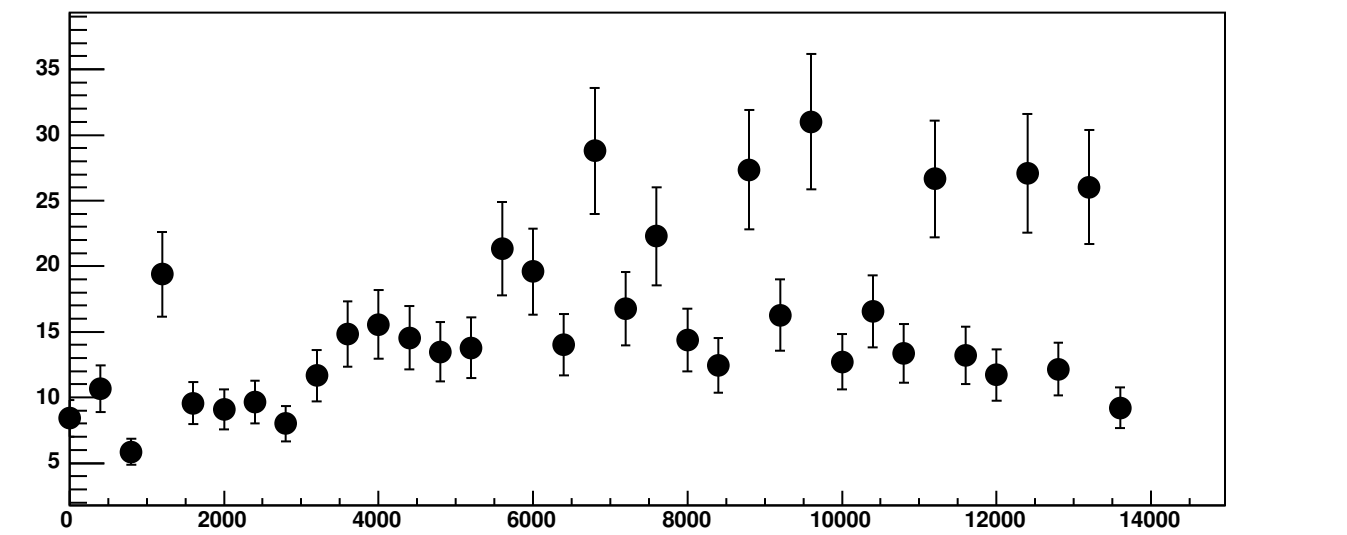

Chip 2, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

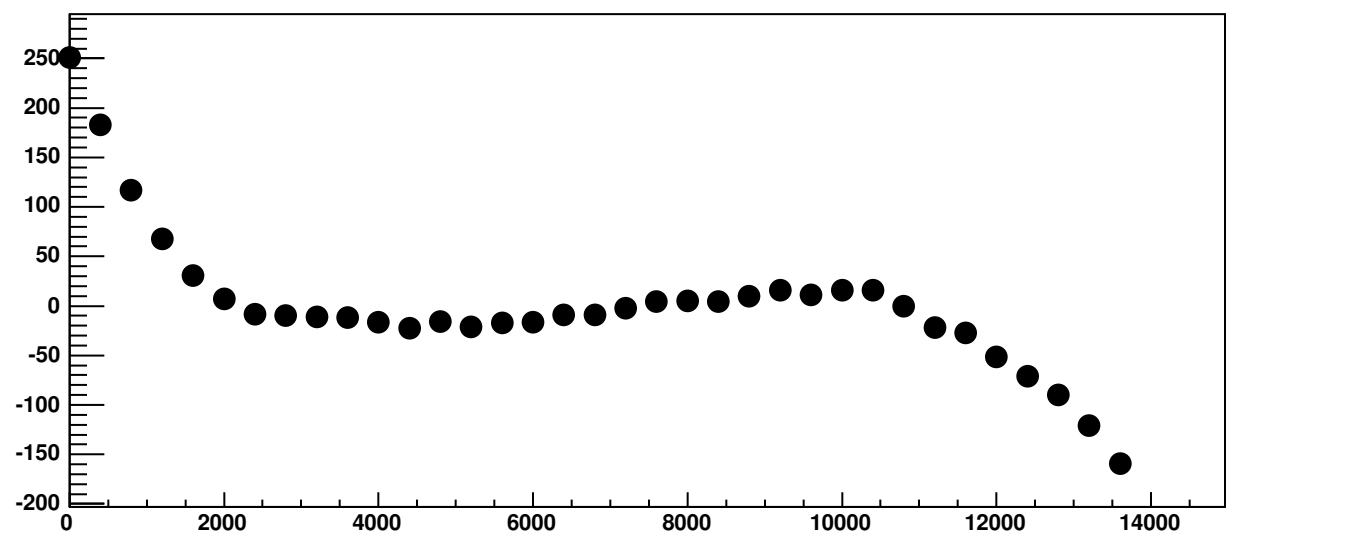
Chip 2, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

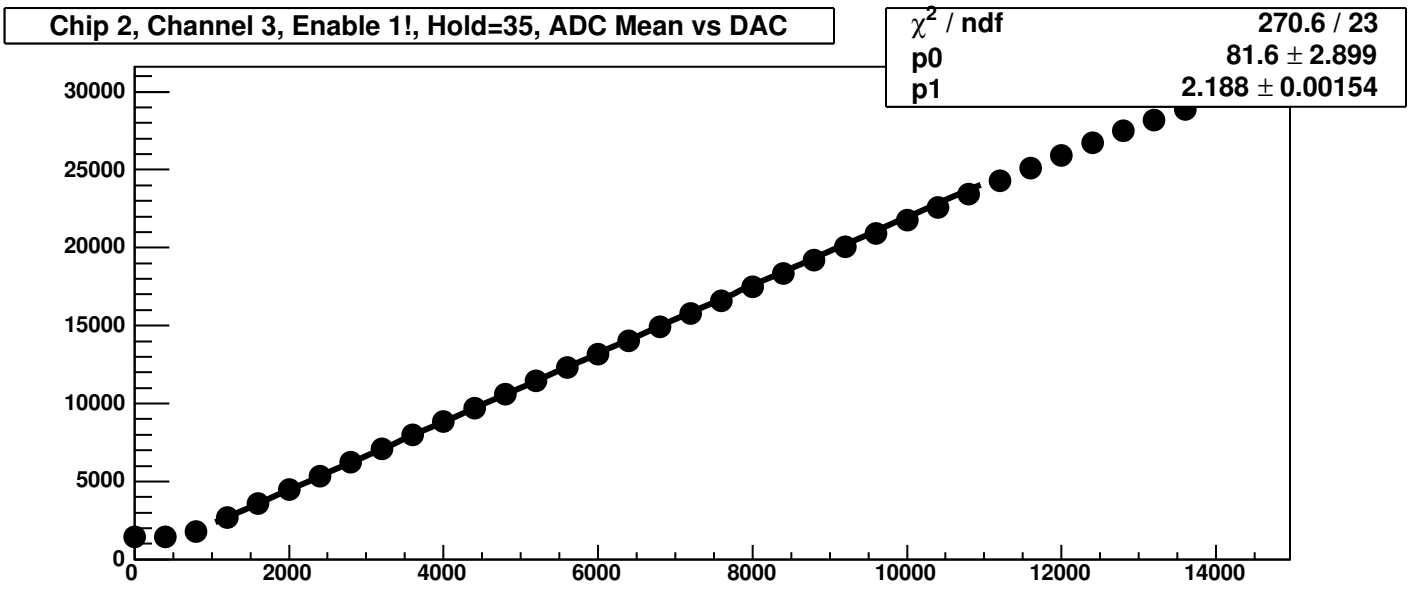


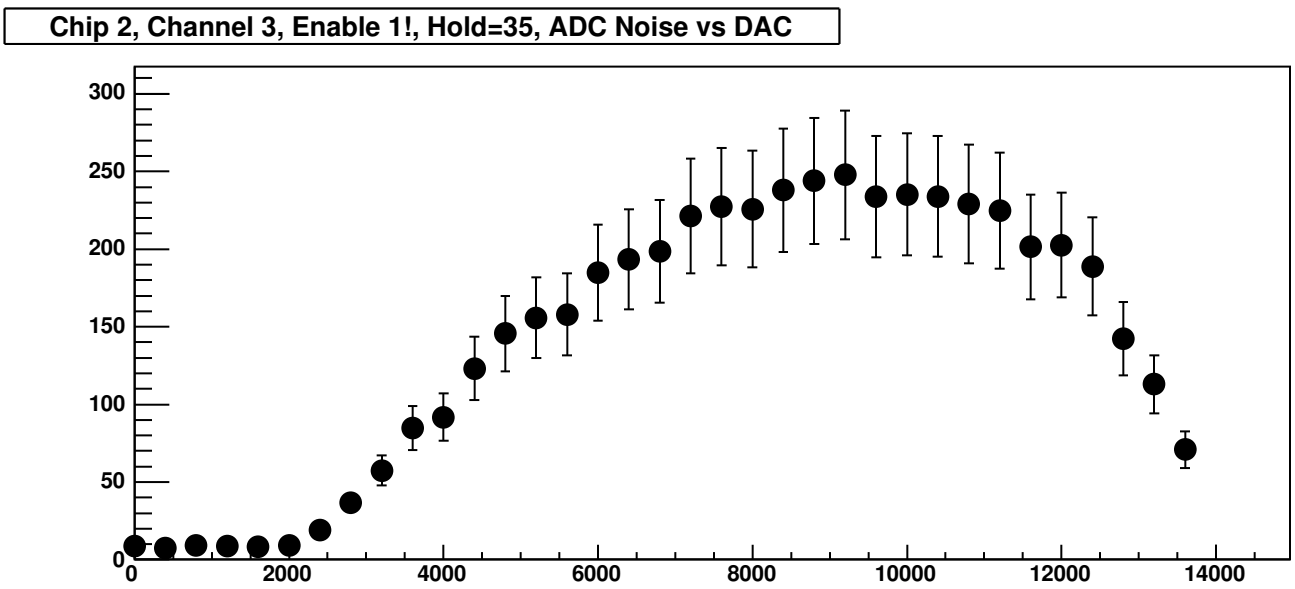
Chip 2, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

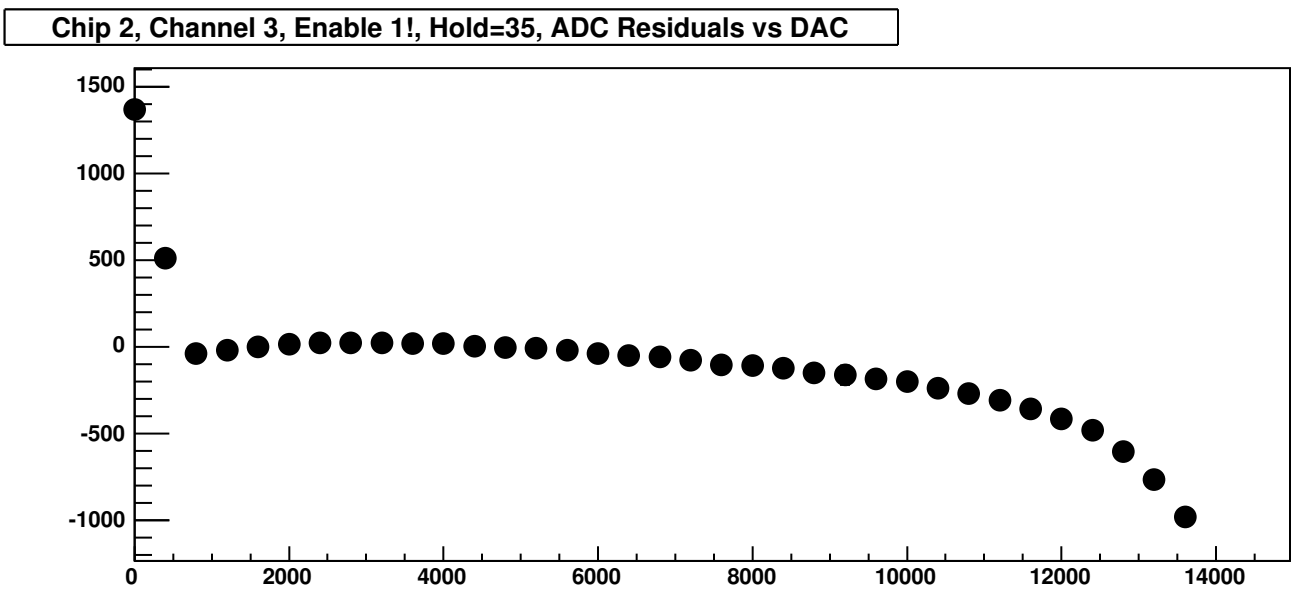


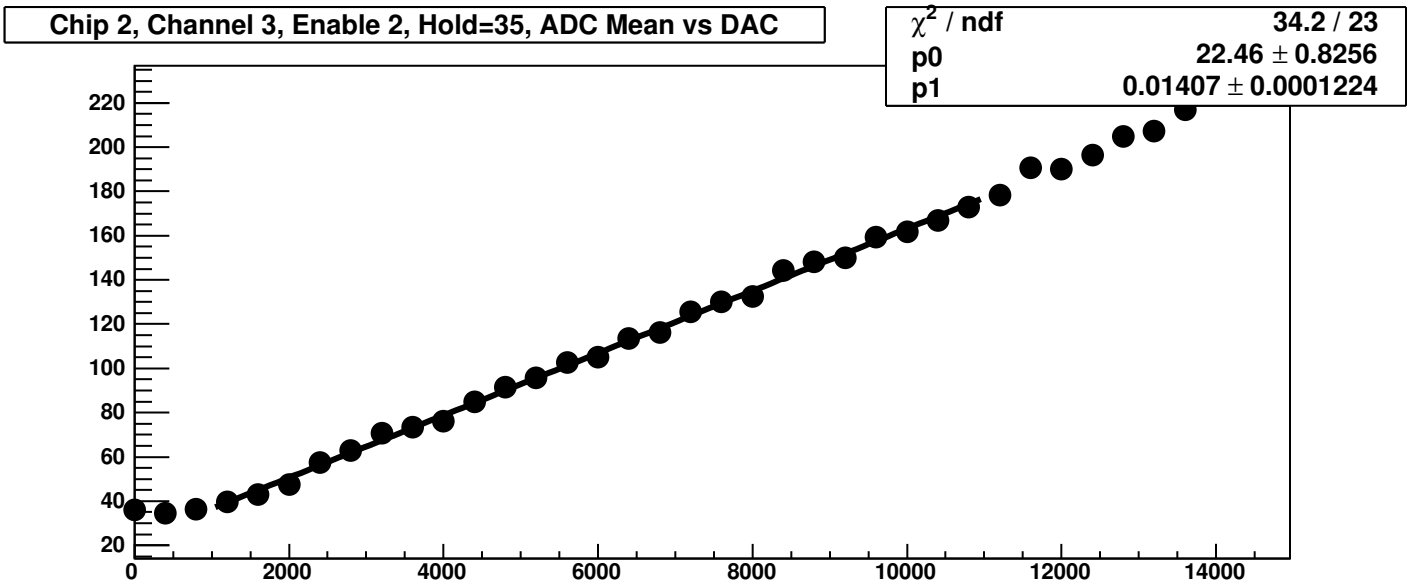
Chip 2, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

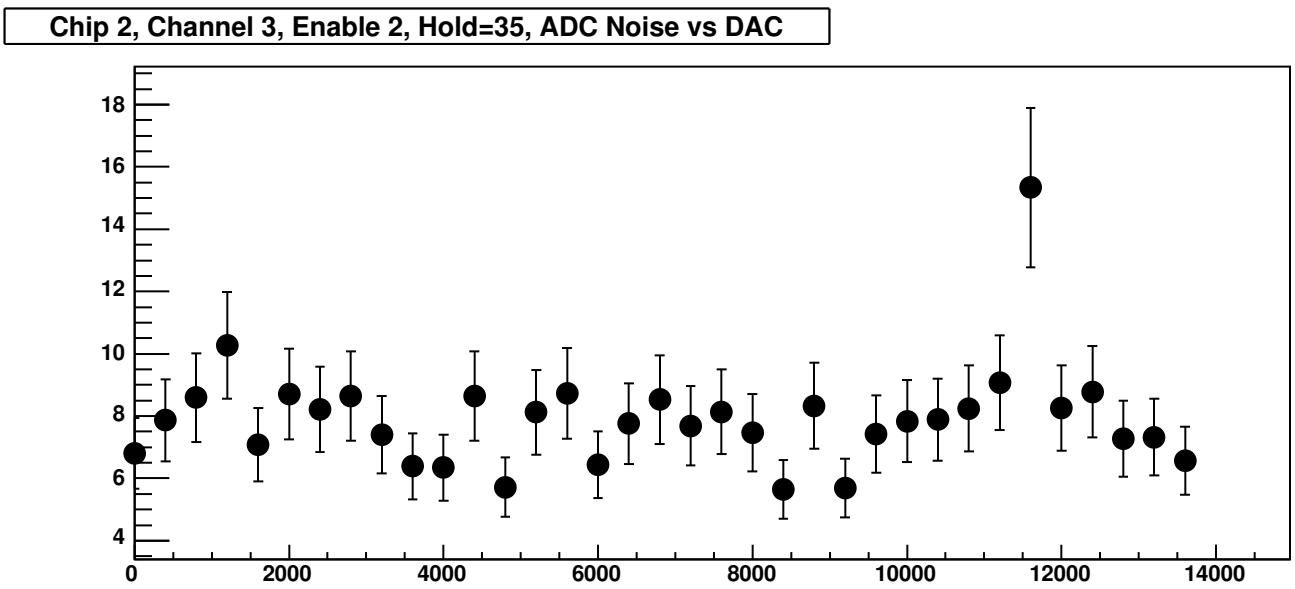


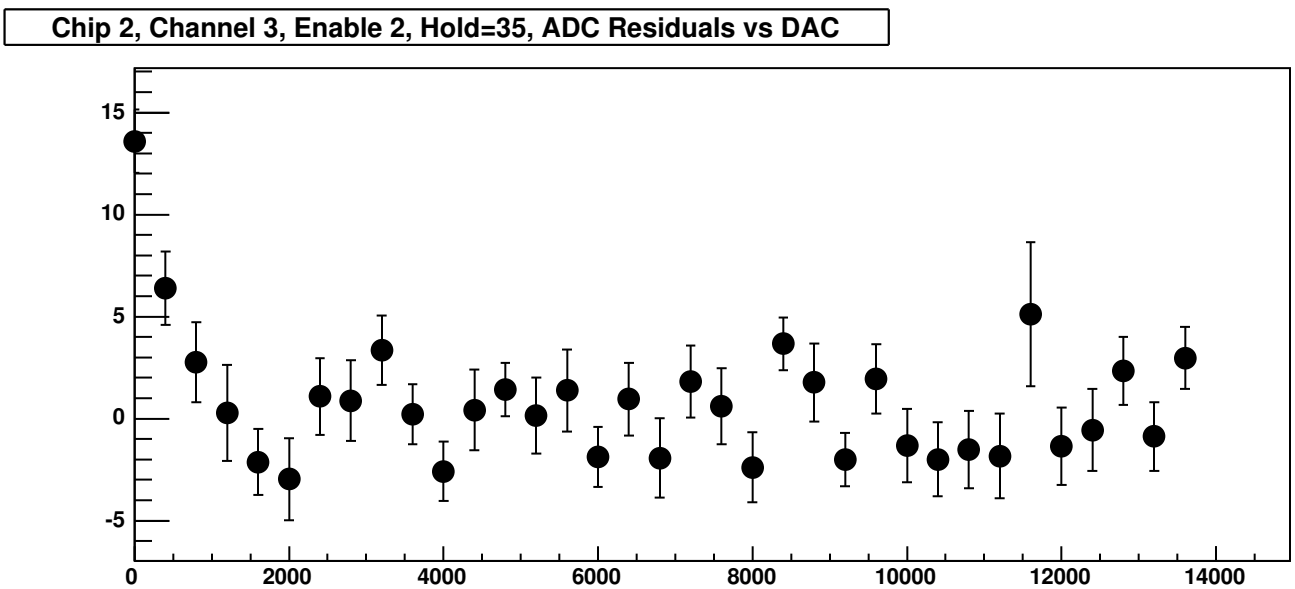

Chip 2, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 270.6 / 23
p0 81.6 ± 2.899
p1 2.188 ± 0.00154
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

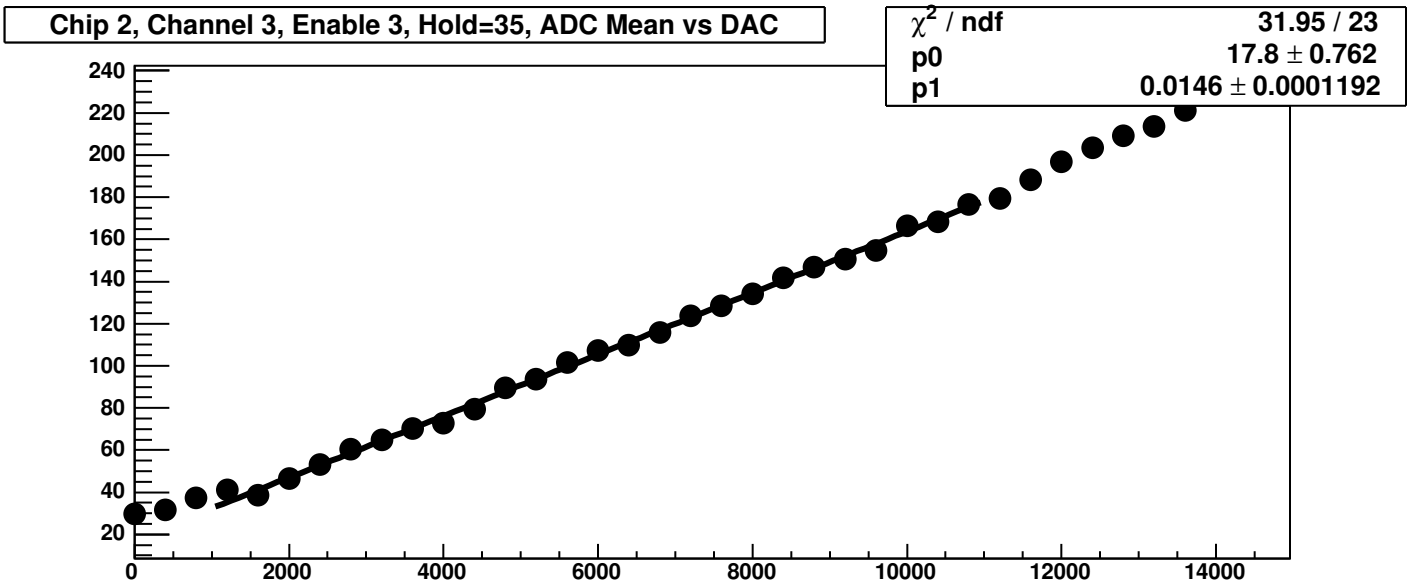

Chip 2, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

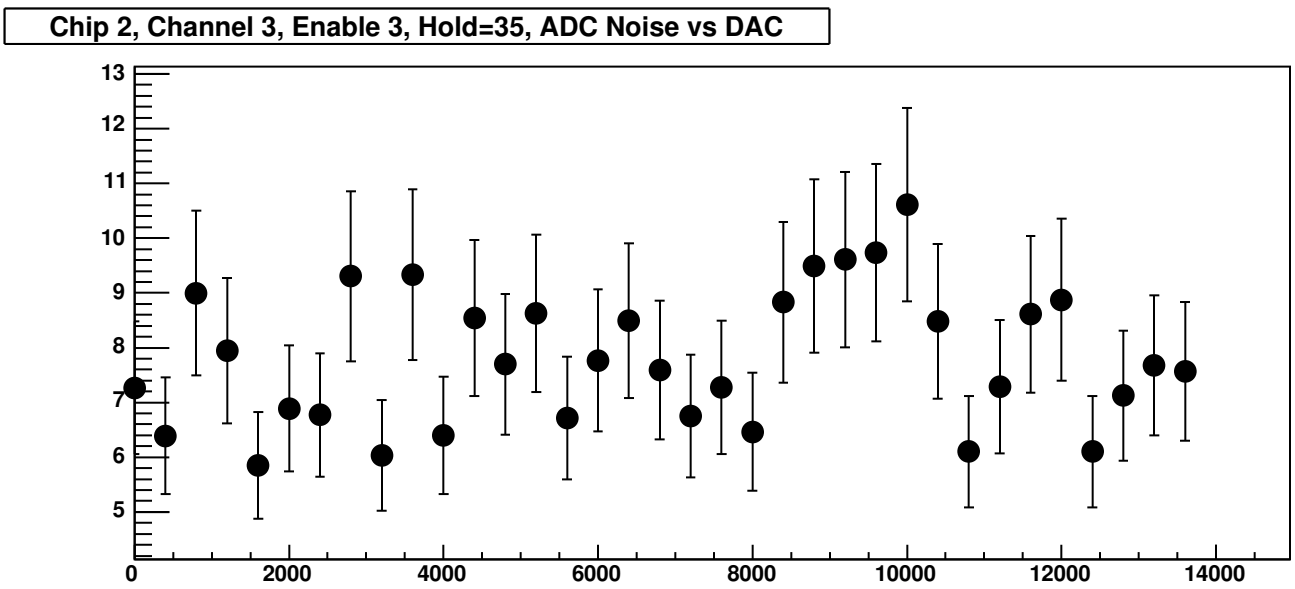

Chip 2, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

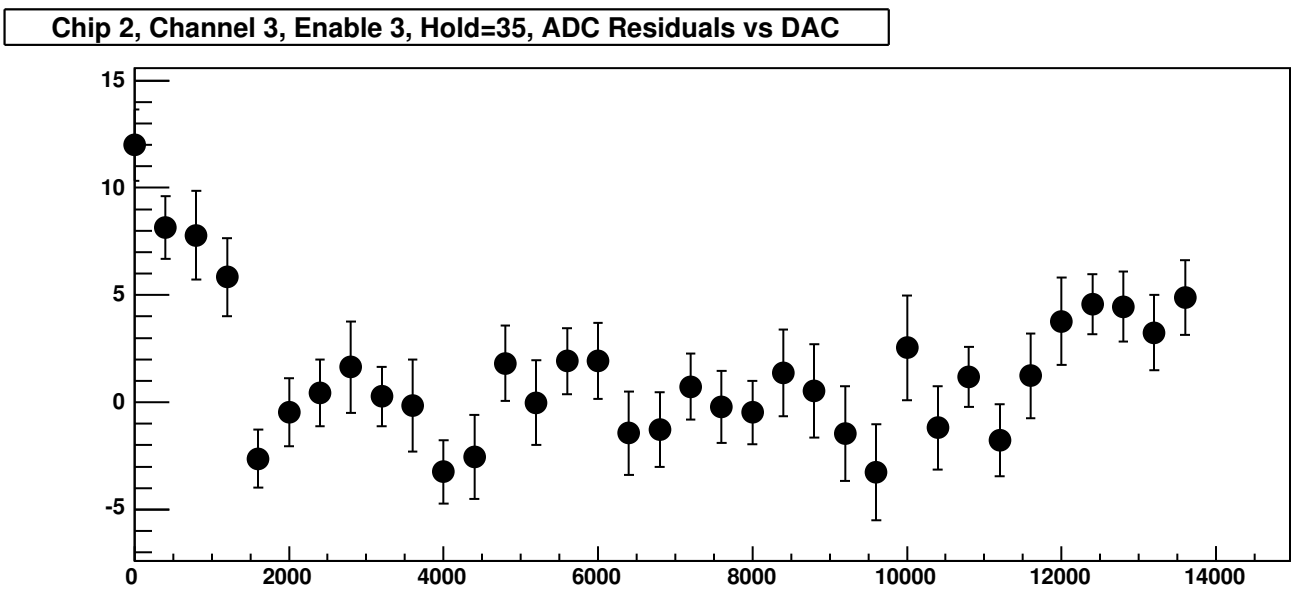

Chip 2, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 34.2 / 23
p0 22.46 ± 0.8256
p1 0.01407 ± 0.0001224
220
200
180
160
140
120
100
80
60
40
20
0
2000
4000
6000
8000
10000
12000
14000

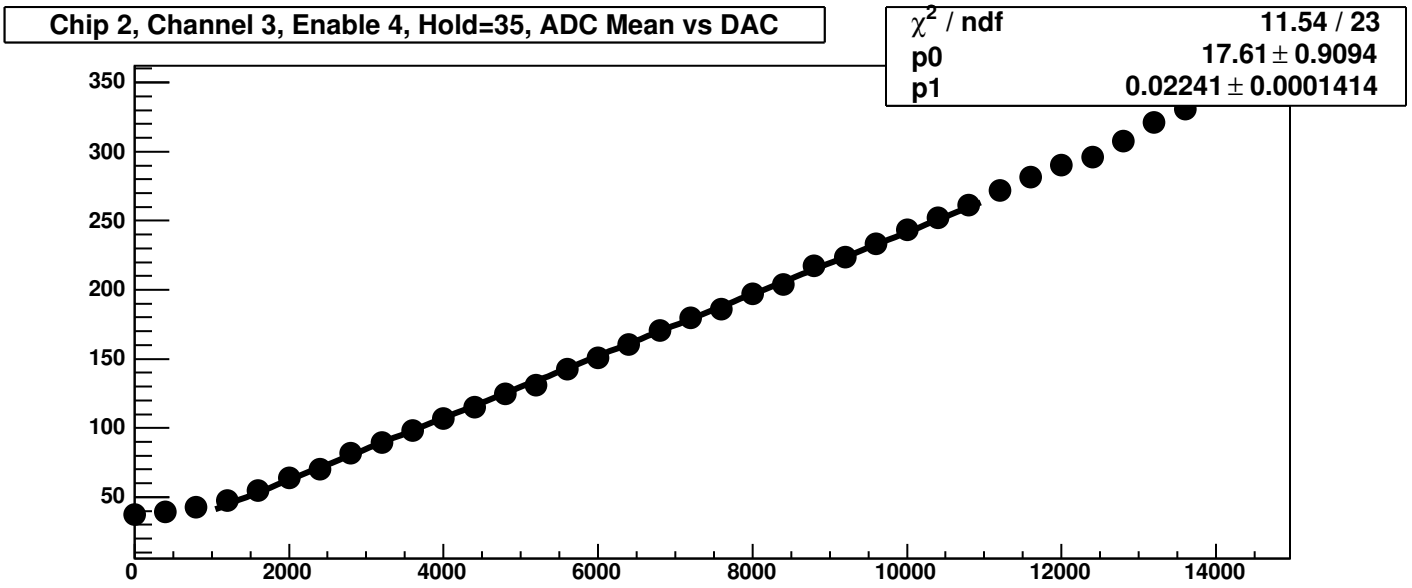

Chip 2, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

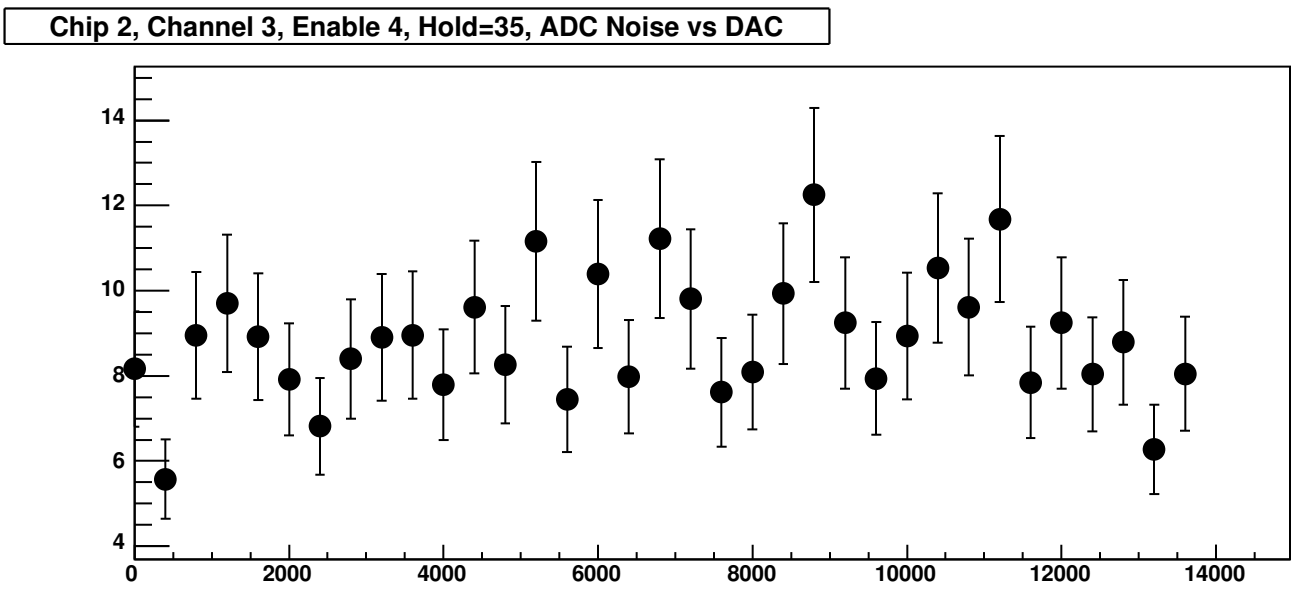

Chip 2, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

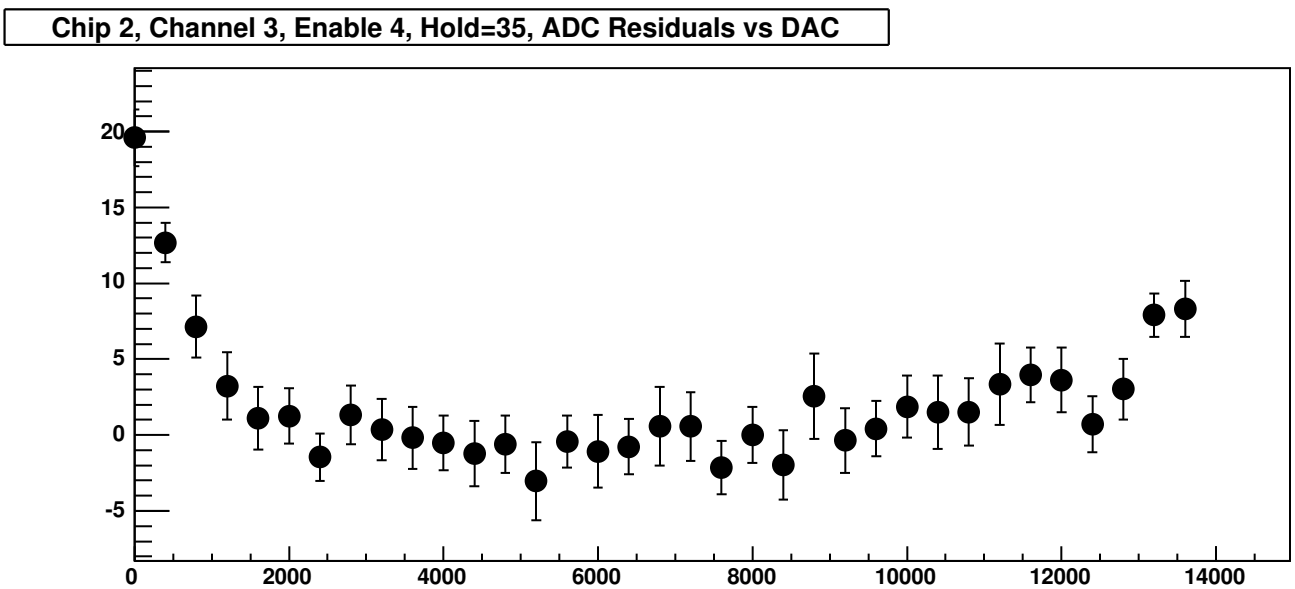

Chip 2, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.95 / 23
p0 17.8 ± 0.762
p1 0.0146 ± 0.0001192
240
220
200
180
160
140
120
100
80
60
40
20
0
2000
4000
6000
8000
10000
12000
14000

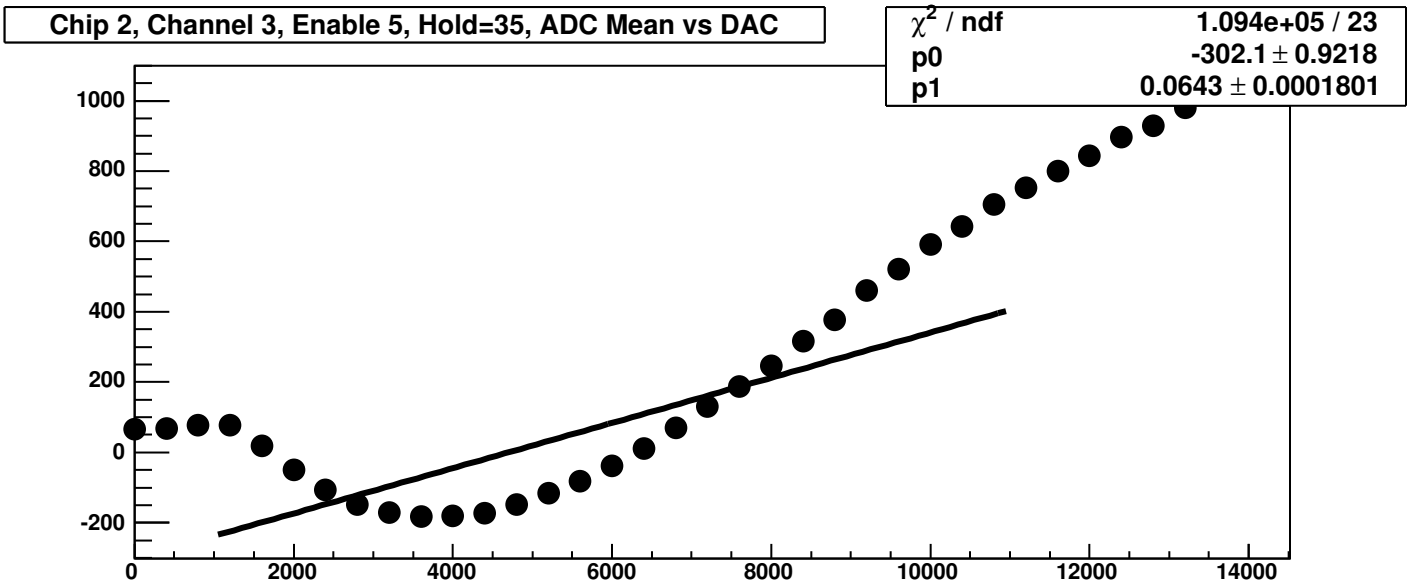

Chip 2, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

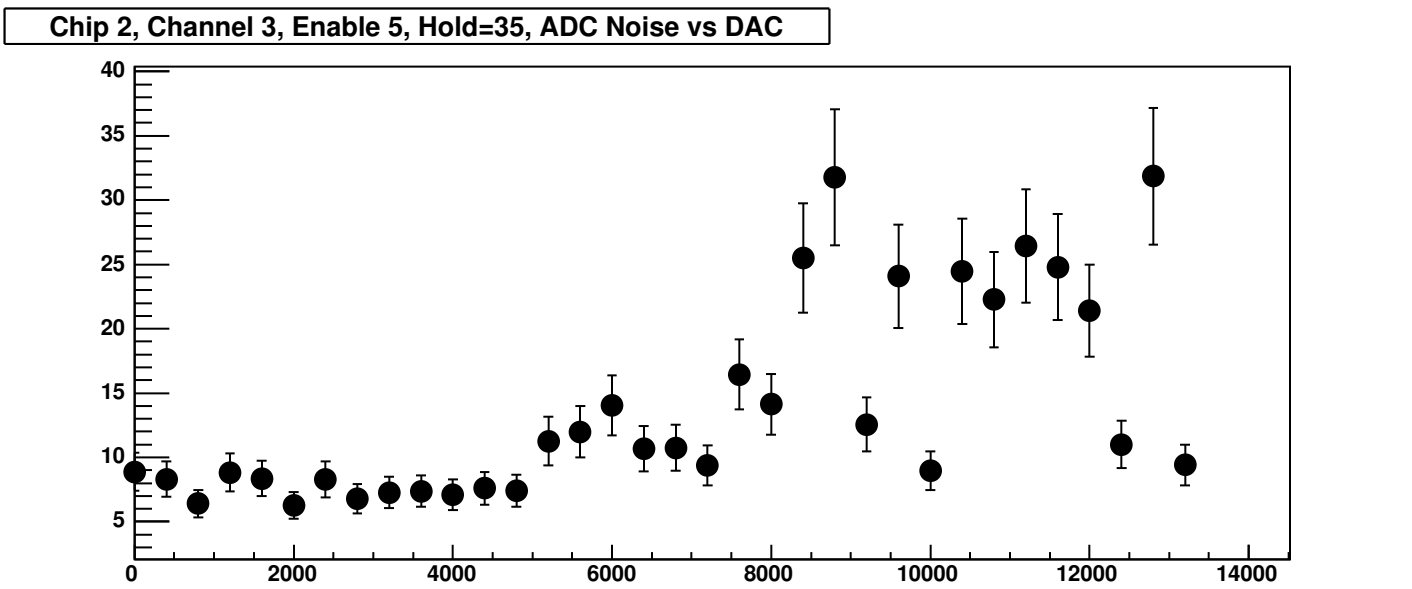

Chip 2, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

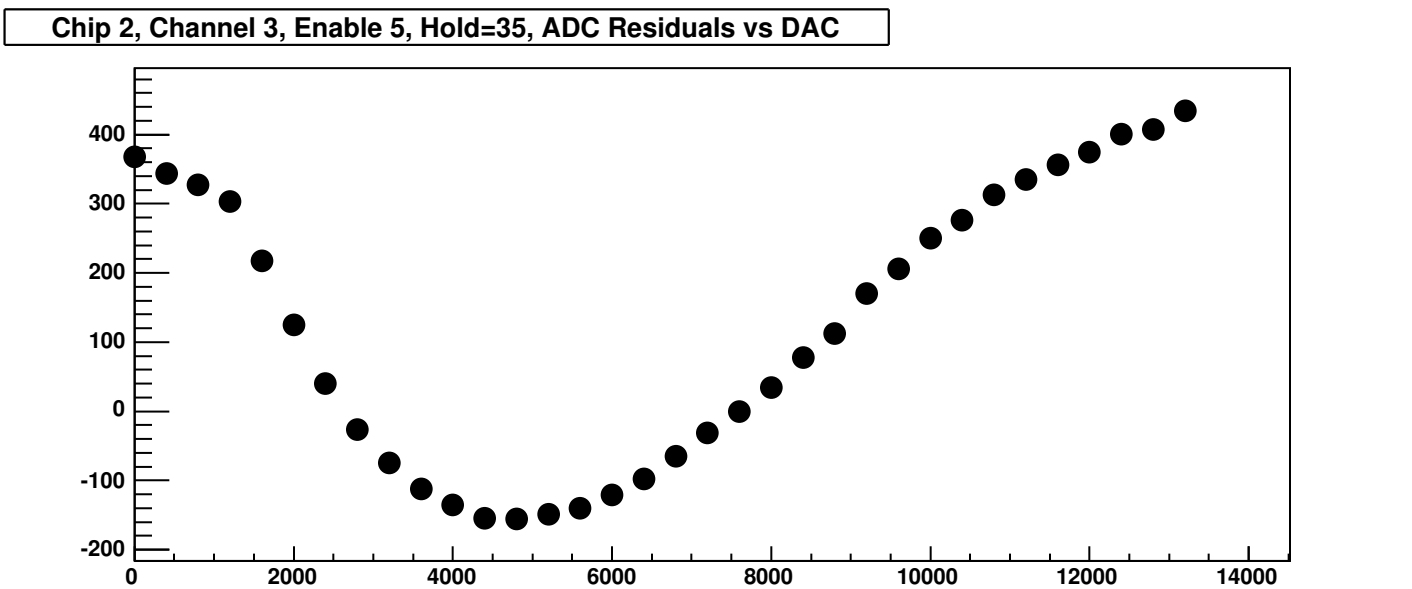

Chip 2, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 11.54 / 23
p0 17.61 ± 0.9094
p1 0.02241 ± 0.0001414
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

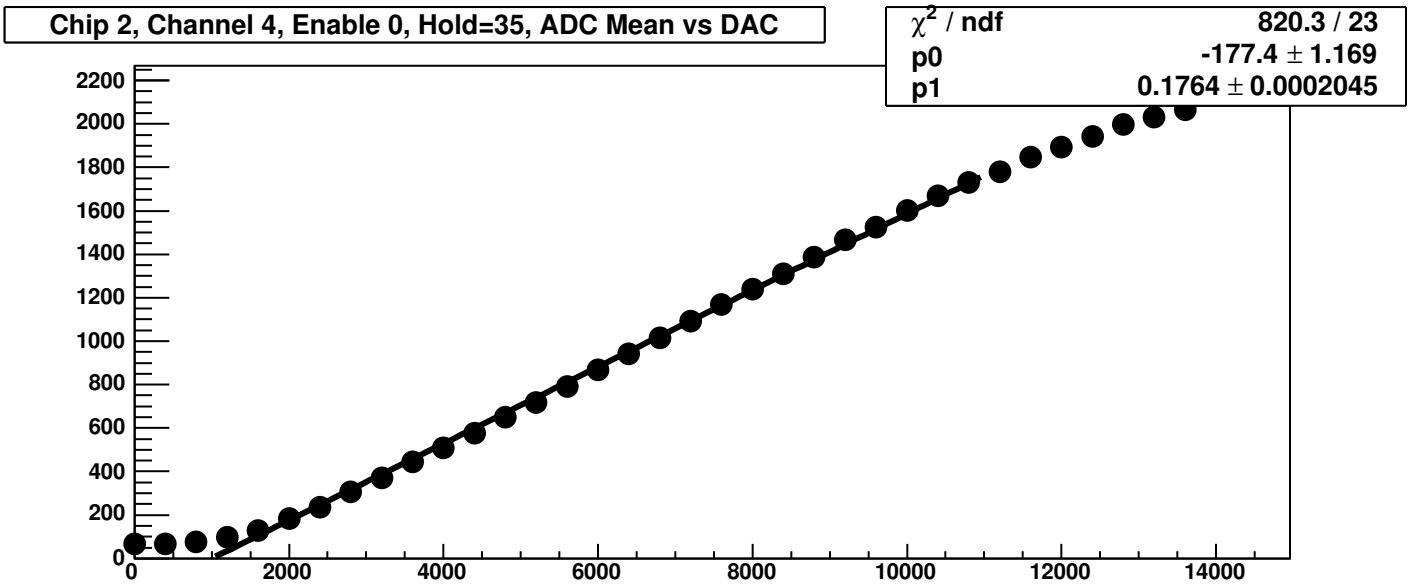

Chip 2, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

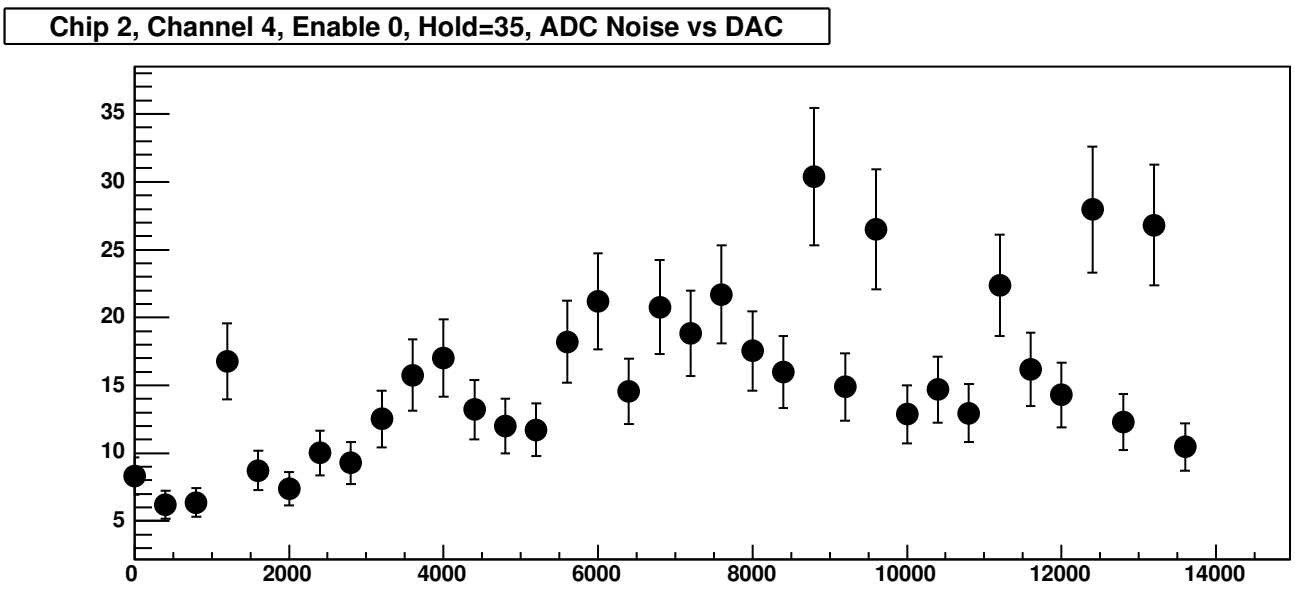

Chip 2, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

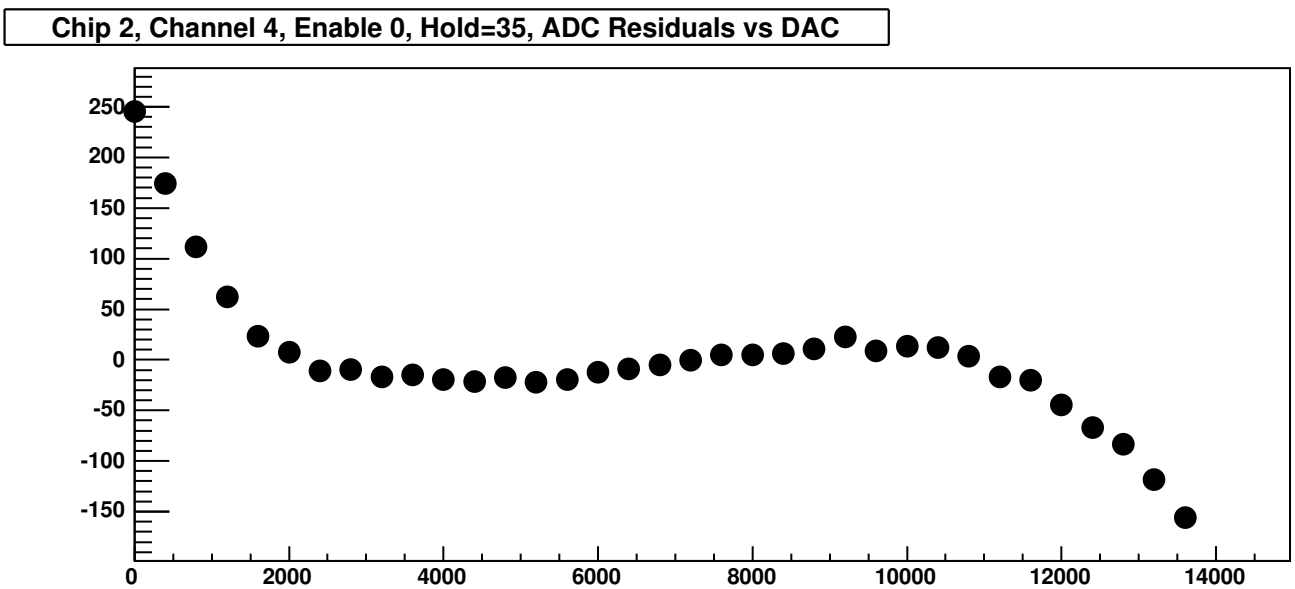

Chip 2, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.094e+05 / 23
p0 -302.1 ± 0.9218
p1 0.0643 ± 0.0001801
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

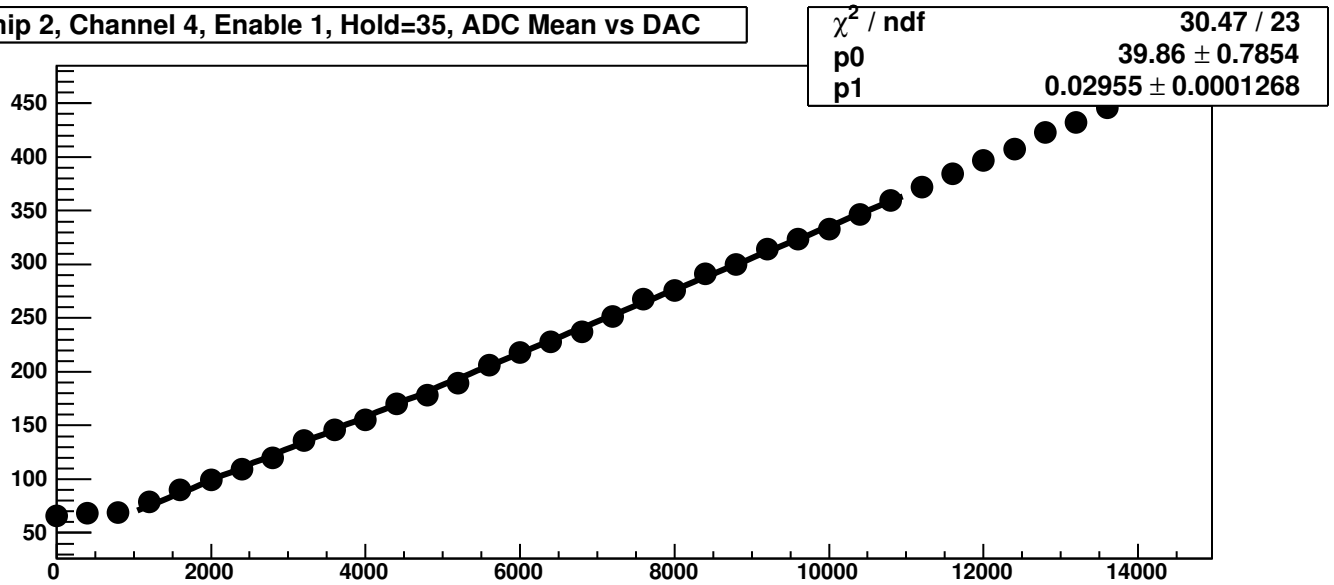

Chip 2, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 820.3 / 23
p0 -177.4 ± 1.169
p1 0.1764 ± 0.0002045
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

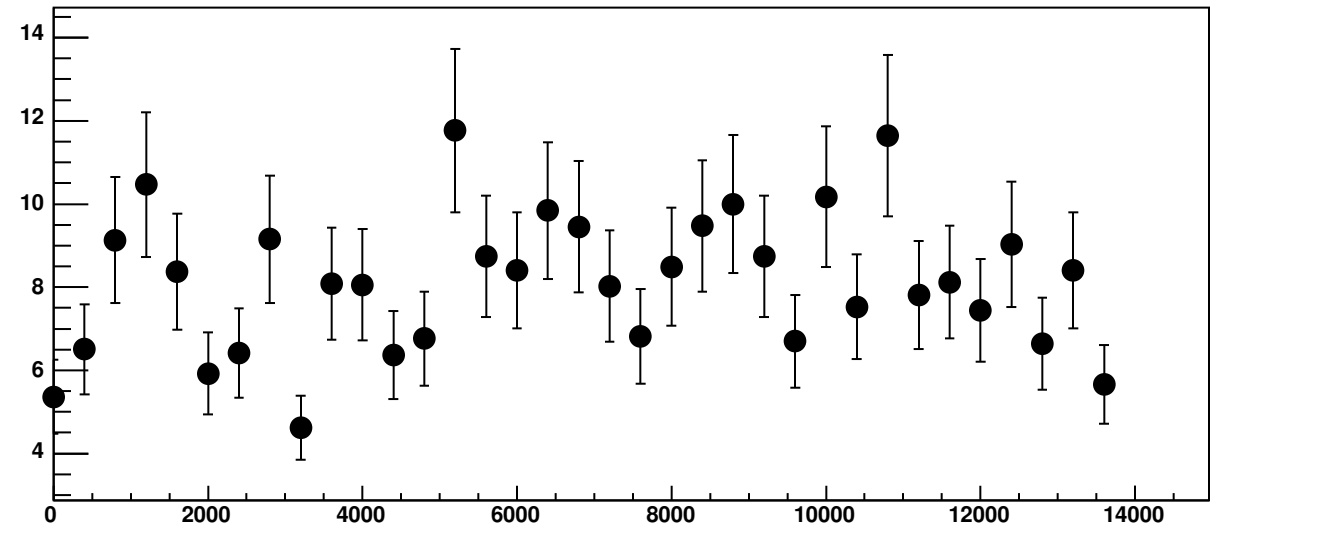

Chip 2, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

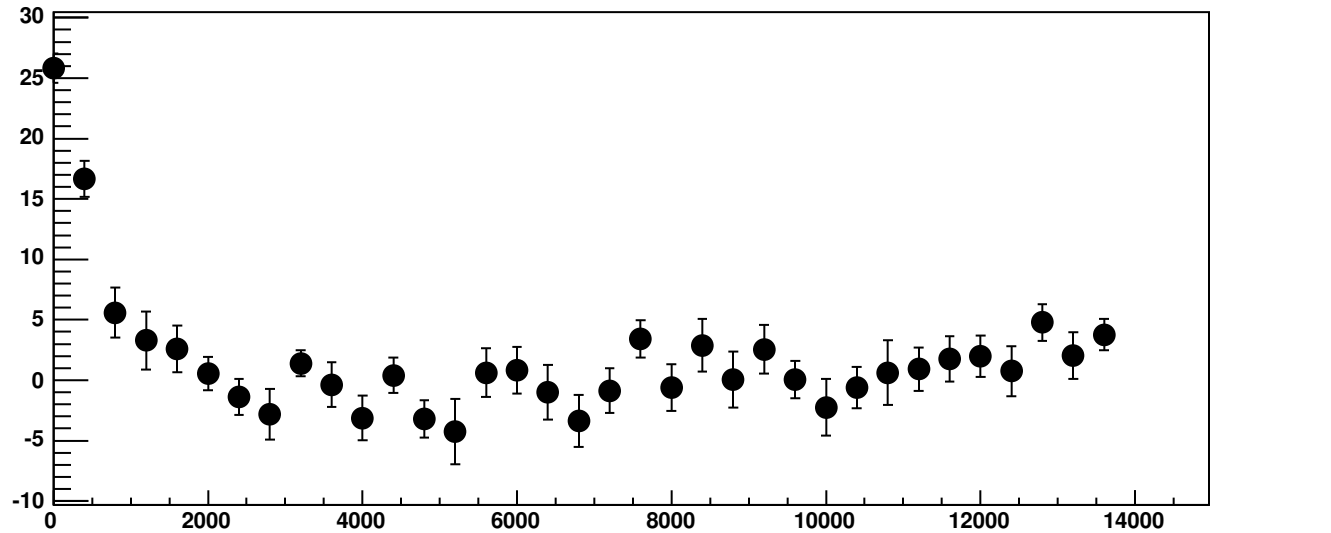
Chip 2, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC

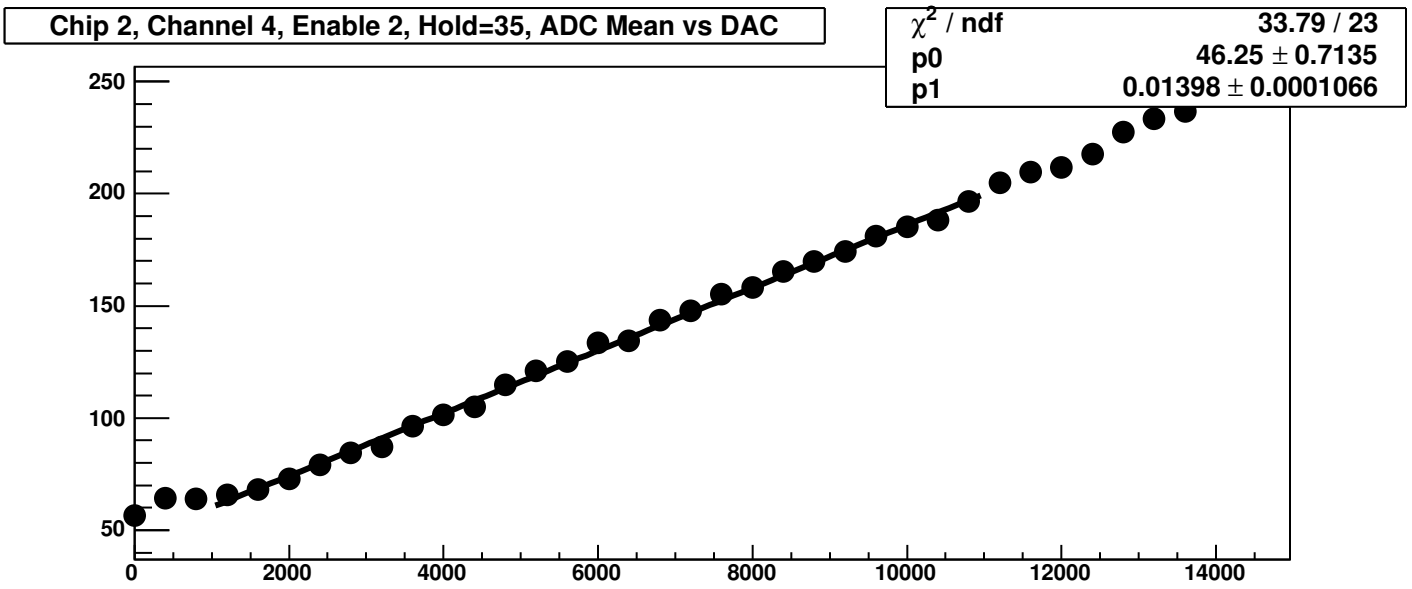


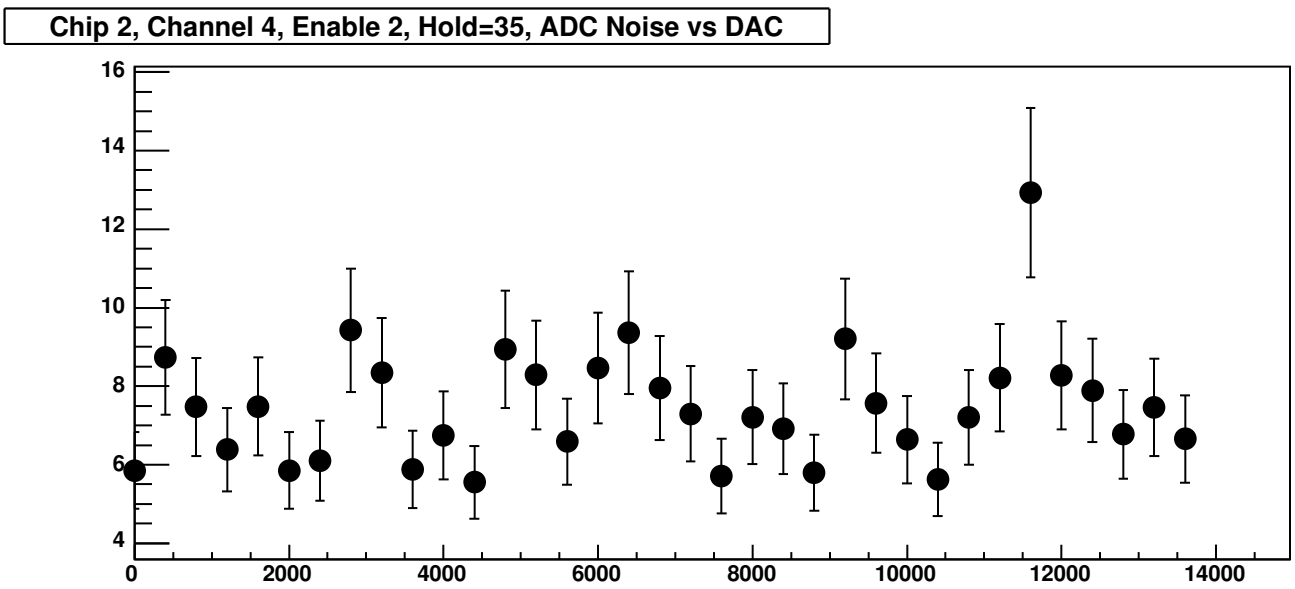
Chip 2, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC

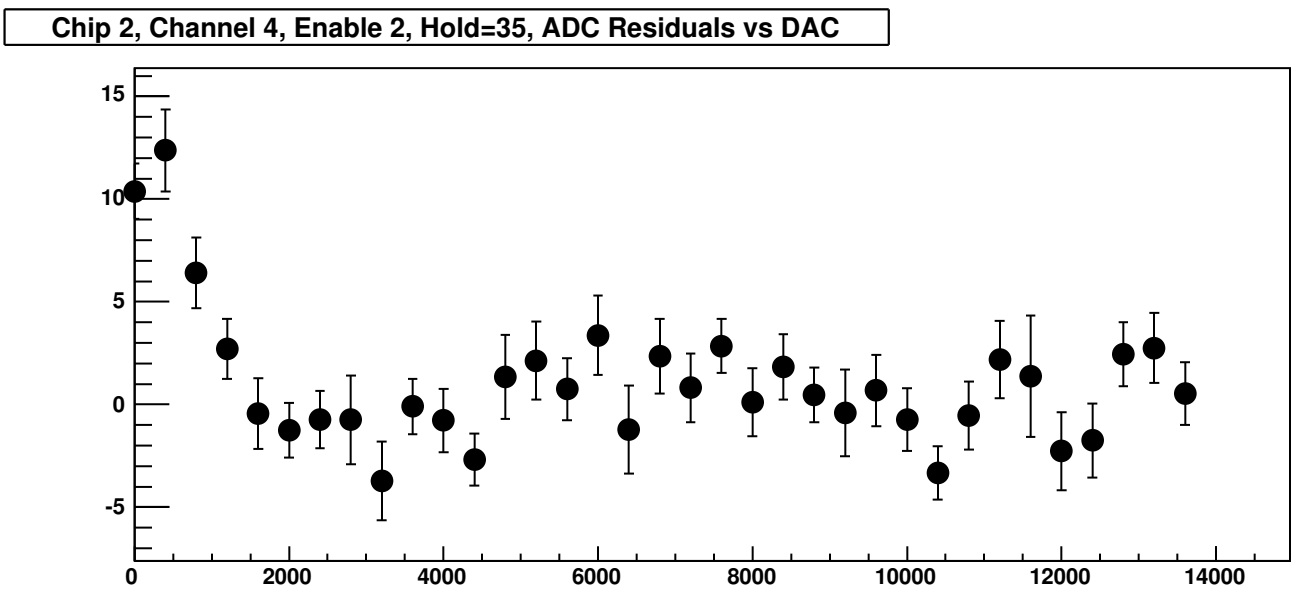


Chip 2, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC

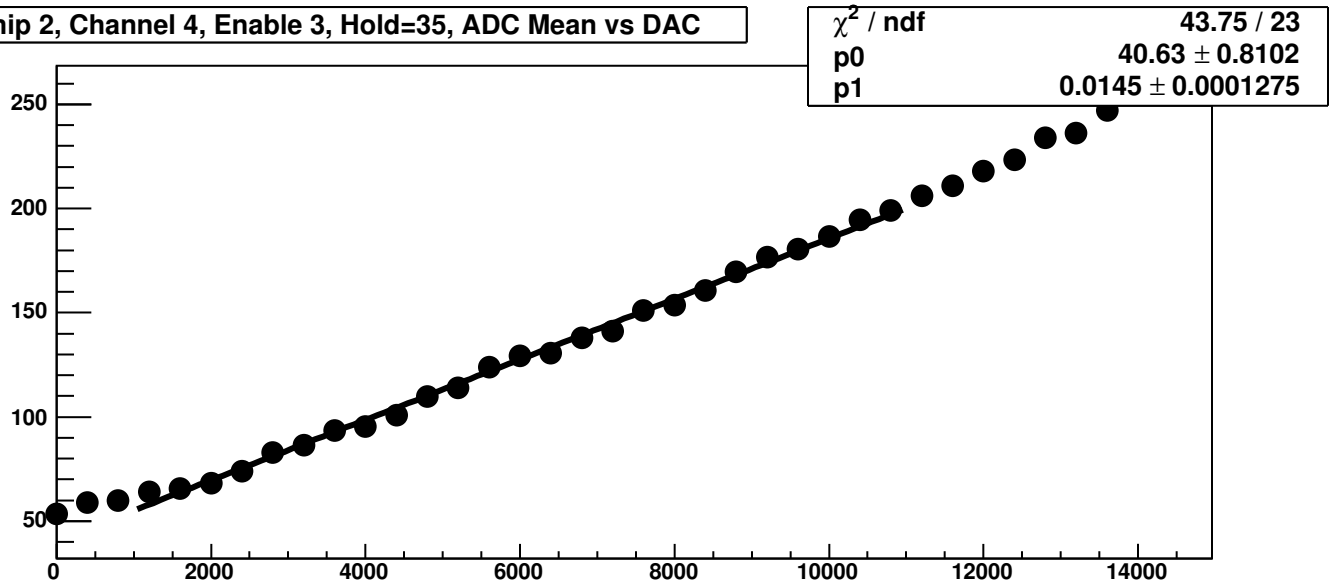



Chip 2, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 33.79 / 23
p0 46.25 ± 0.7135
p1 0.01398 ± 0.0001066
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

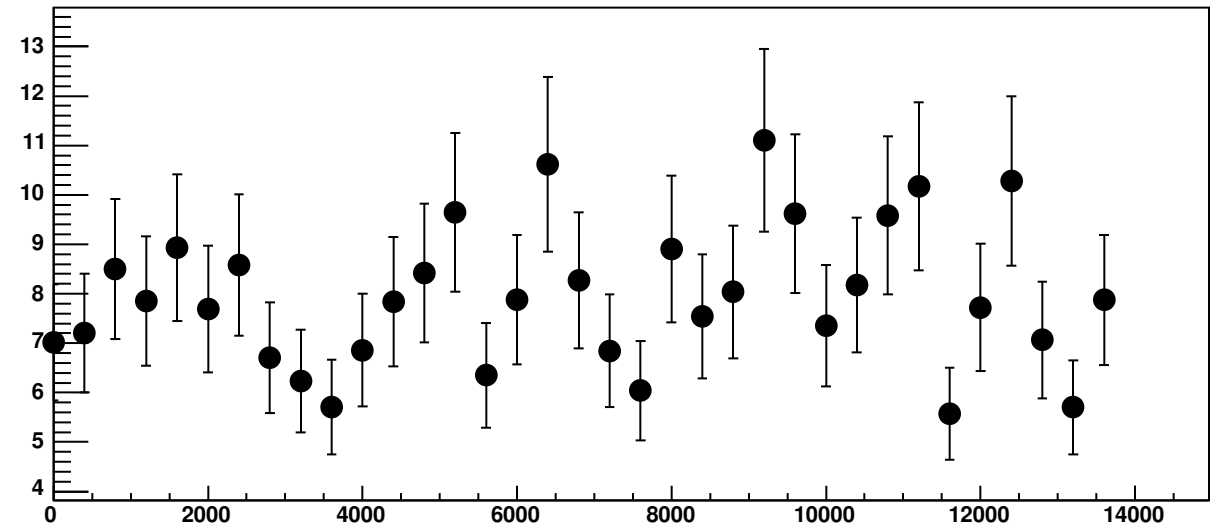

Chip 2, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

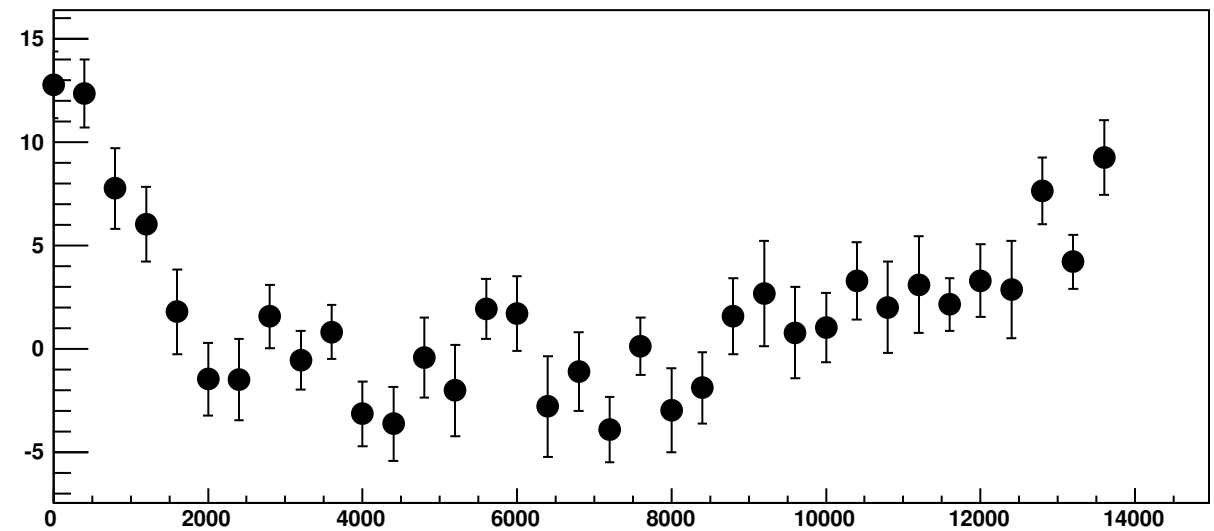
Chip 2, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

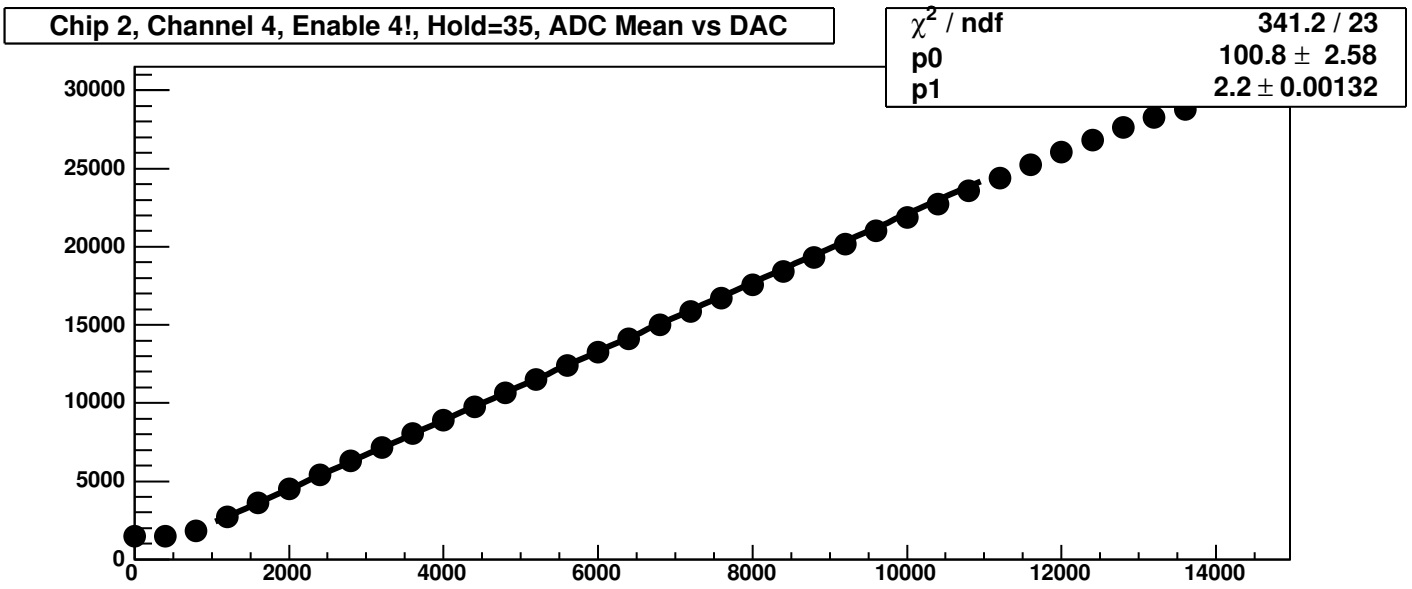


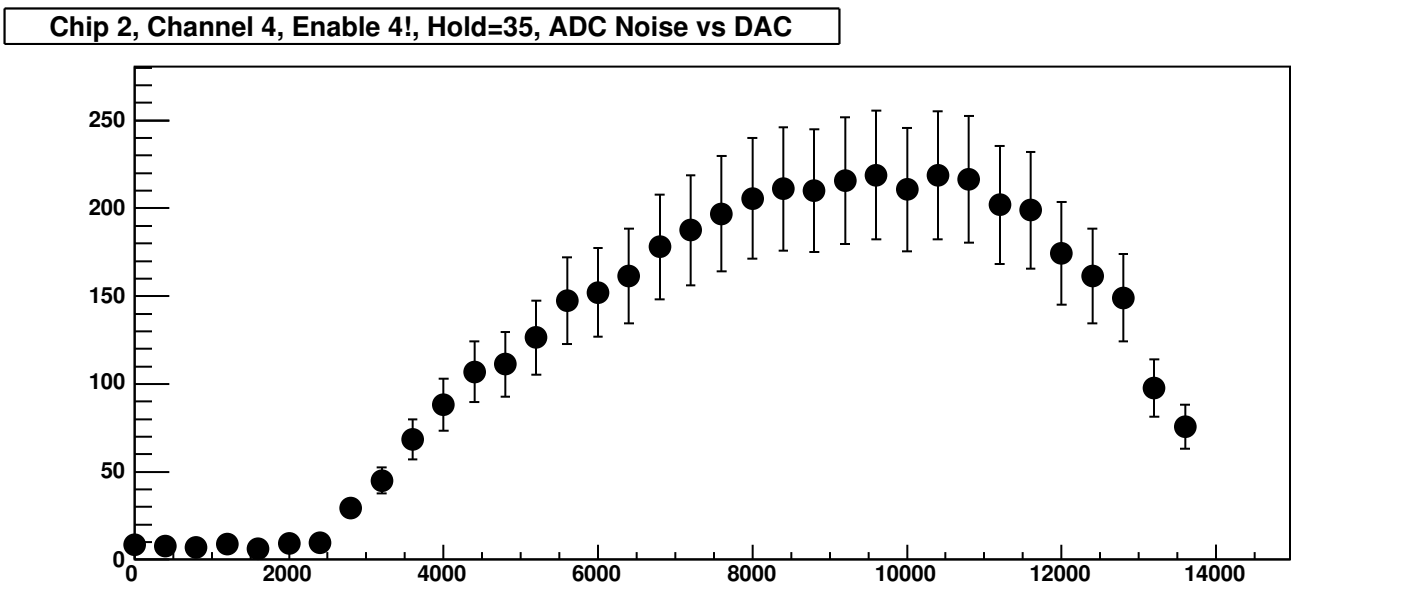
Chip 2, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC

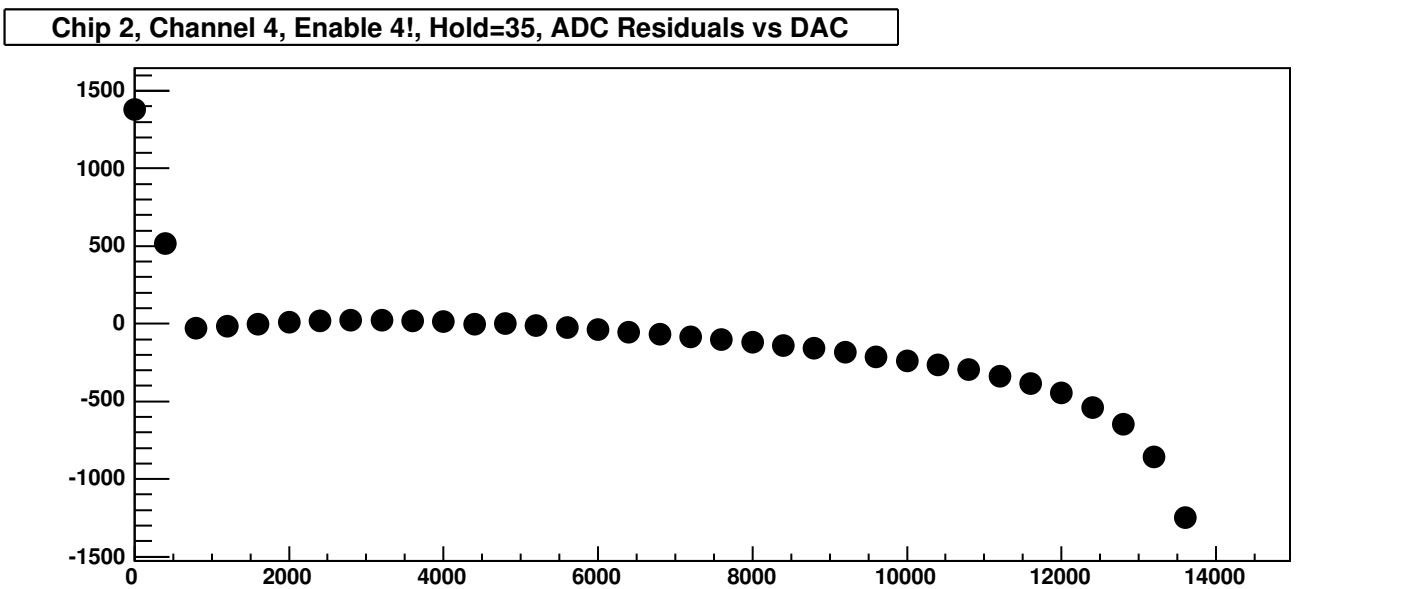


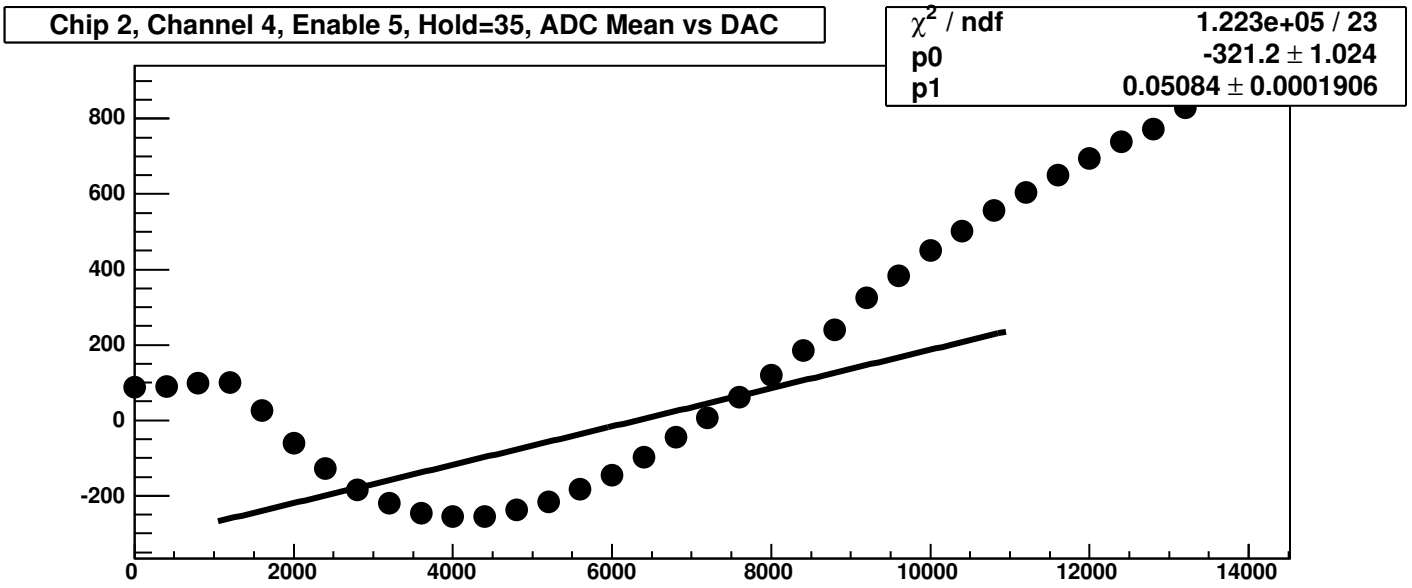
Chip 2, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

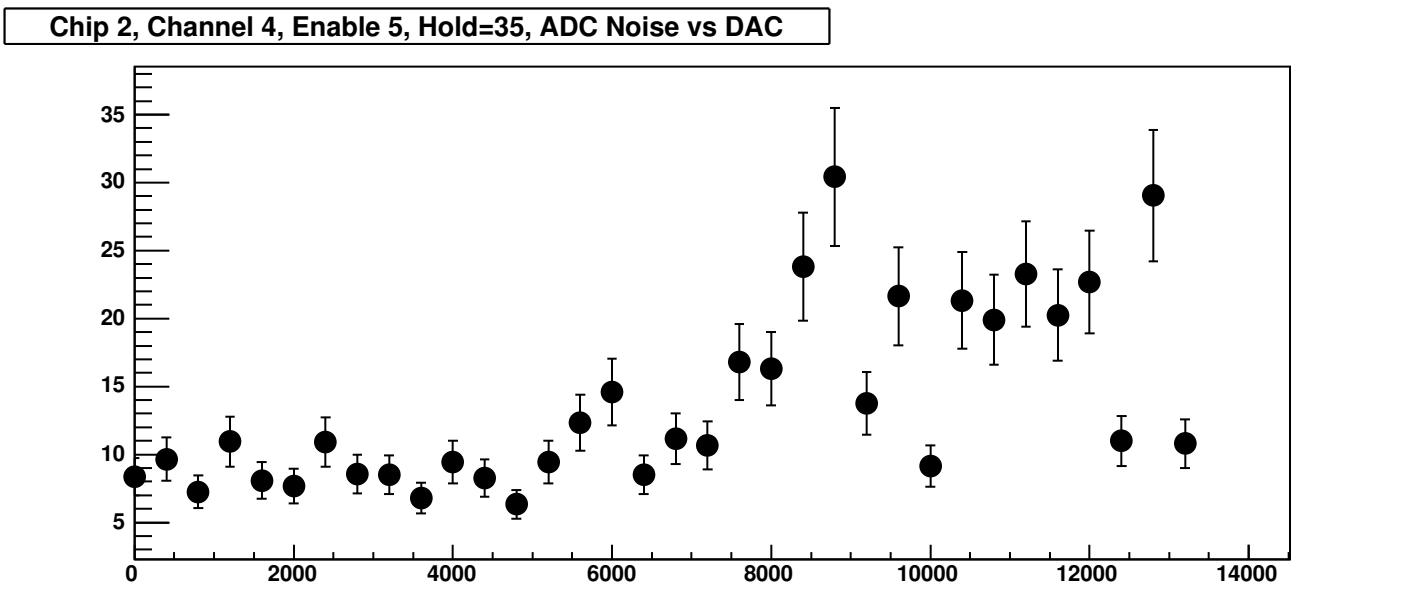


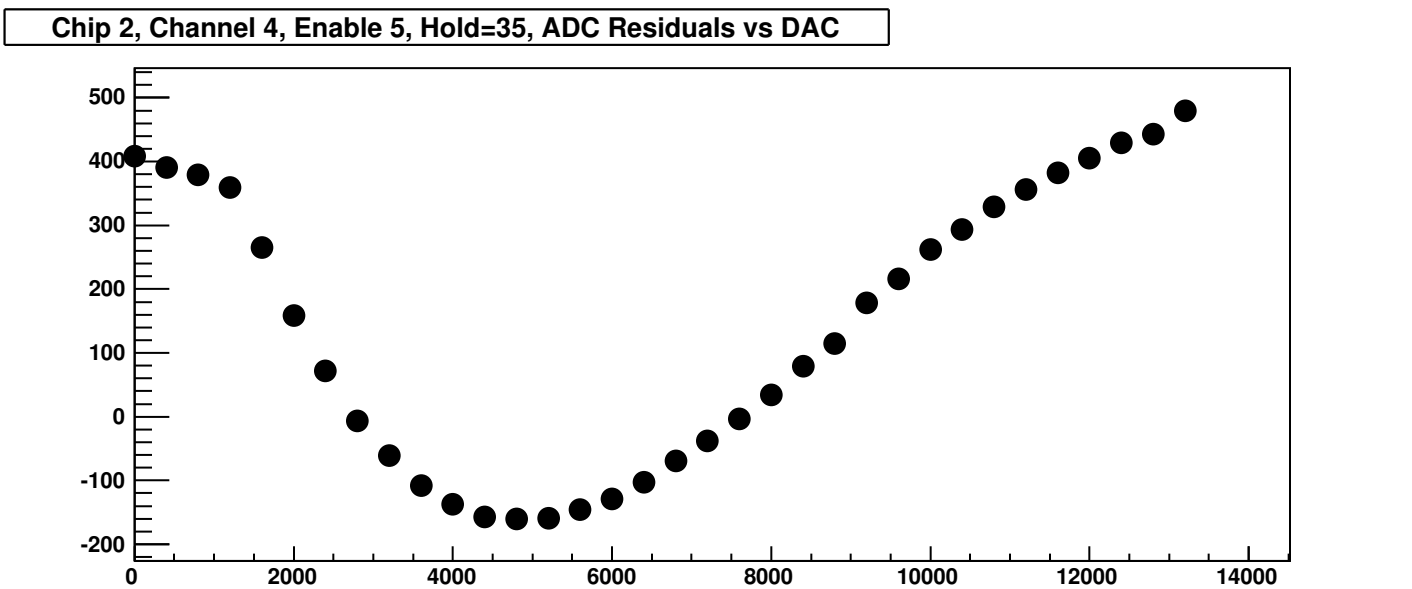

Chip 2, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 341.2 / 23
p0 100.8 ± 2.58
p1 2.2 ± 0.00132
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

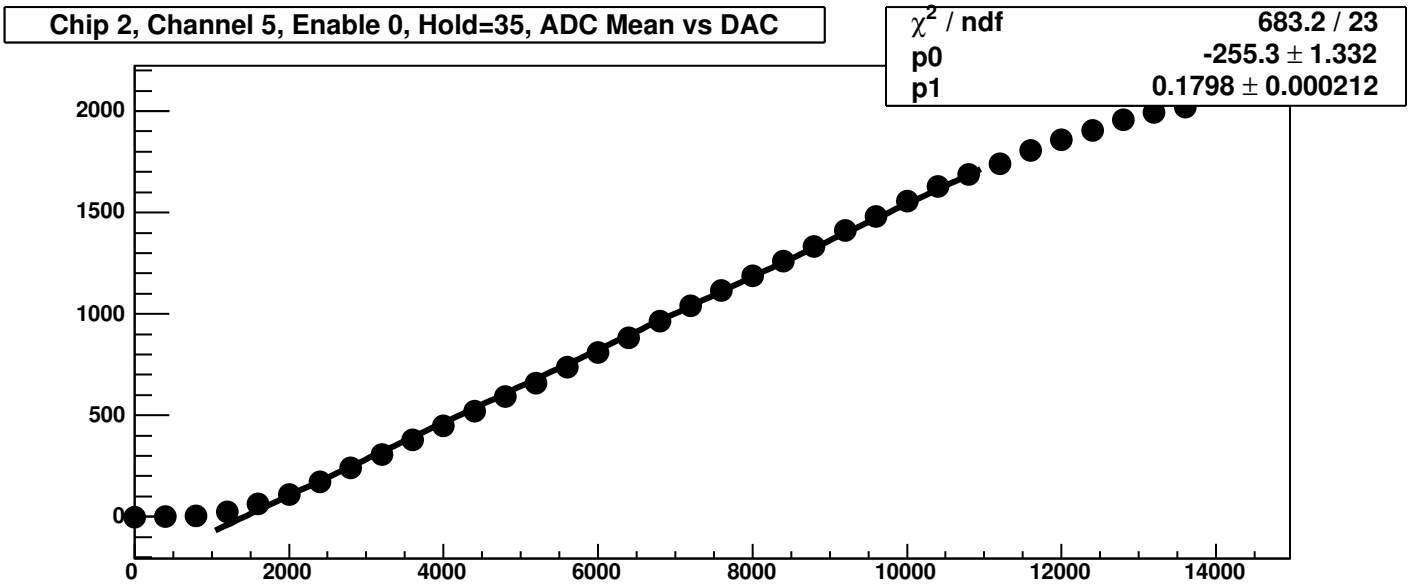

Chip 2, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

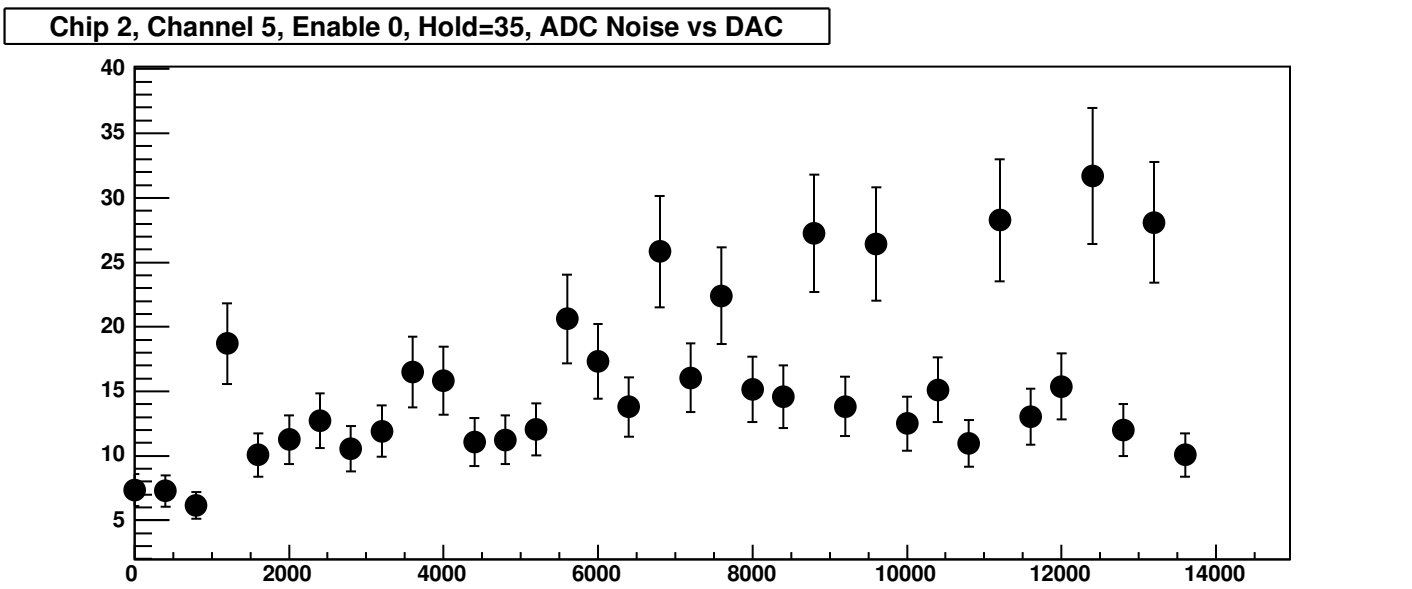

Chip 2, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

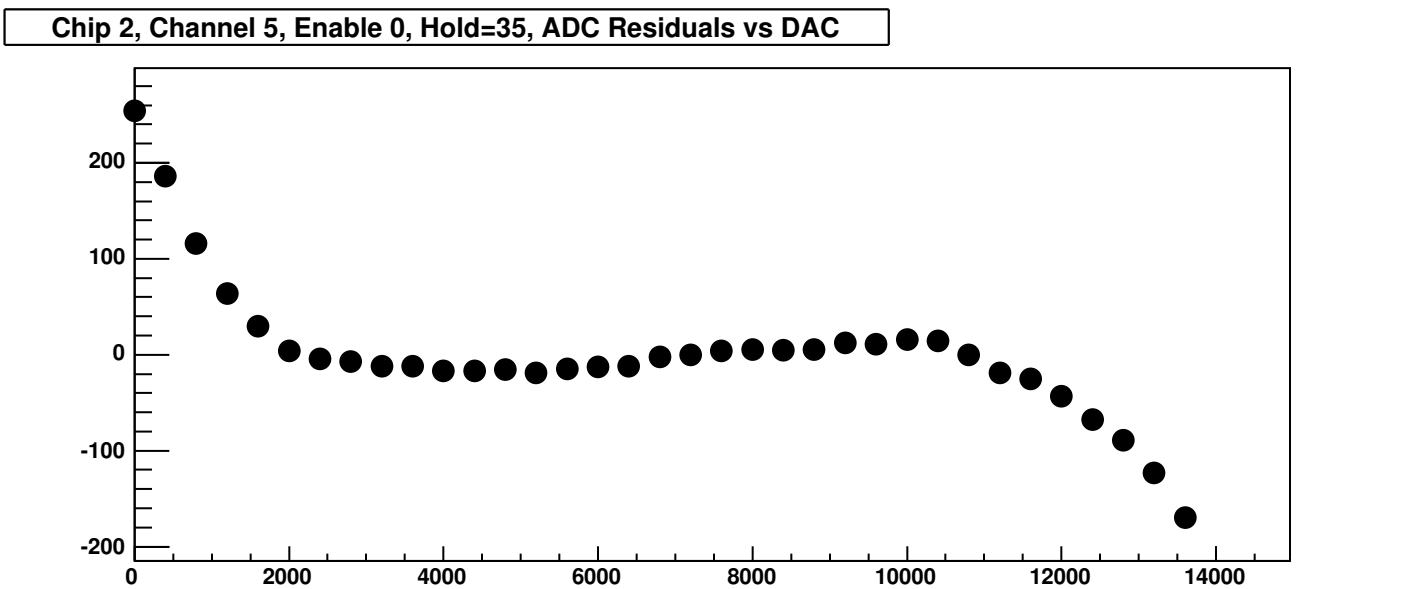

Chip 2, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.223e+05 / 23
p0 -321.2 ± 1.024
p1 0.05084 ± 0.0001906
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

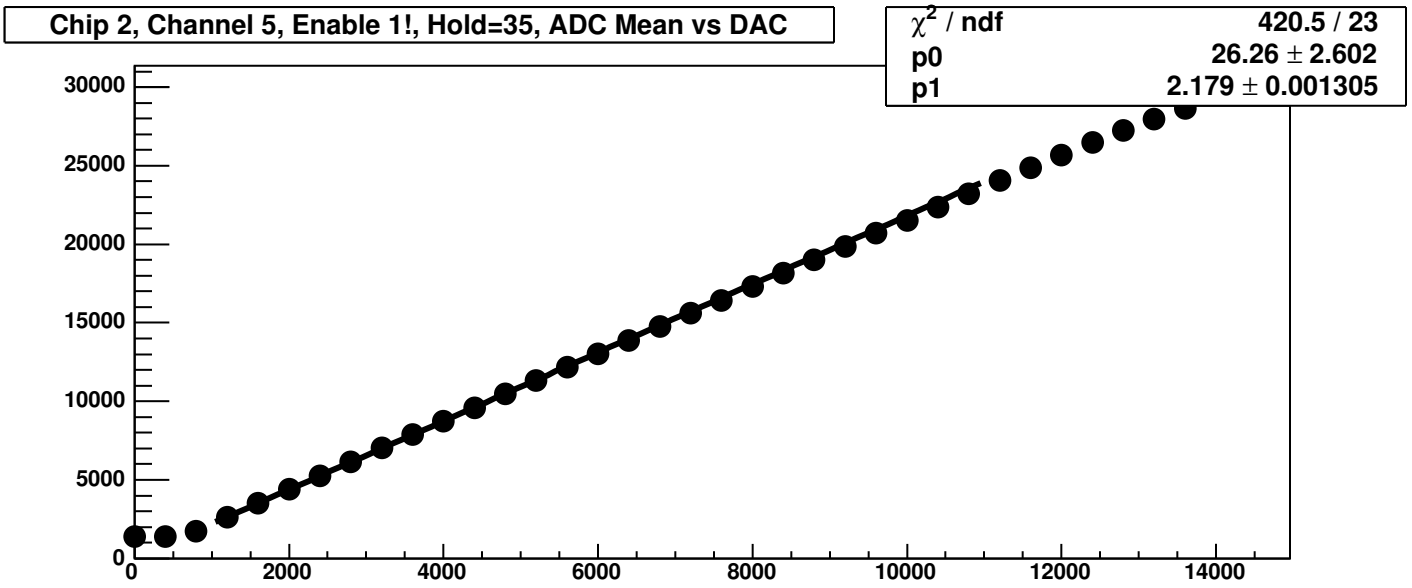

Chip 2, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

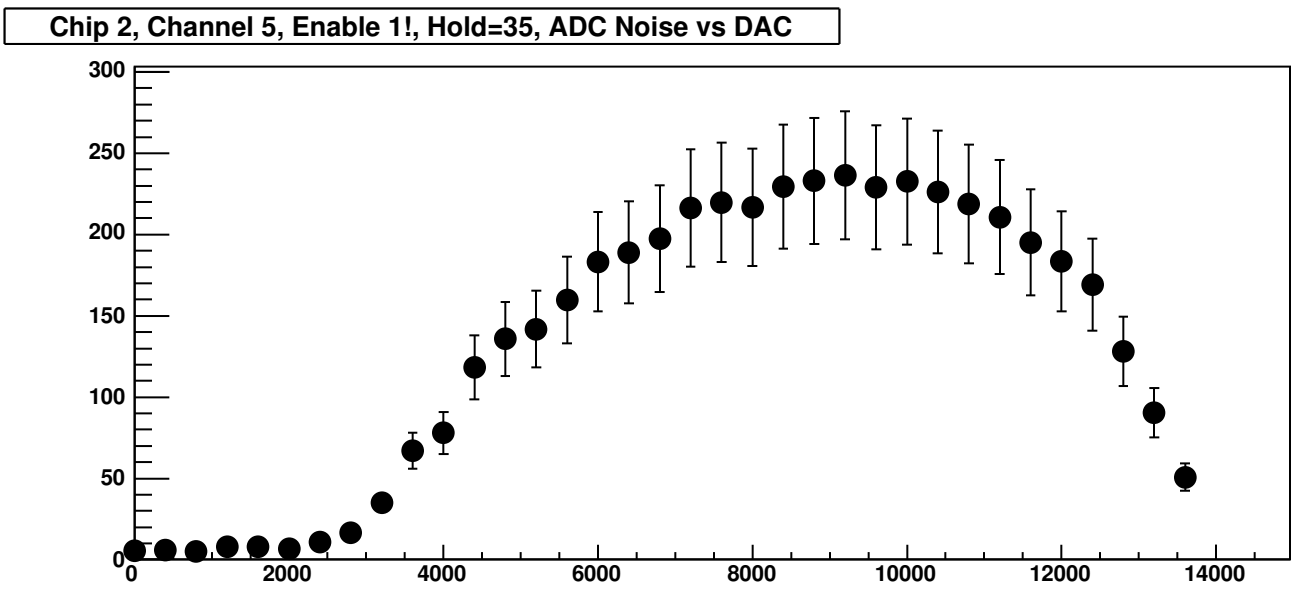

Chip 2, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

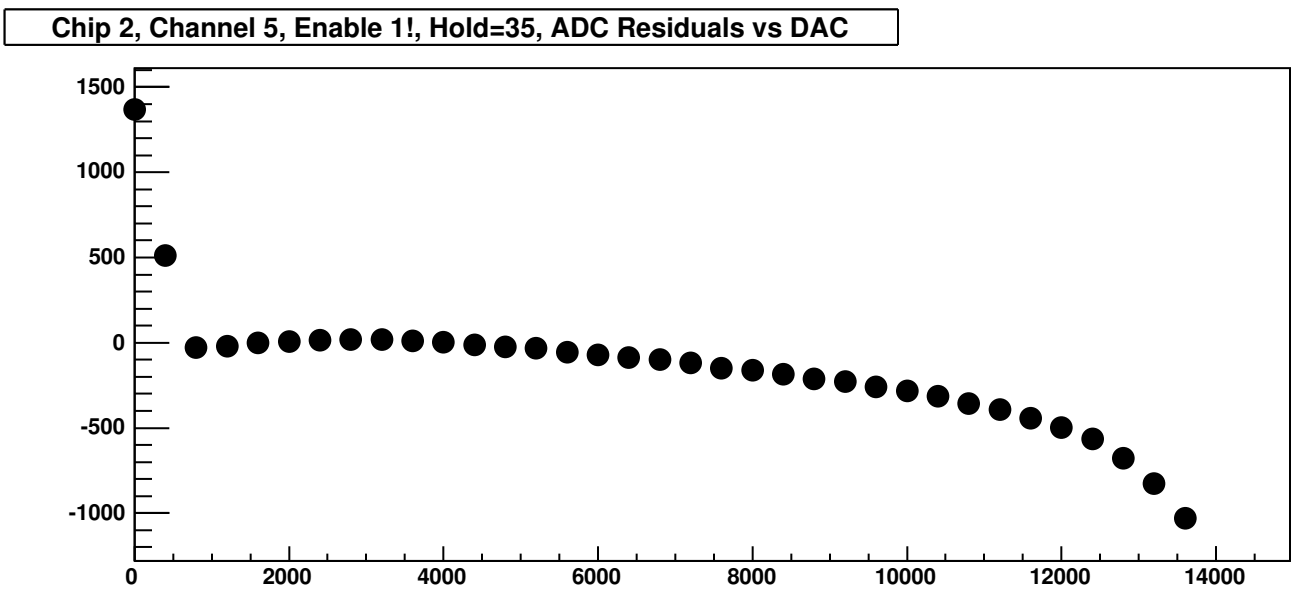

Chip 2, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 683.2 / 23
p0 -255.3 ± 1.332
p1 0.1798 ± 0.000212
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

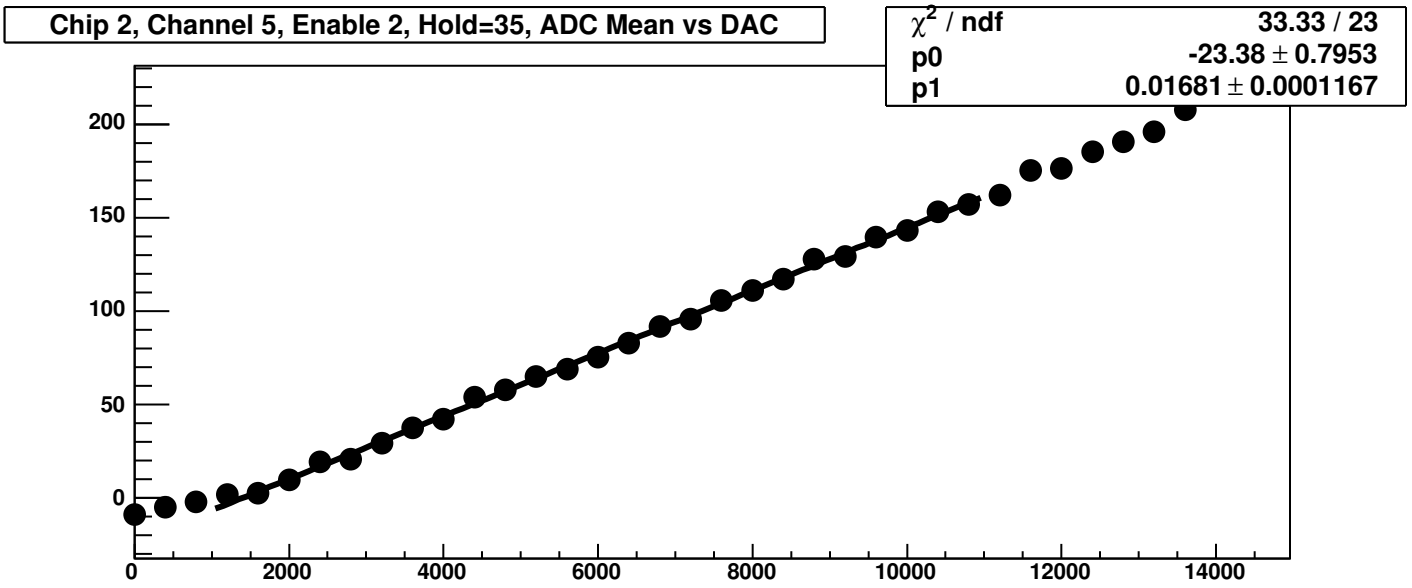

Chip 2, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

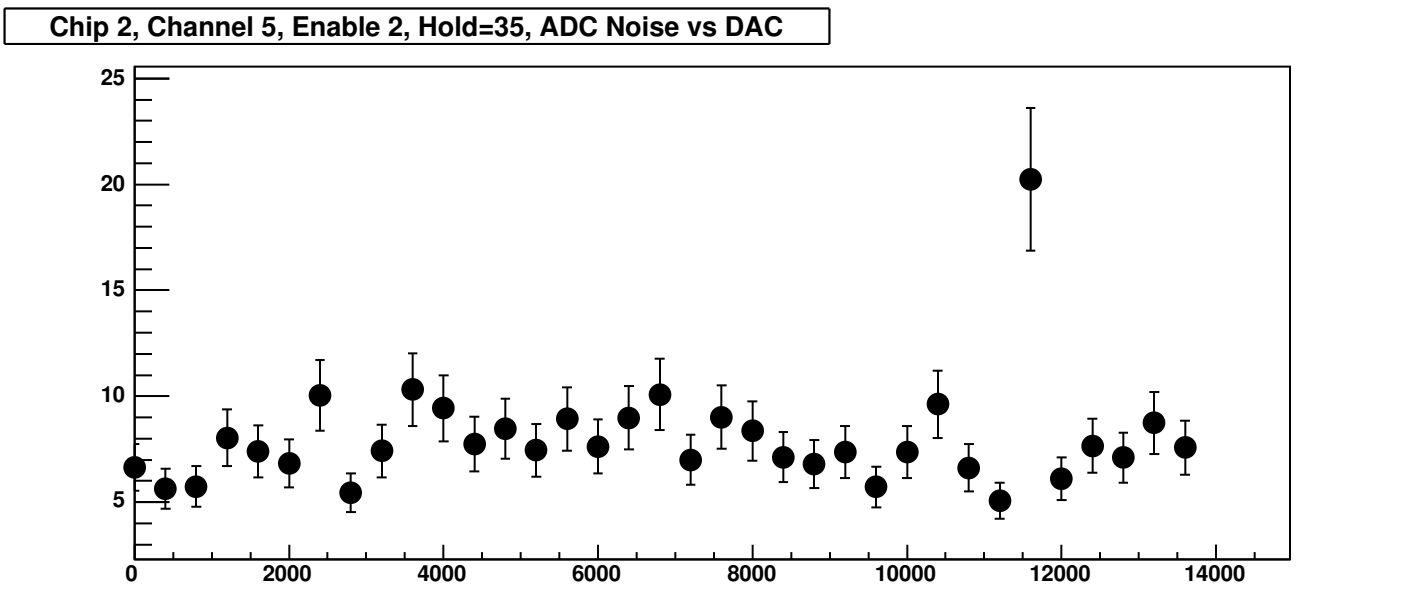

Chip 2, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

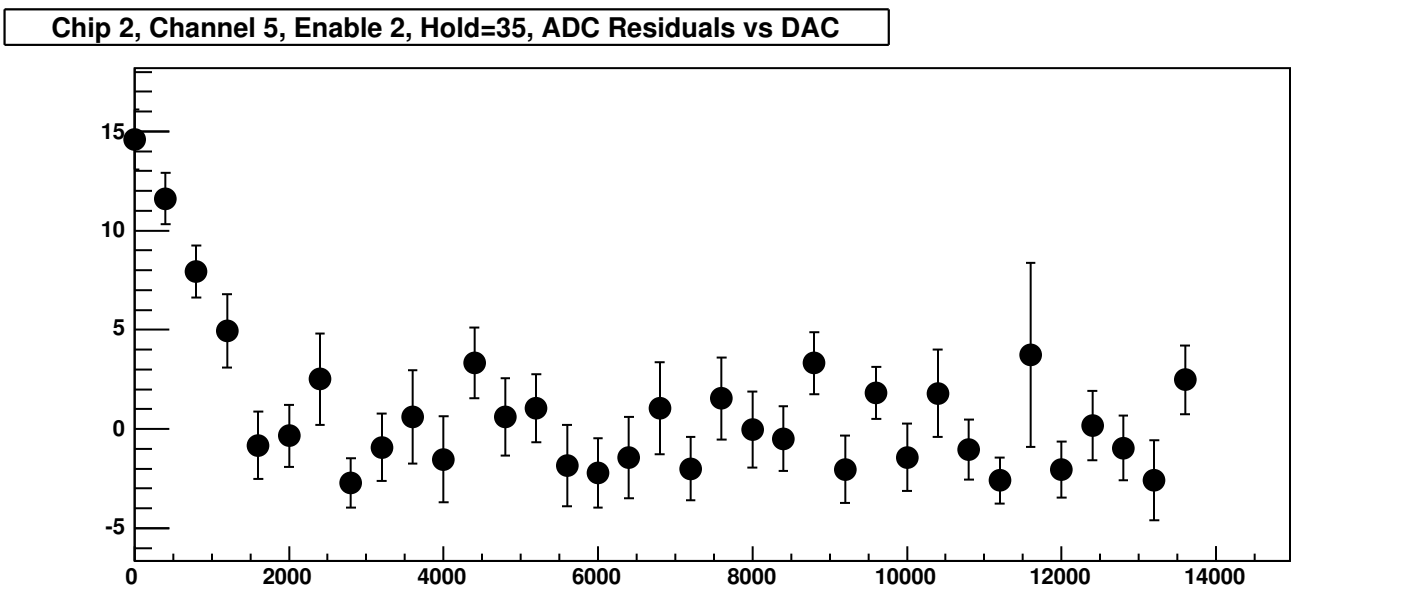

Chip 2, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 420.5 / 23
p0 26.26 ± 2.602
p1 2.179 ± 0.001305
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

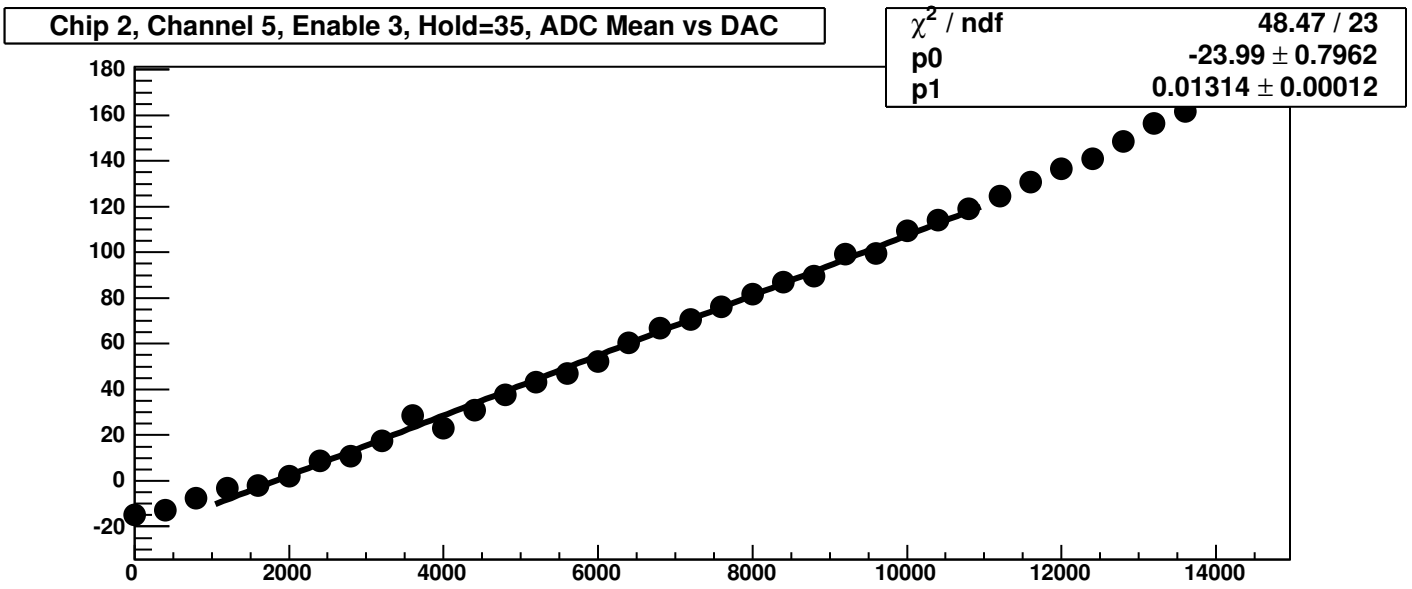

Chip 2, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

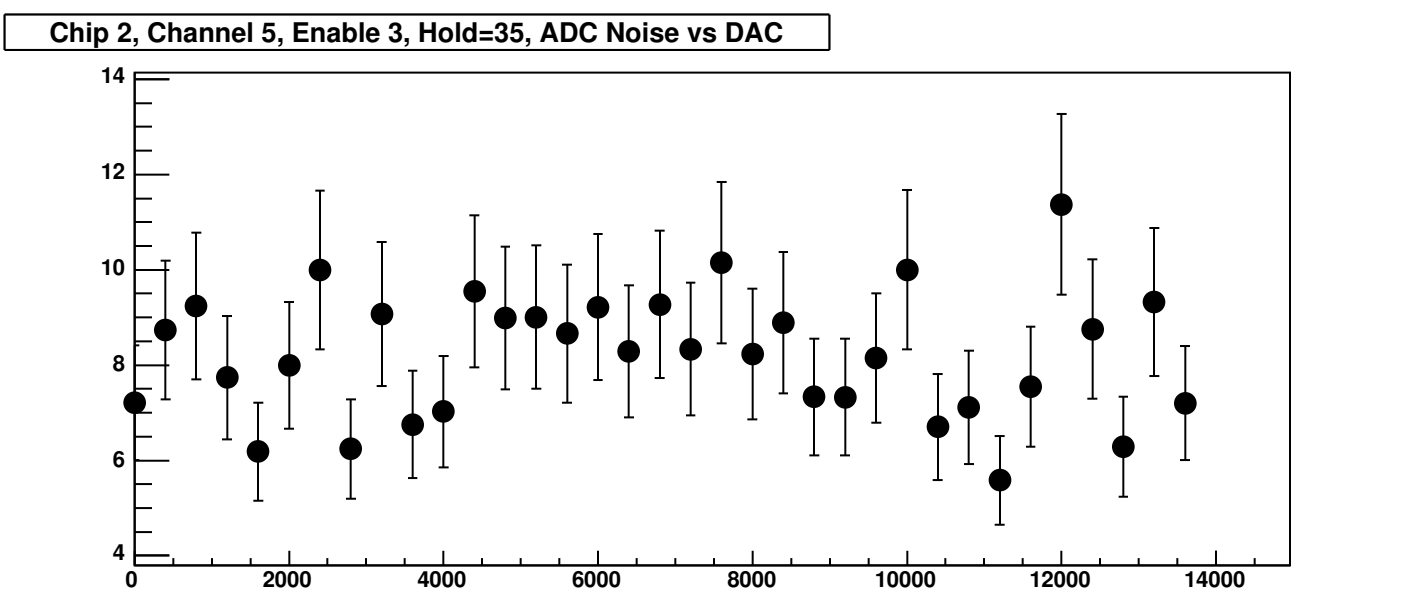

Chip 2, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

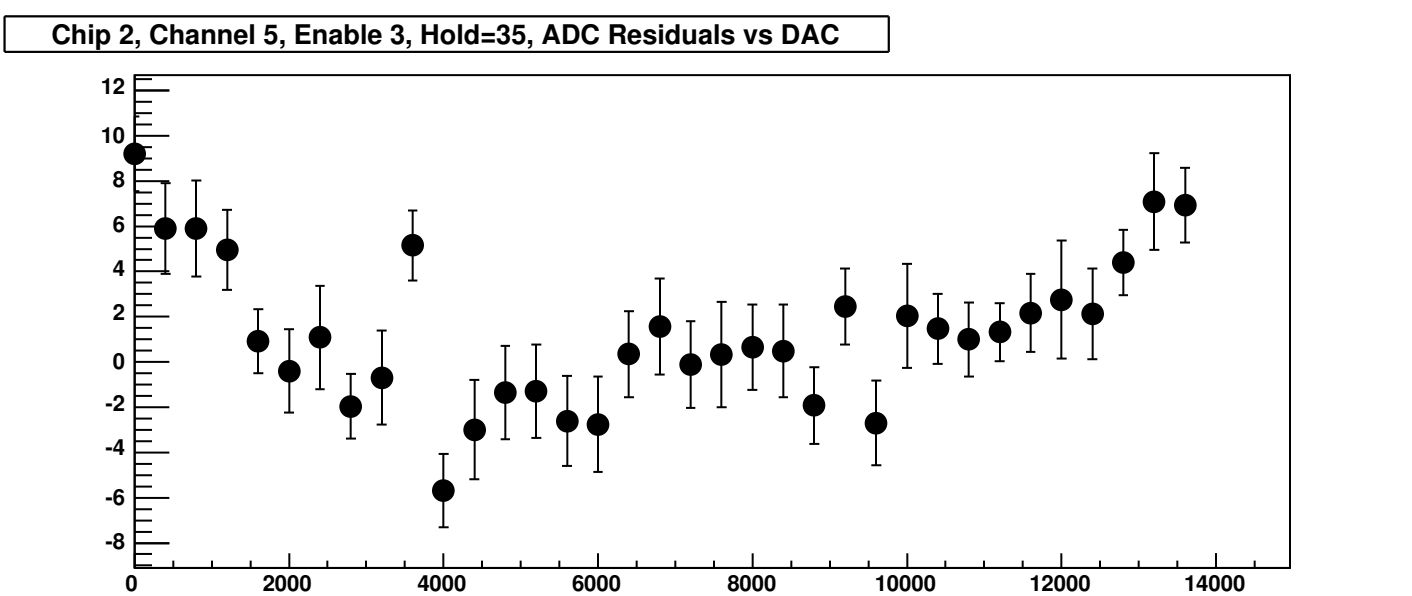

Chip 2, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 33.33 / 23
p0 -23.38 ± 0.7953
p1 0.01681 ± 0.0001167
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

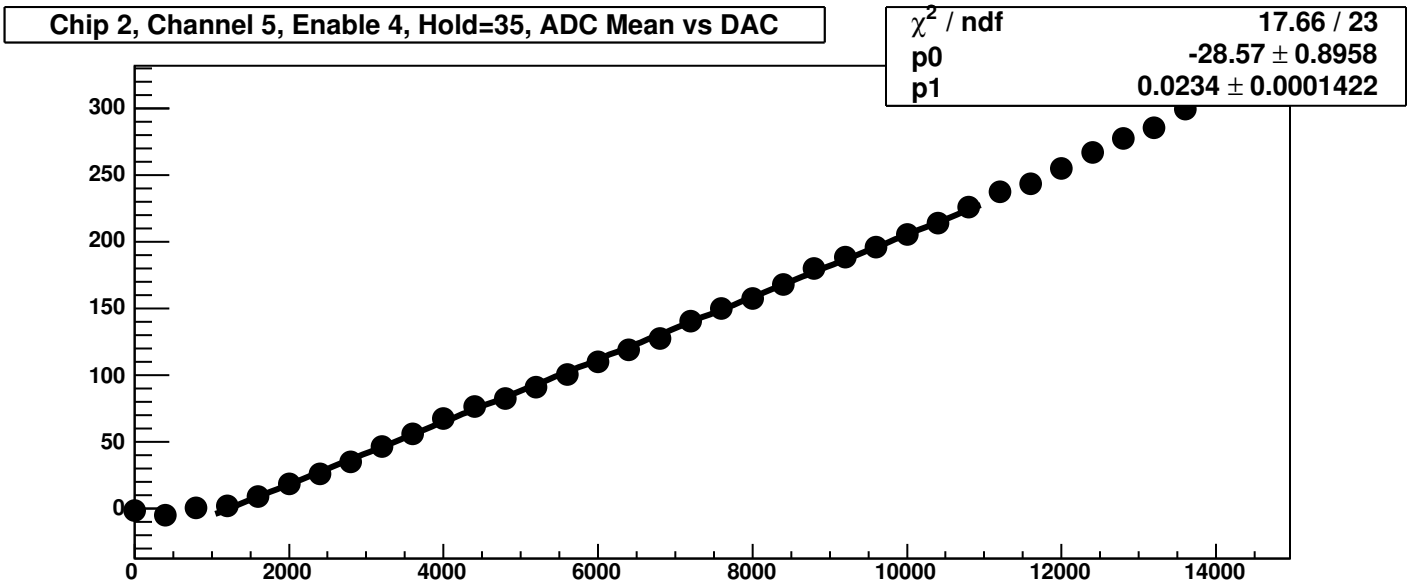

Chip 2, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

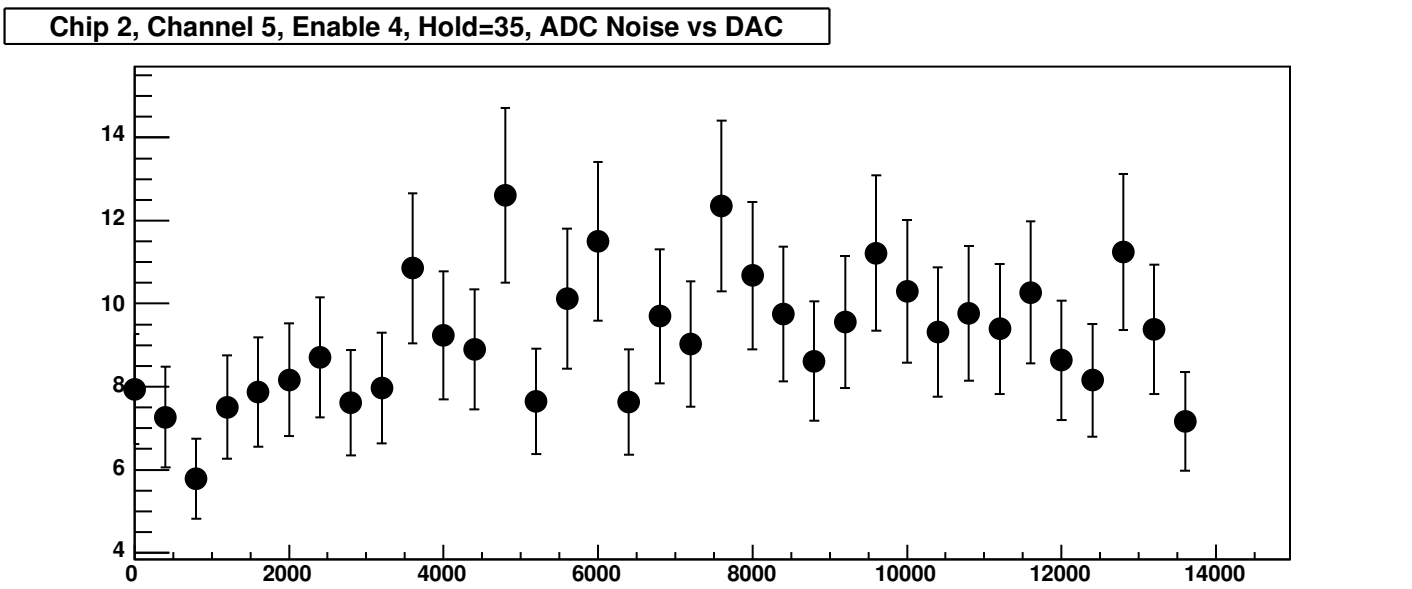

Chip 2, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

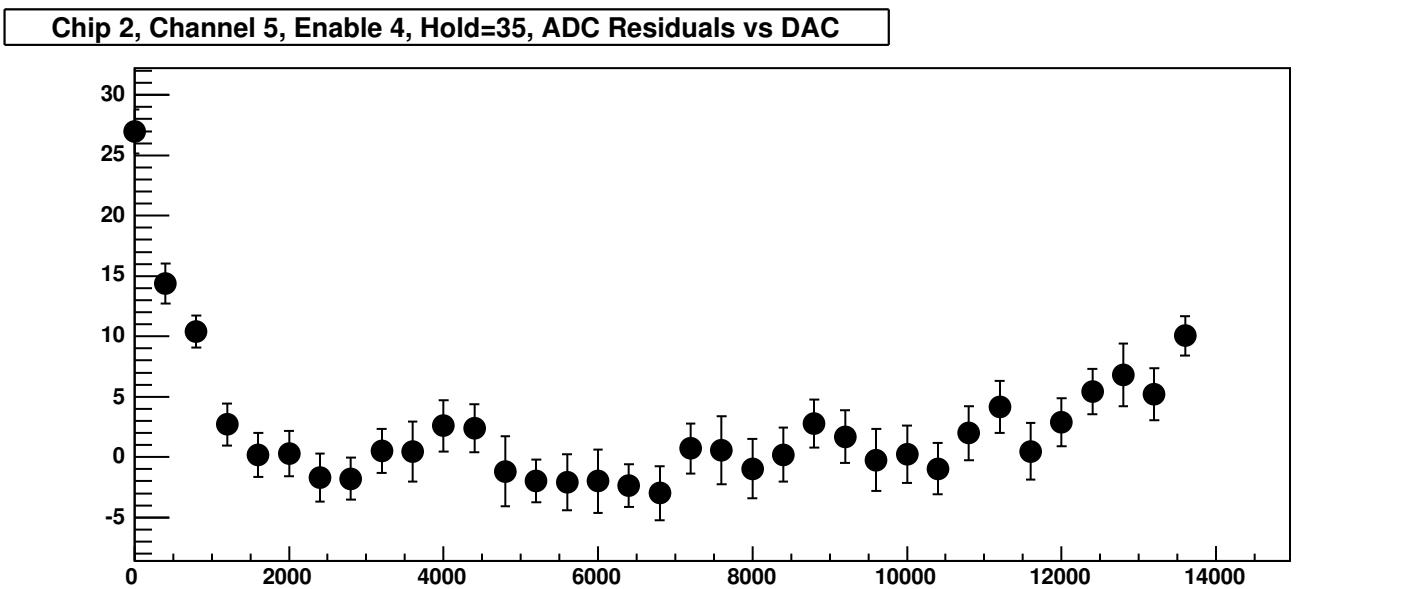

Chip 2, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 48.47 / 23
p0 -23.99 ± 0.7962
p1 0.01314 ± 0.00012
180
160
140
120
100
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000

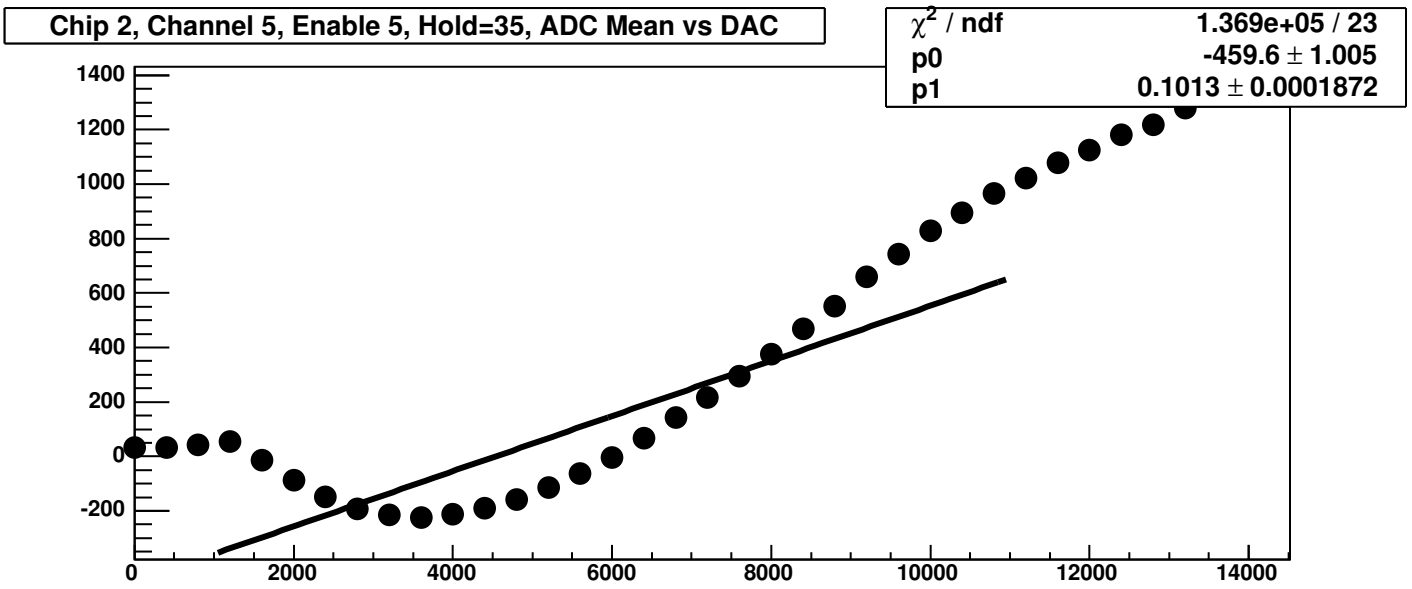

Chip 2, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

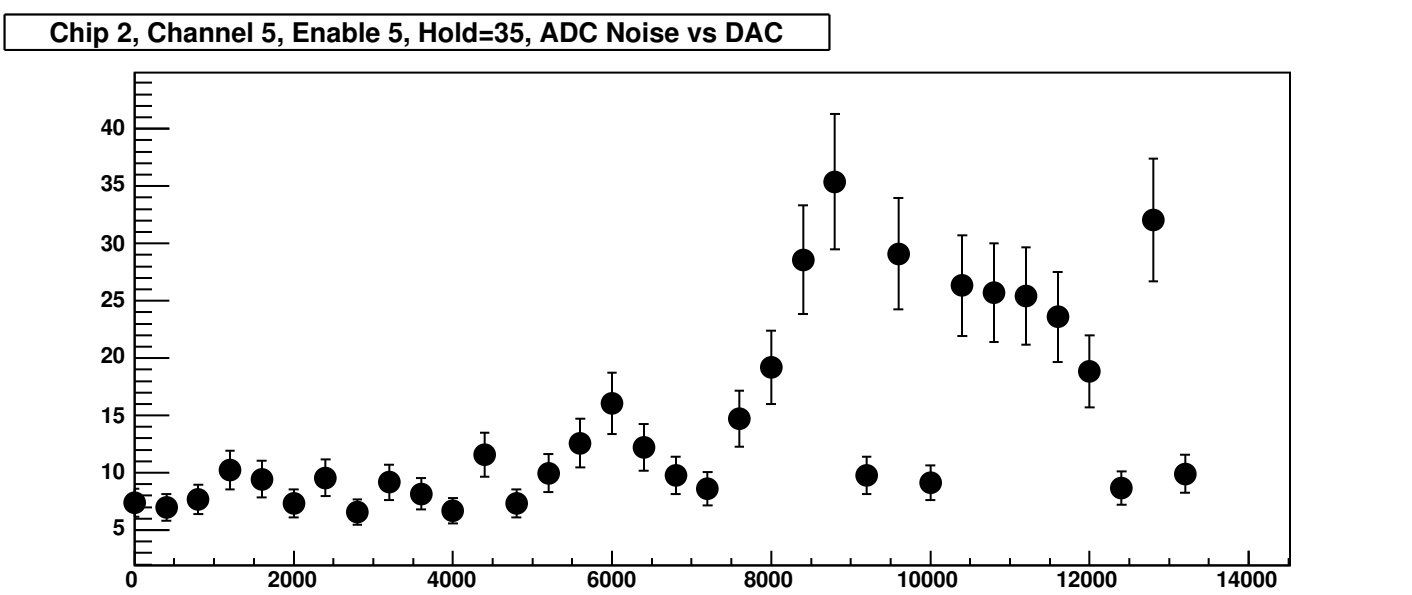

Chip 2, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

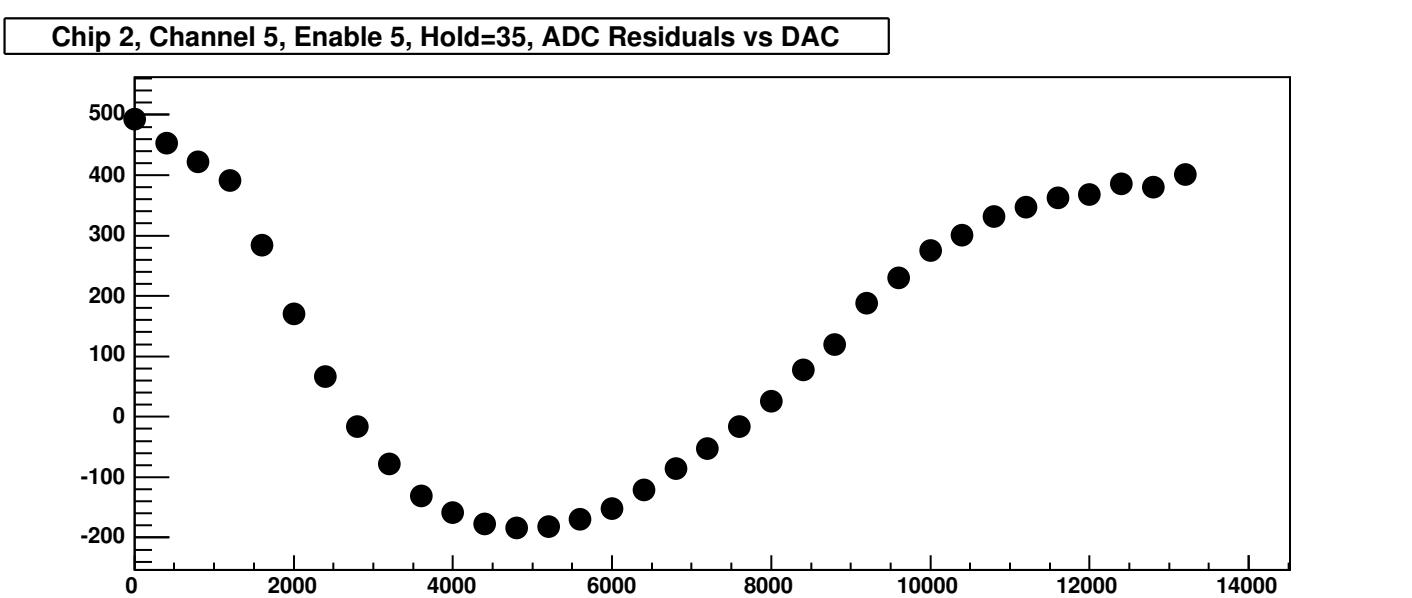

Chip 2, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 17.66 / 23
p0 -28.57 ± 0.8958
p1 0.0234 ± 0.0001422
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

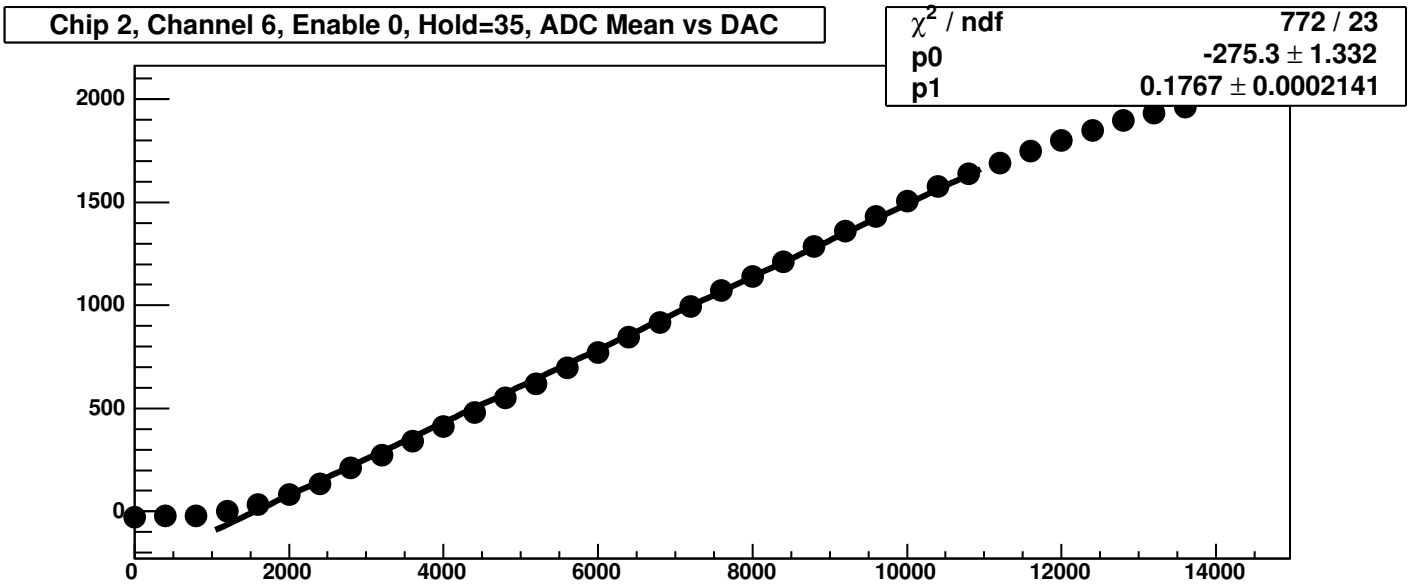

Chip 2, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

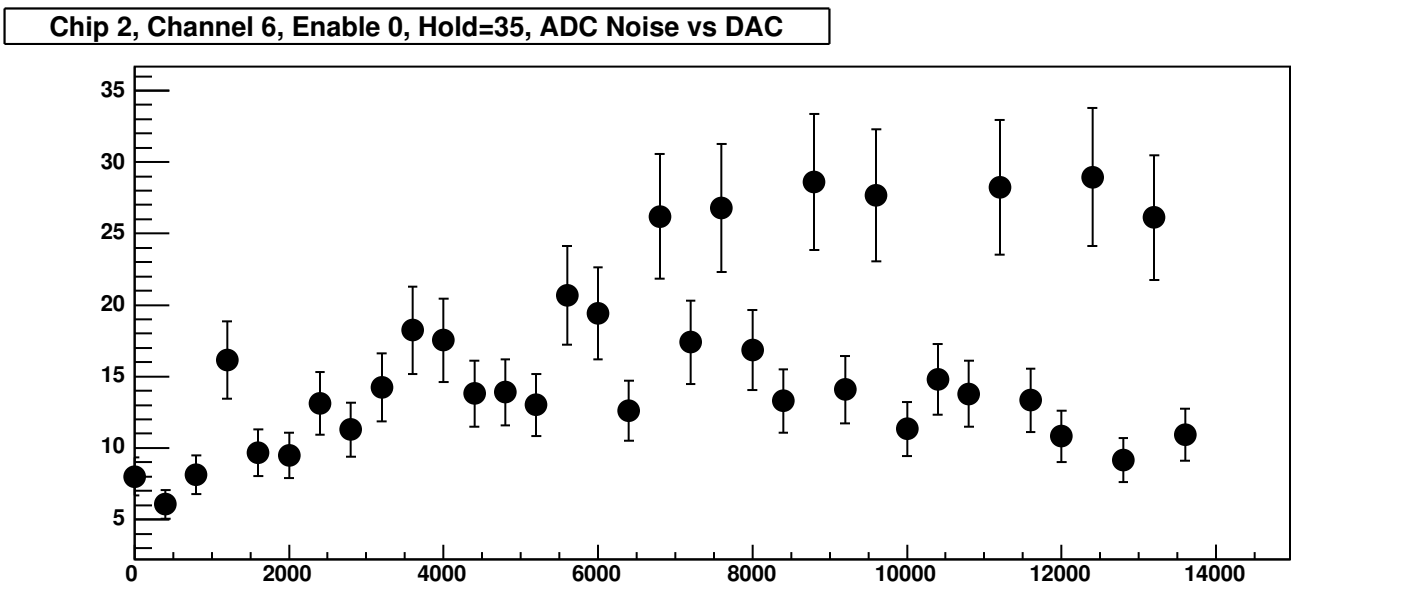

Chip 2, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

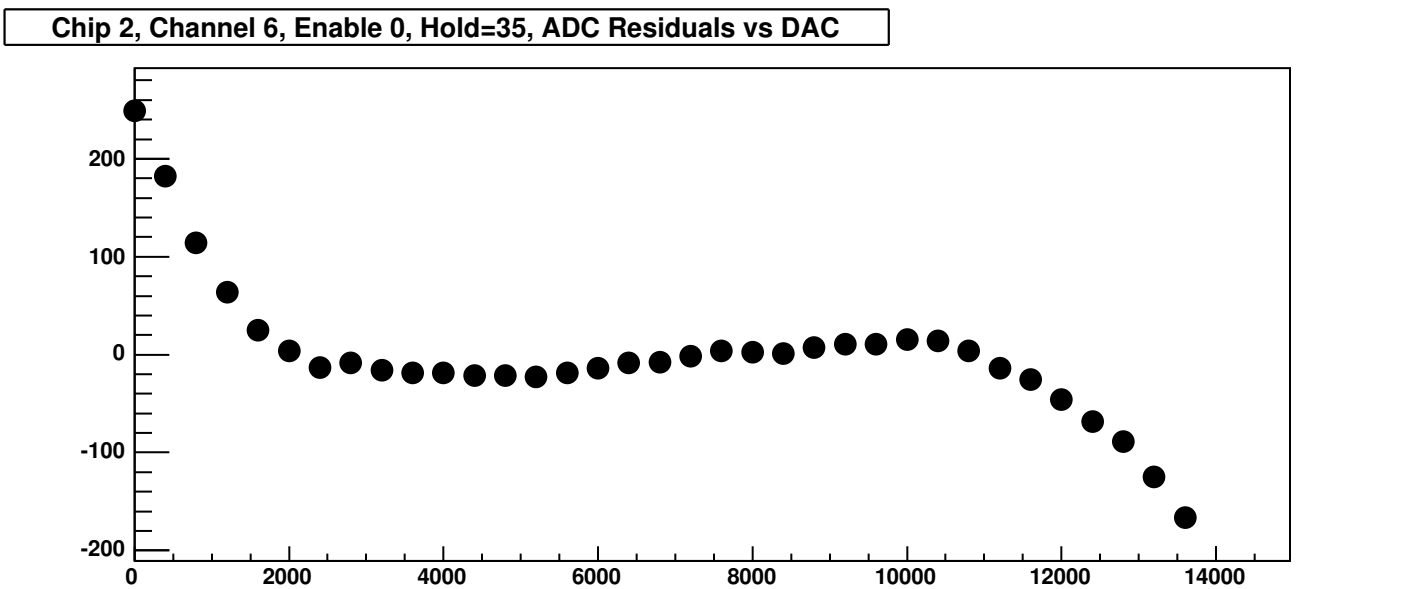

Chip 2, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.369e+05 / 23
p0 -459.6 ± 1.005
p1 0.1013 ± 0.0001872
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

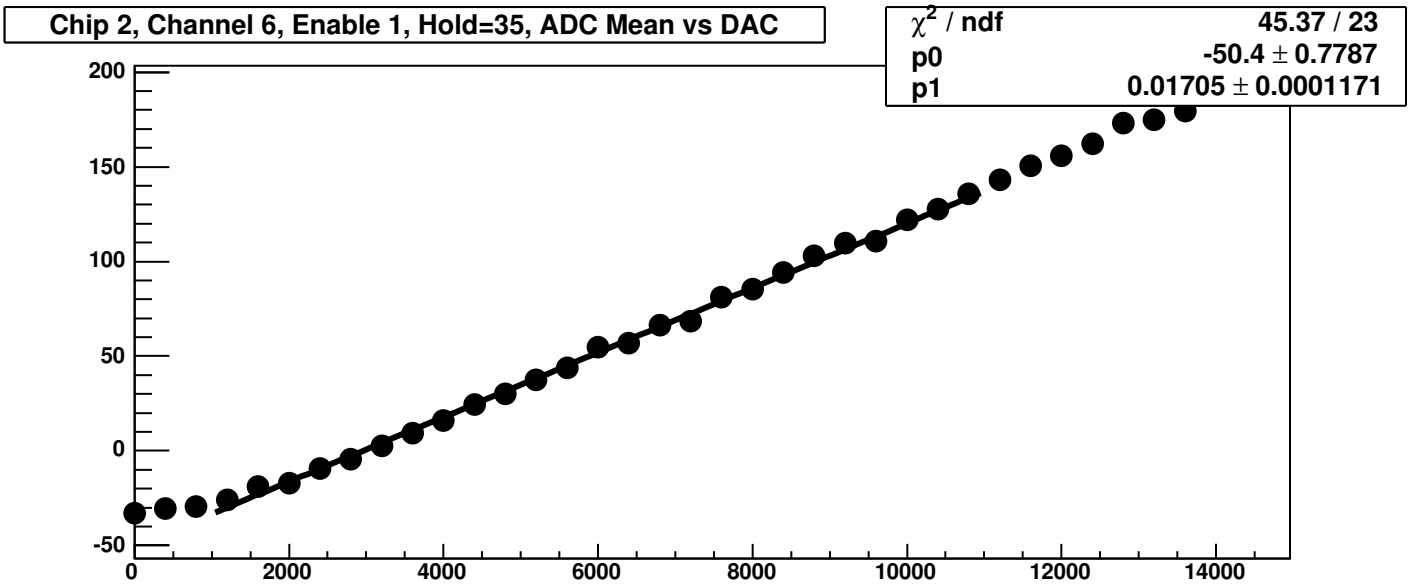

Chip 2, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

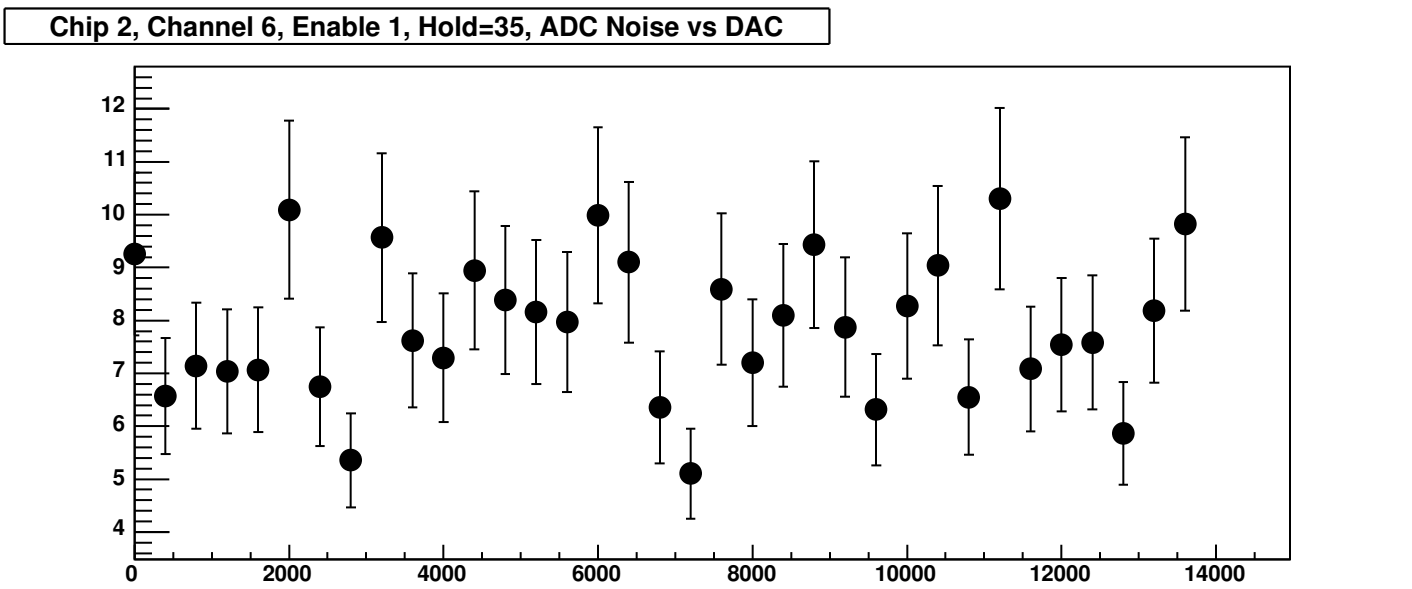

Chip 2, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

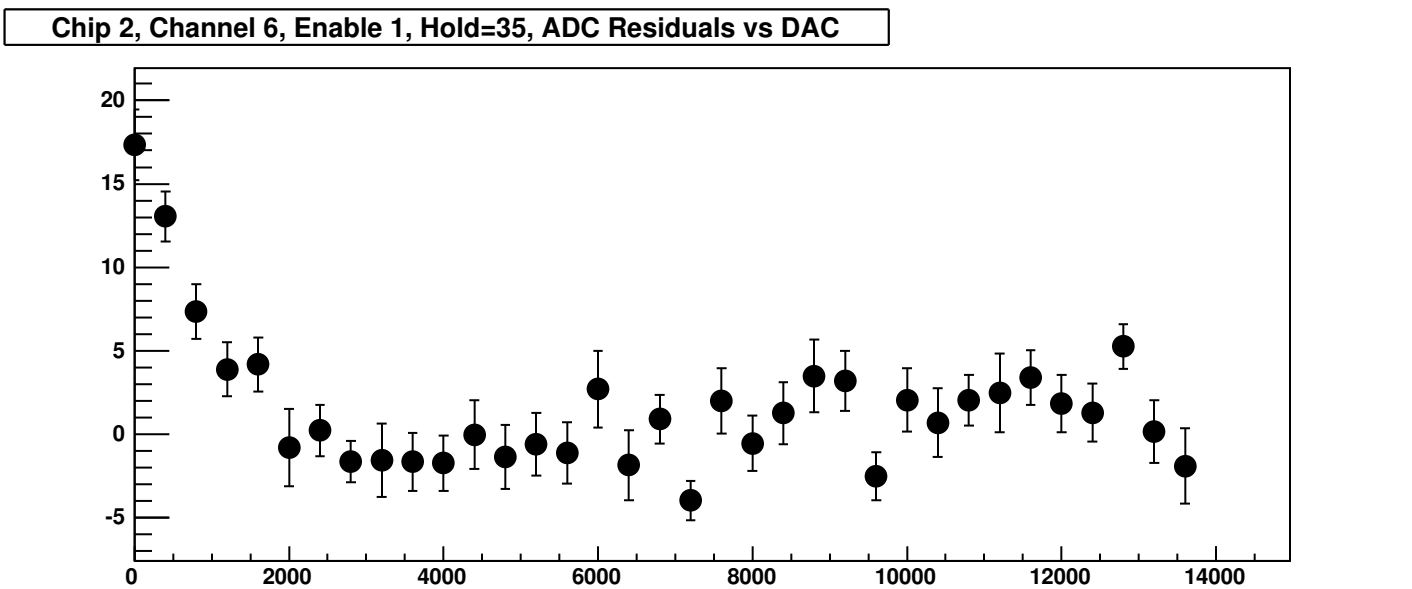

Chip 2, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 772 / 23
p0 -275.3 ± 1.332
p1 0.1767 ± 0.0002141
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

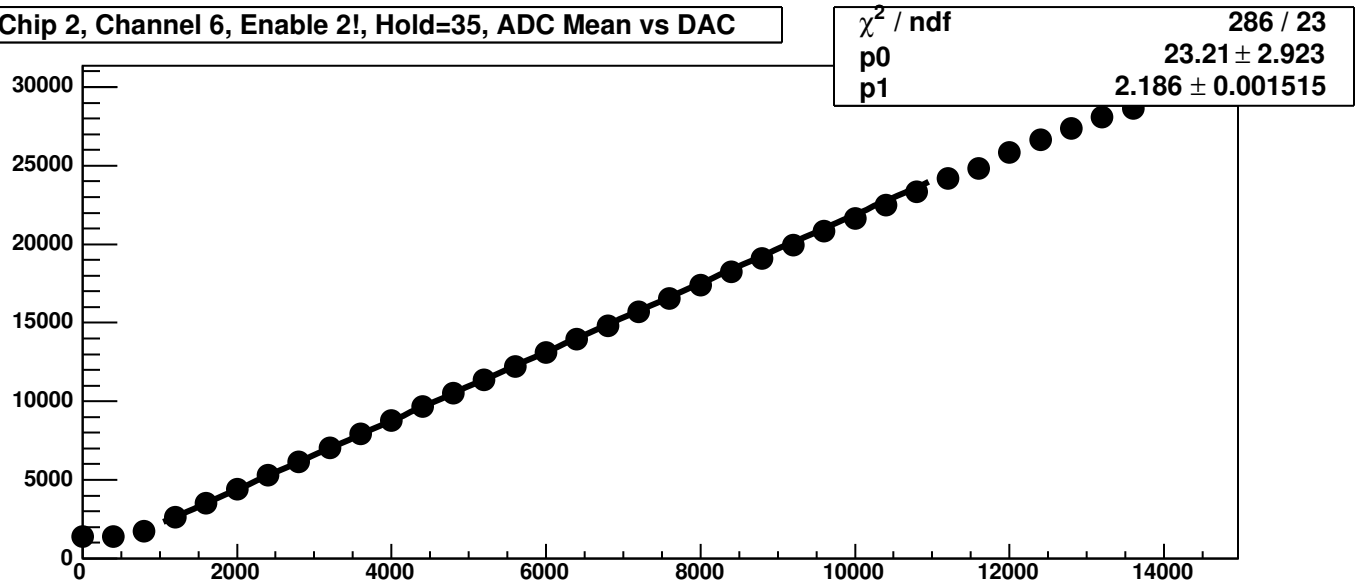

Chip 2, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 45.37 / 23
p0 -50.4 ± 0.7787
p1 0.01705 ± 0.0001171
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

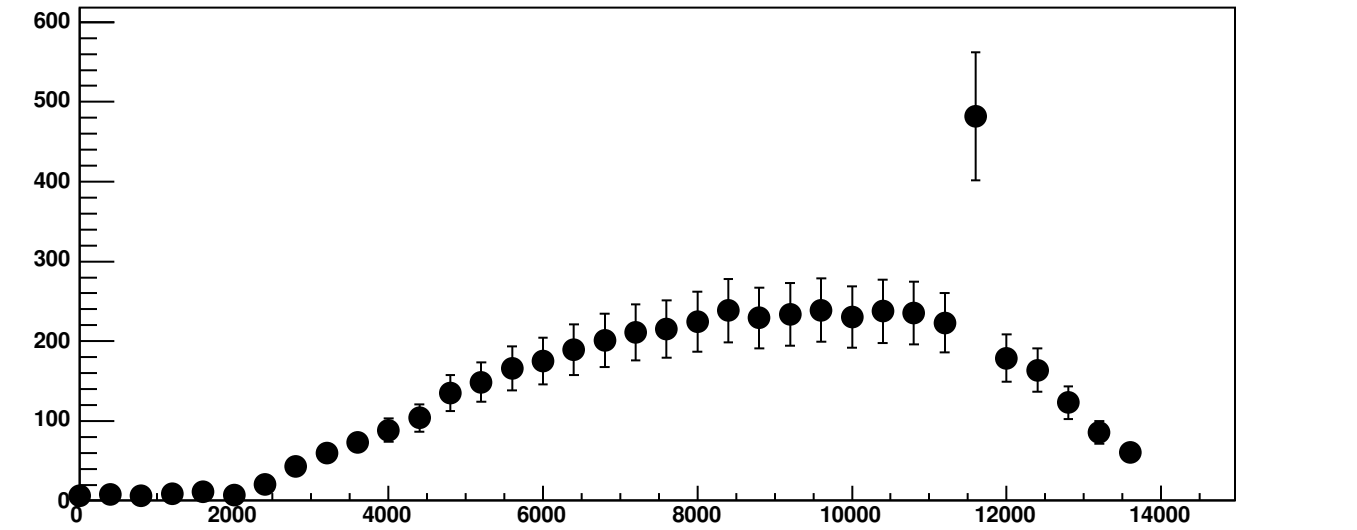

Chip 2, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

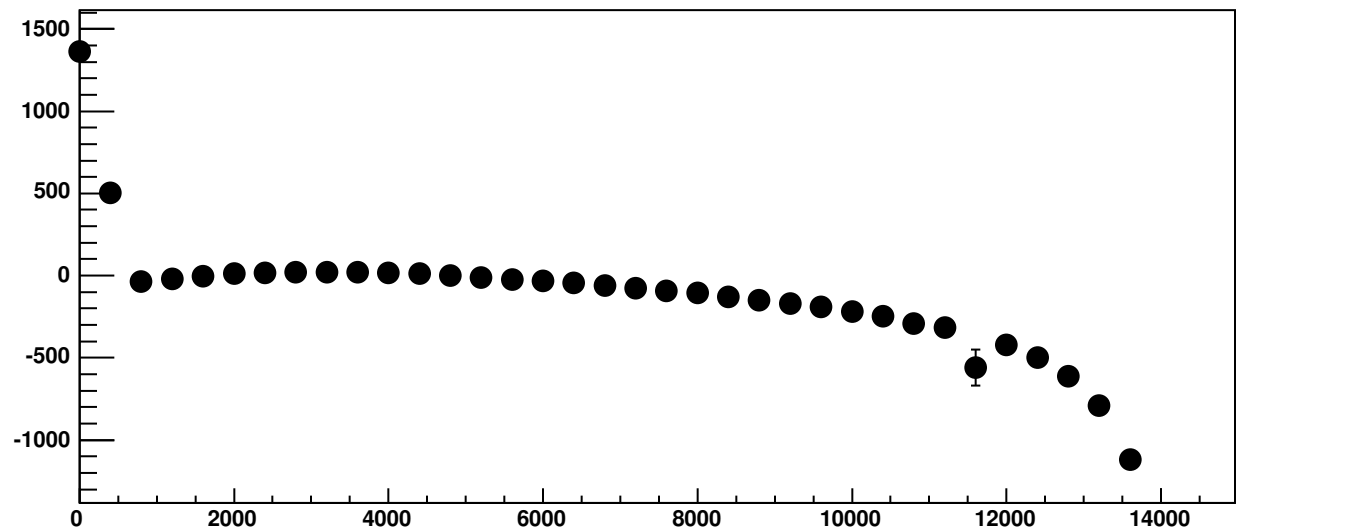
Chip 2, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC

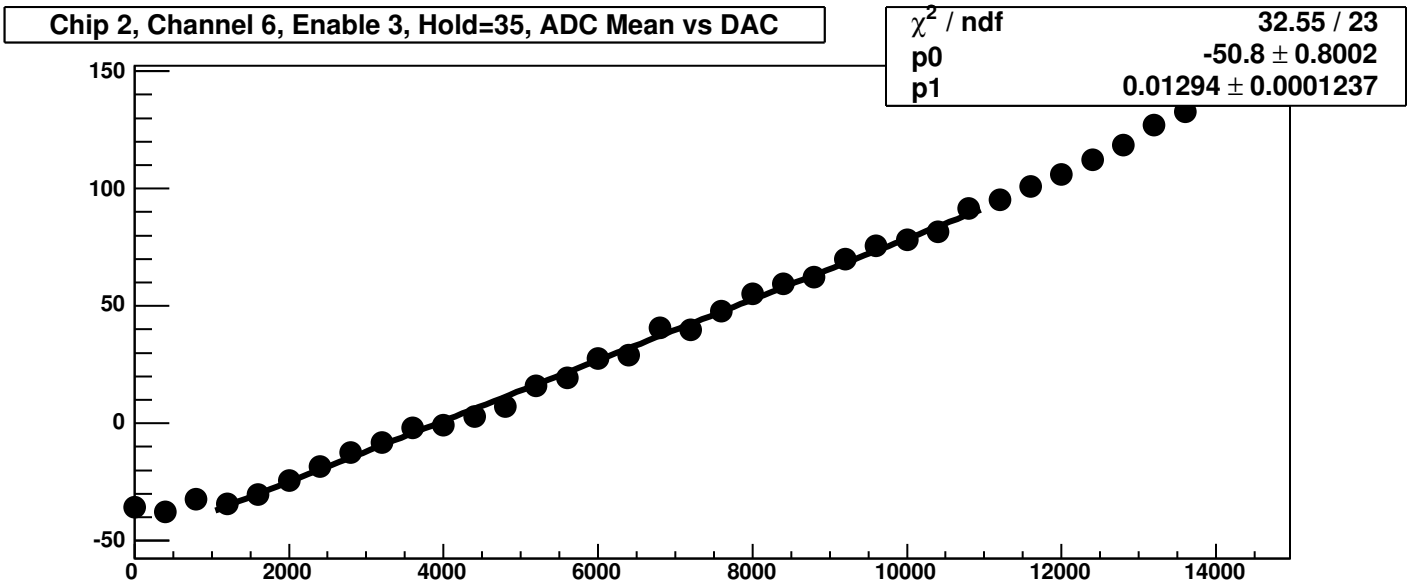


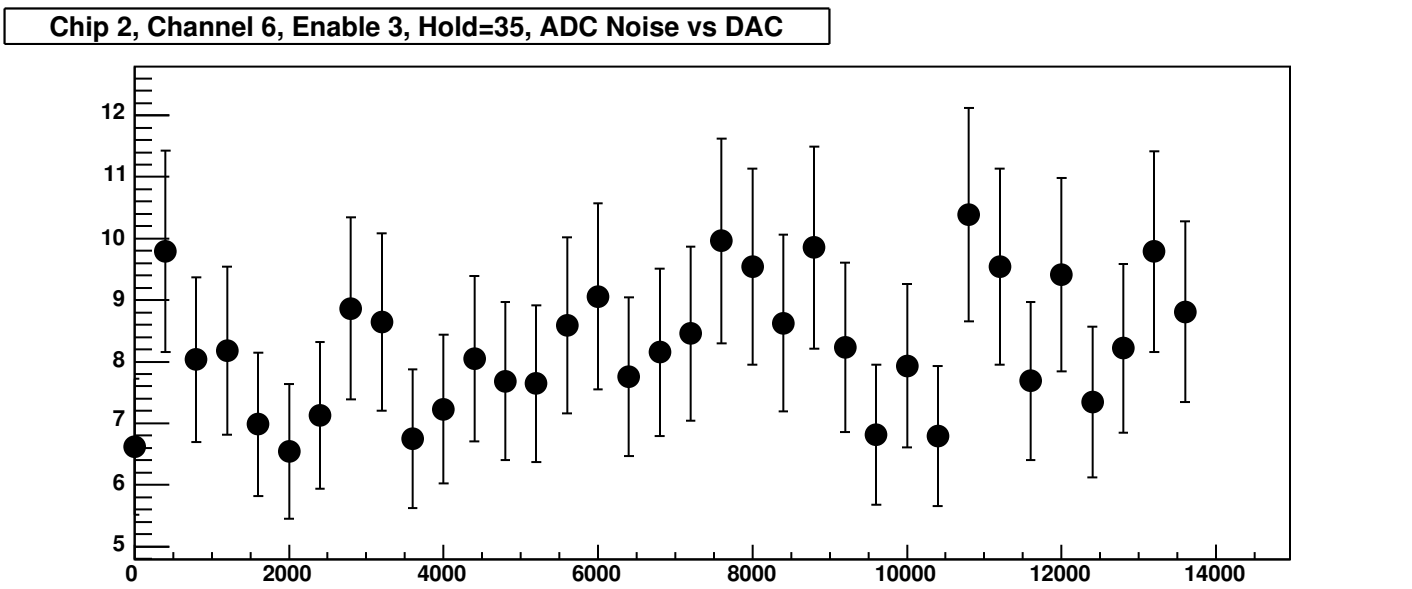
Chip 2, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC

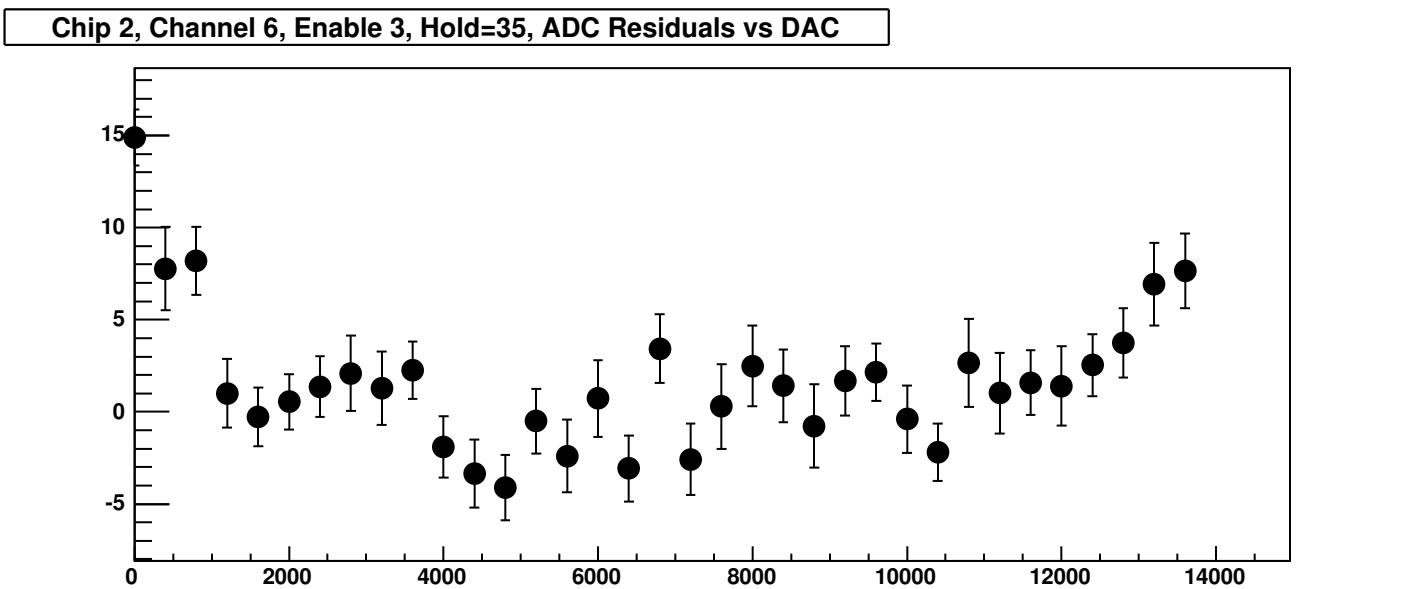


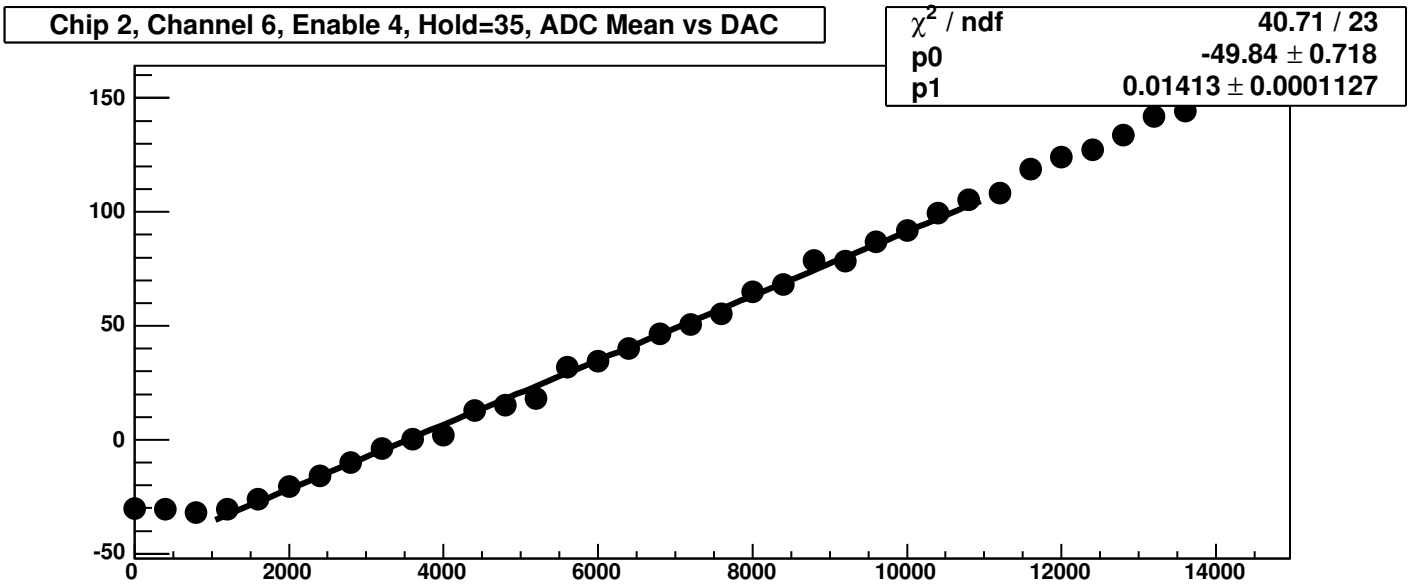
Chip 2, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC

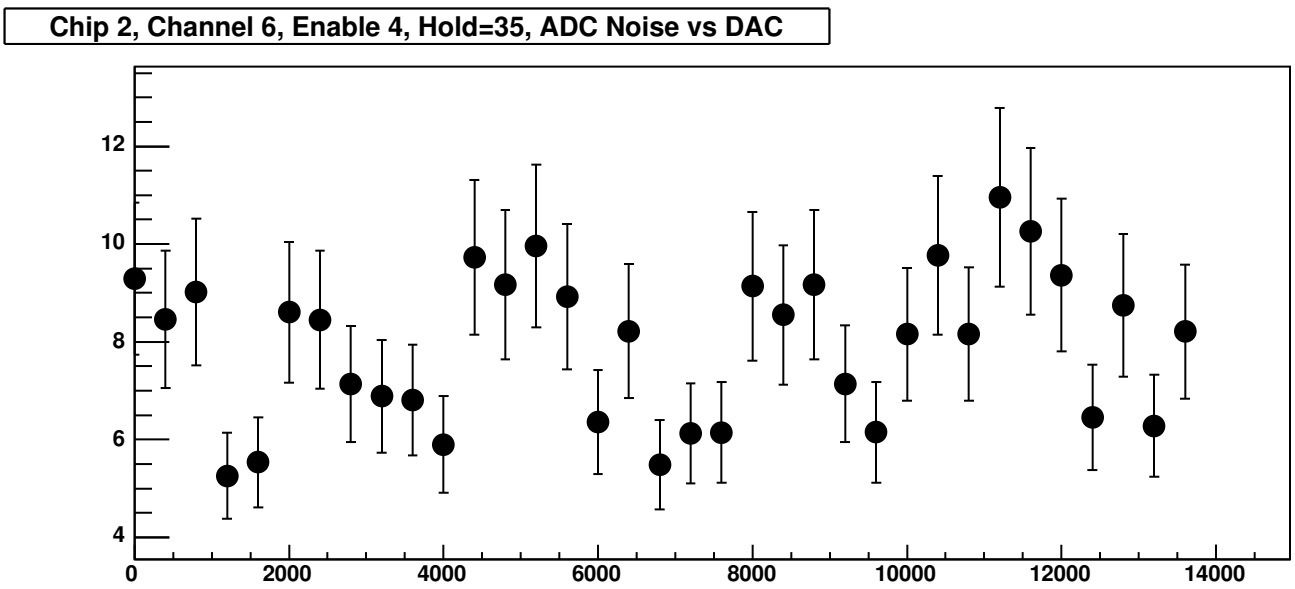


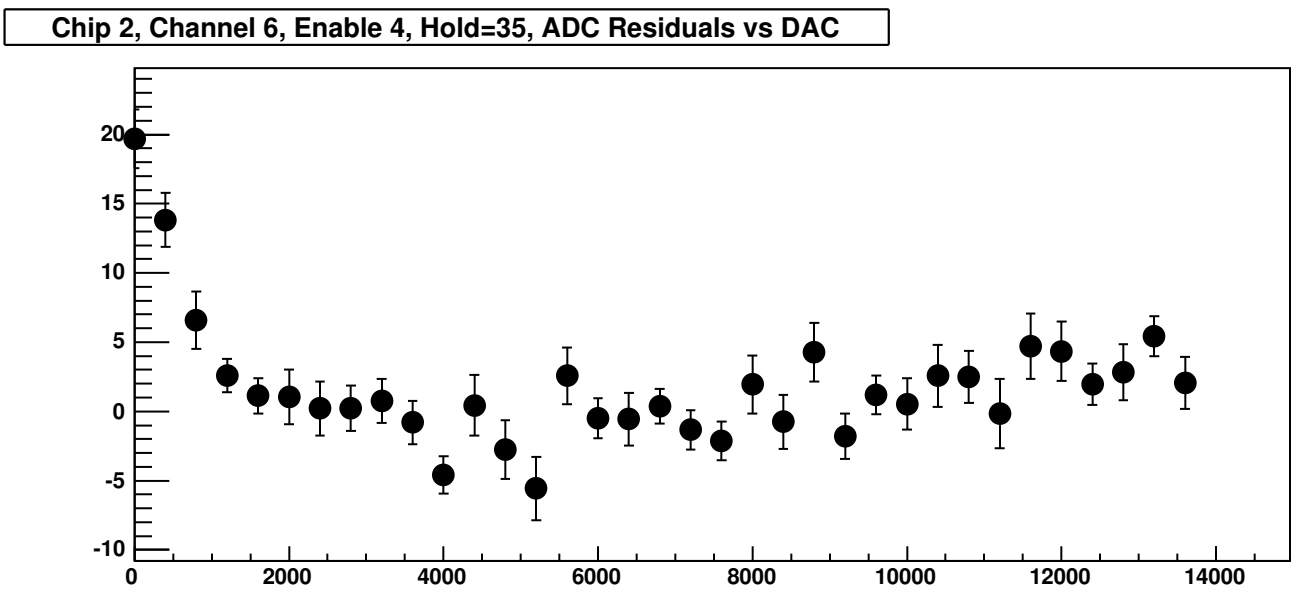

Chip 2, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 32.55 / 23
p0 -50.8 ± 0.8002
p1 0.01294 ± 0.0001237
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

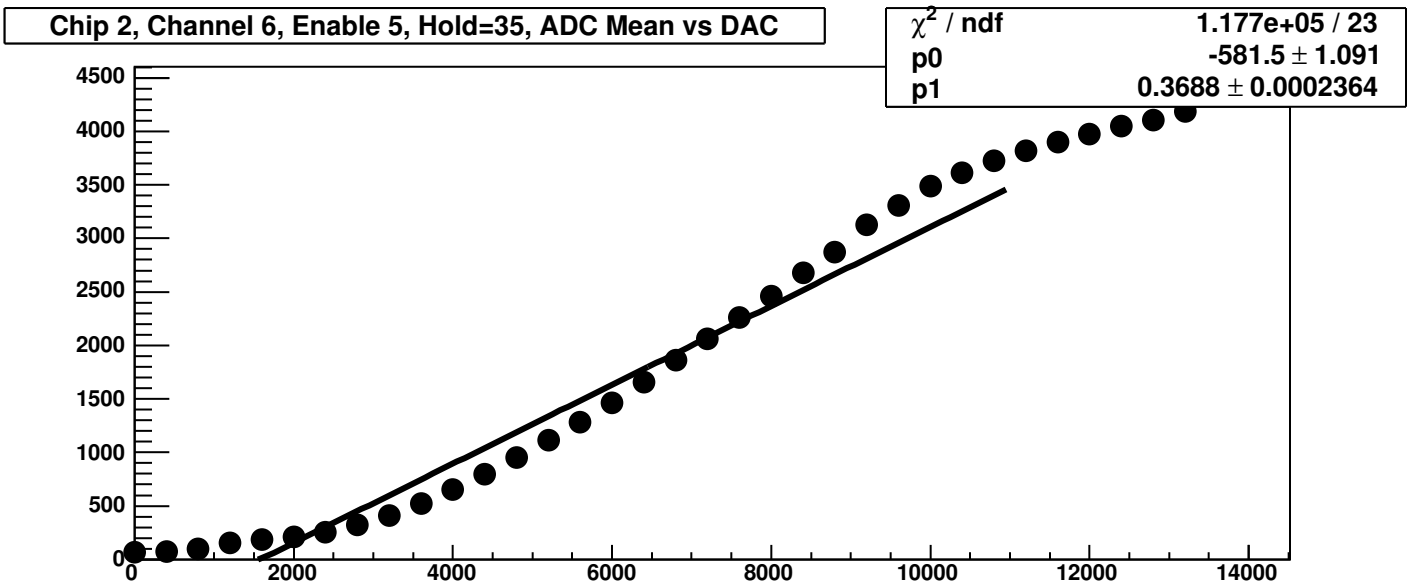

Chip 2, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

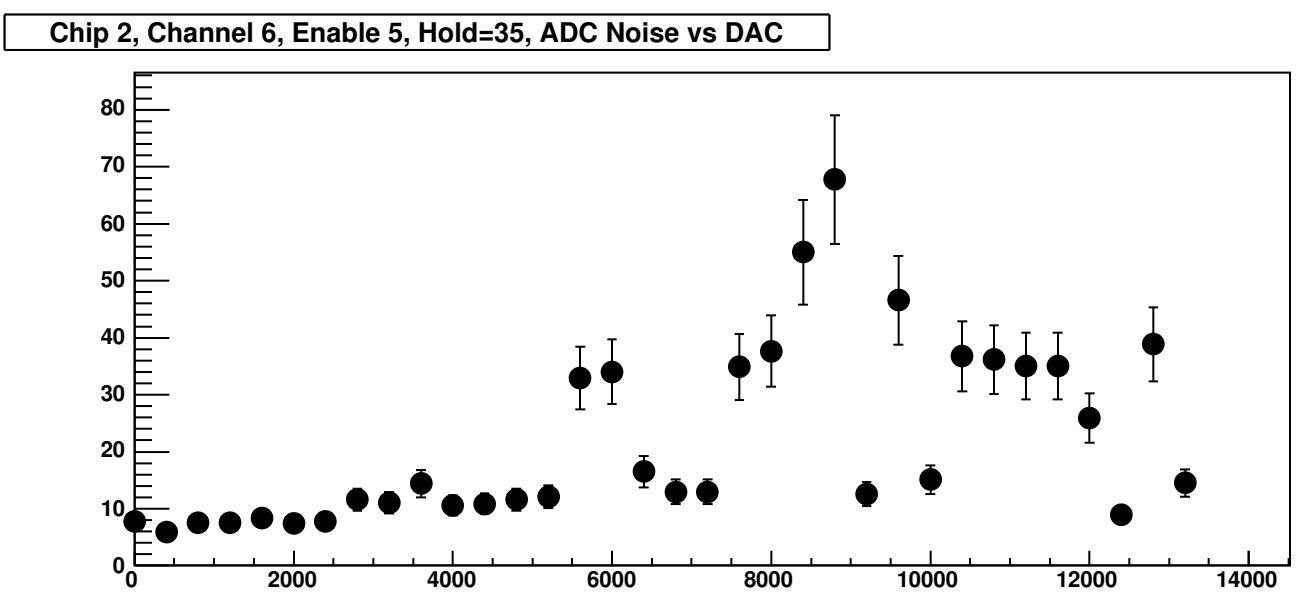

Chip 2, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

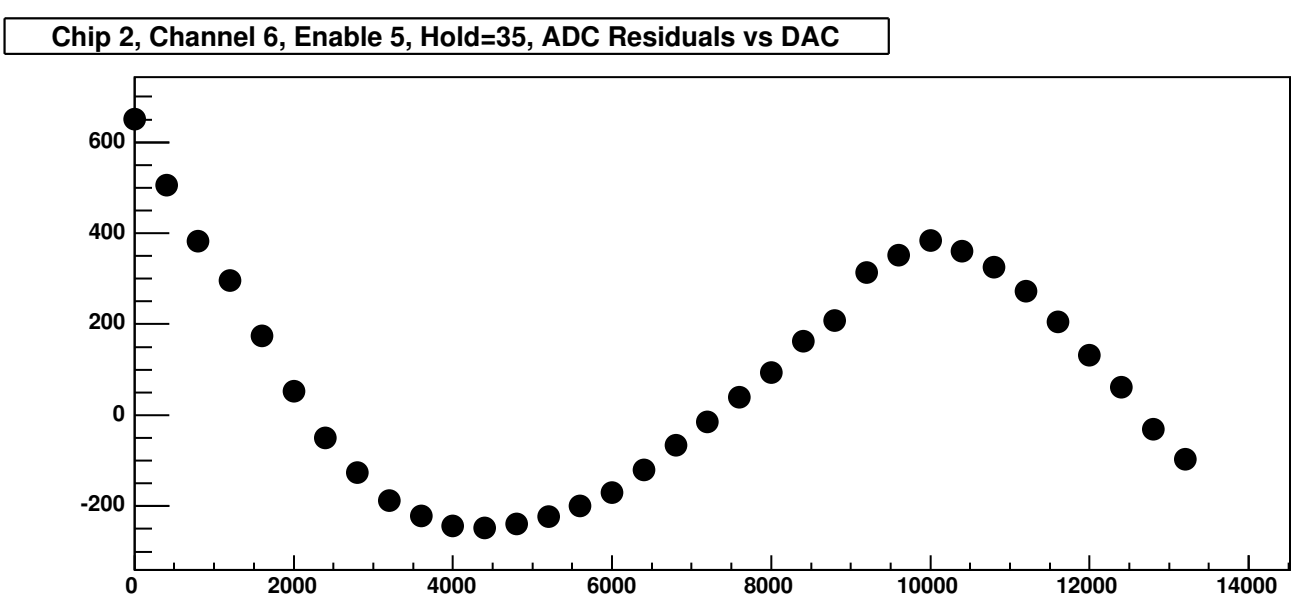

Chip 2, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 40.71 / 23
p0 -49.84 ± 0.718
p1 0.01413 ± 0.0001127
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

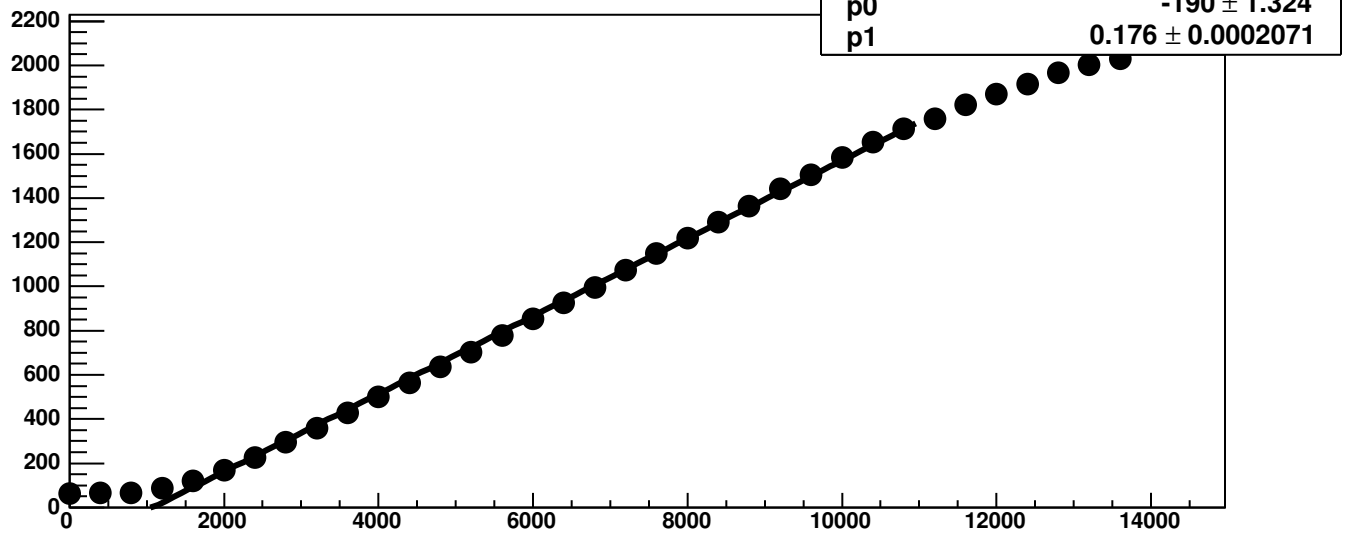

Chip 2, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.177e+05 / 23
p0 -581.5 ± 1.091
p1 0.3688 ± 0.0002364
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

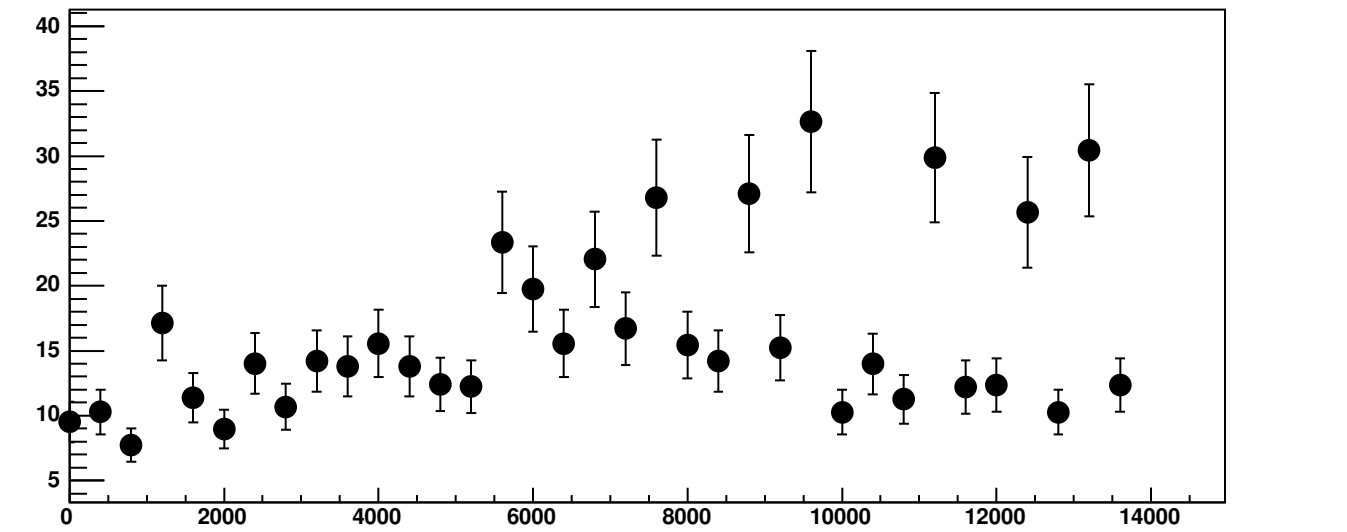

Chip 2, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

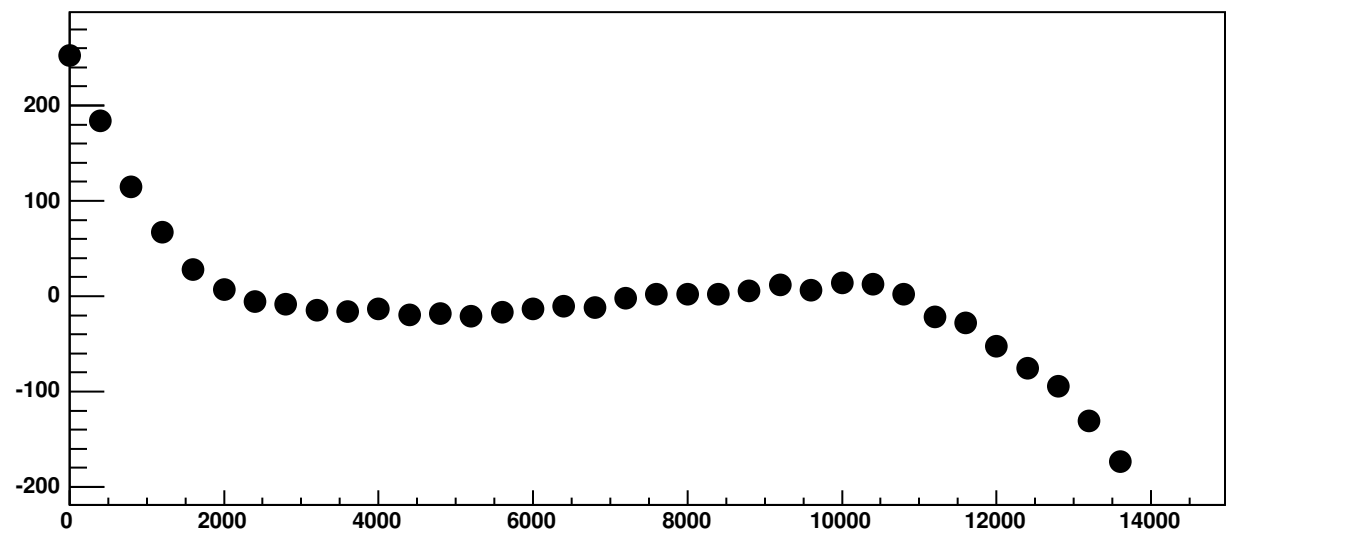
Chip 2, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

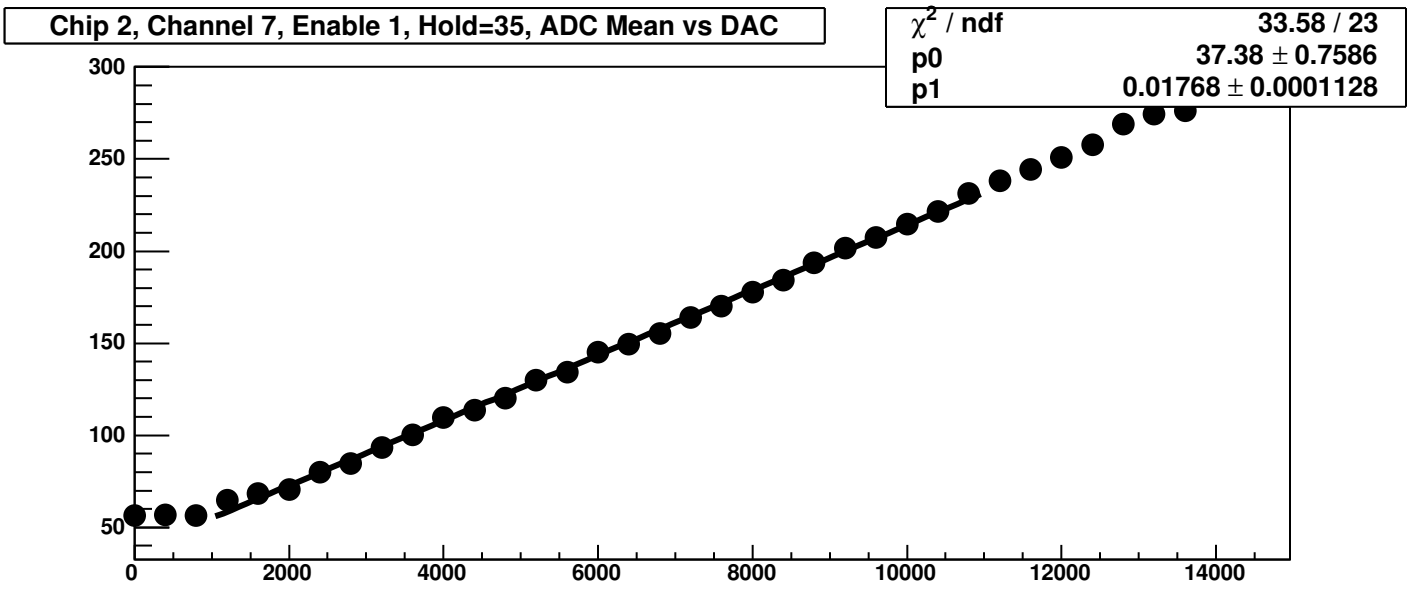


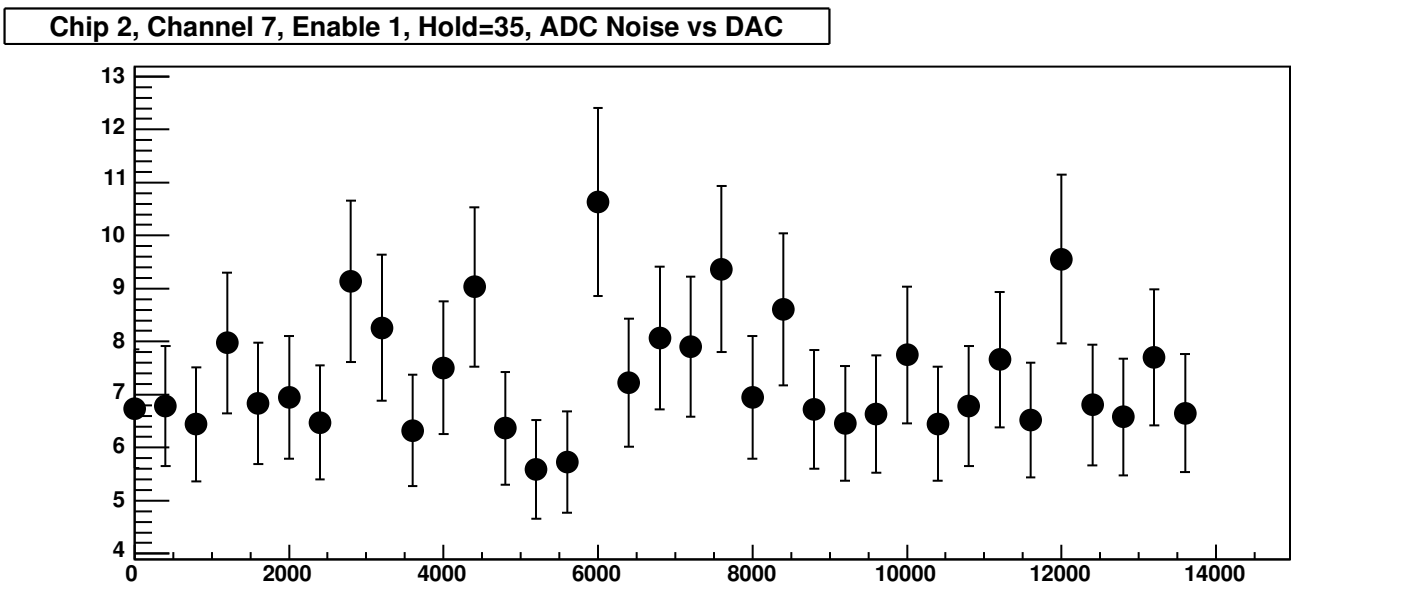
Chip 2, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC

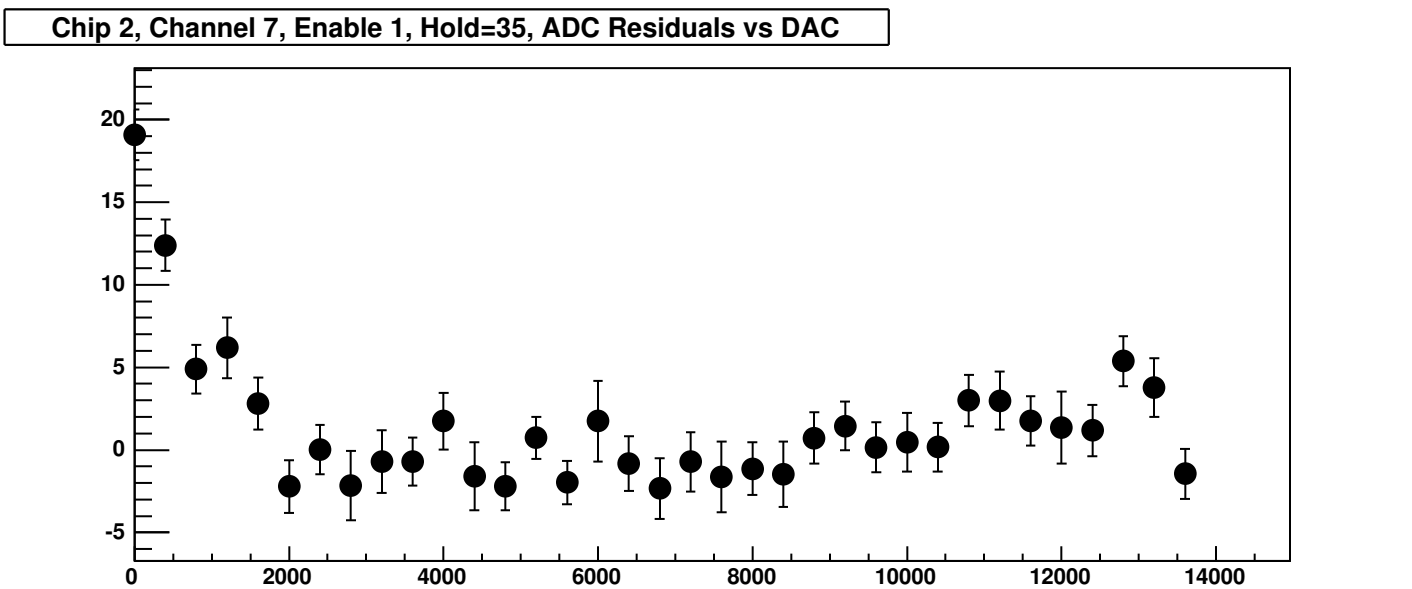


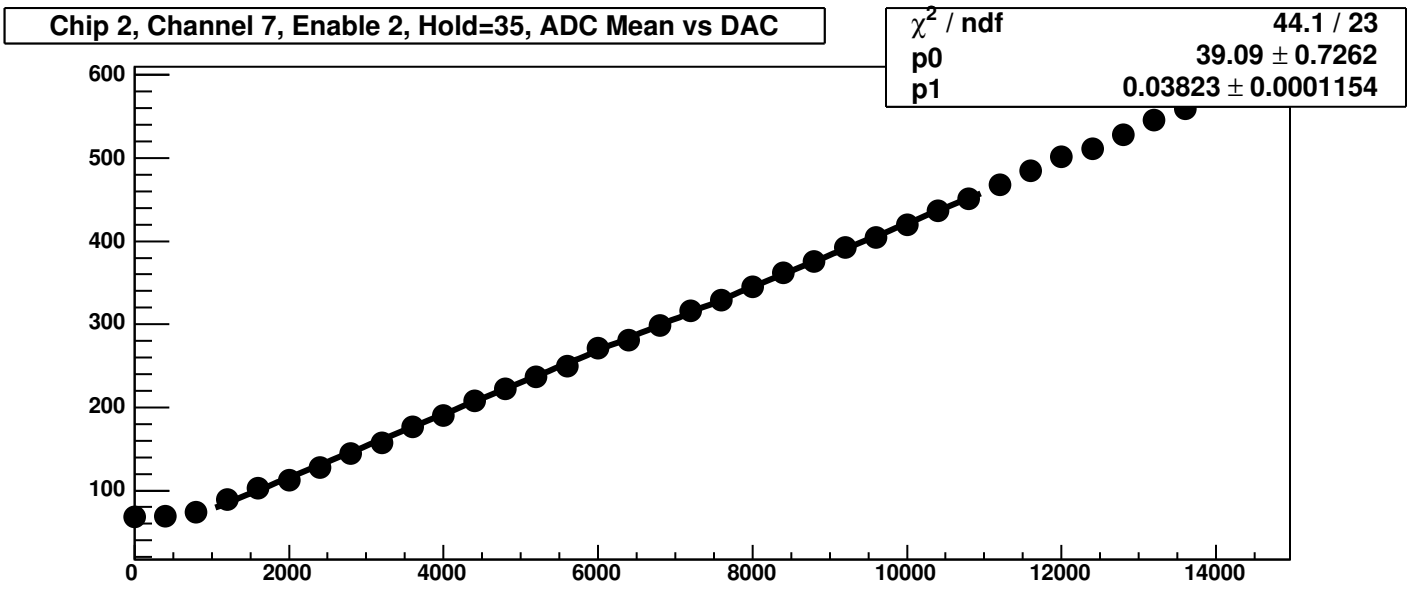
Chip 2, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

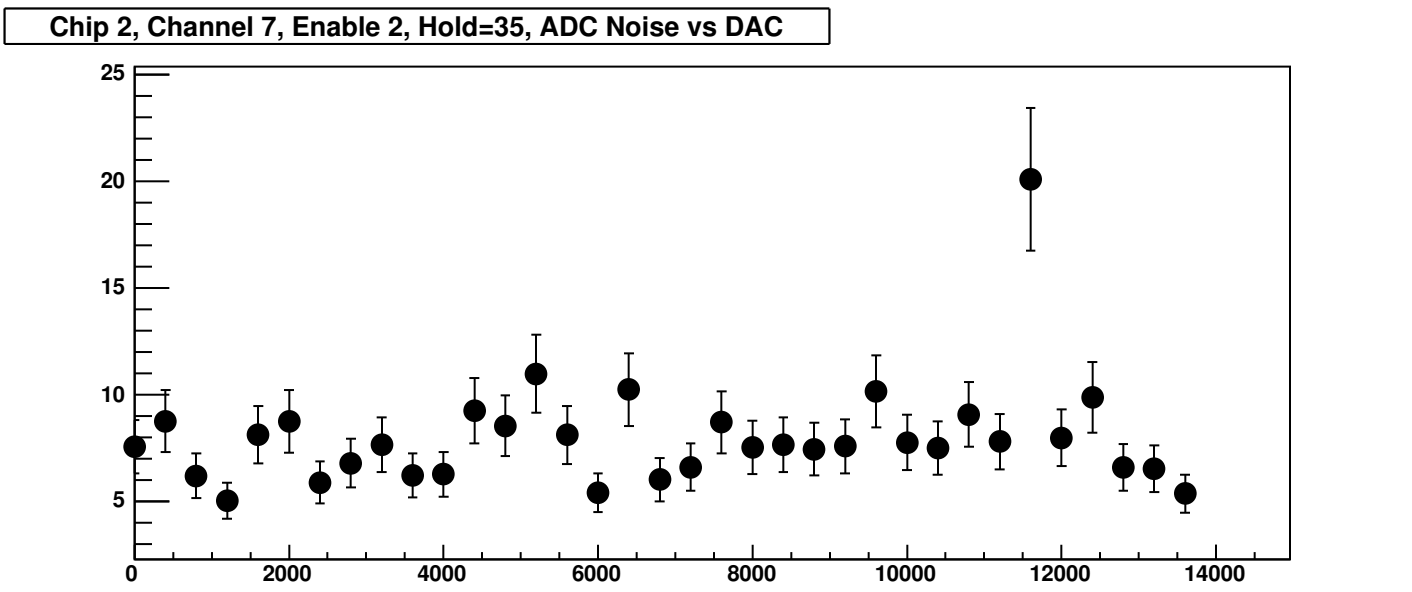


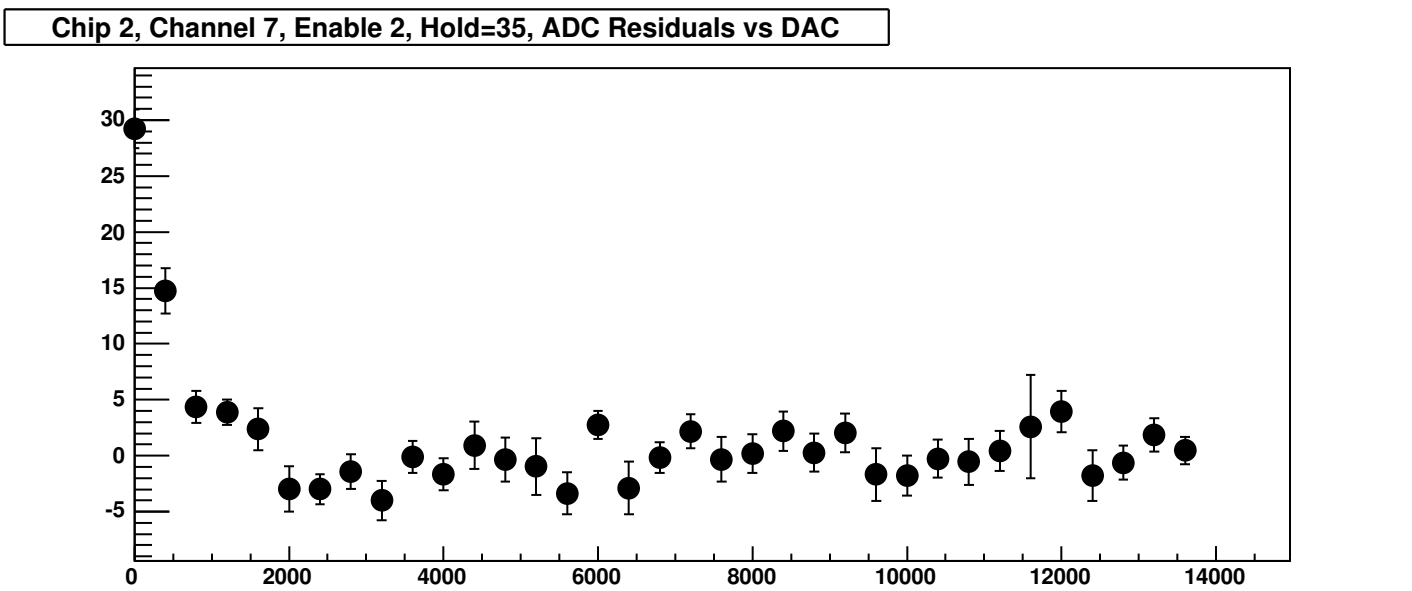

Chip 2, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.58 / 23
p0 37.38 ± 0.7586
p1 0.01768 ± 0.0001128
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

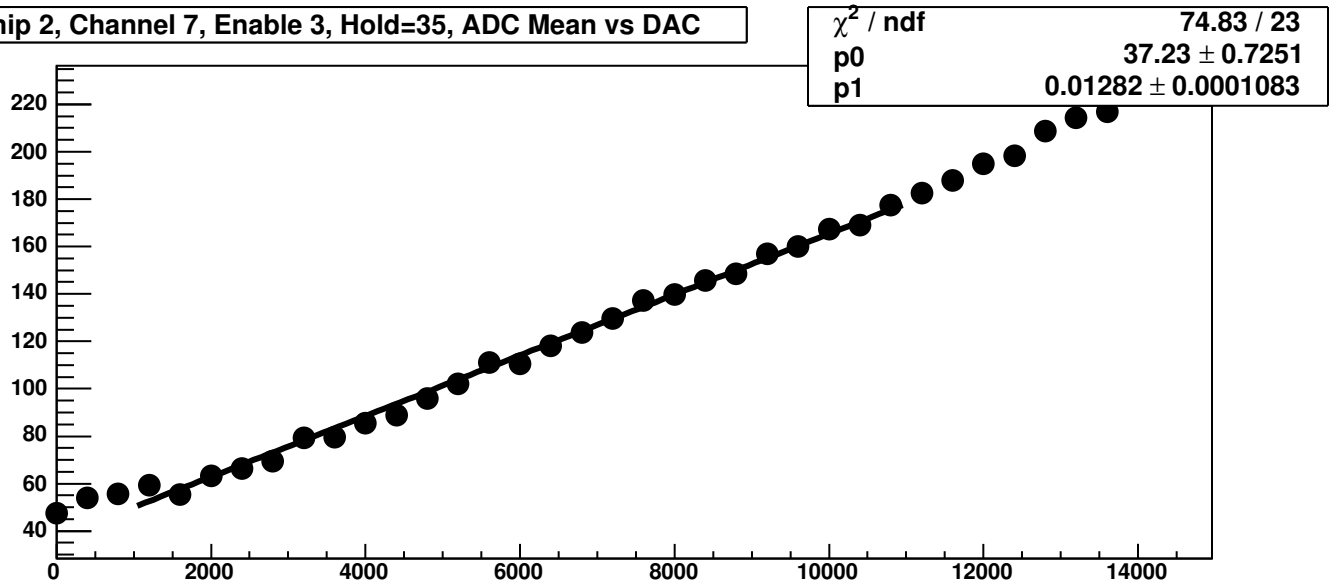

Chip 2, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 44.1 / 23
p0 39.09 ± 0.7262
p1 0.03823 ± 0.0001154
600
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000

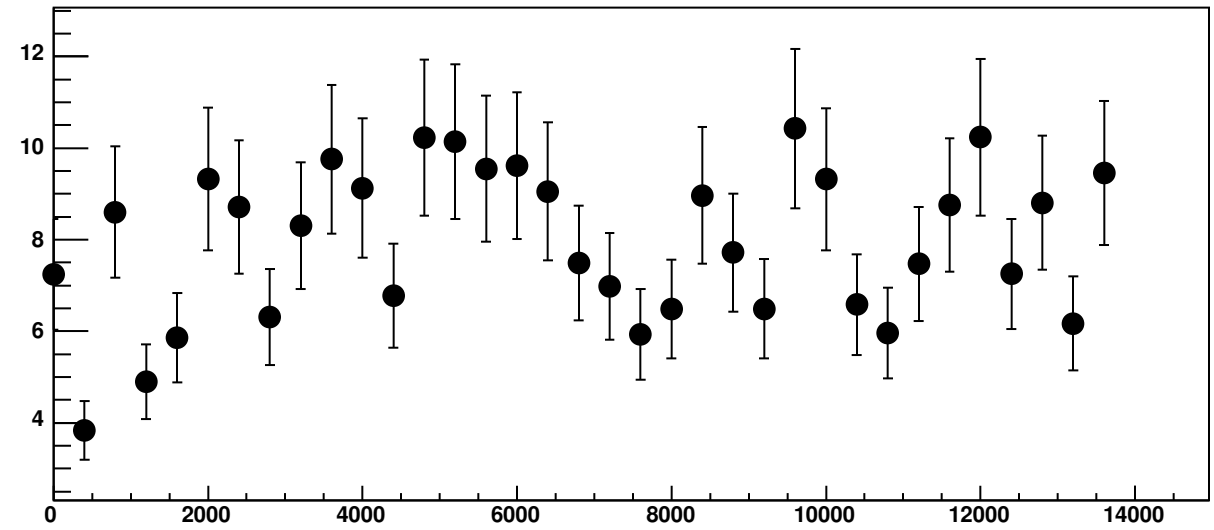

Chip 2, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

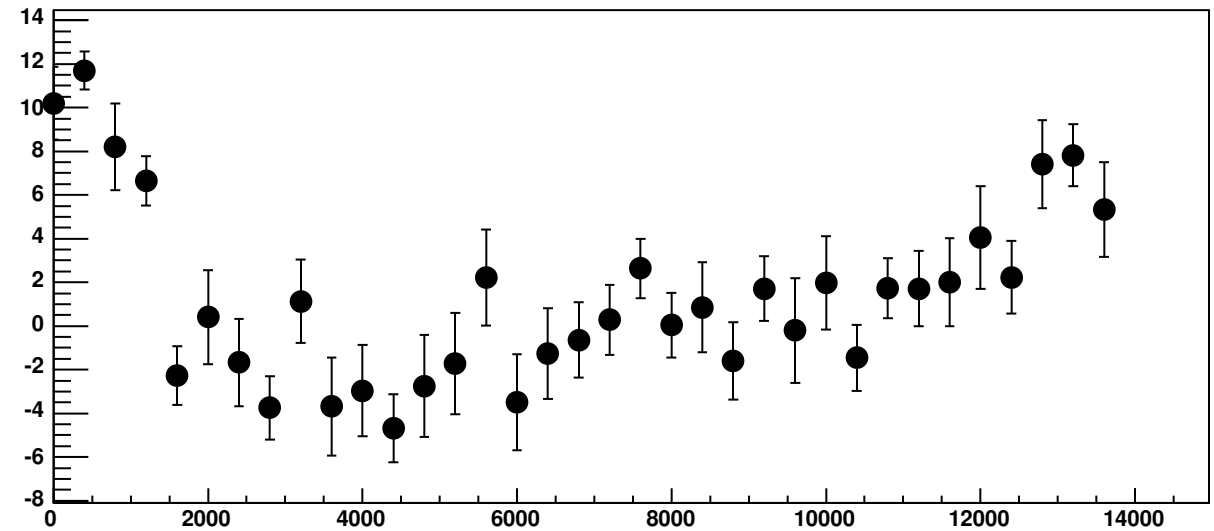
Chip 2, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

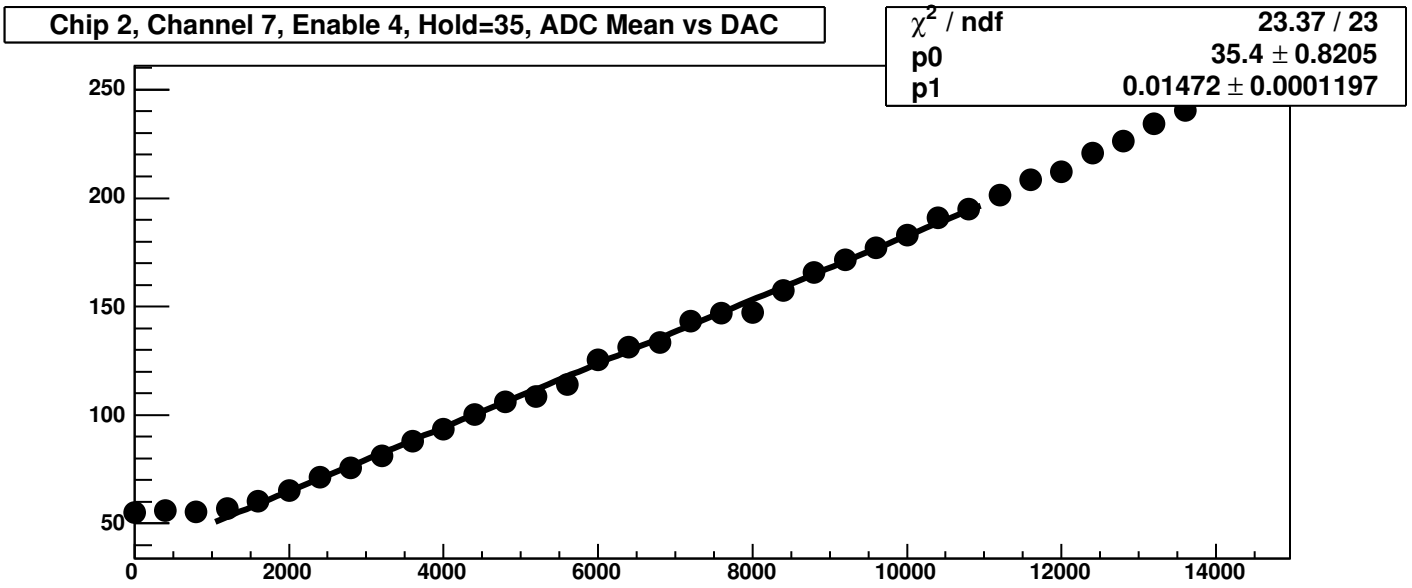


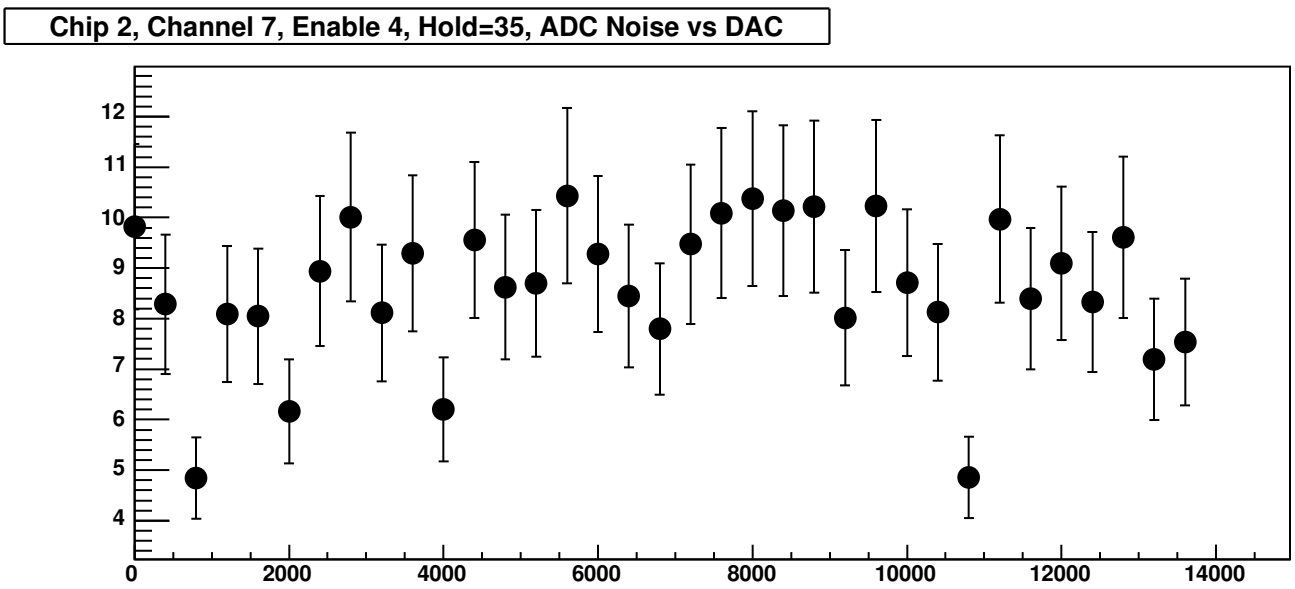
Chip 2, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC

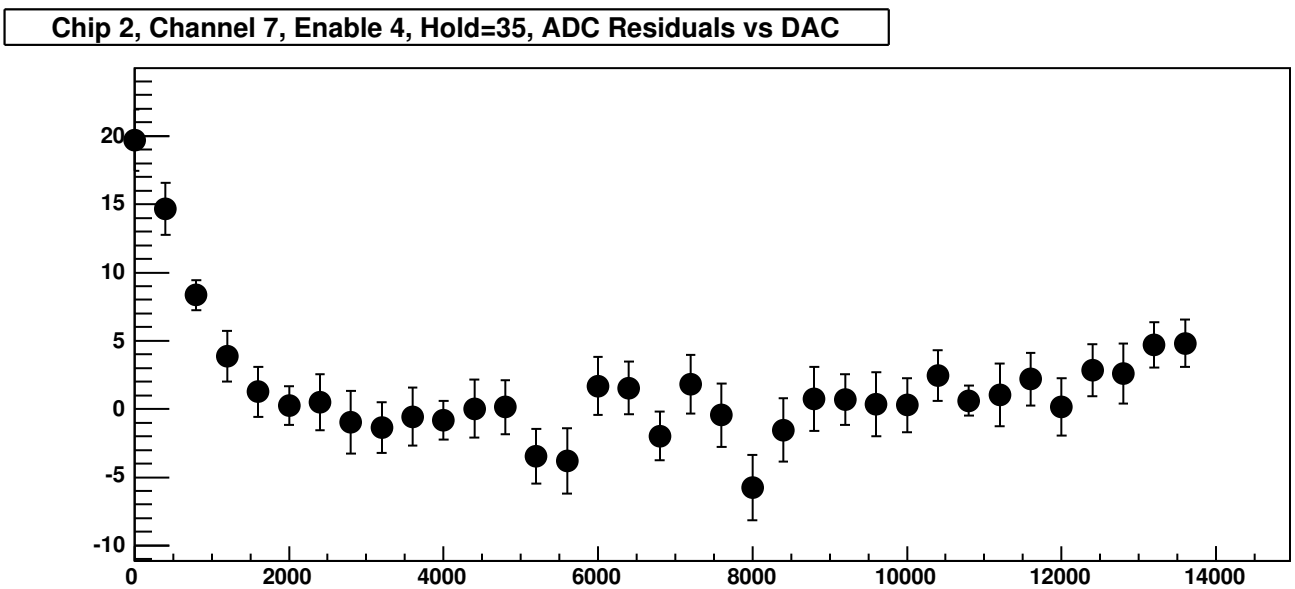


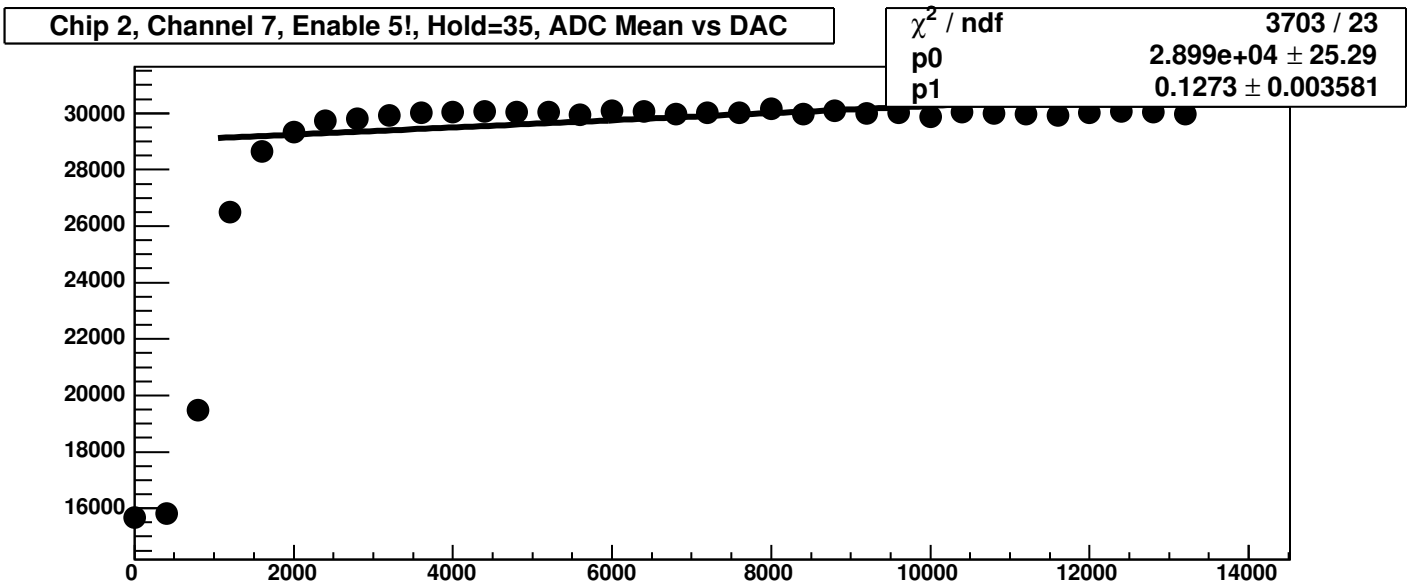
Chip 2, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

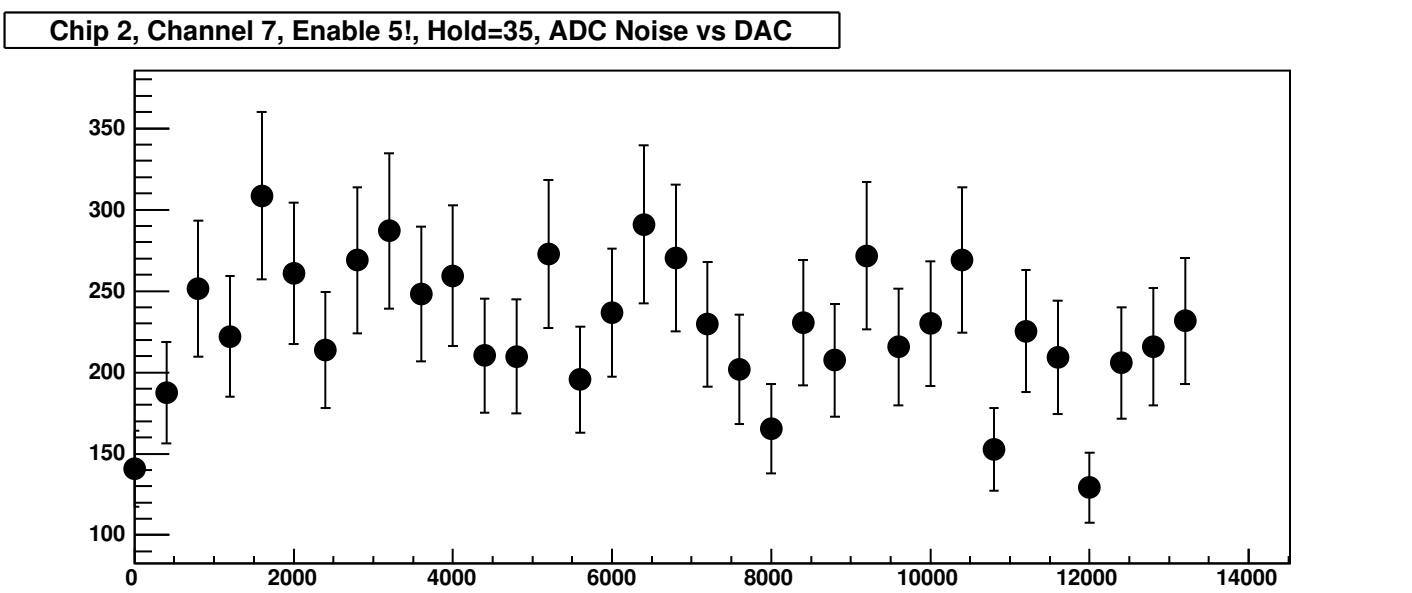


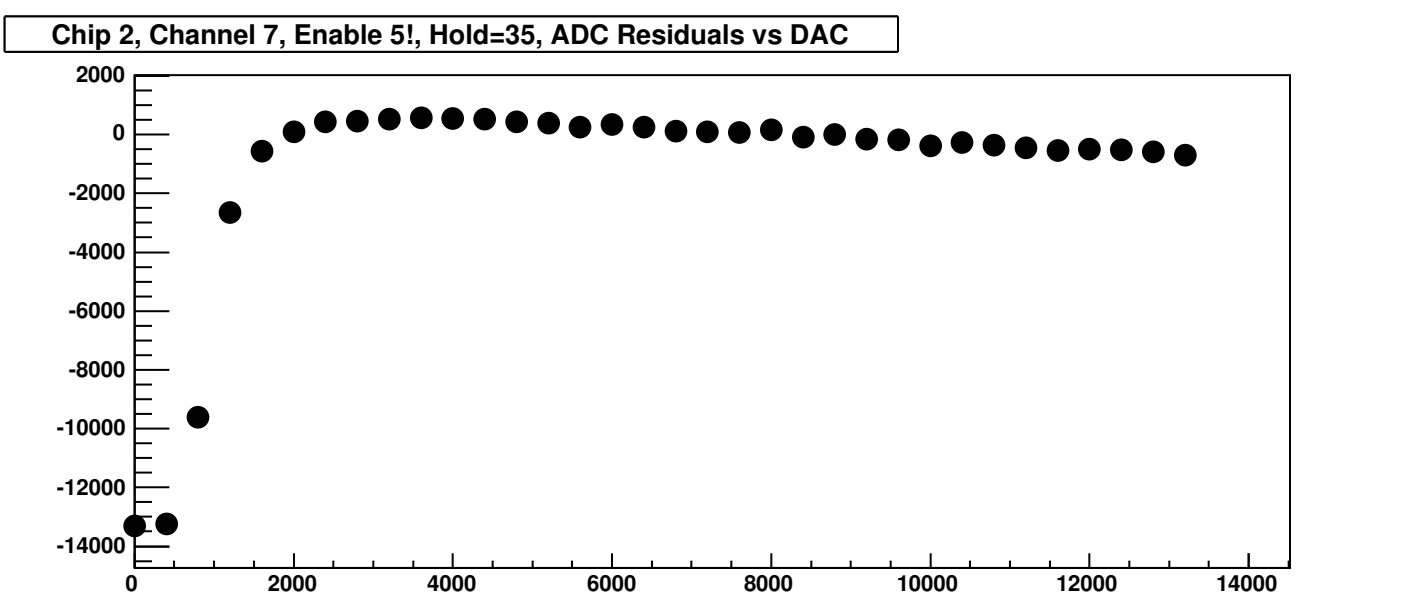

Chip 2, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 23.37 / 23
p0 35.4 ± 0.8205
p1 0.01472 ± 0.0001197
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

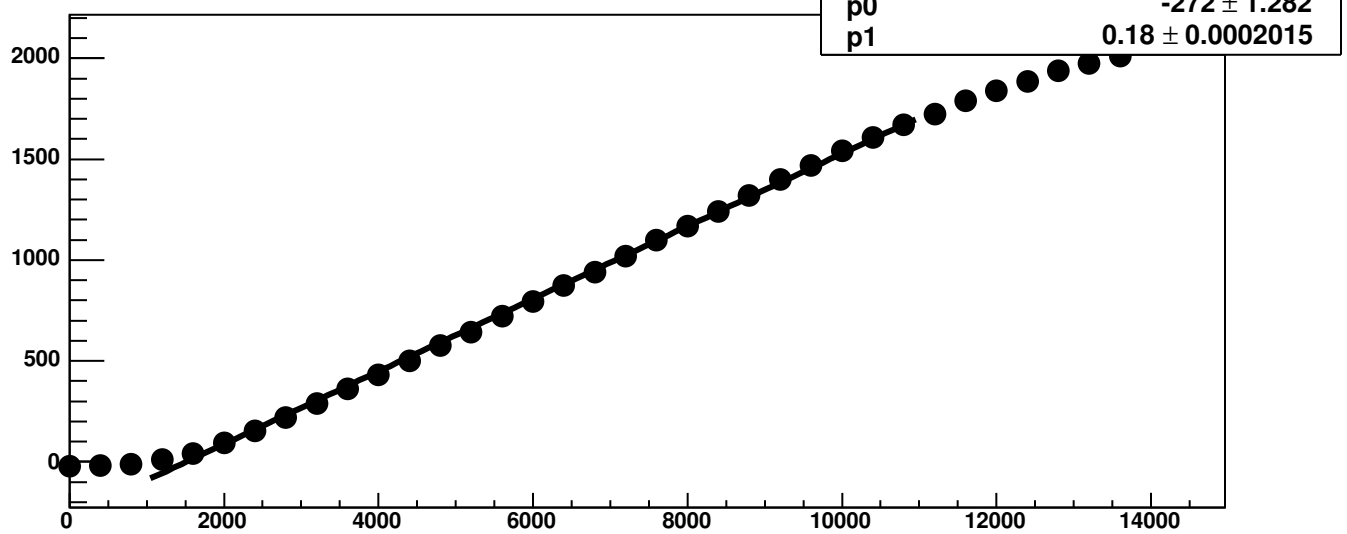

Chip 2, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3703 / 23
p0 2.899e+04 ± 25.29
p1 0.1273 ± 0.003581
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

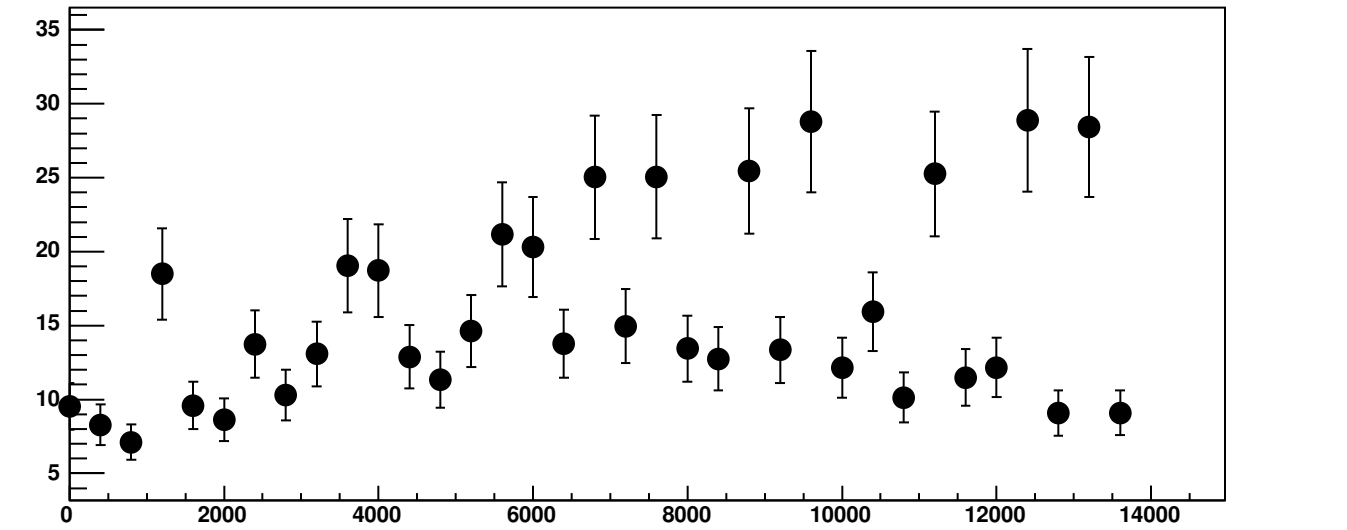

Chip 2, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

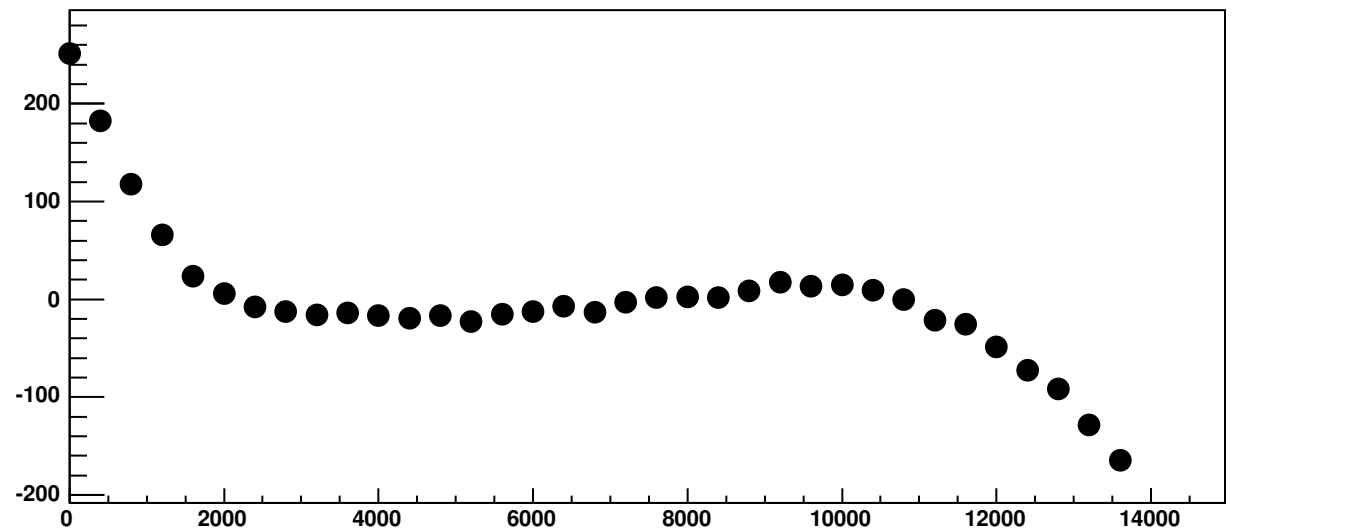
Chip 2, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



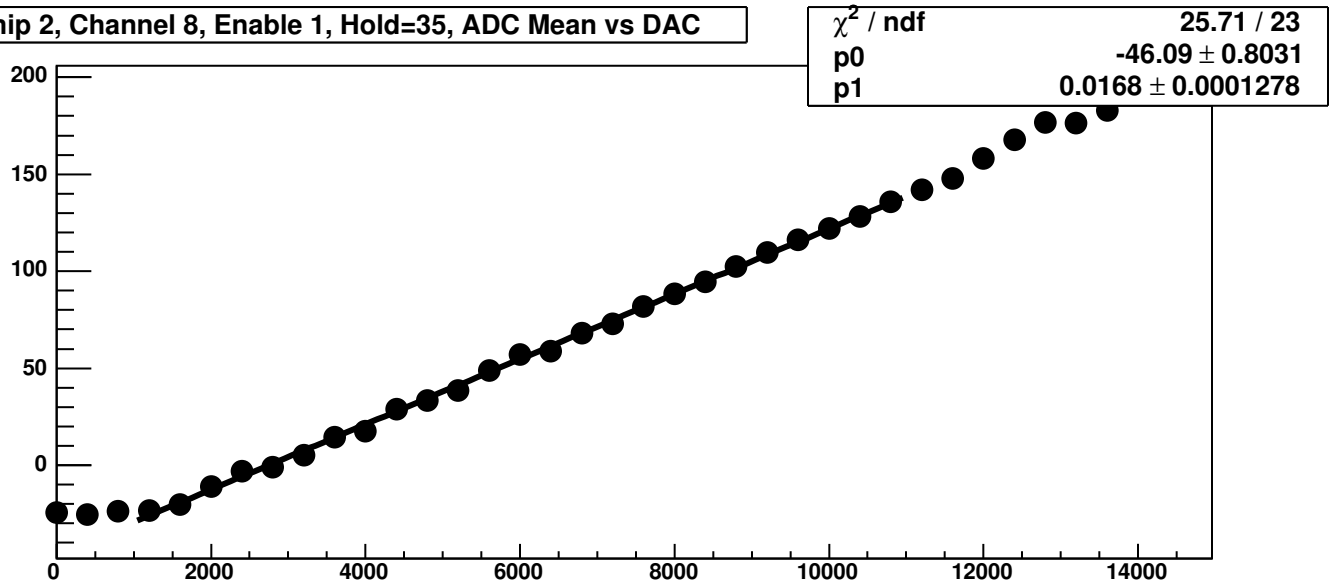
Chip 2, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



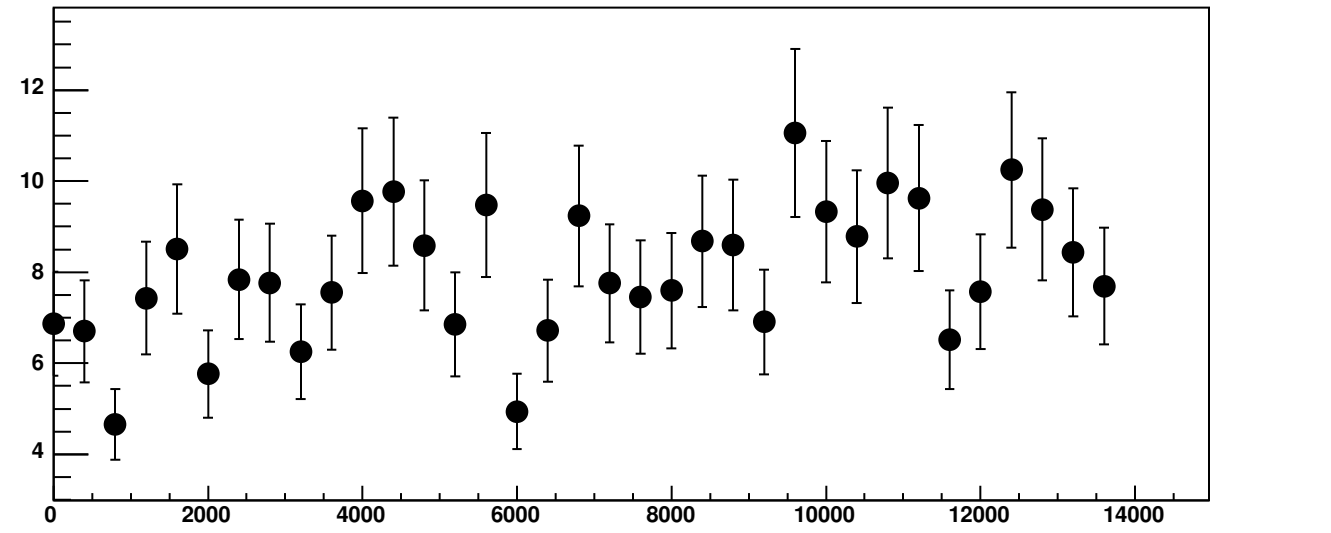
Chip 2, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



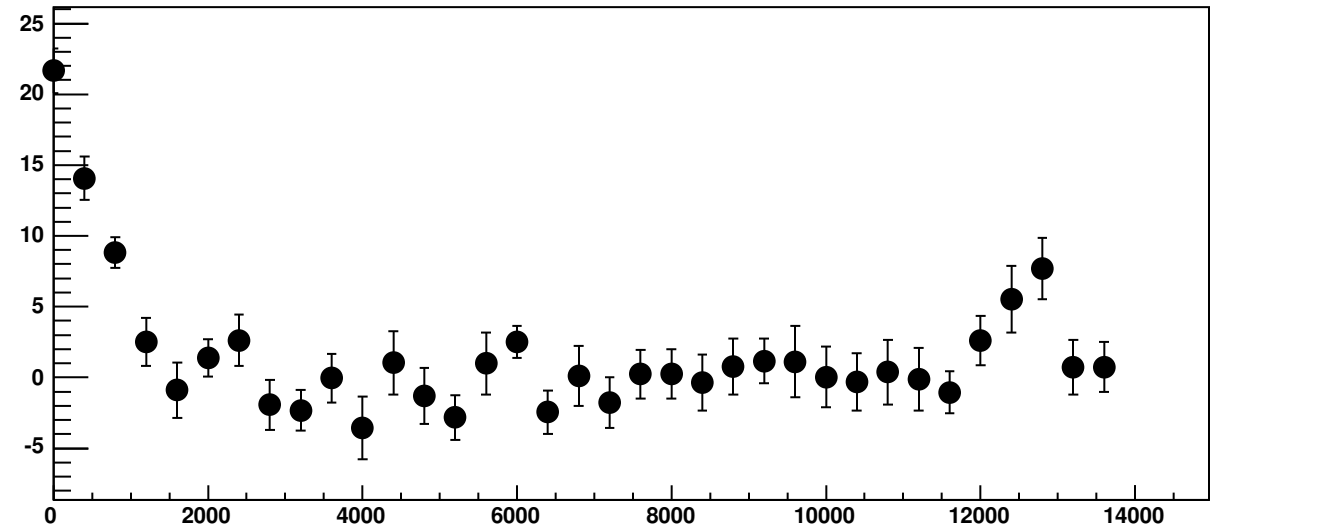
Chip 2, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

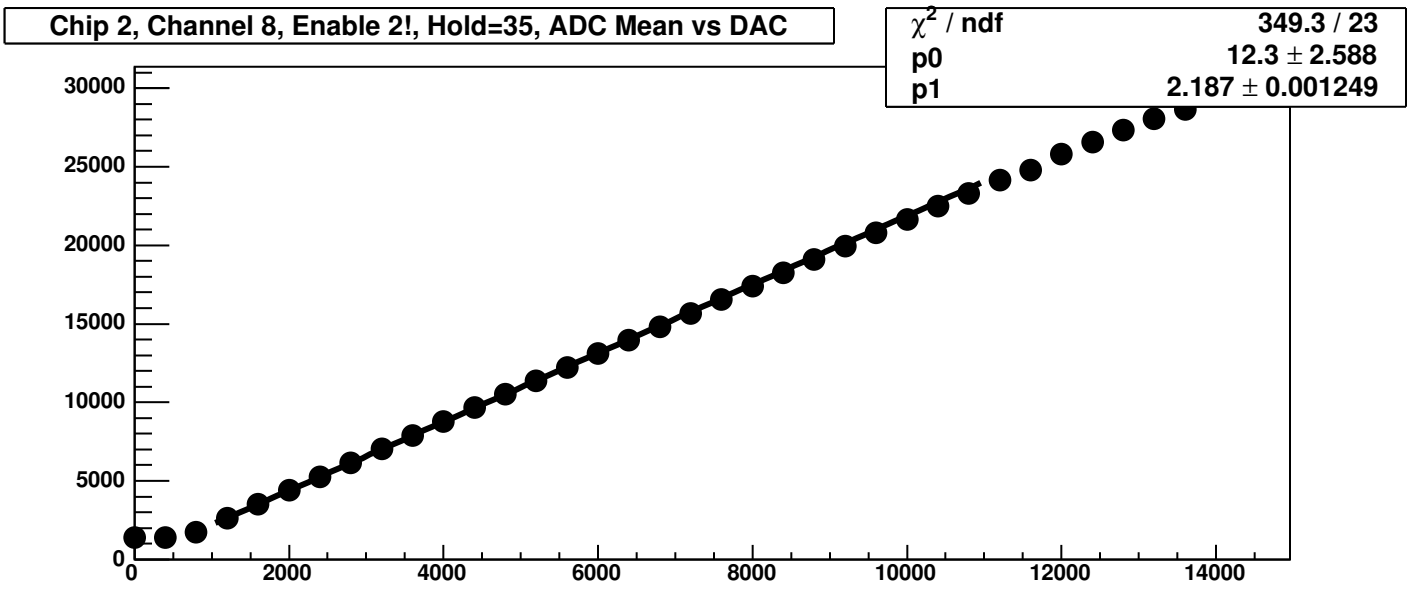


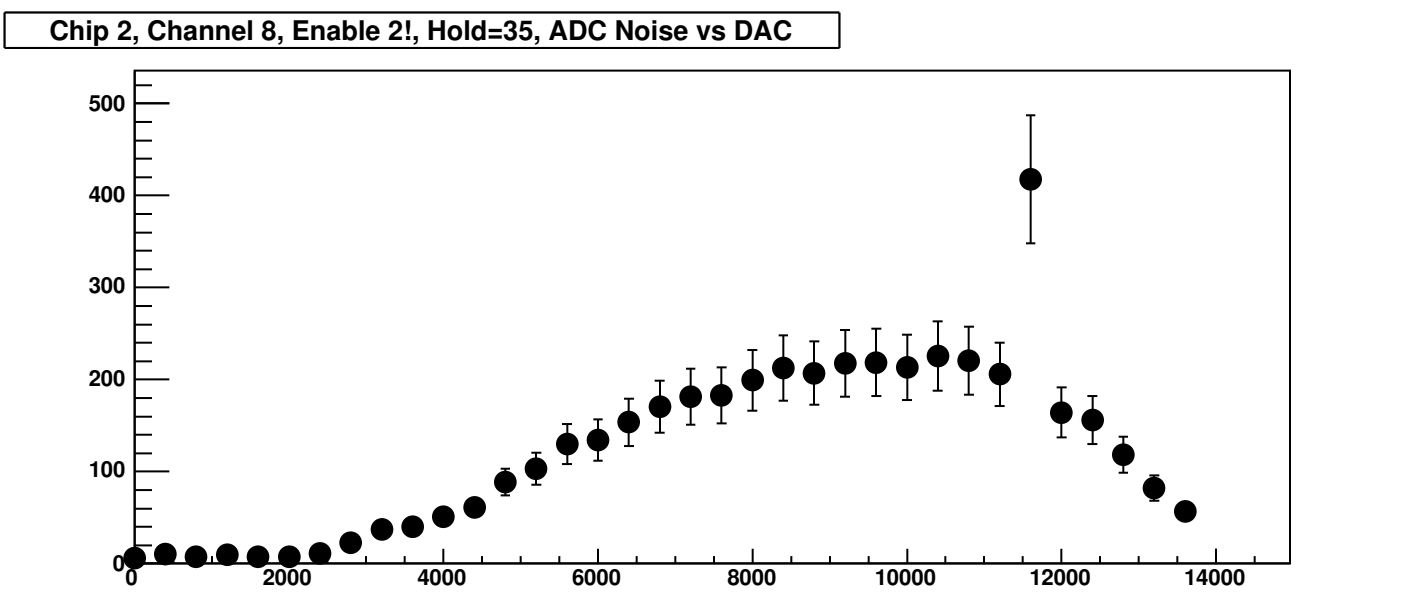
Chip 2, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC

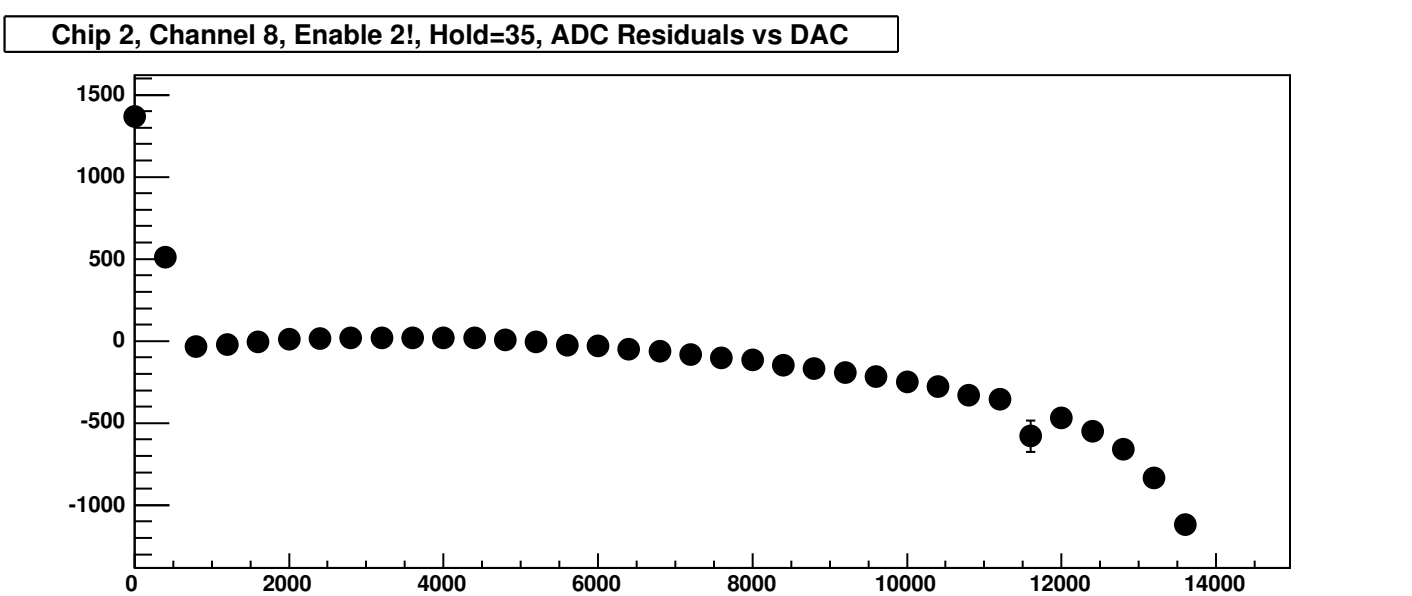


Chip 2, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

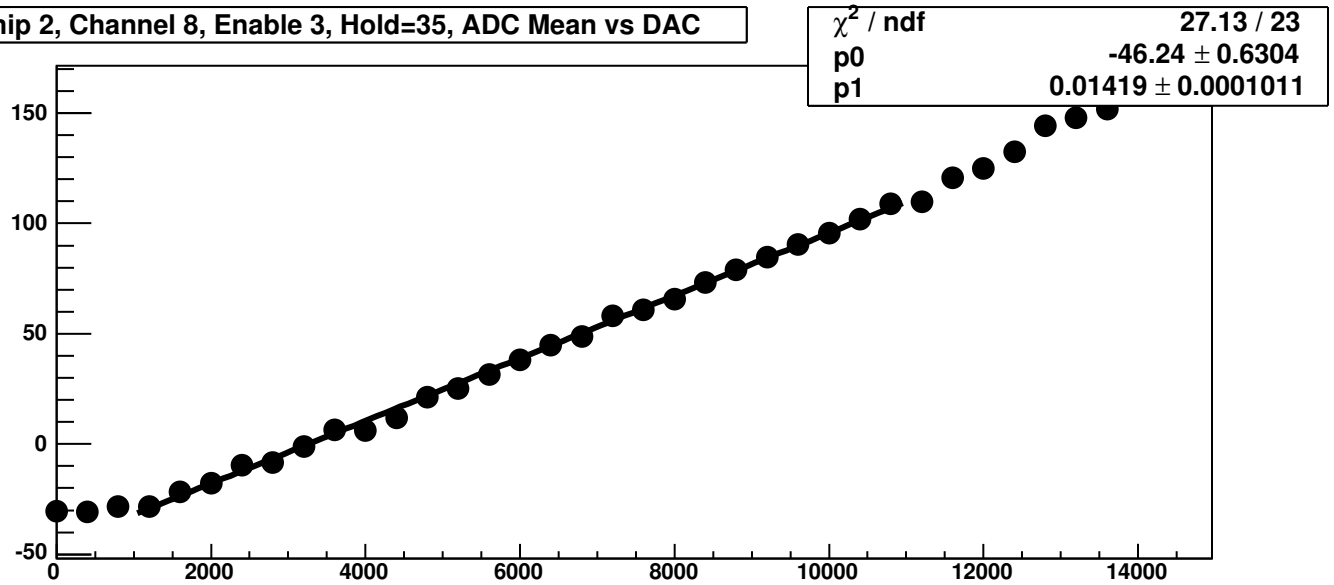



Chip 2, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 349.3 / 23
p0 12.3 ± 2.588
p1 2.187 ± 0.001249
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

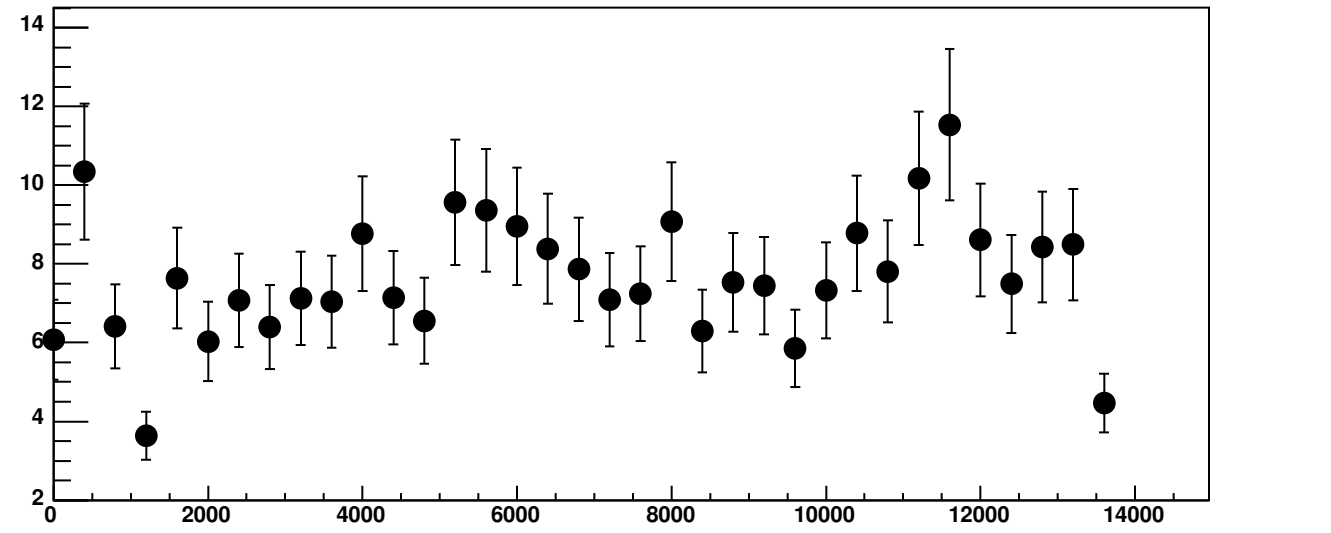

Chip 2, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

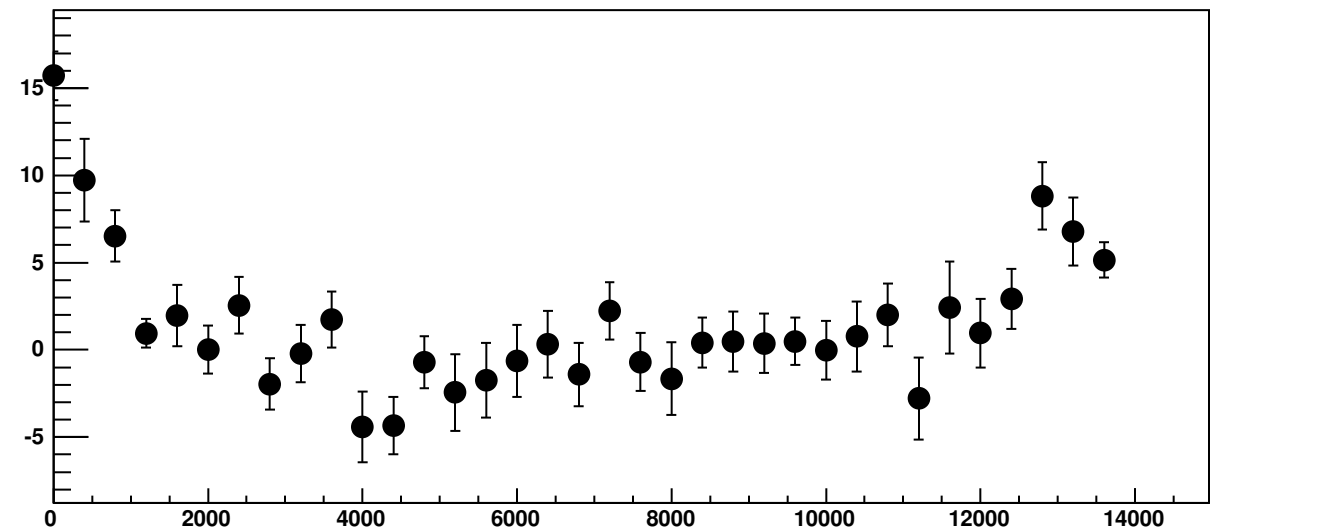
Chip 2, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

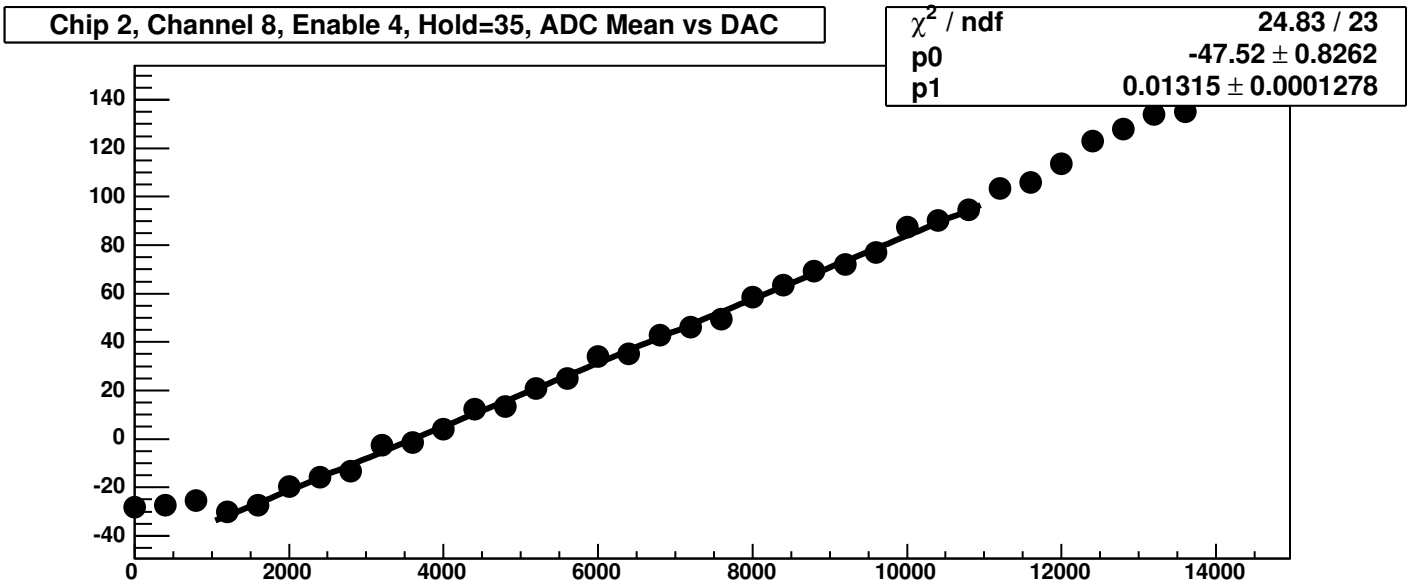


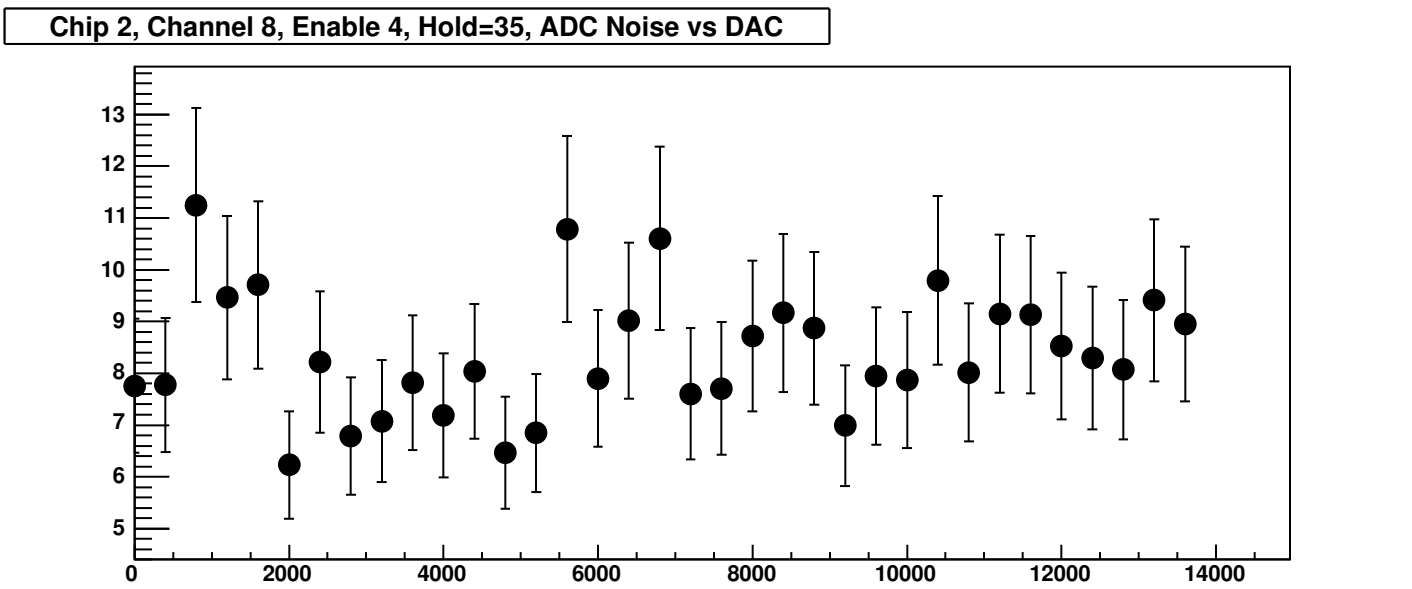
Chip 2, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC

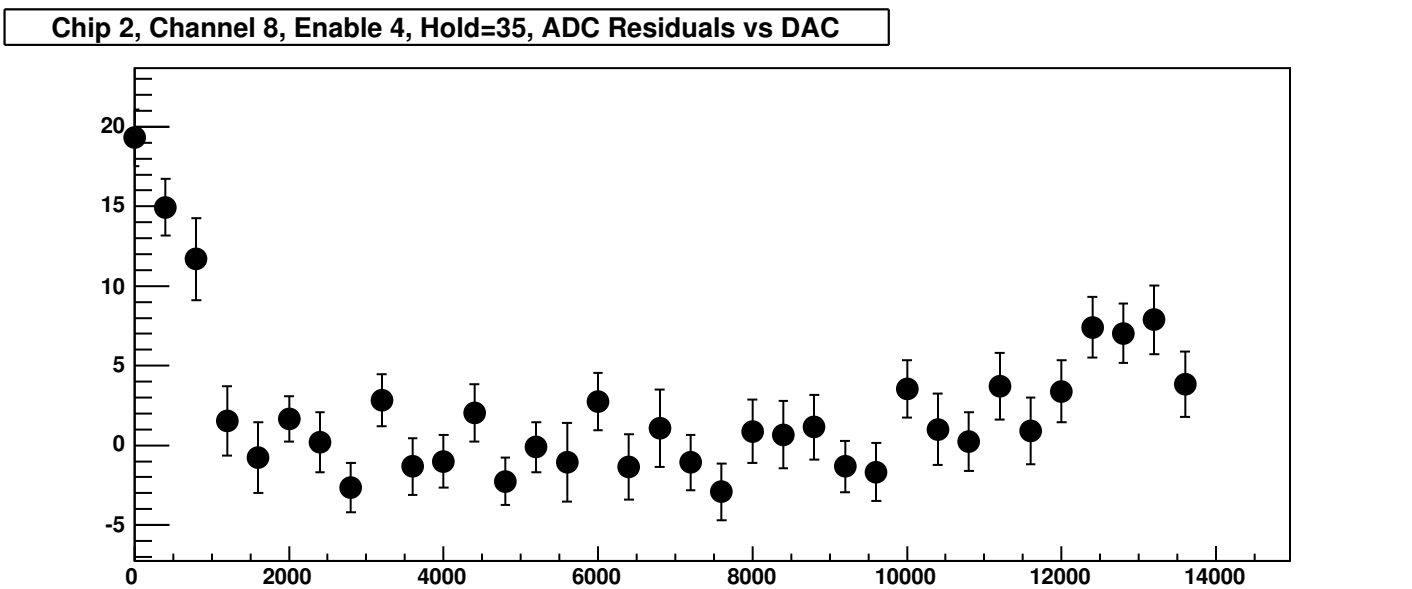


Chip 2, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC



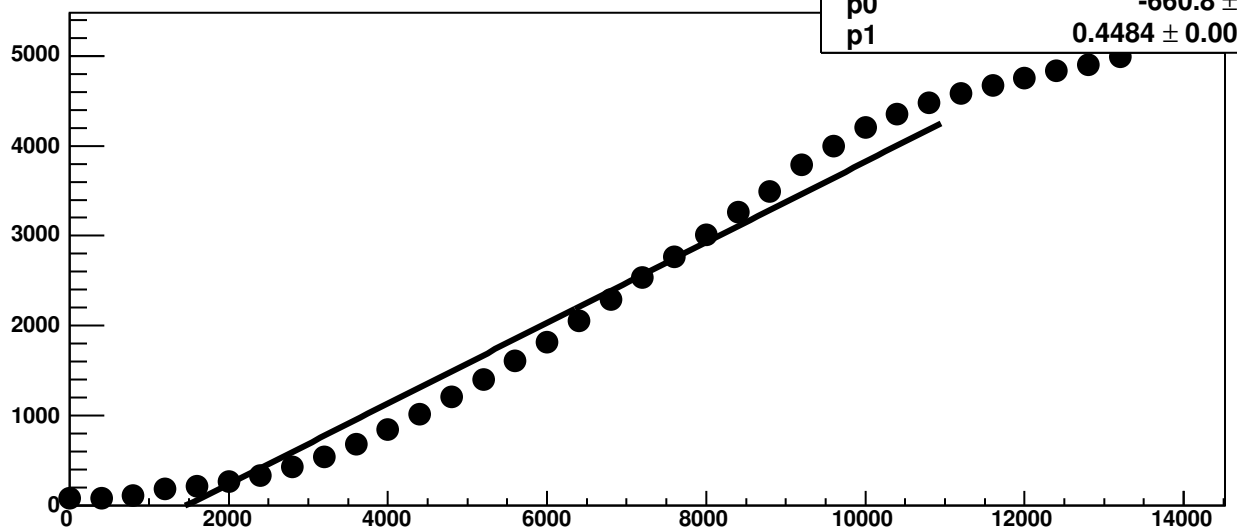

Chip 2, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.83 / 23
p0 -47.52 ± 0.8262
p1 0.01315 ± 0.0001278
140
120
100
80
60
40
20
0
-20
-40
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

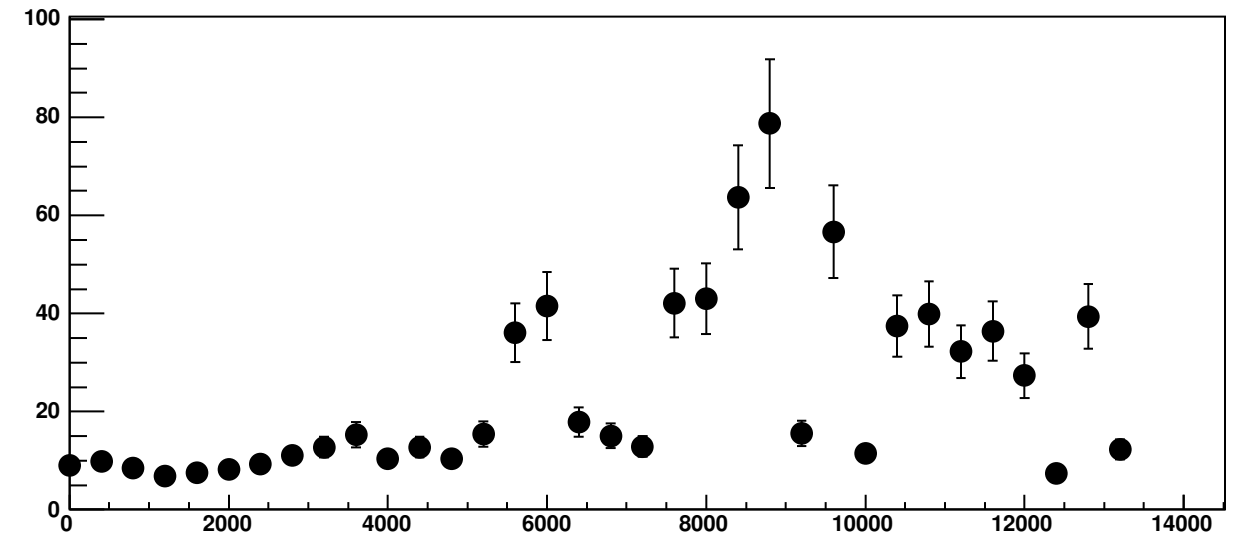

Chip 2, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

Chip 2, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC

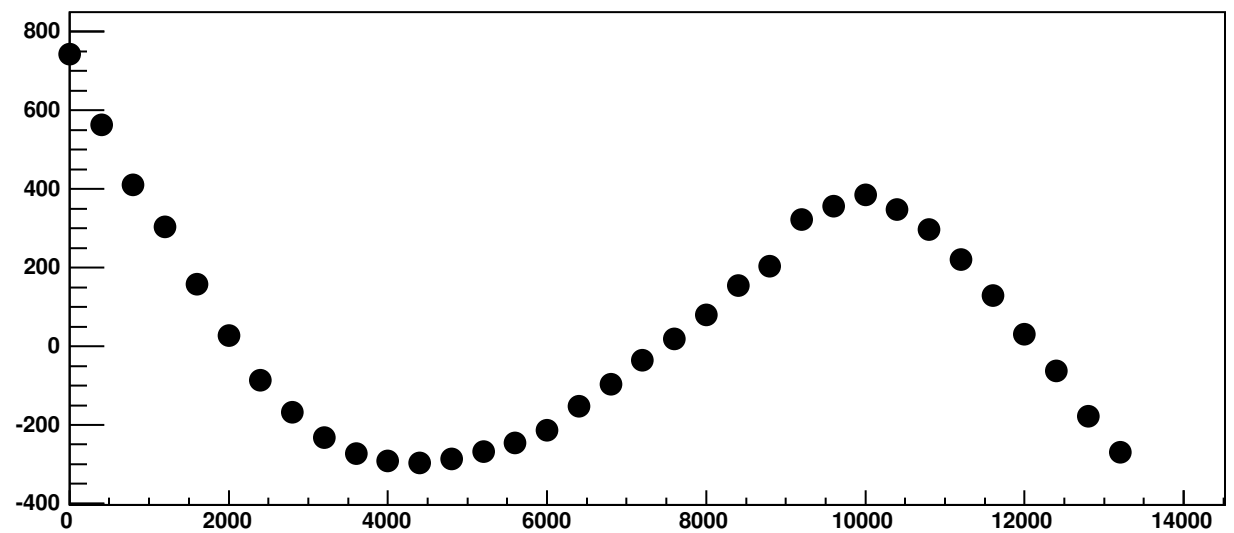
χ^2 / ndf	1.463e+05 / 23
p0	-660.8 ± 1.072
p1	0.4484 ± 0.0002317

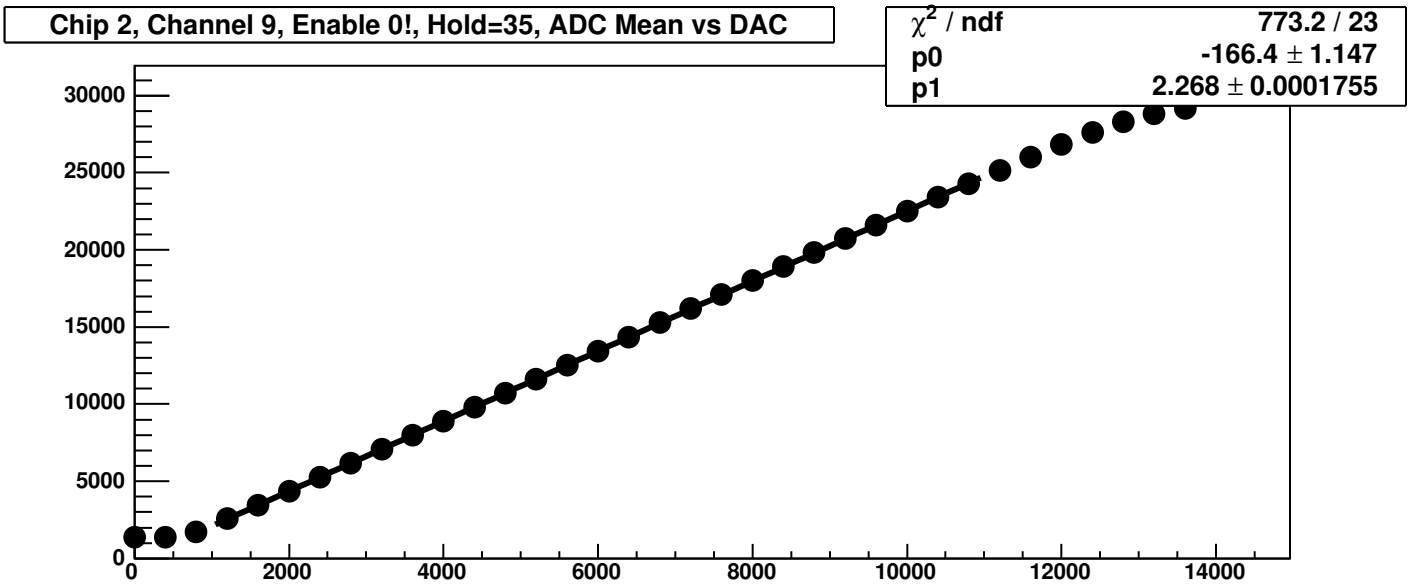


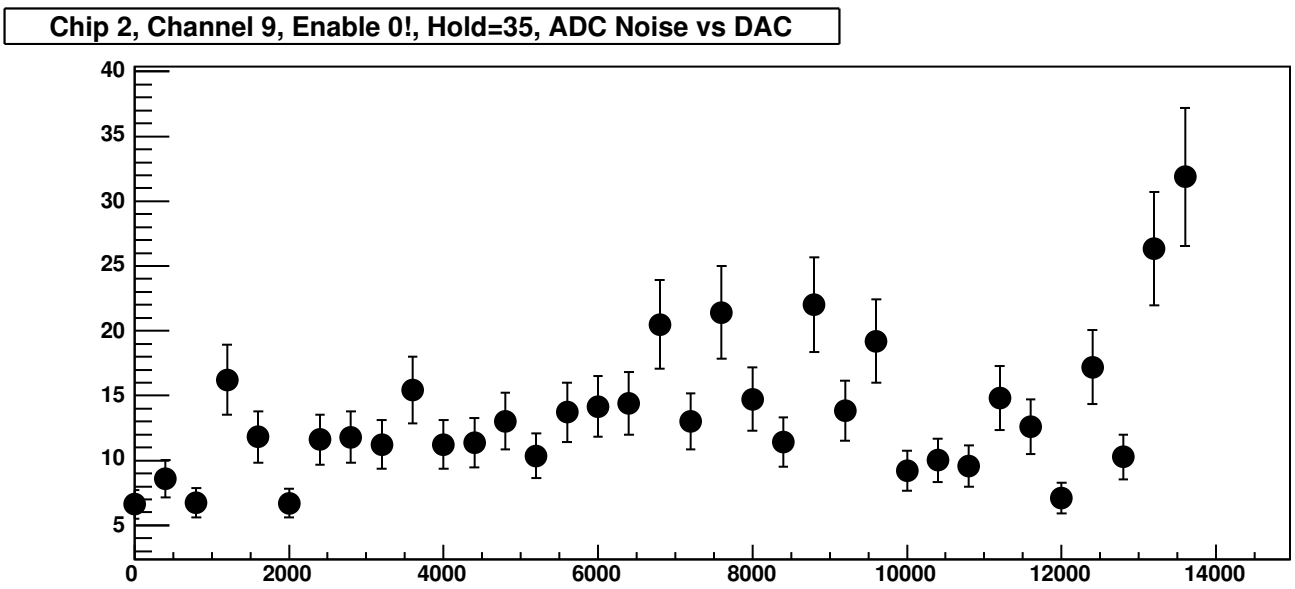
Chip 2, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC

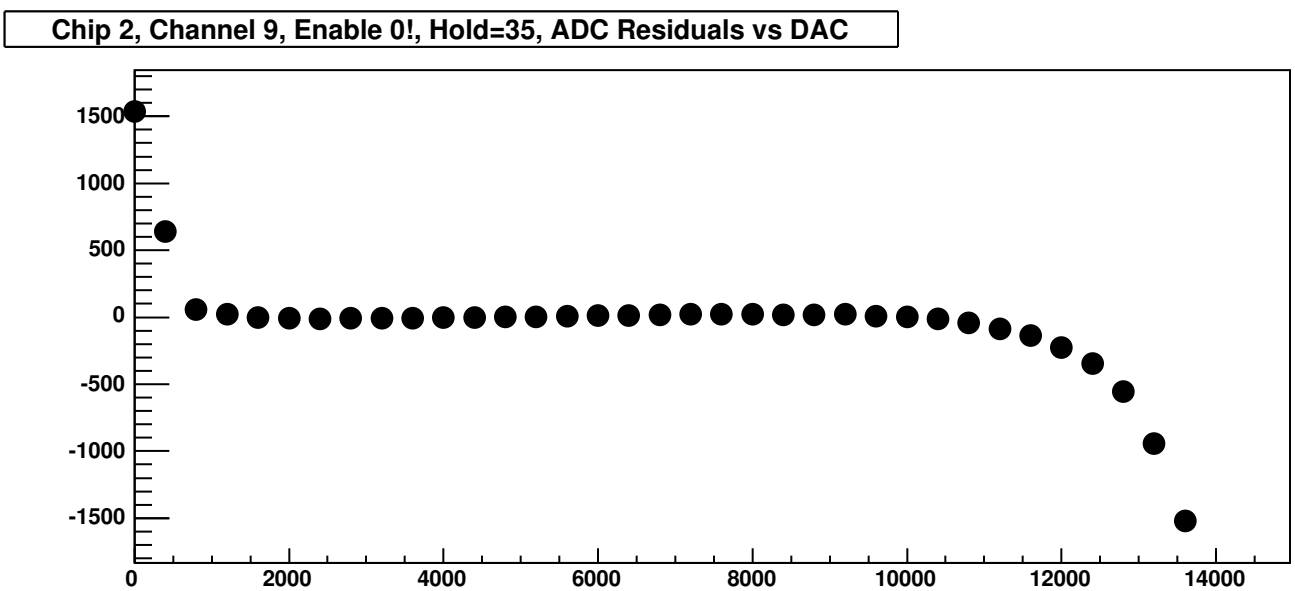


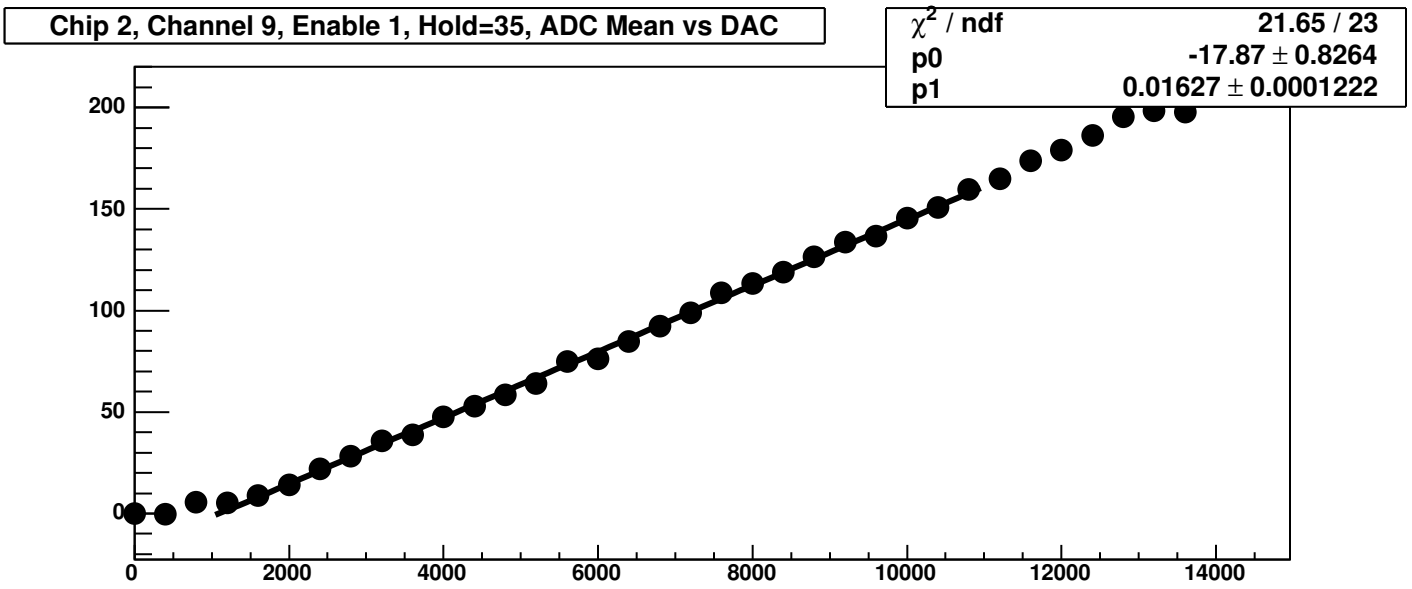
Chip 2, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC

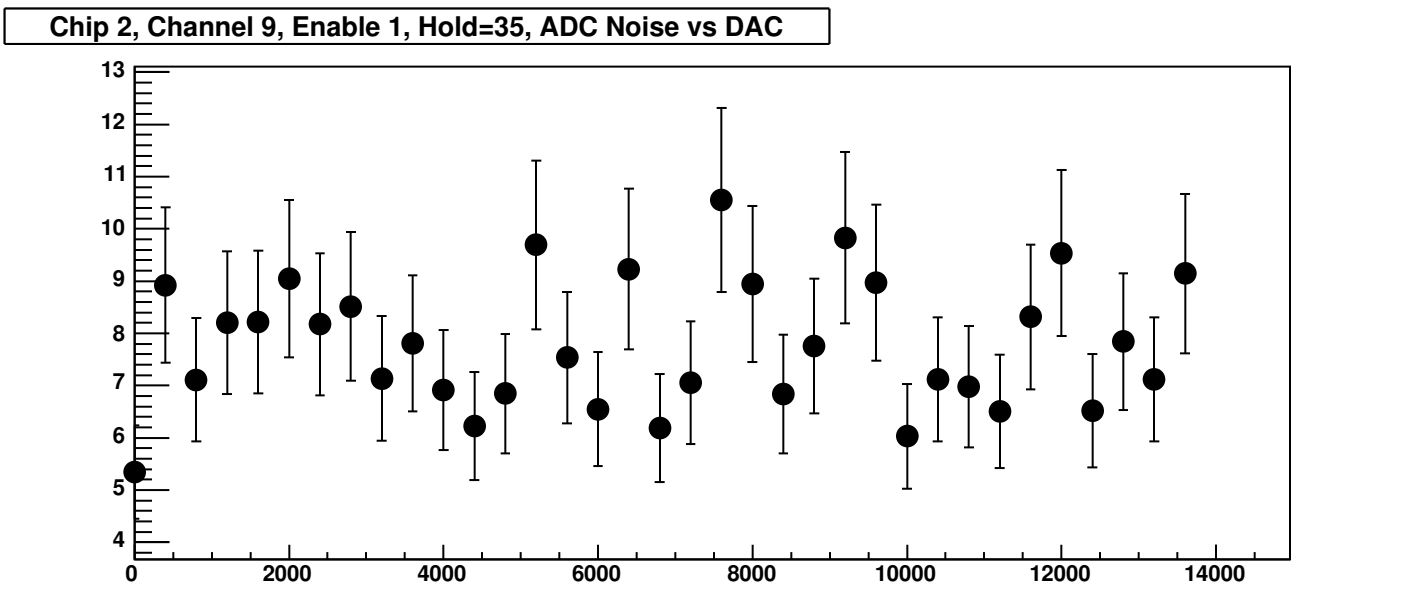


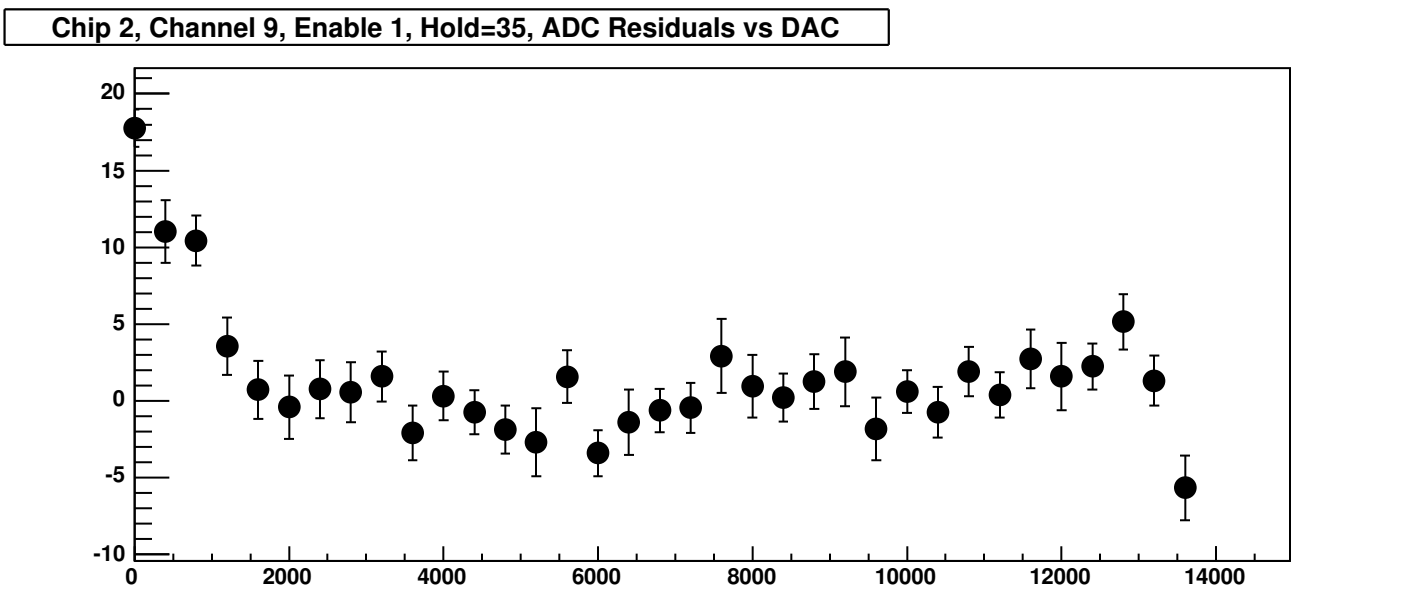

Chip 2, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 773.2 / 23
p0 -166.4 ± 1.147
p1 2.268 ± 0.0001755
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

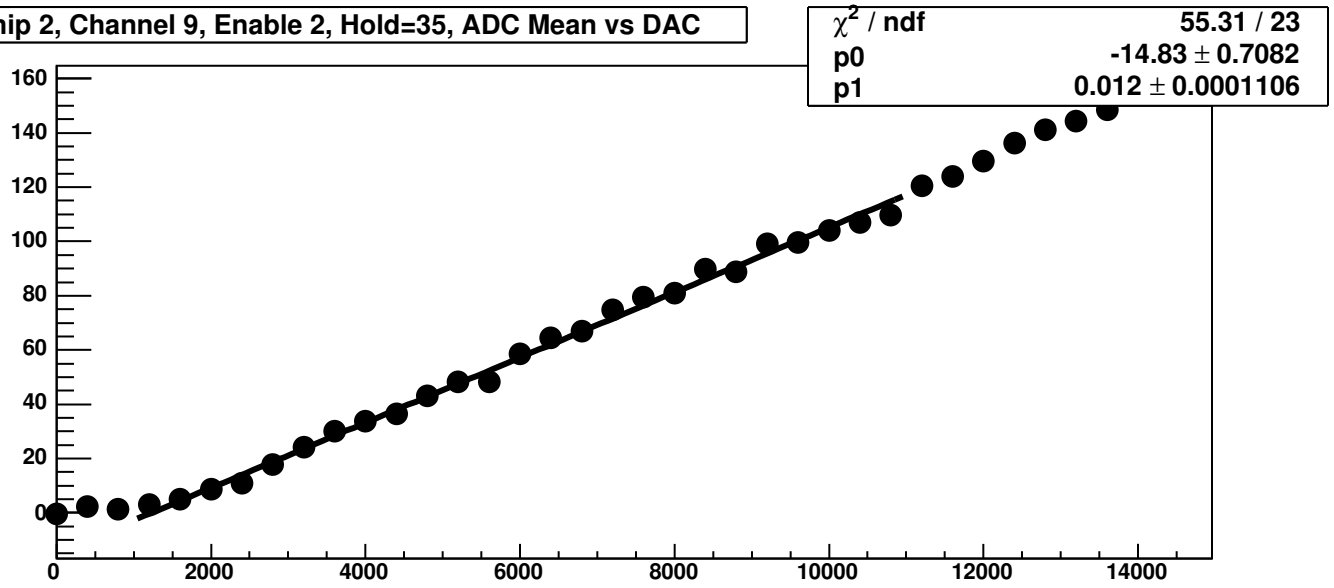

Chip 2, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 21.65 / 23
p0 -17.87 ± 0.8264
p1 0.01627 ± 0.0001222
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

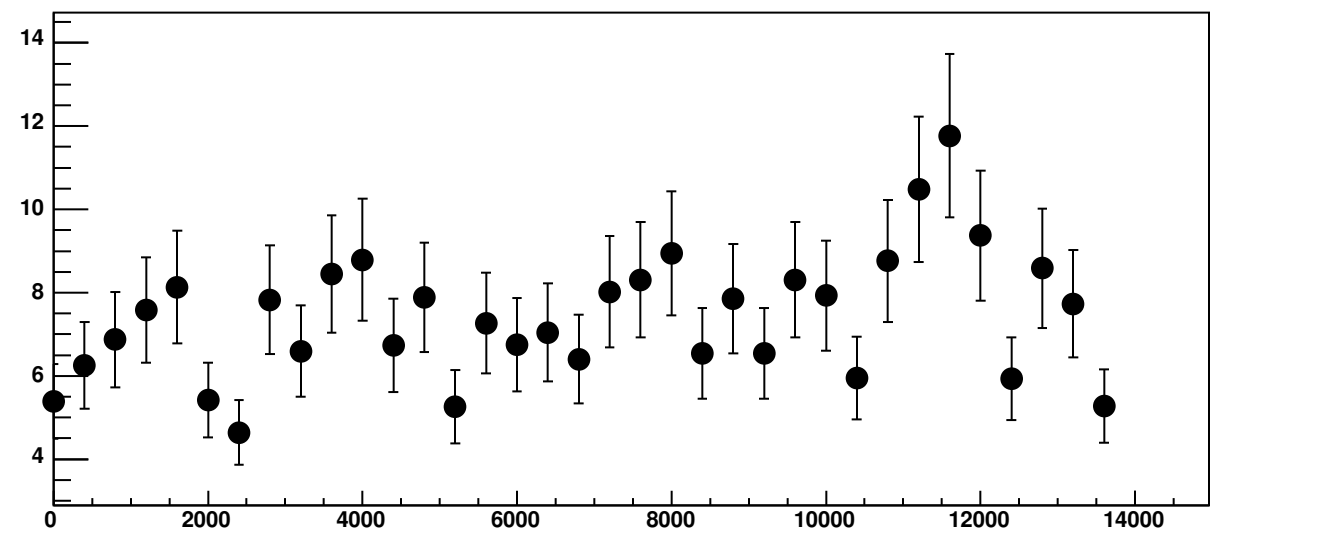

Chip 2, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

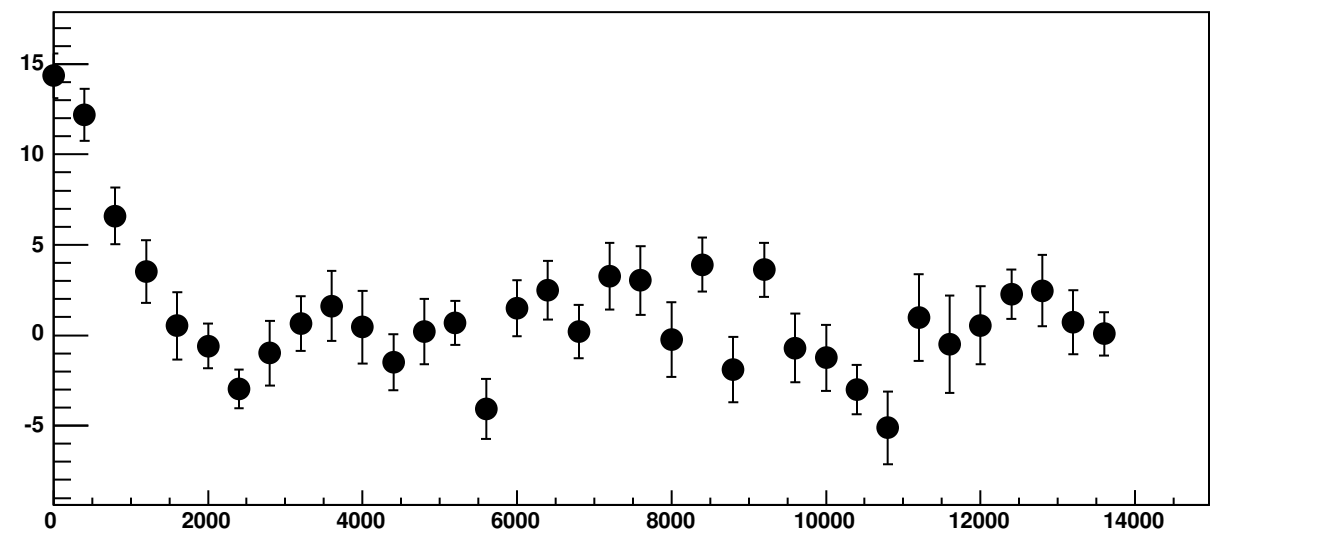
Chip 2, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC

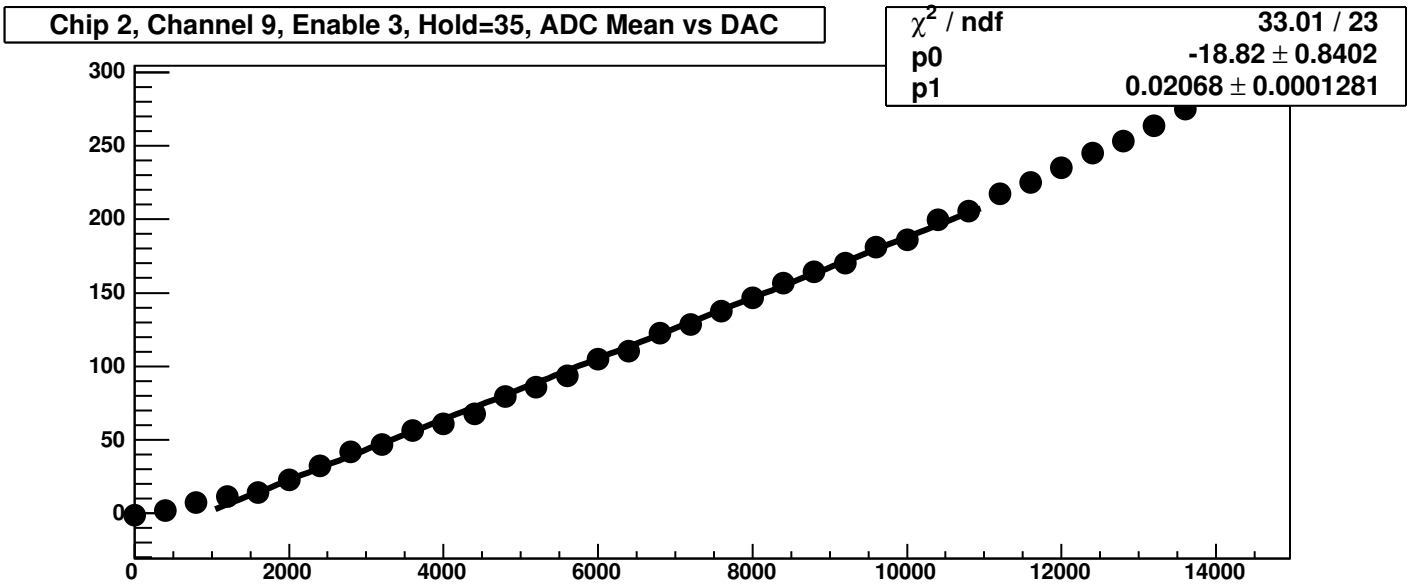


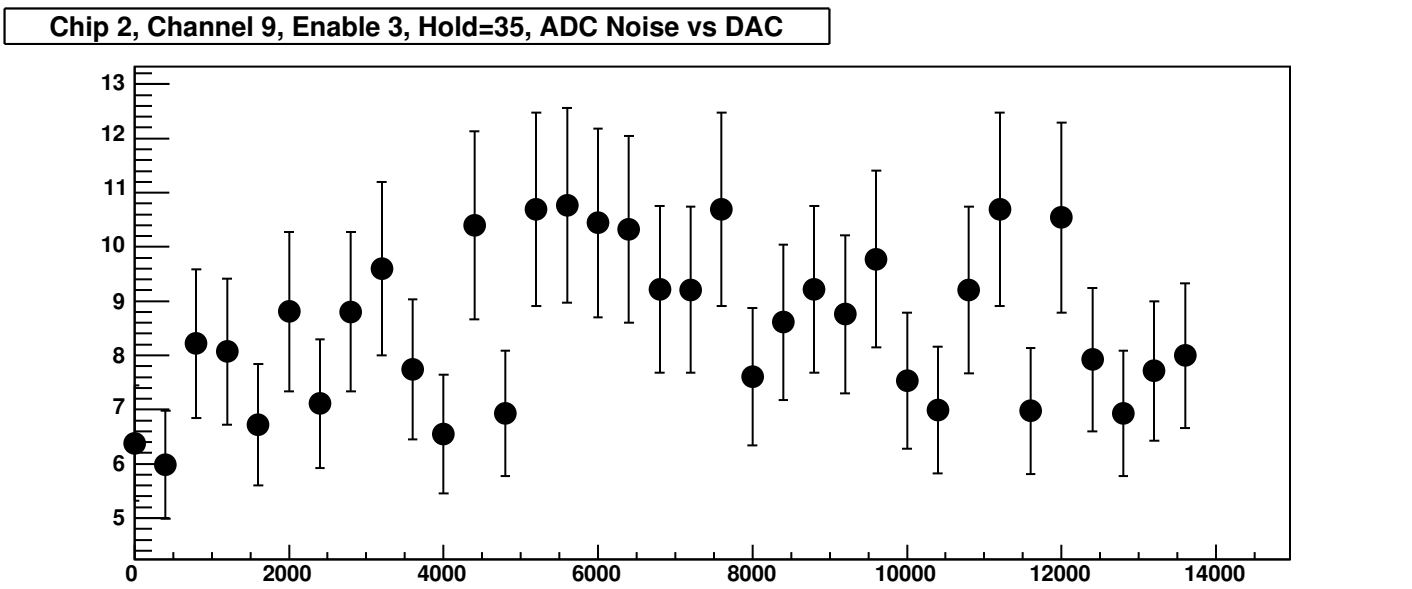
Chip 2, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC

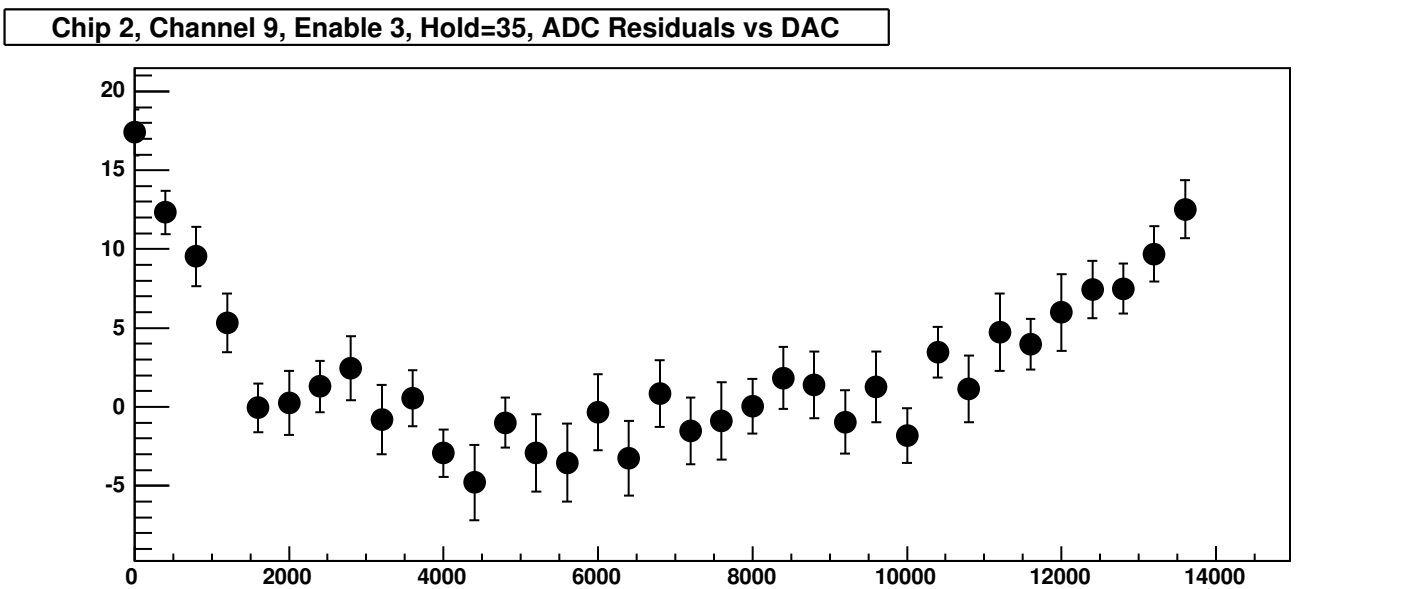


Chip 2, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC

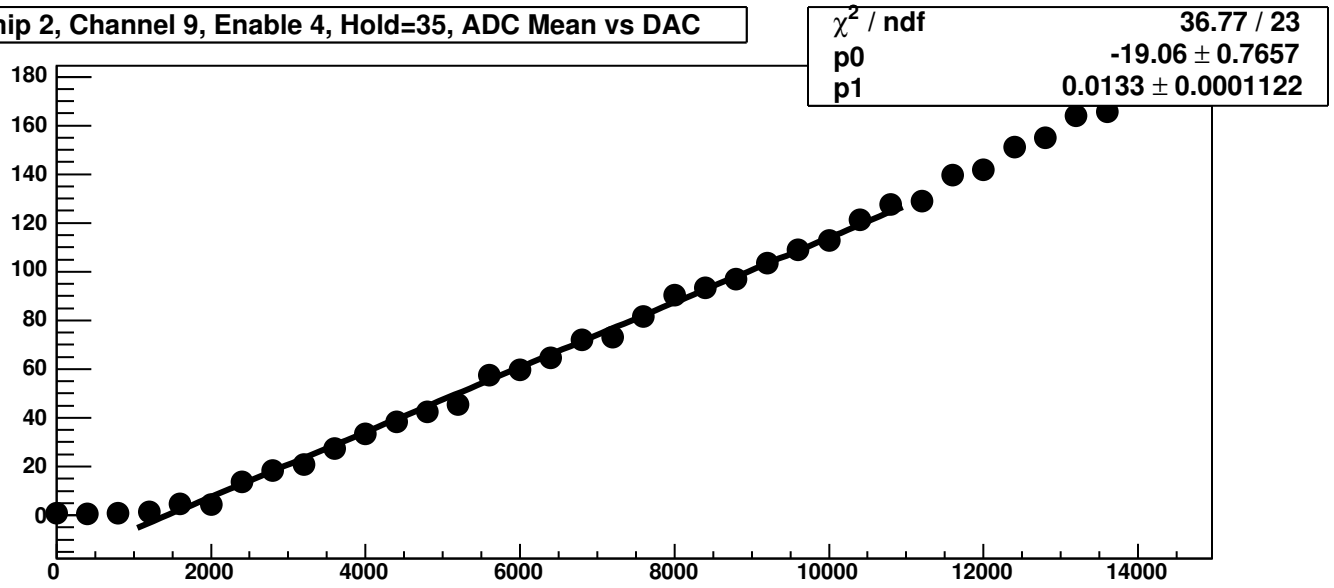



Chip 2, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 33.01 / 23
p0 -18.82 ± 0.8402
p1 0.02068 ± 0.0001281
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

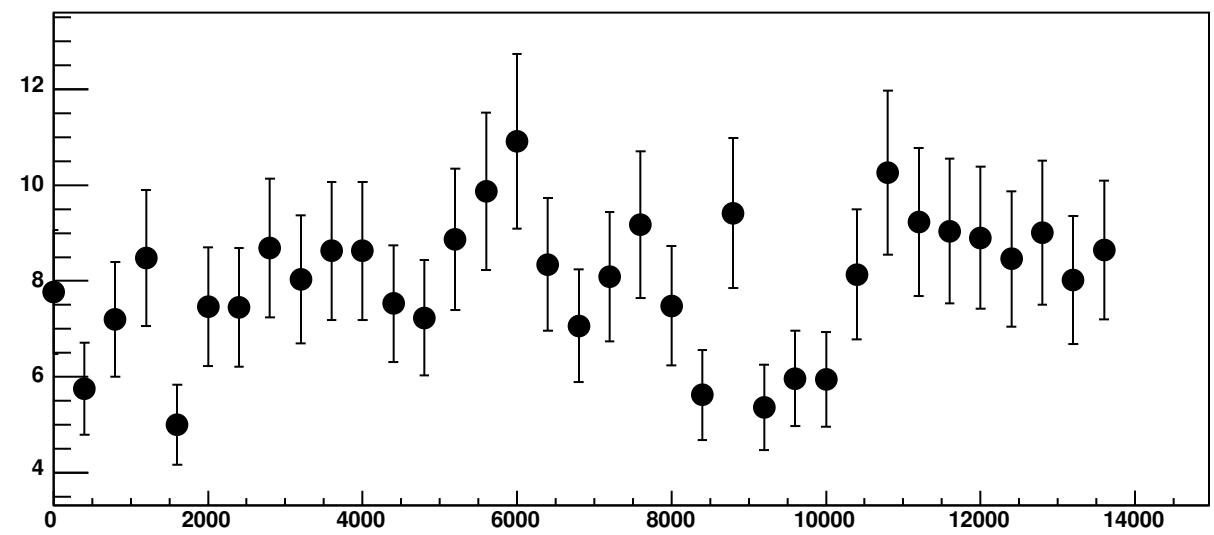

Chip 2, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

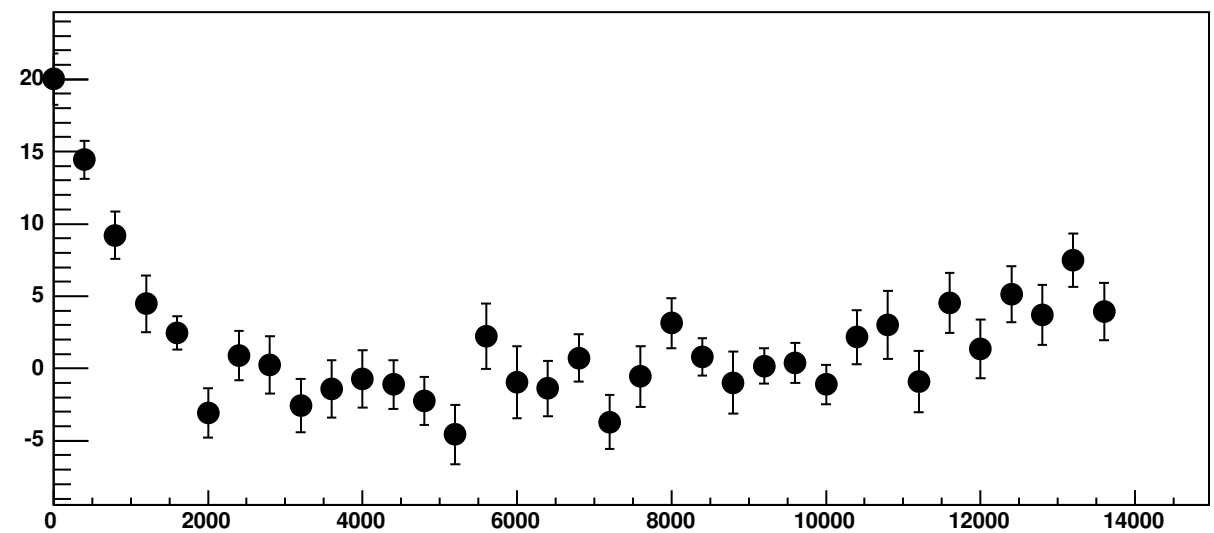
Chip 2, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



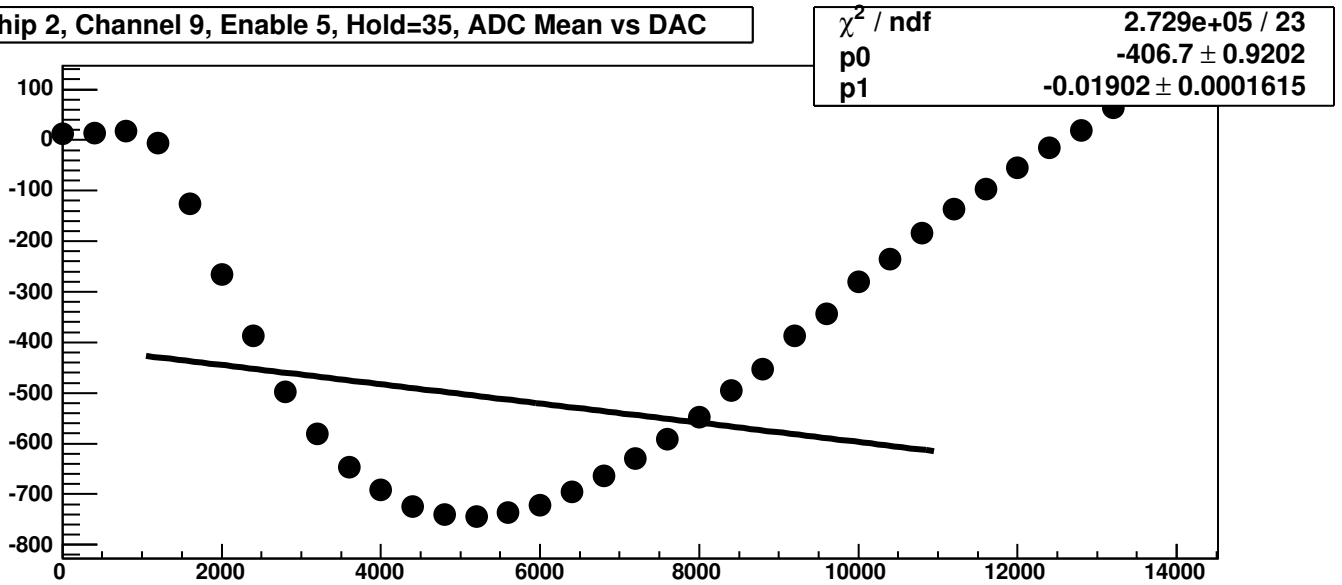
Chip 2, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



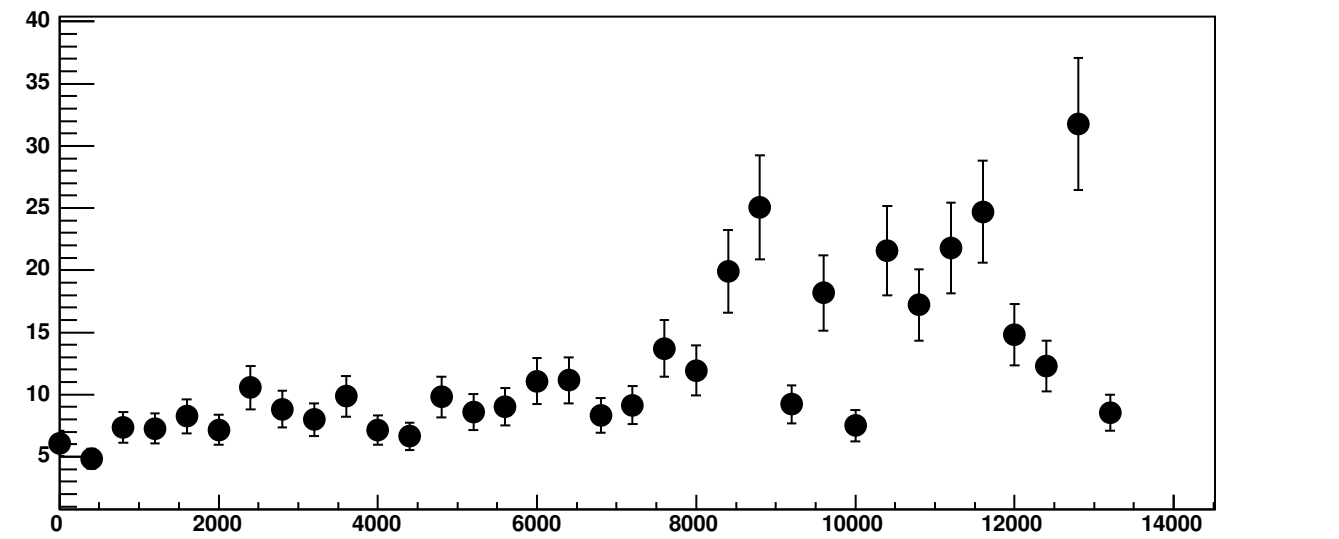
Chip 2, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



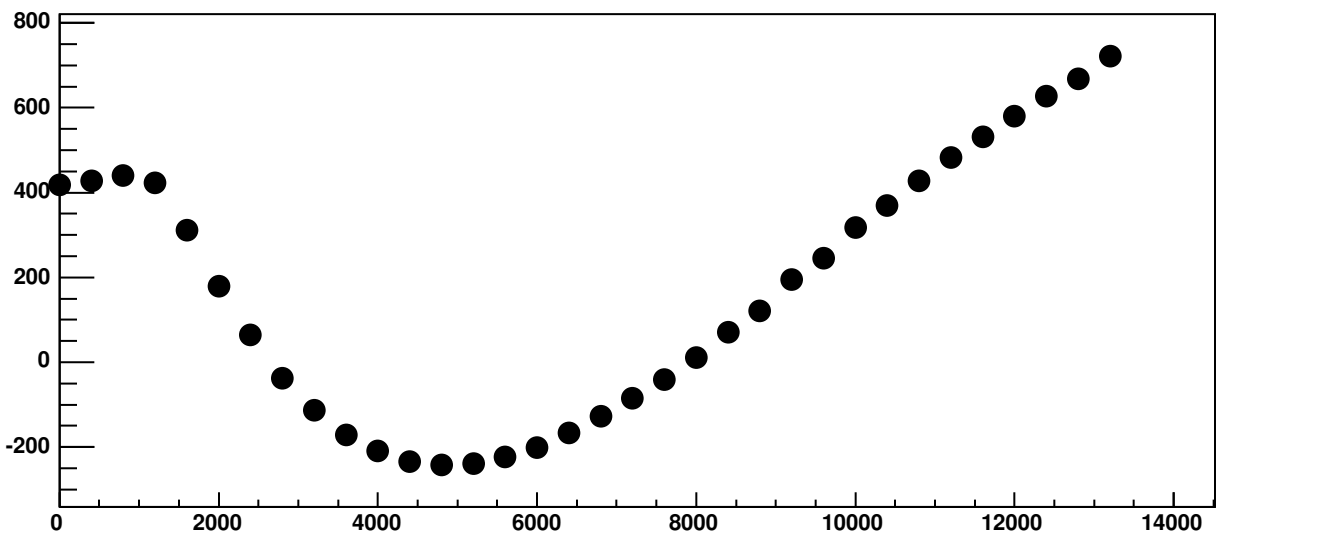
Chip 2, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

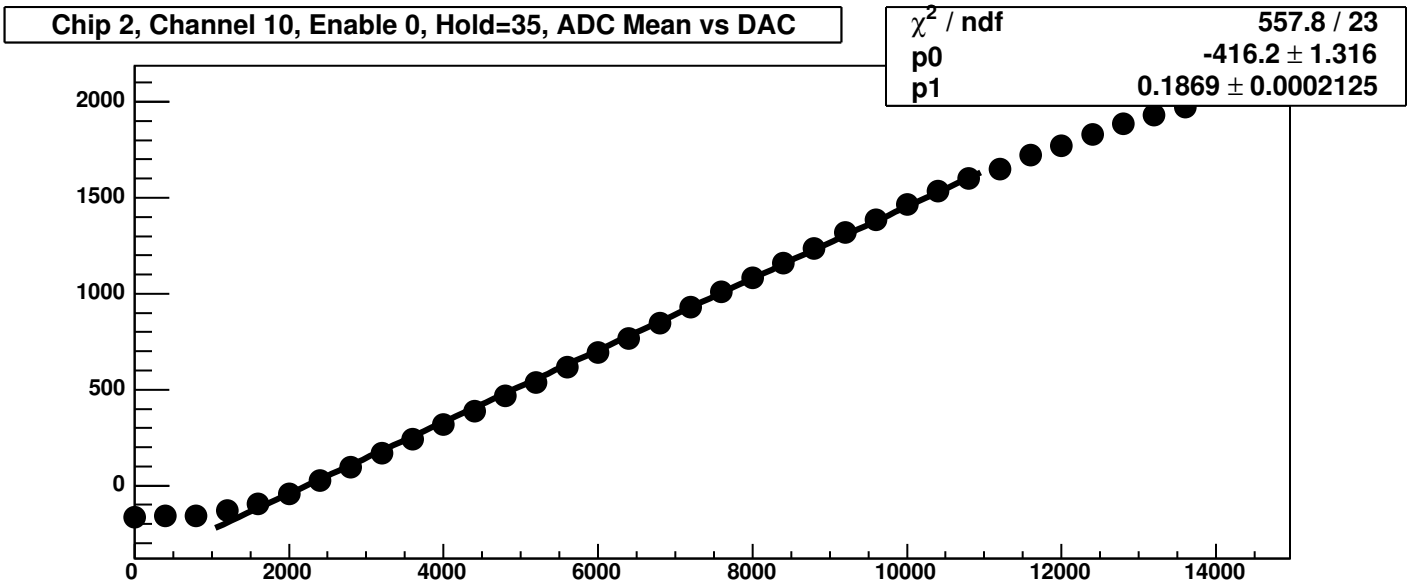


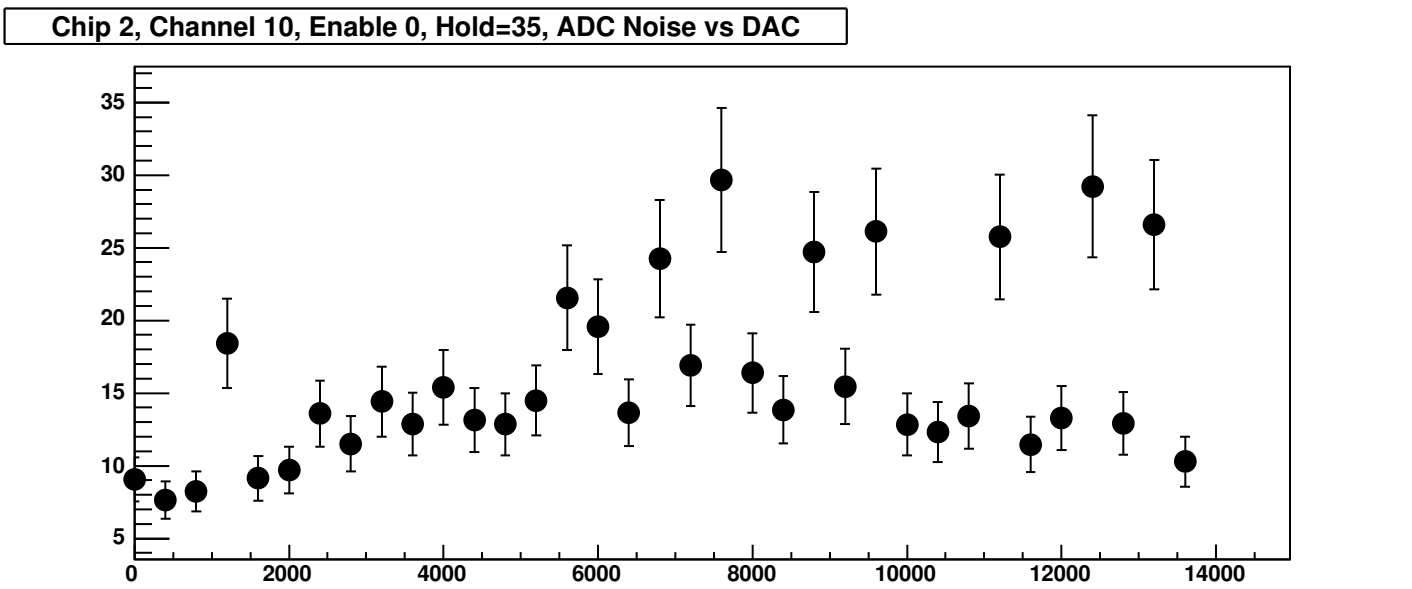
Chip 2, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

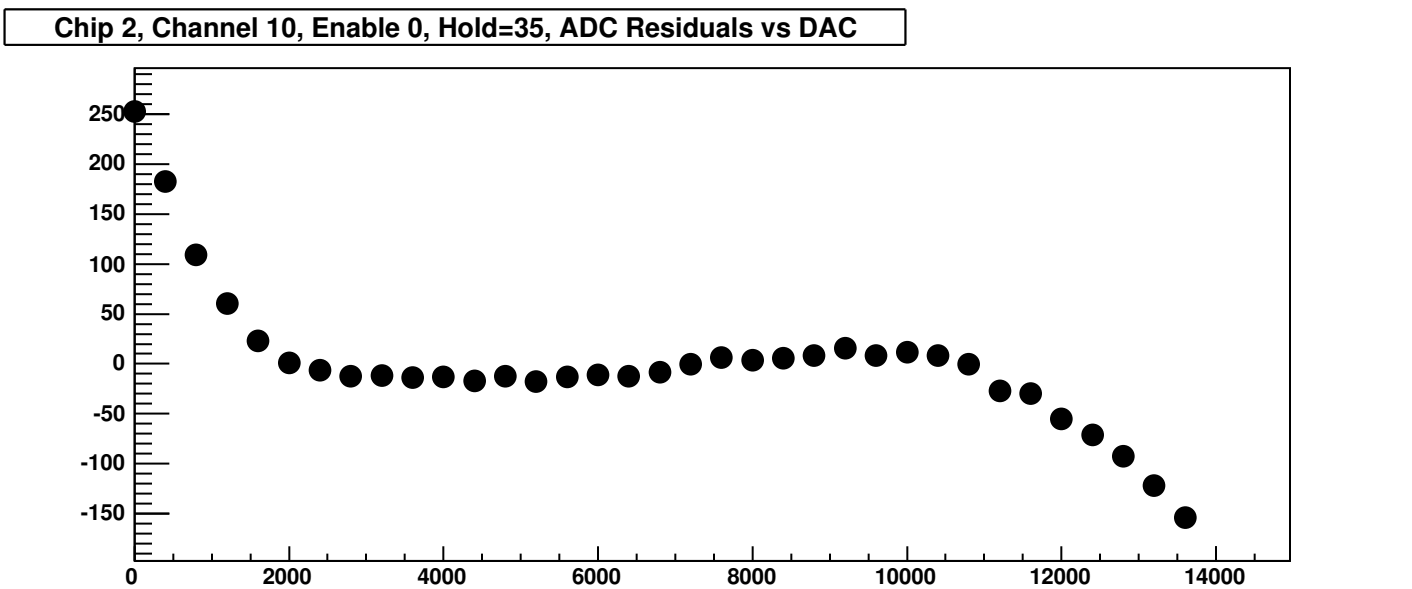


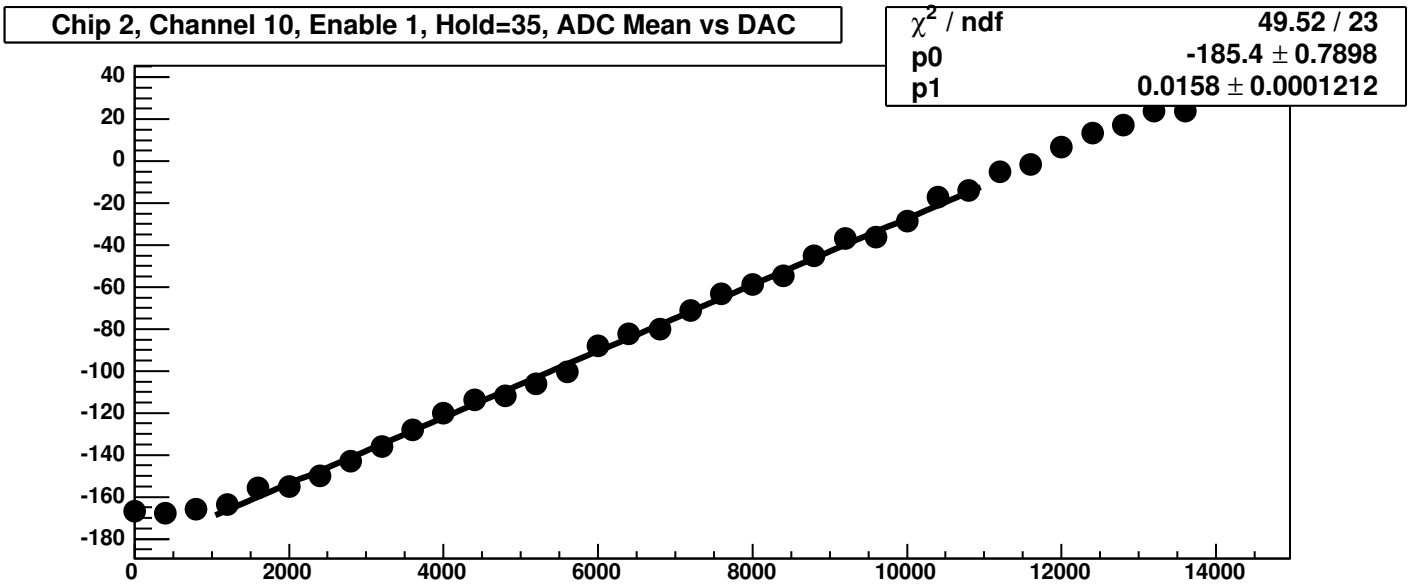
Chip 2, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

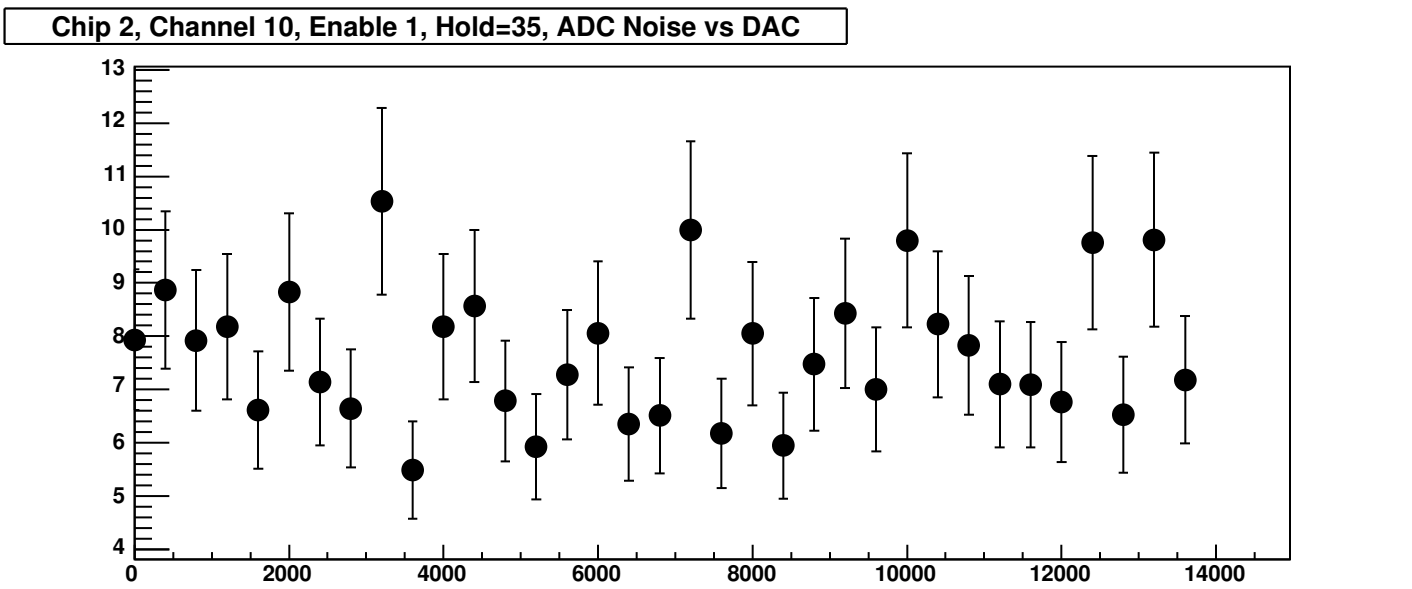


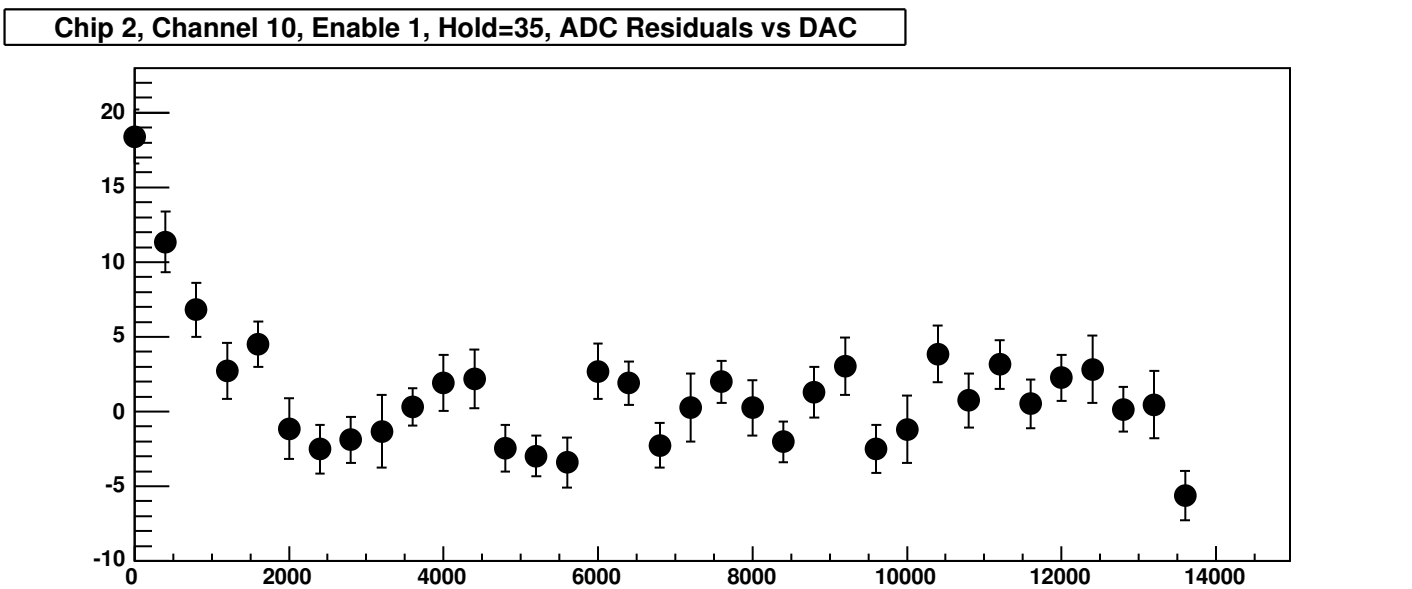

Chip 2, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 557.8 / 23
p0 -416.2 ± 1.316
p1 0.1869 ± 0.0002125
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

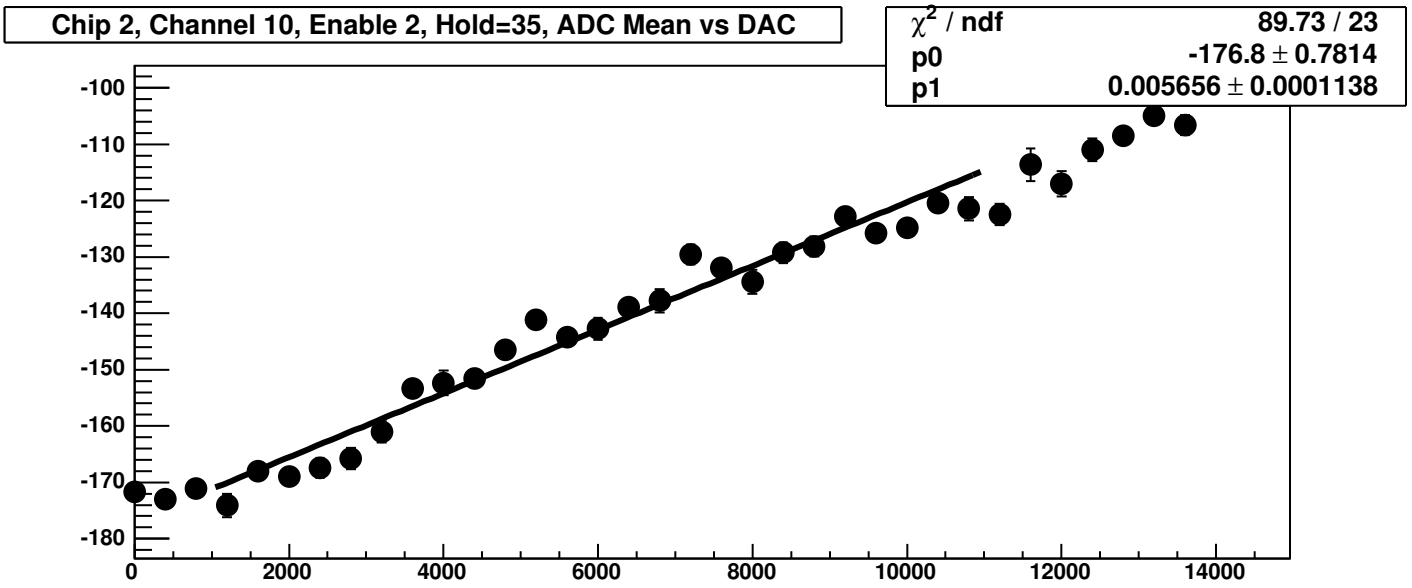

Chip 2, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

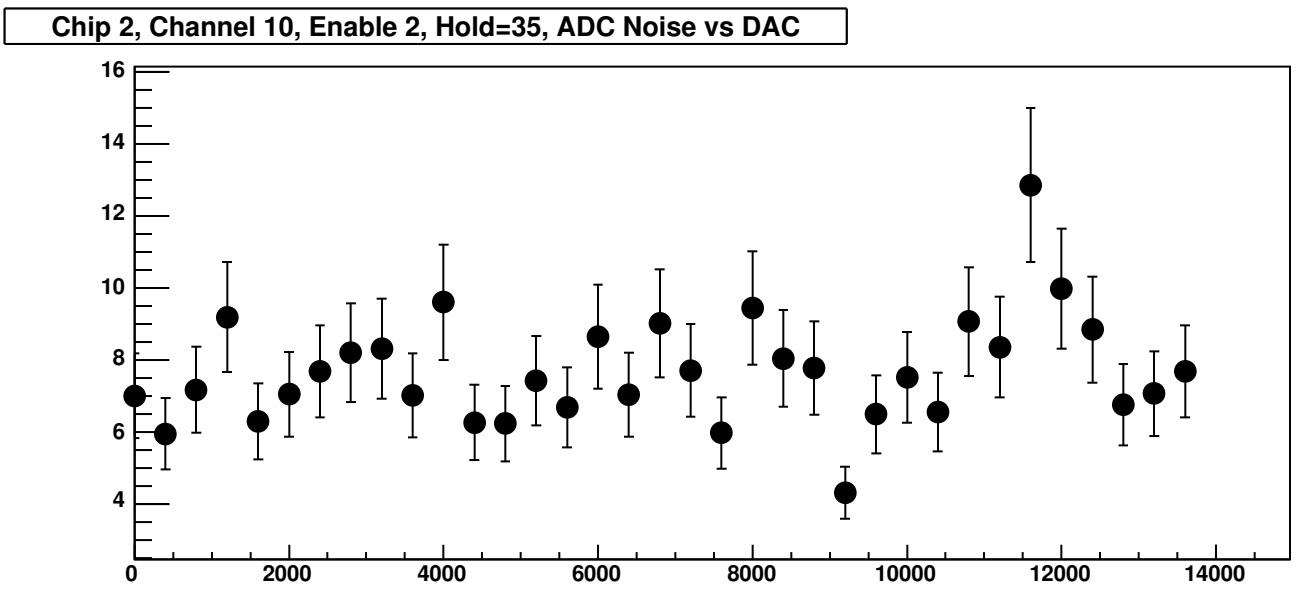

Chip 2, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

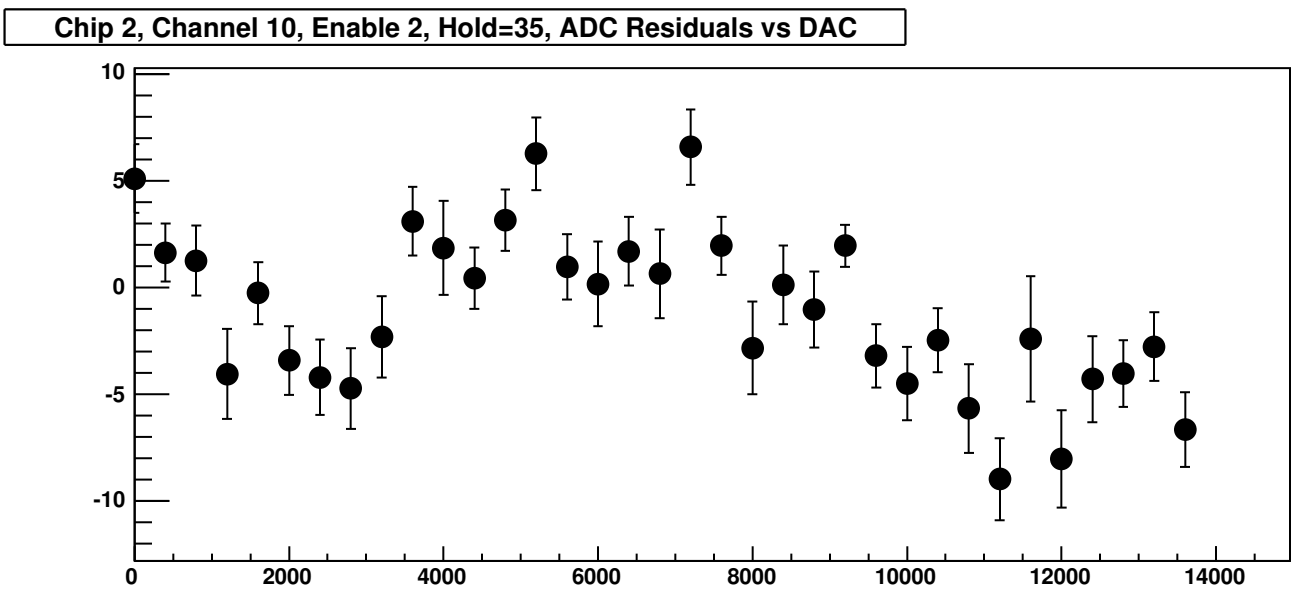

Chip 2, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 49.52 / 23
p0 -185.4 ± 0.7898
p1 0.0158 ± 0.0001212
40
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000

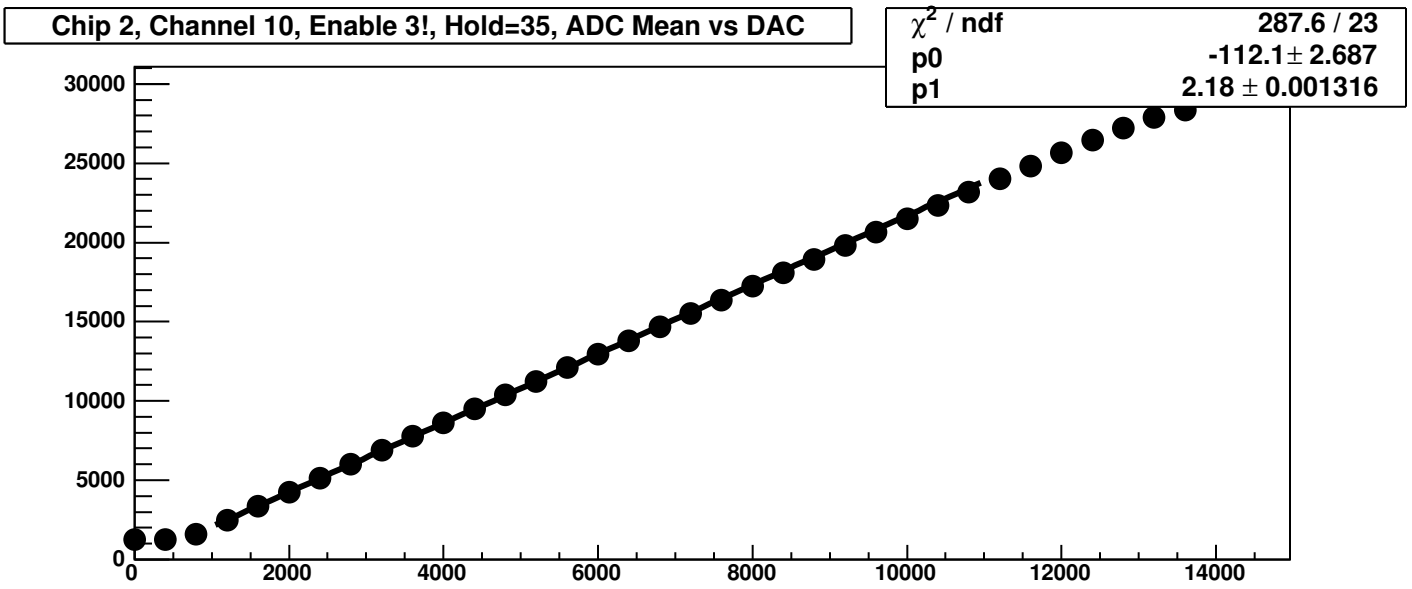

Chip 2, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

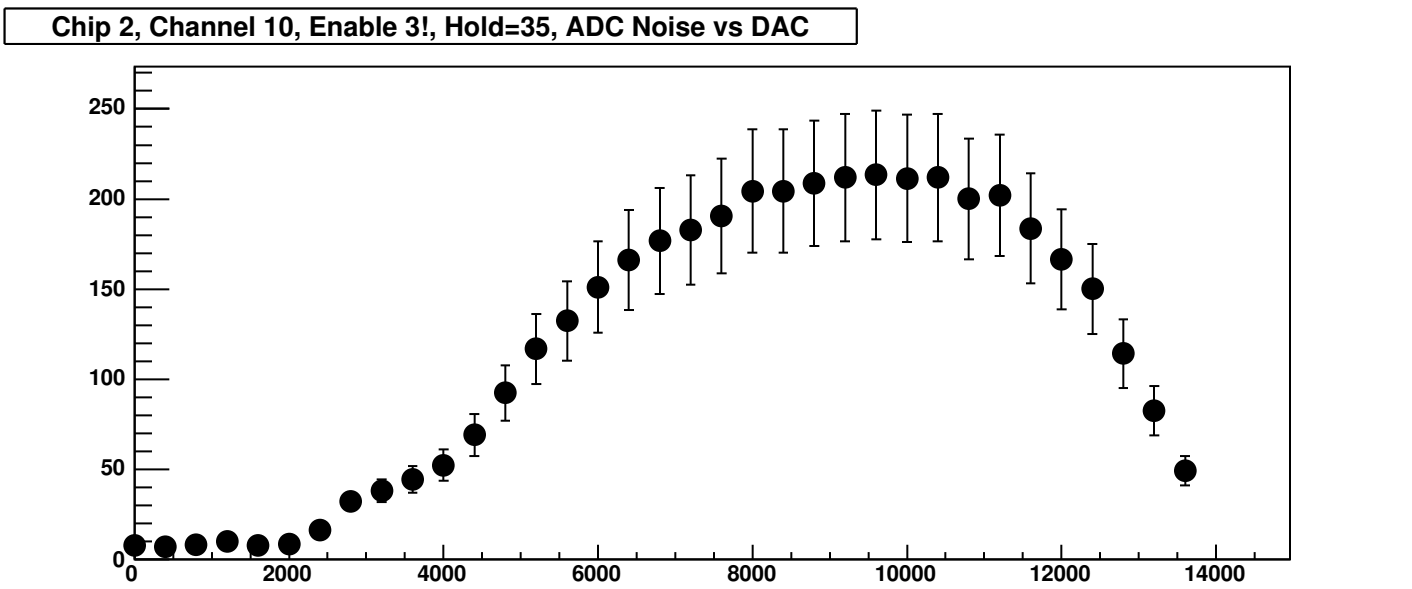

Chip 2, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

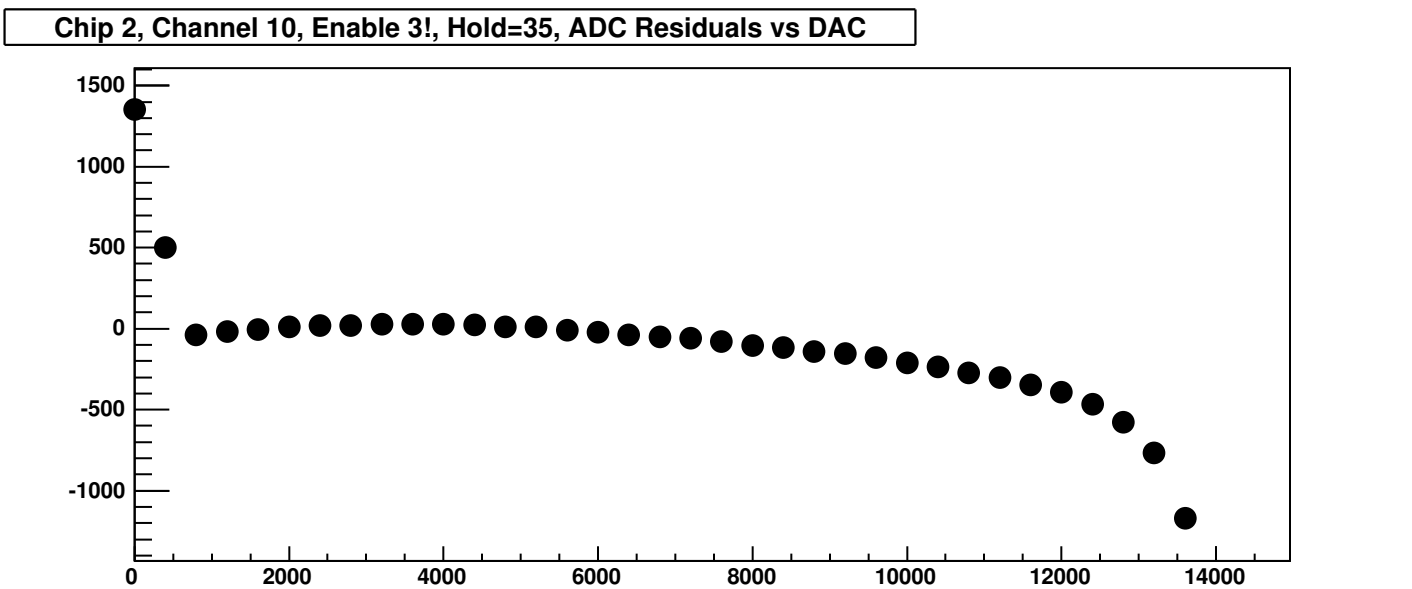

Chip 2, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 89.73 / 23
p0 -176.8 ± 0.7814
p1 0.005656 ± 0.0001138
-100
-110
-120
-130
-140
-150
-160
-170
-180
0
2000
4000
6000
8000
10000
12000
14000

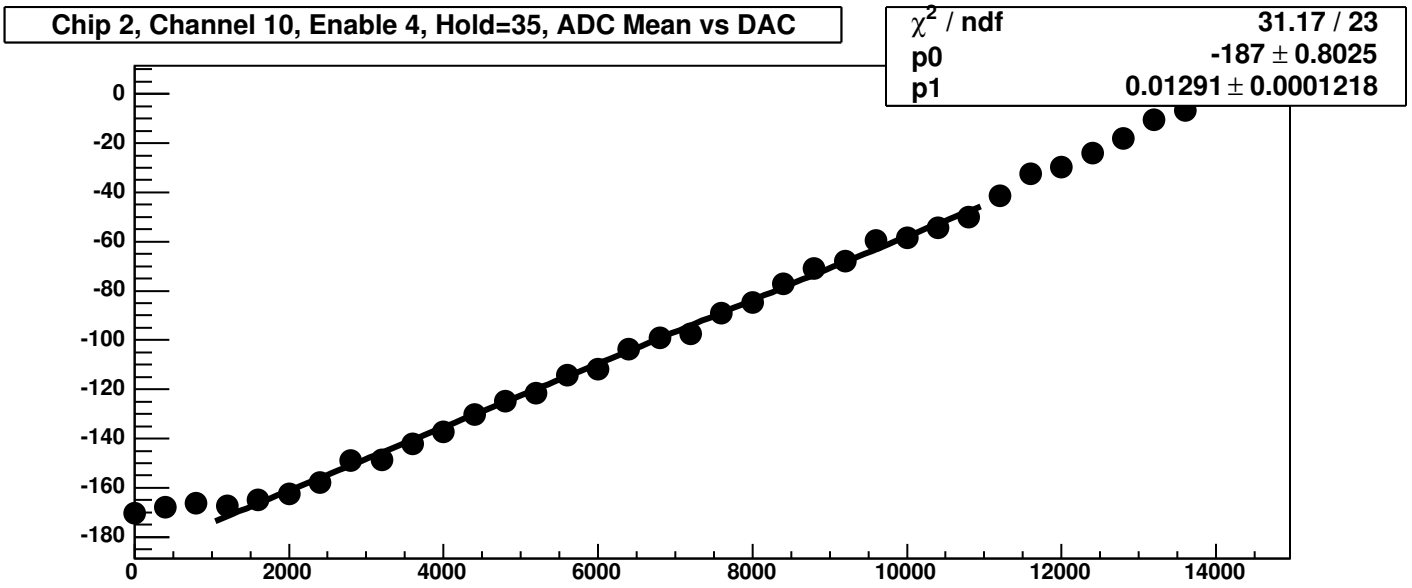

Chip 2, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

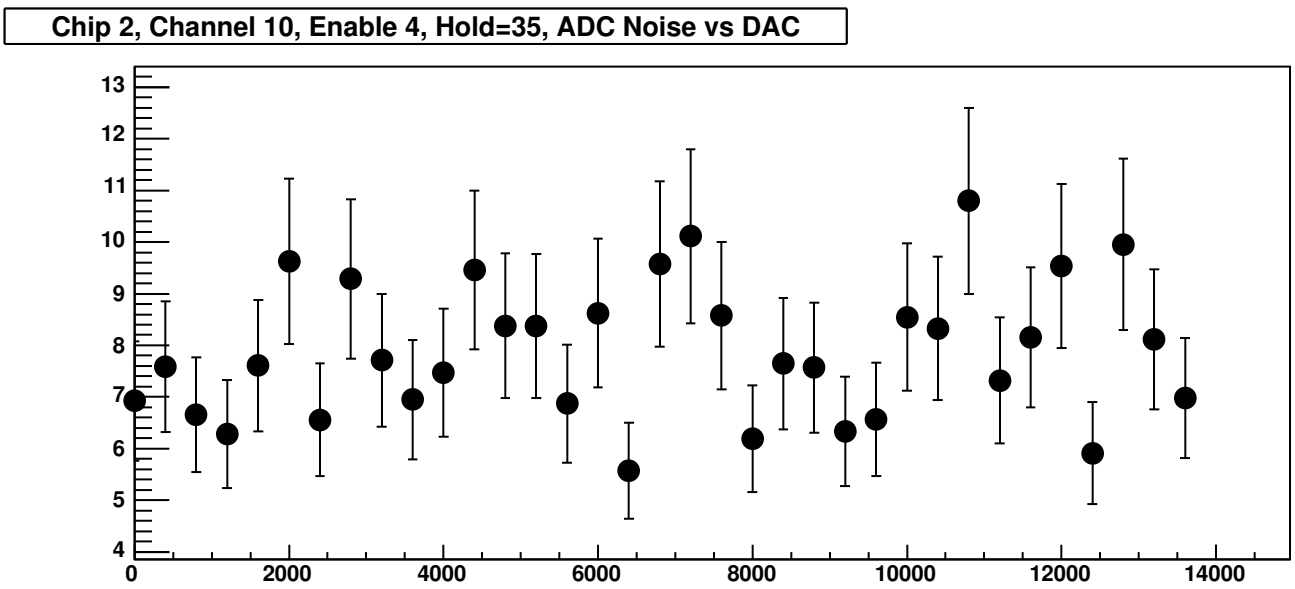

Chip 2, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

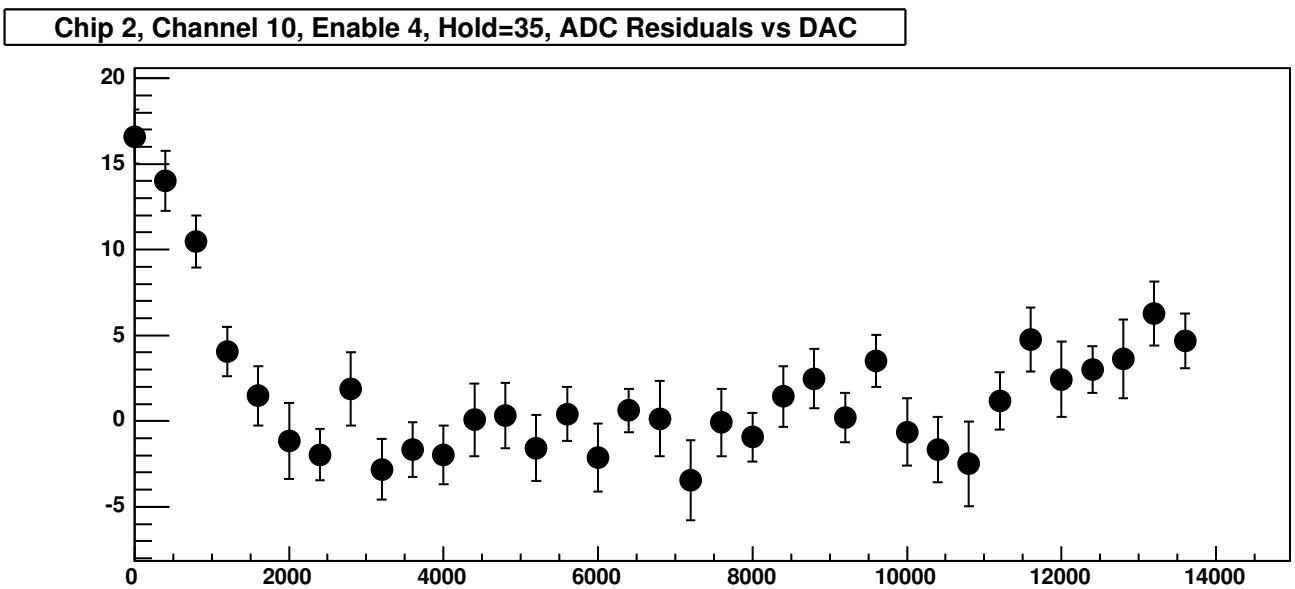

Chip 2, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 287.6 / 23
p0 -112.1 ± 2.687
p1 2.18 ± 0.001316
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

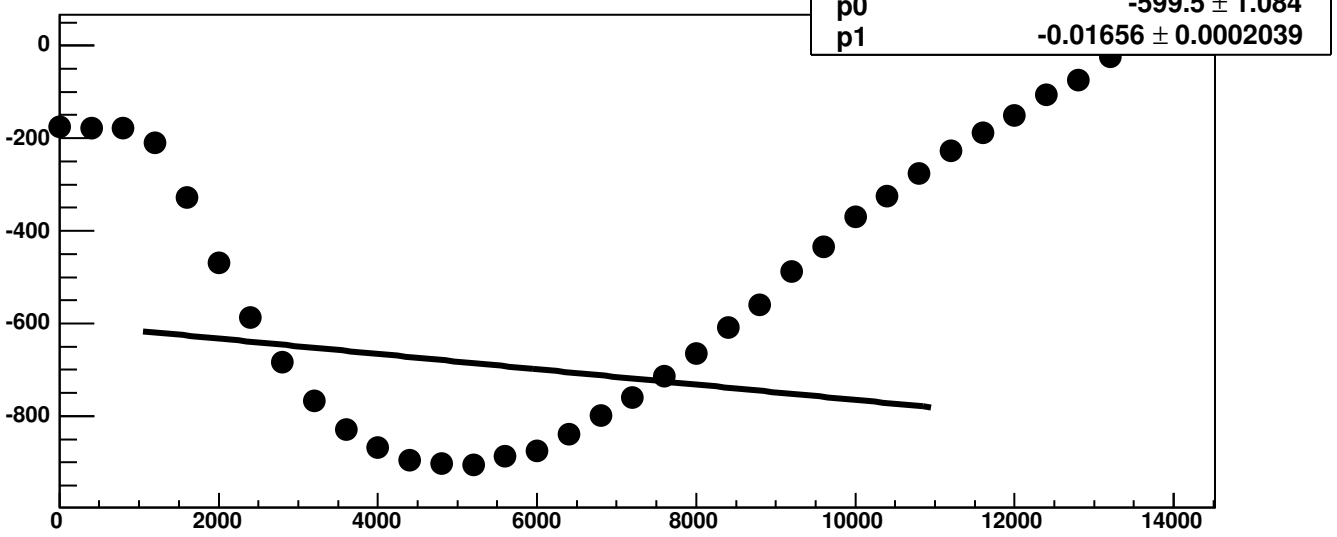

Chip 2, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 31.17 / 23
p0 -187 ± 0.8025
p1 0.01291 ± 0.0001218
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000

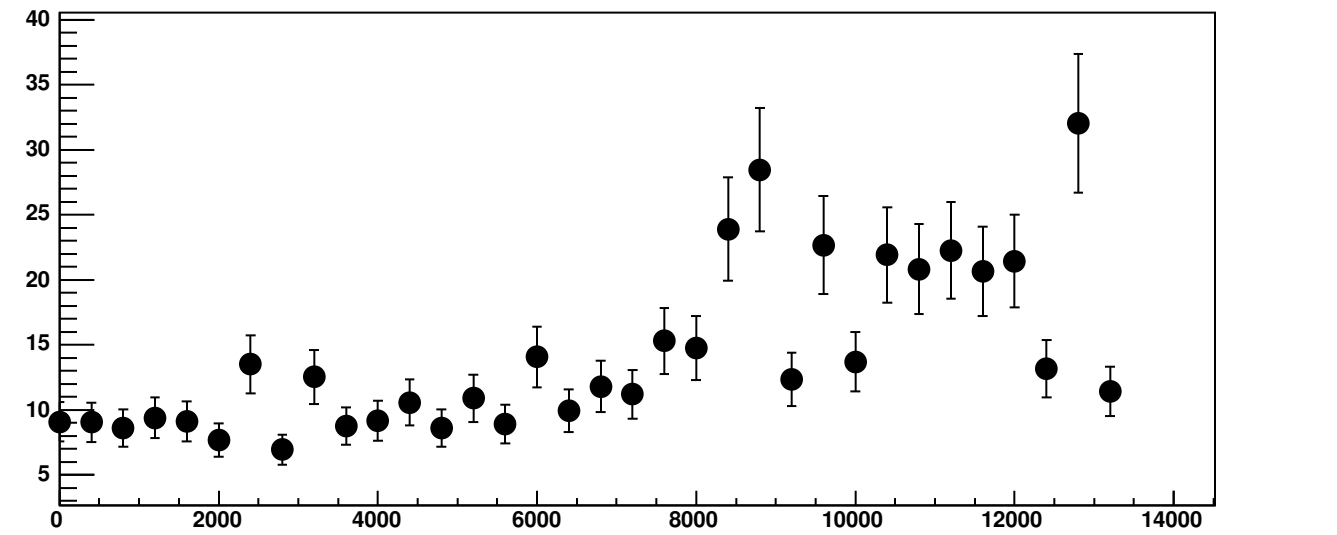

Chip 2, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

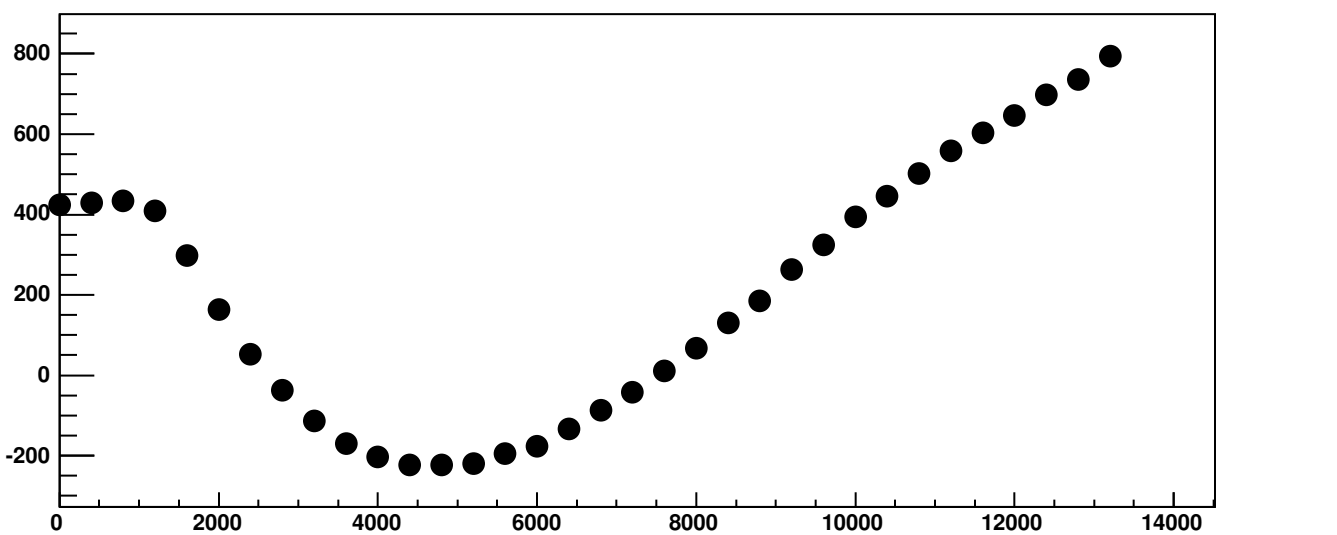
Chip 2, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

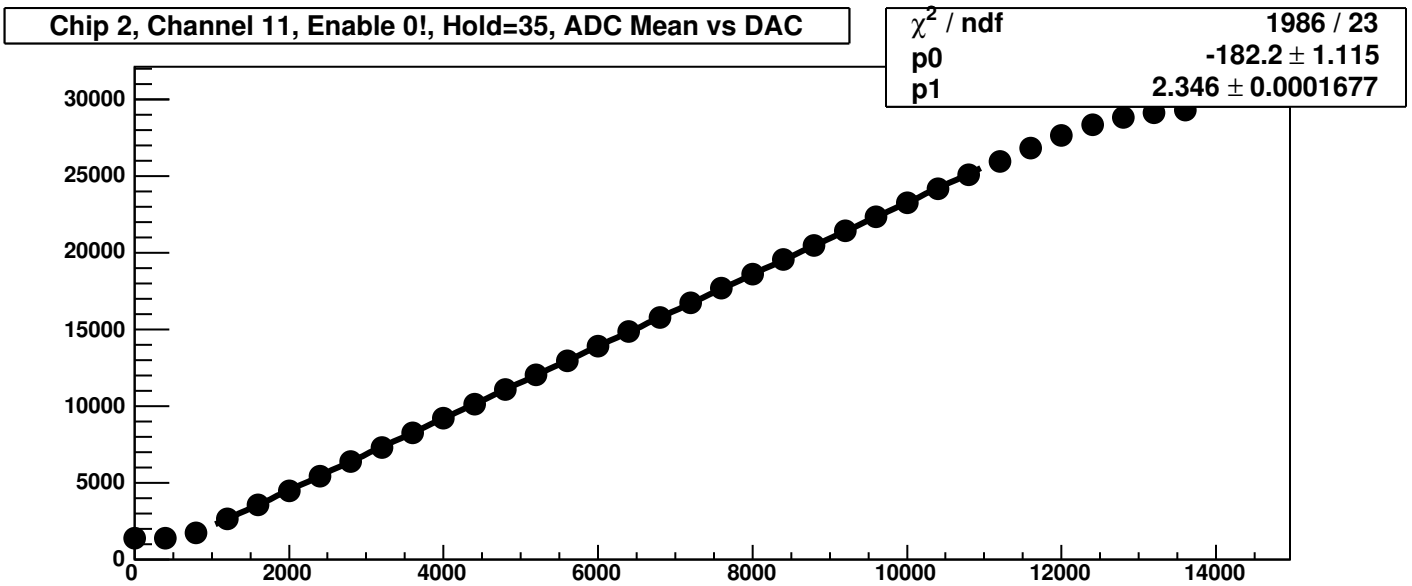


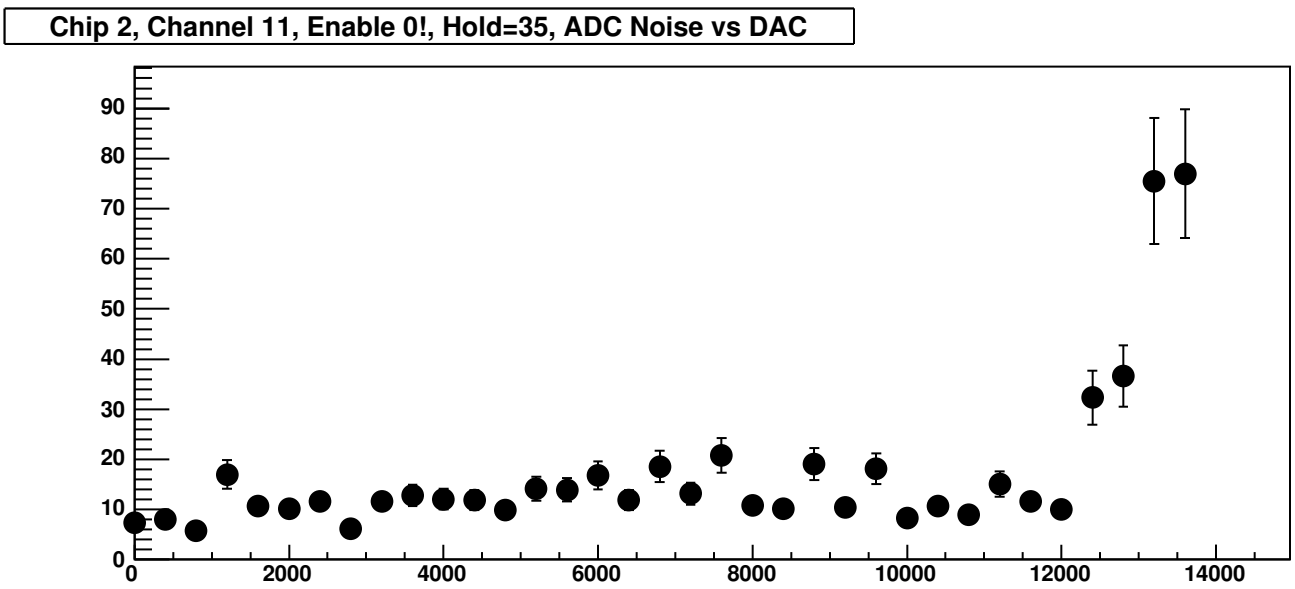
Chip 2, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

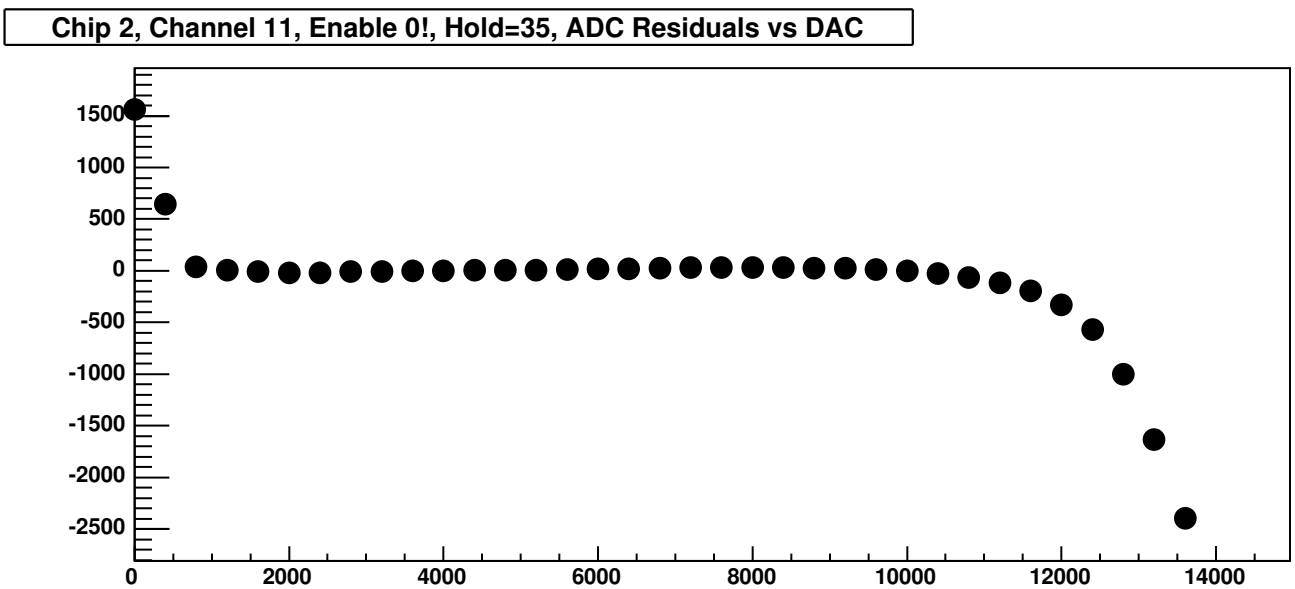


Chip 2, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

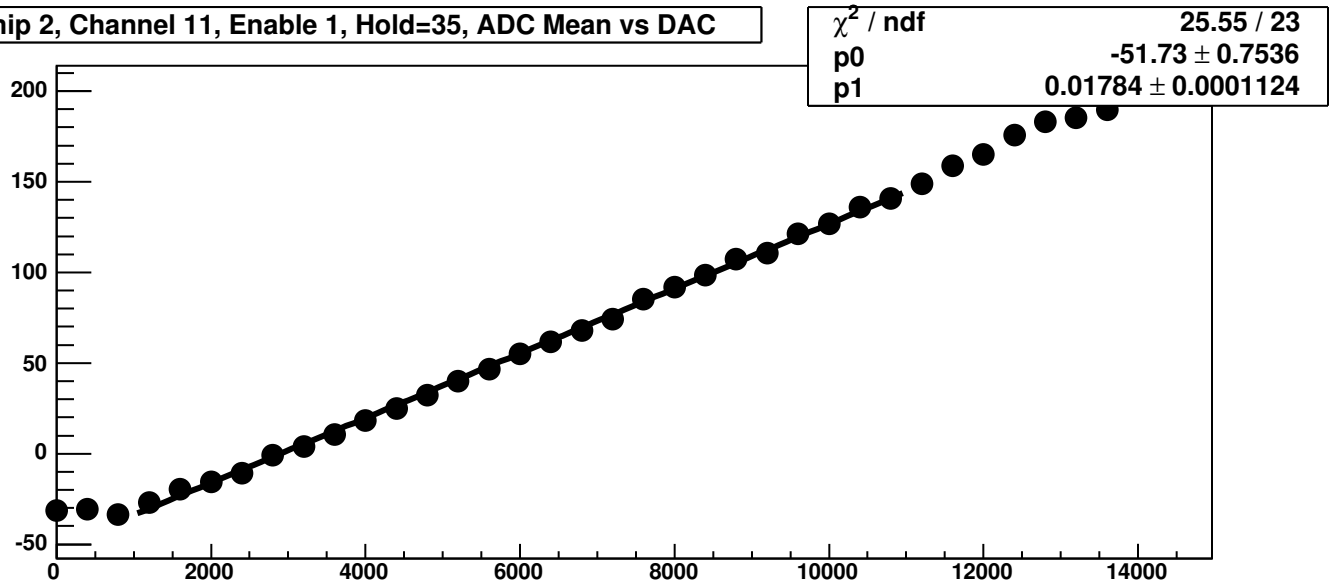



Chip 2, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1986 / 23
p0 -182.2 ± 1.115
p1 2.346 ± 0.0001677
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

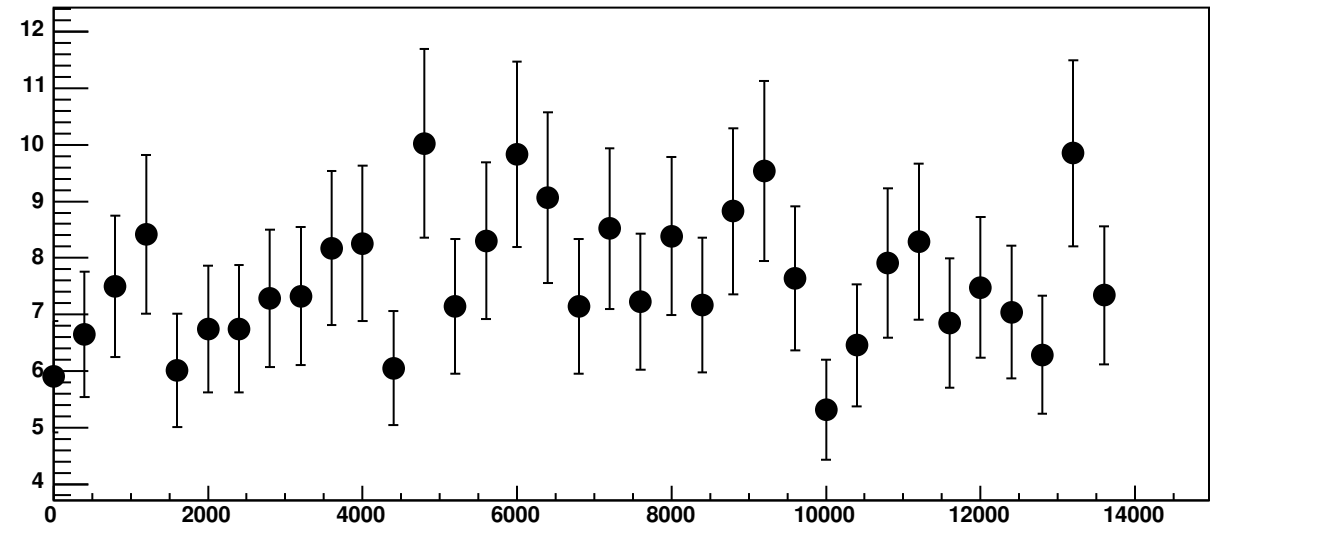

Chip 2, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

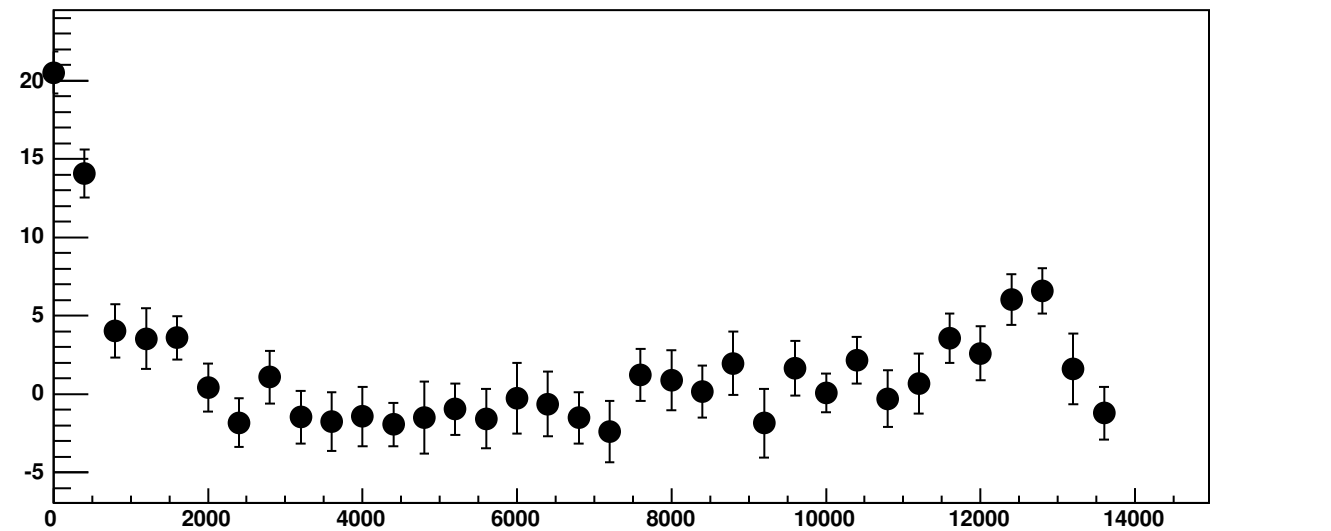
Chip 2, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

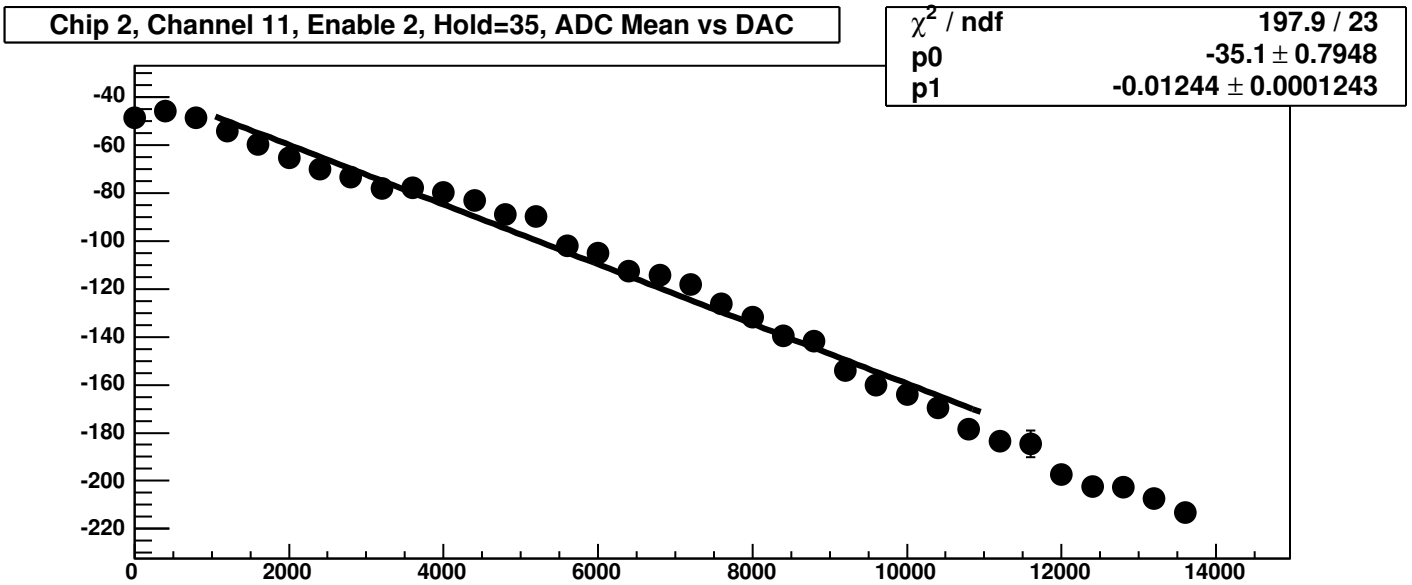


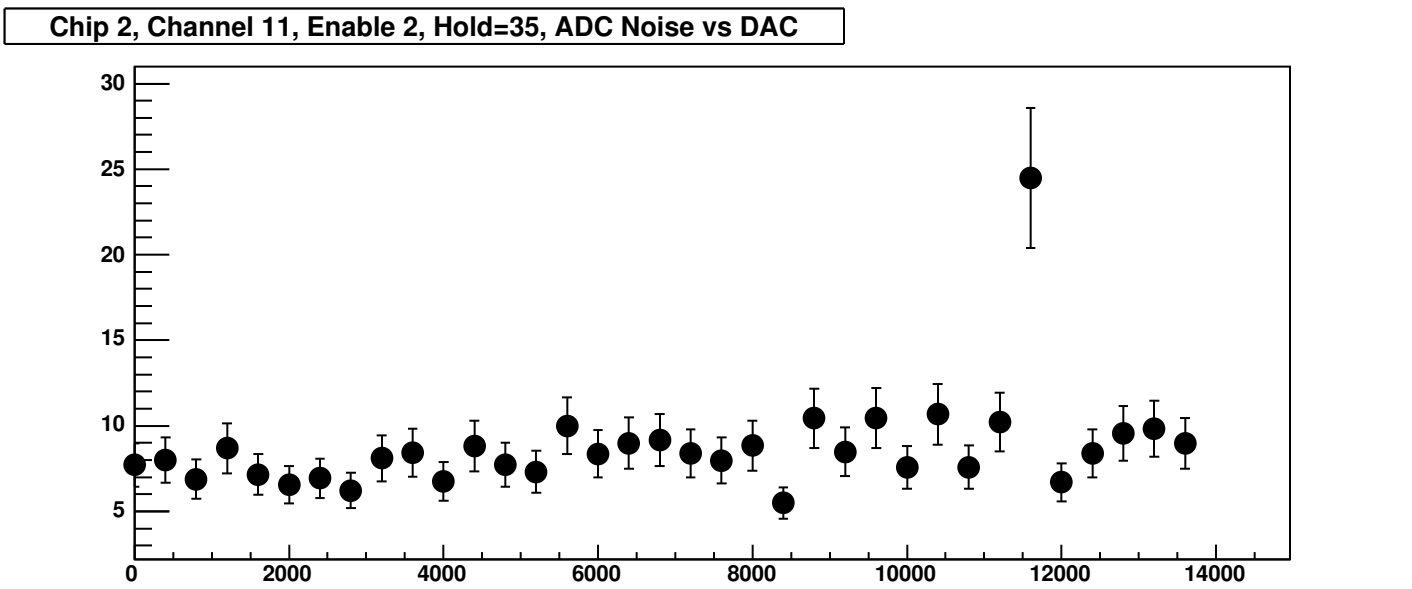
Chip 2, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

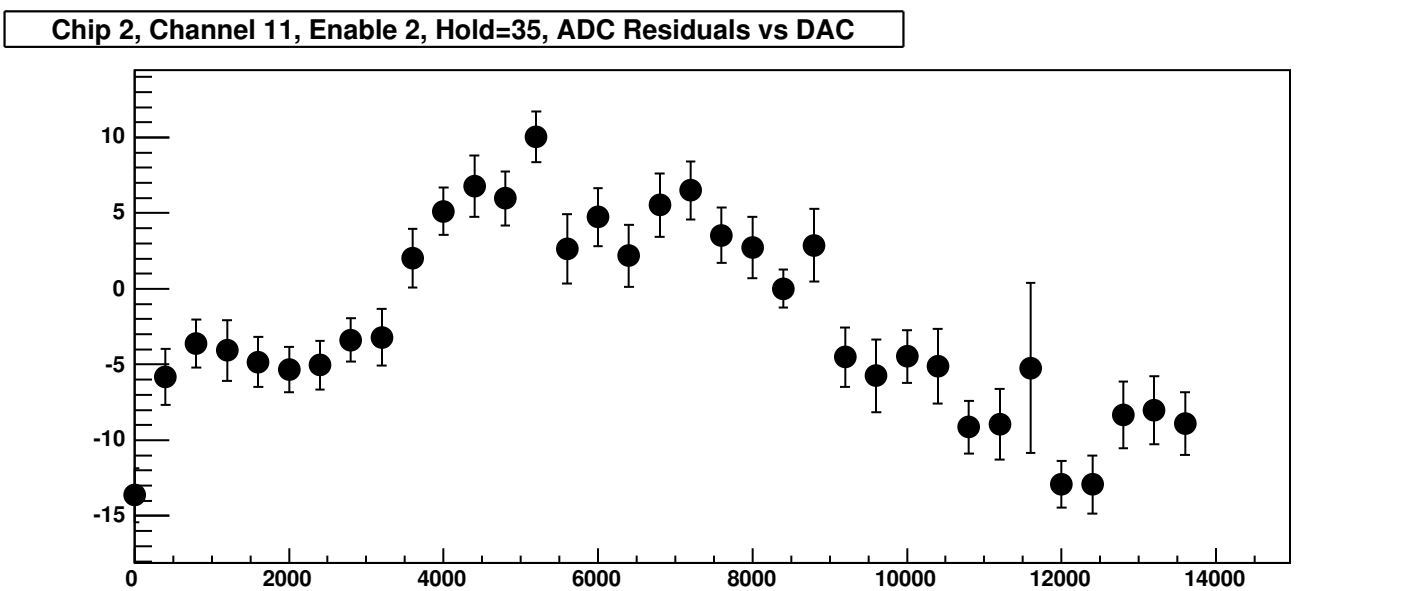


Chip 2, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

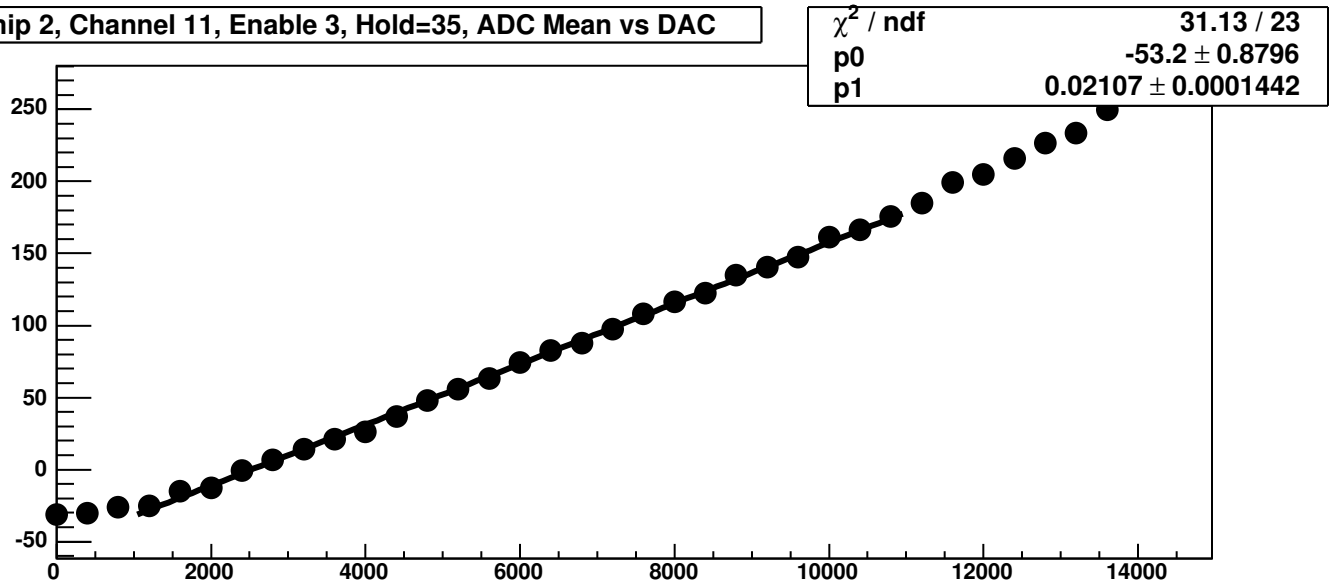



Chip 2, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 197.9 / 23
p0 -35.1 ± 0.7948
p1 -0.01244 ± 0.0001243
-40
-60
-80
-100
-120
-140
-160
-180
-200
-220
0
2000
4000
6000
8000
10000
12000
14000

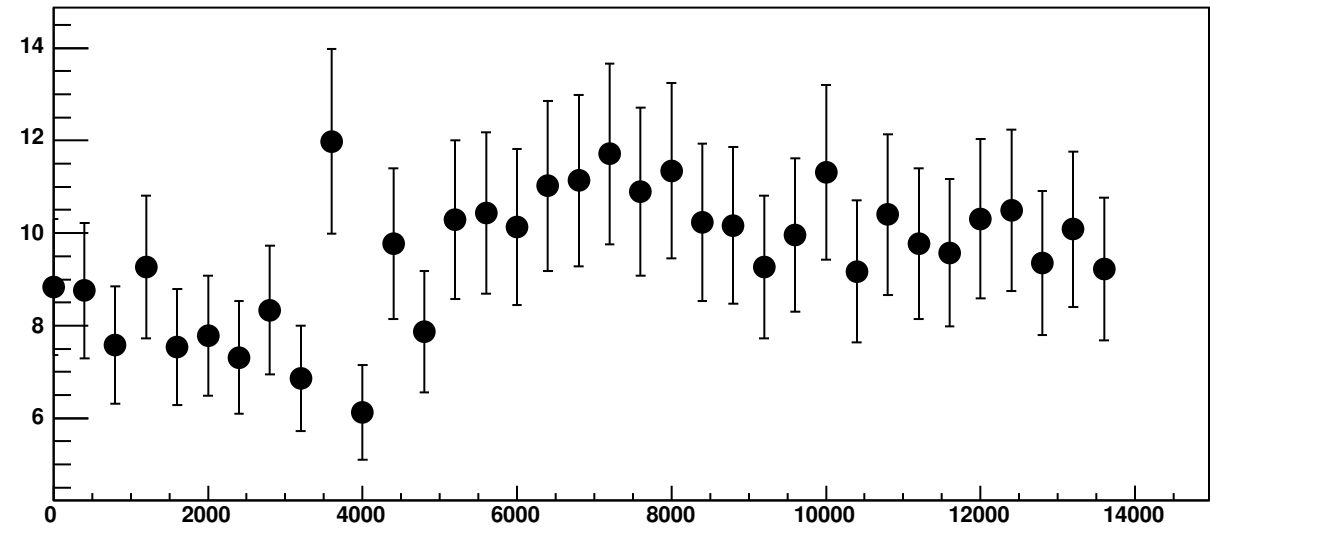

Chip 2, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

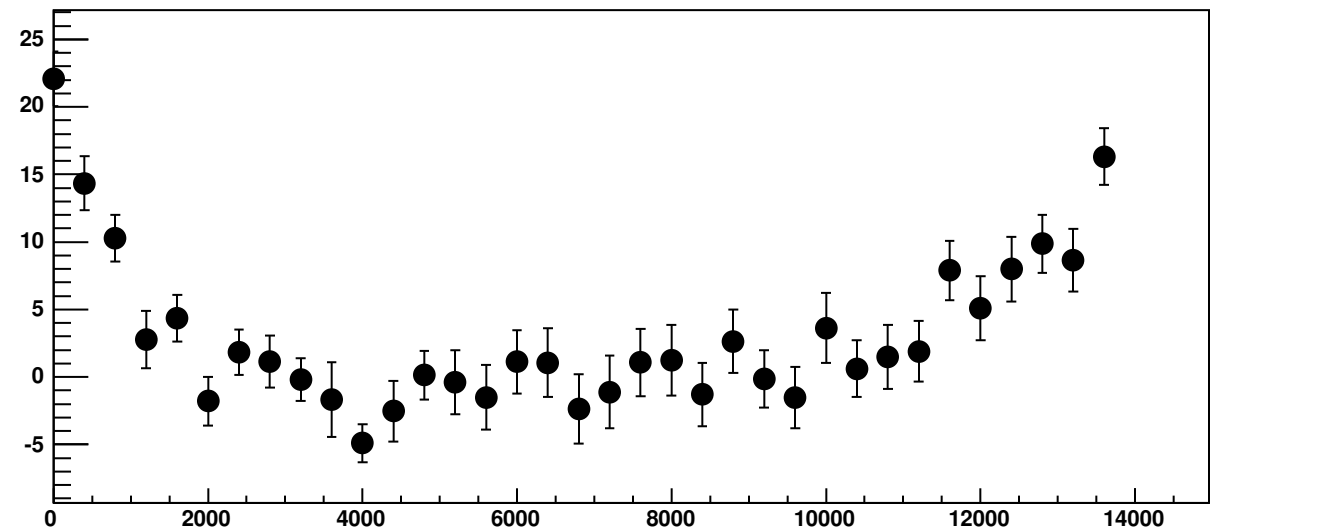
Chip 2, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC



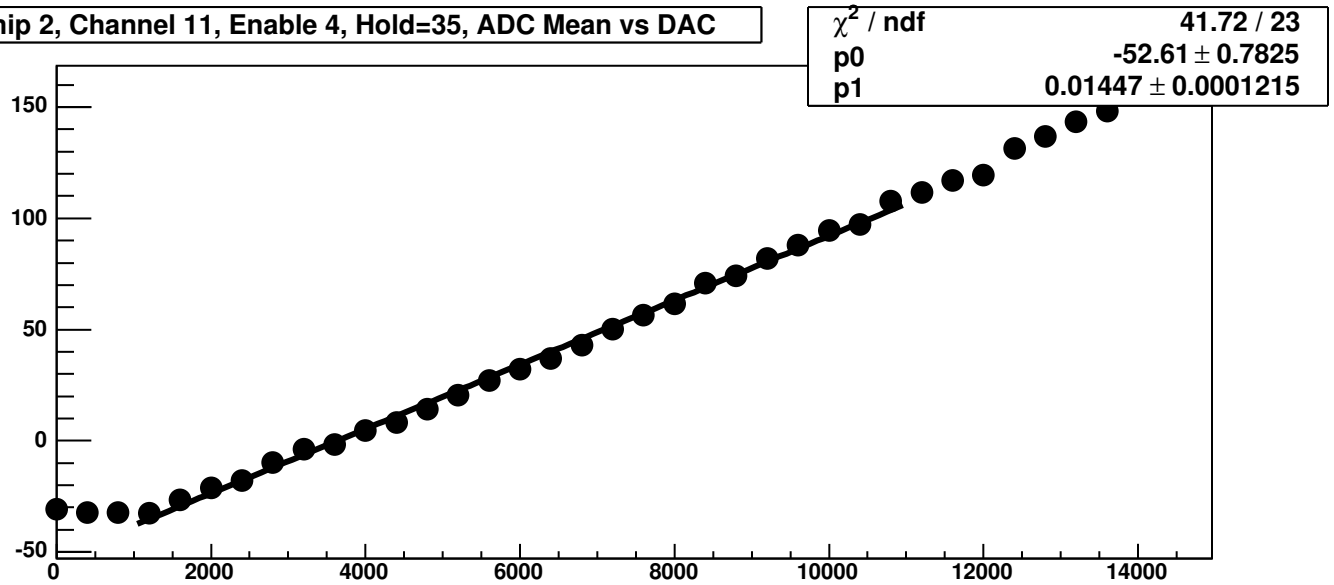
Chip 2, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



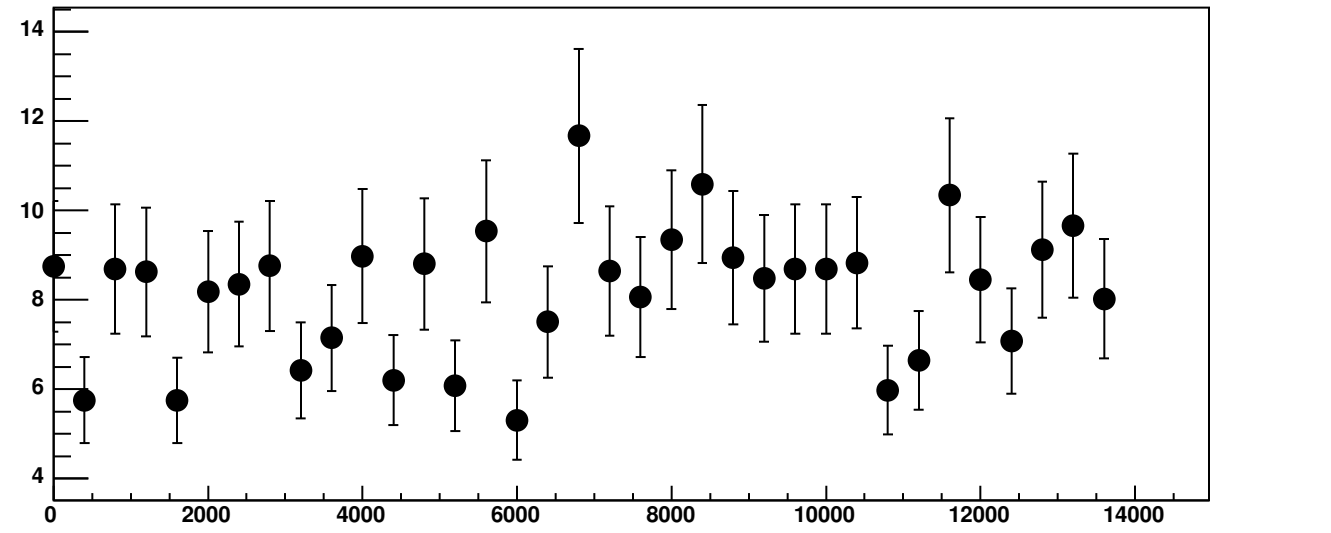
Chip 2, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC



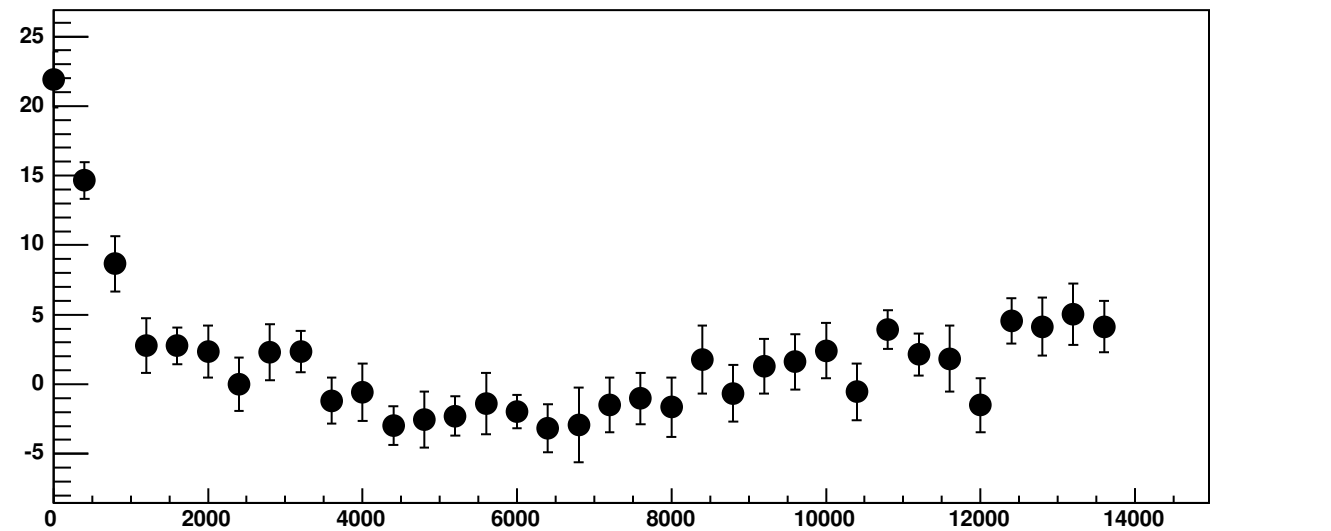
Chip 2, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

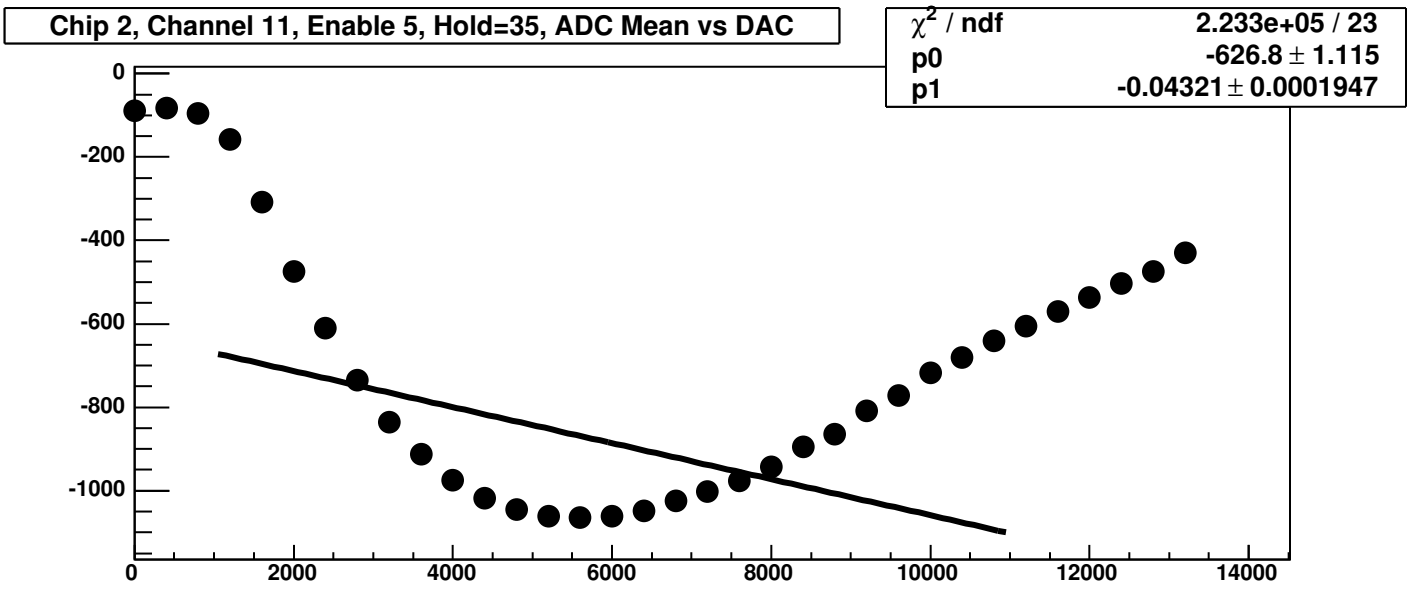


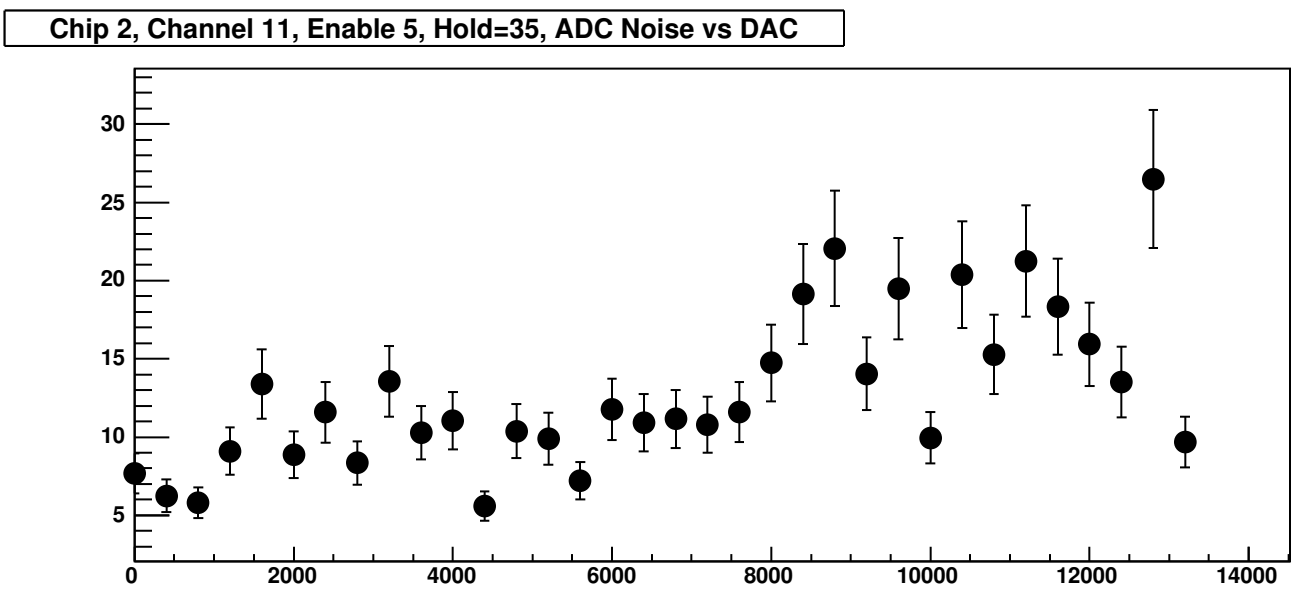
Chip 2, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

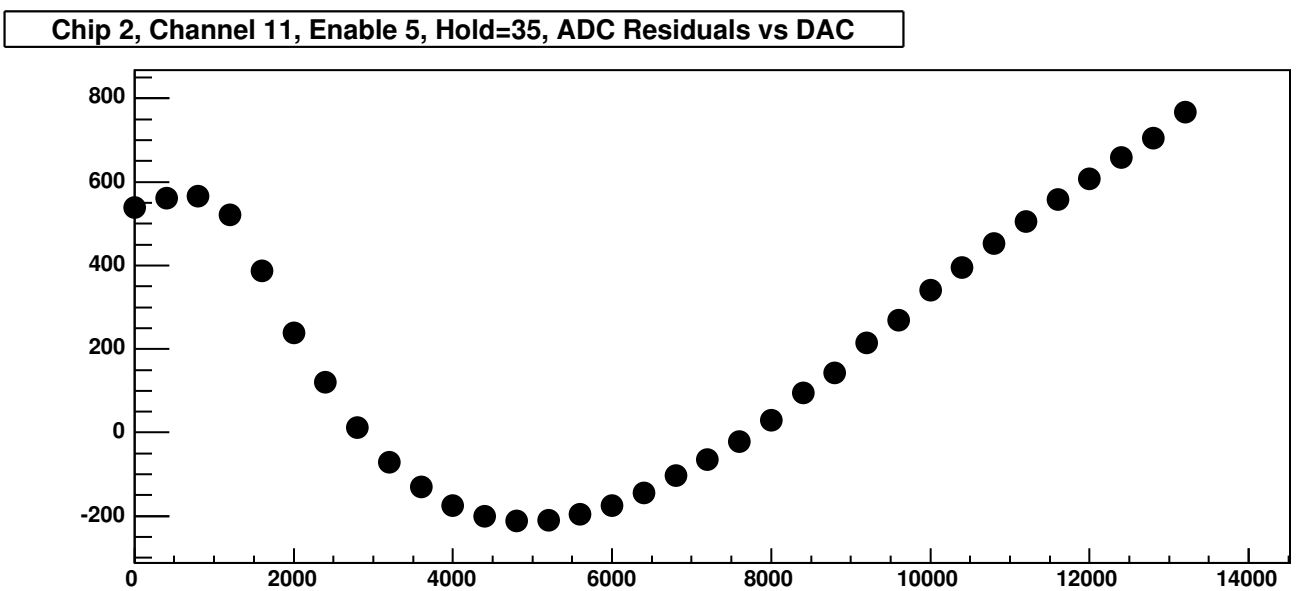


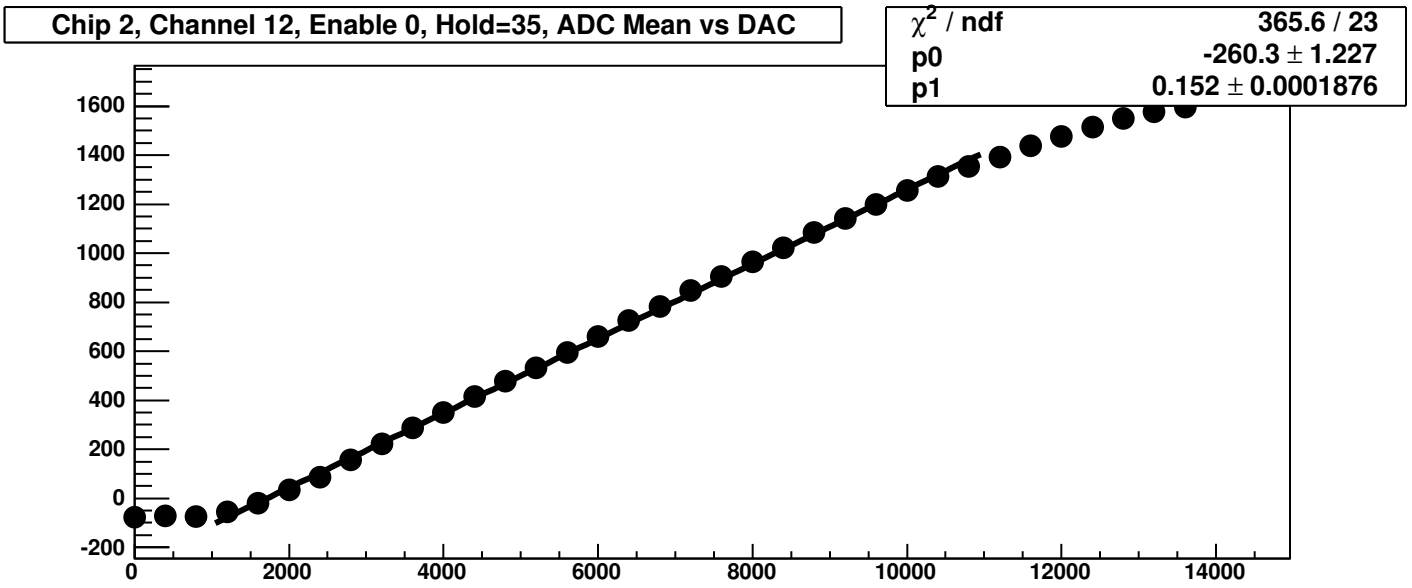
Chip 2, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

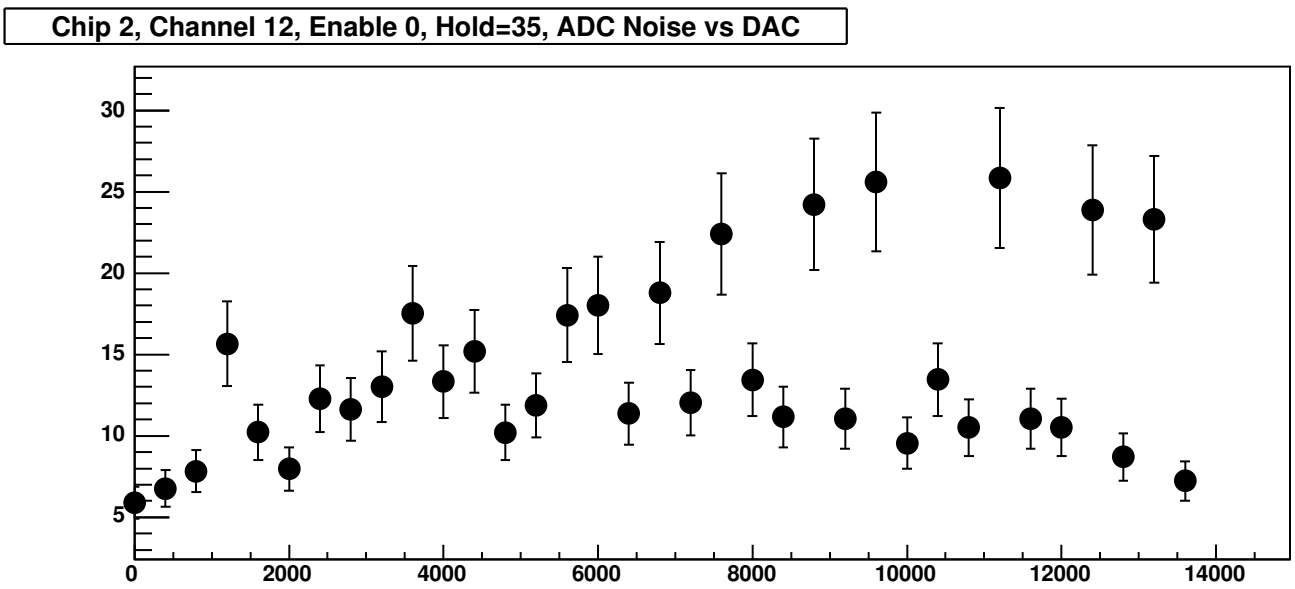


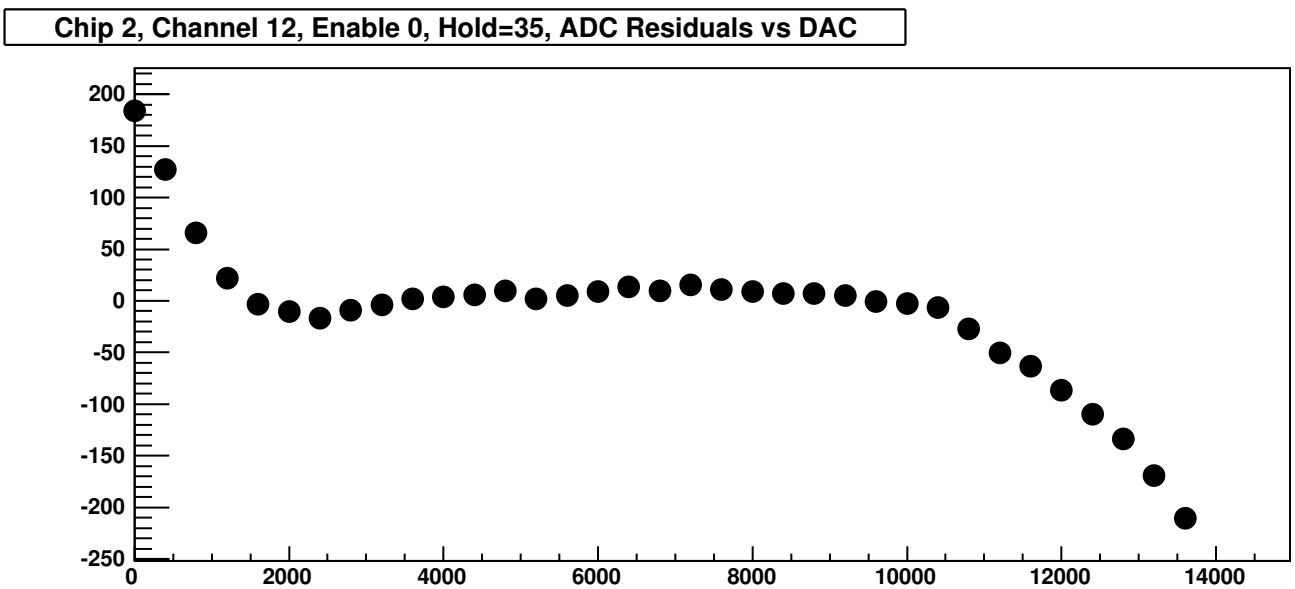

Chip 2, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2.233e+05 / 23
p0 -626.8 ± 1.115
p1 -0.04321 ± 0.0001947
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

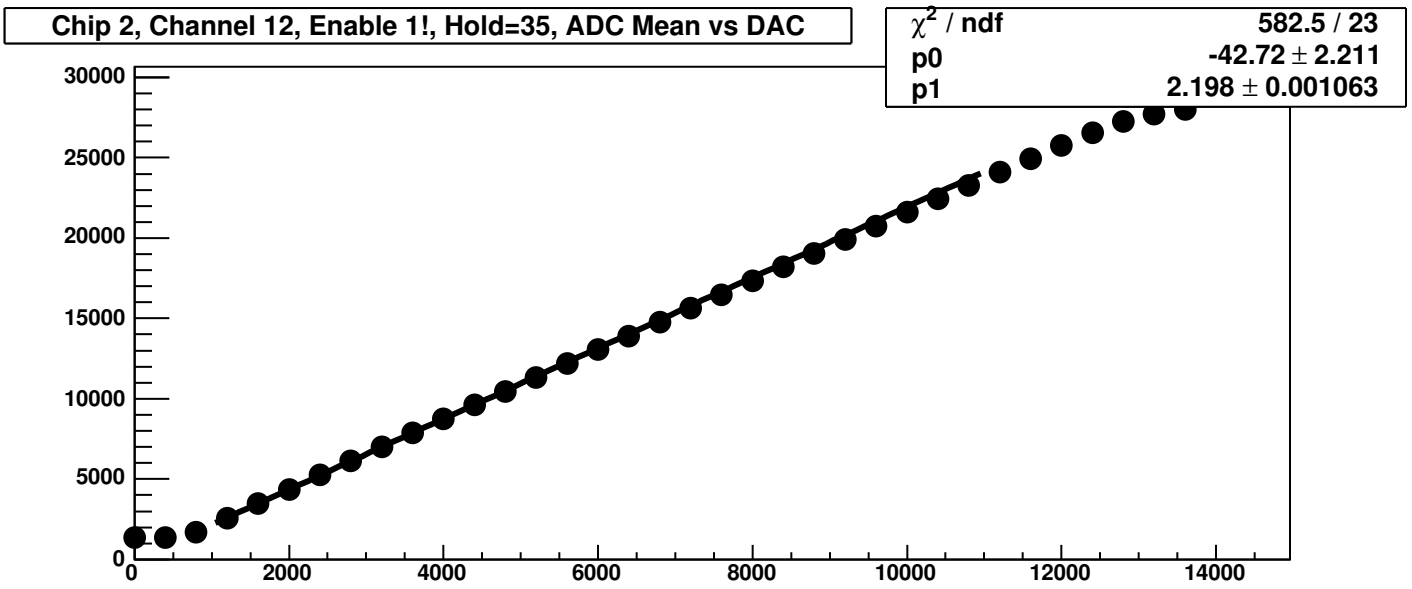

Chip 2, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

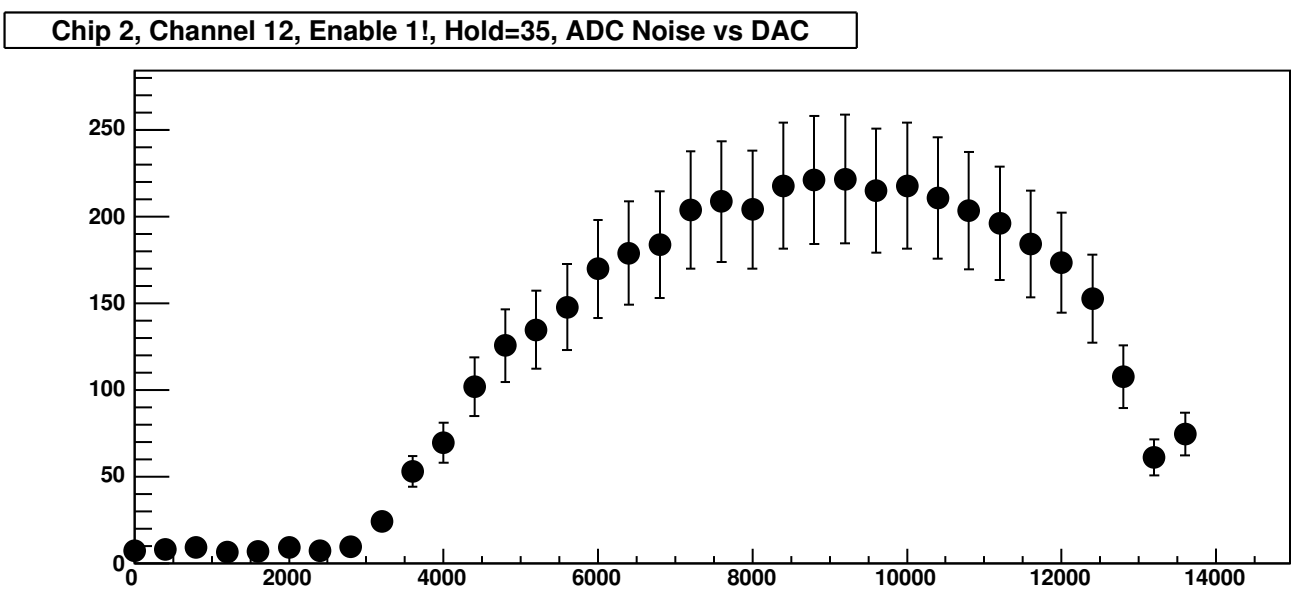

Chip 2, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

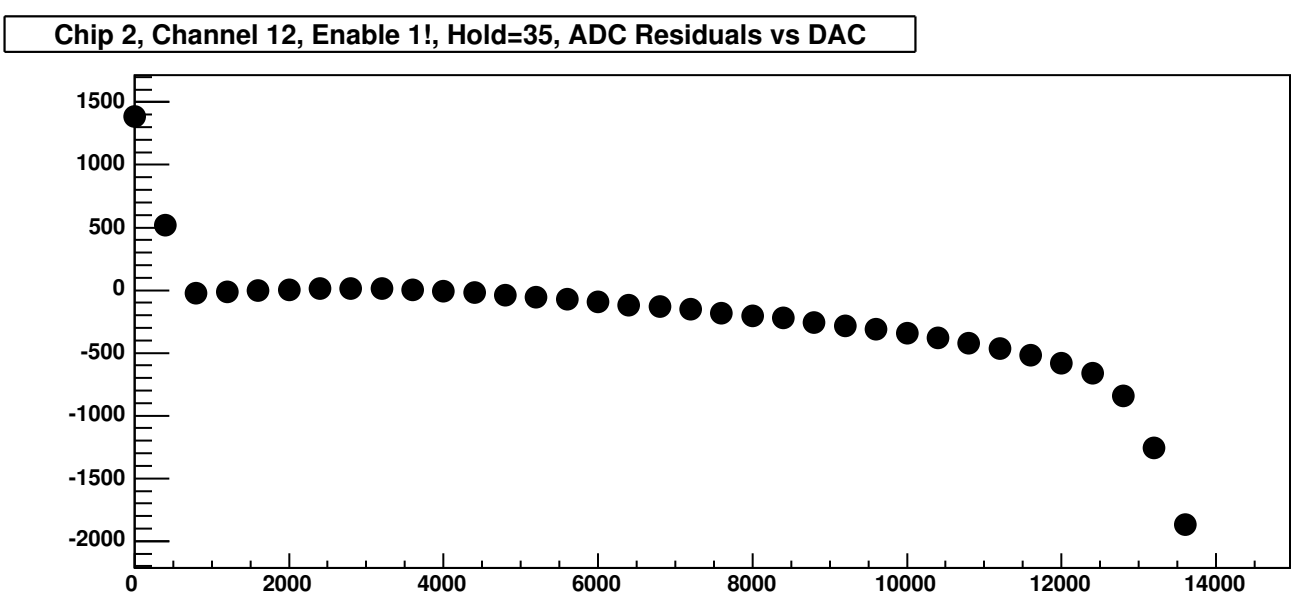

Chip 2, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 365.6 / 23
p0 -260.3 ± 1.227
p1 0.152 ± 0.0001876
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

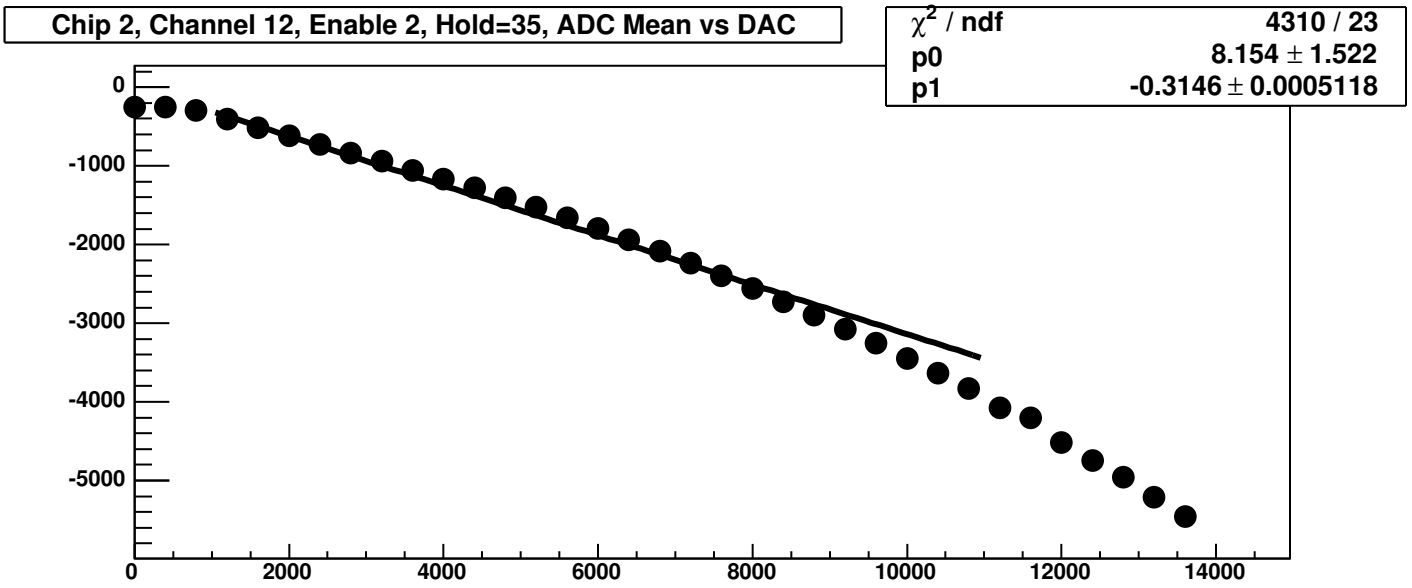

Chip 2, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

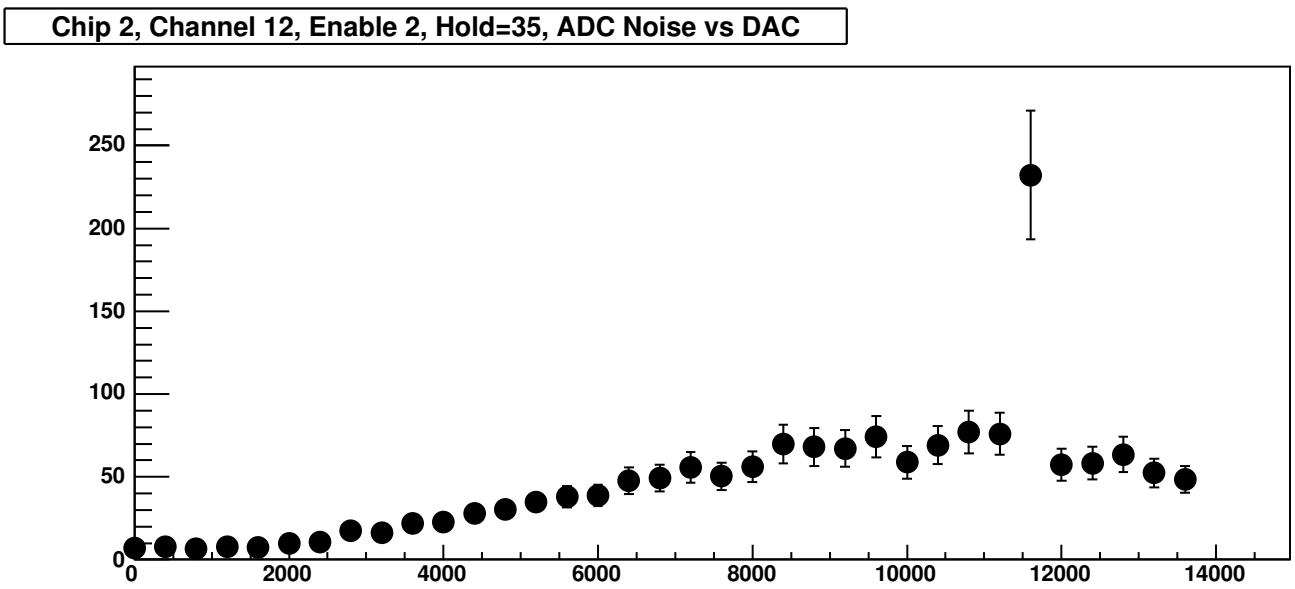

Chip 2, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

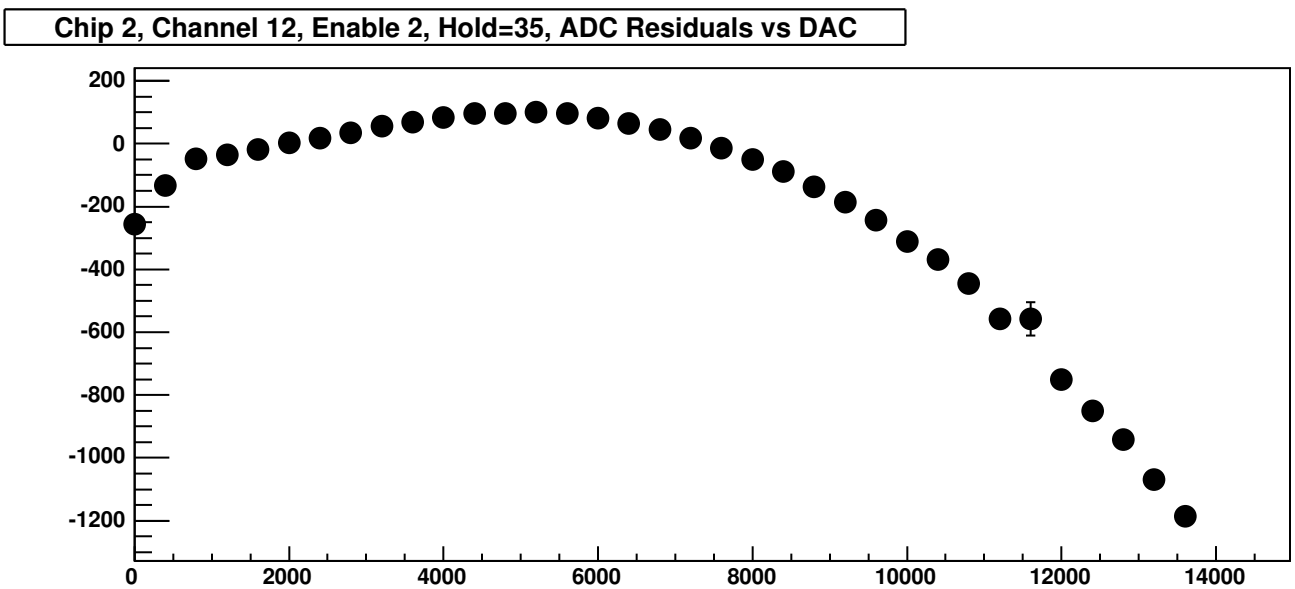

Chip 2, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 582.5 / 23
p0 -42.72 ± 2.211
p1 2.198 ± 0.001063
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

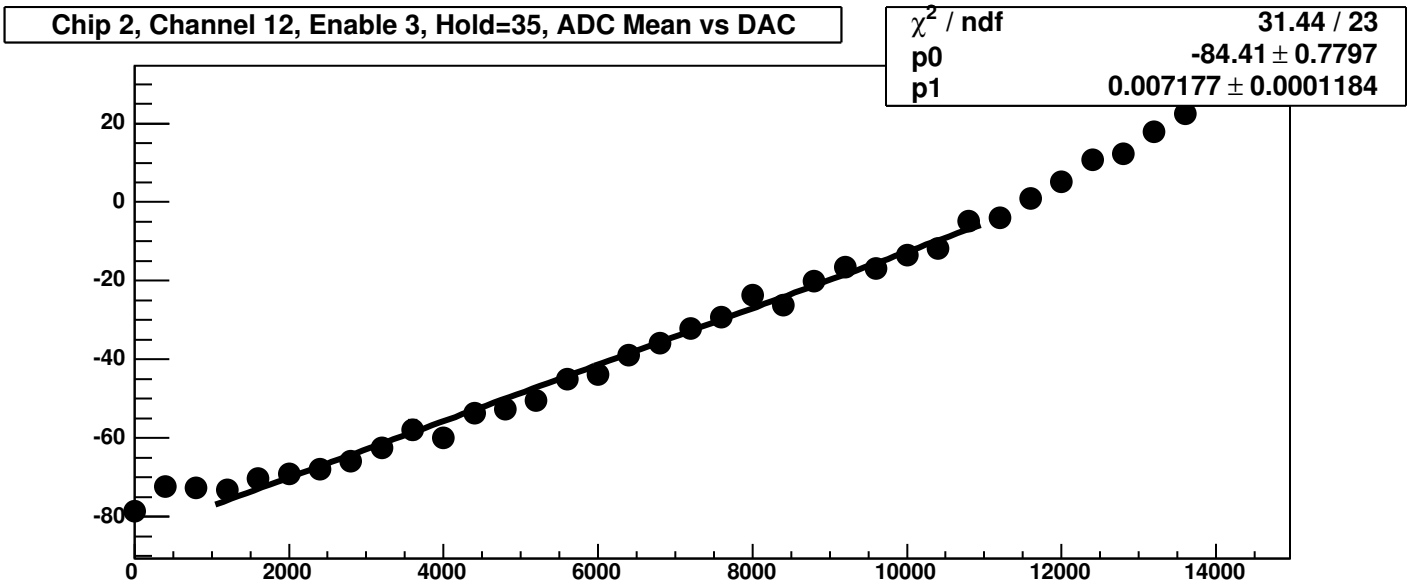

Chip 2, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

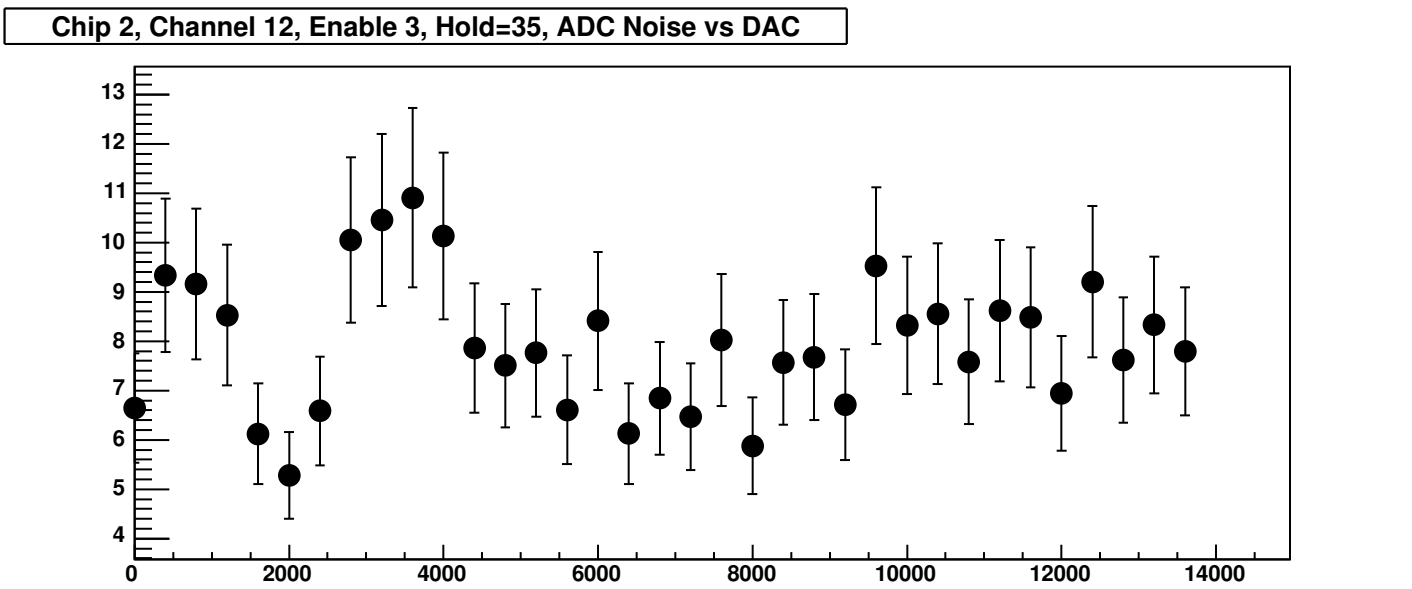

Chip 2, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

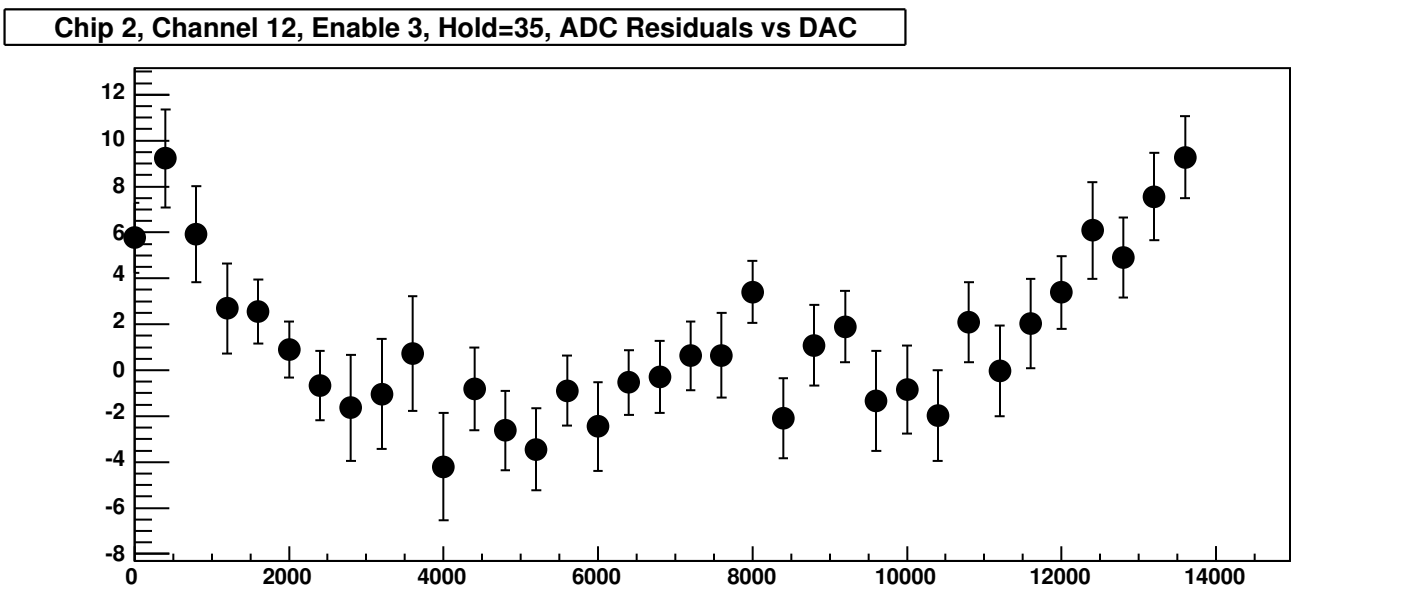

Chip 2, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 4310 / 23
p0 8.154 ± 1.522
p1 -0.3146 ± 0.0005118
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000

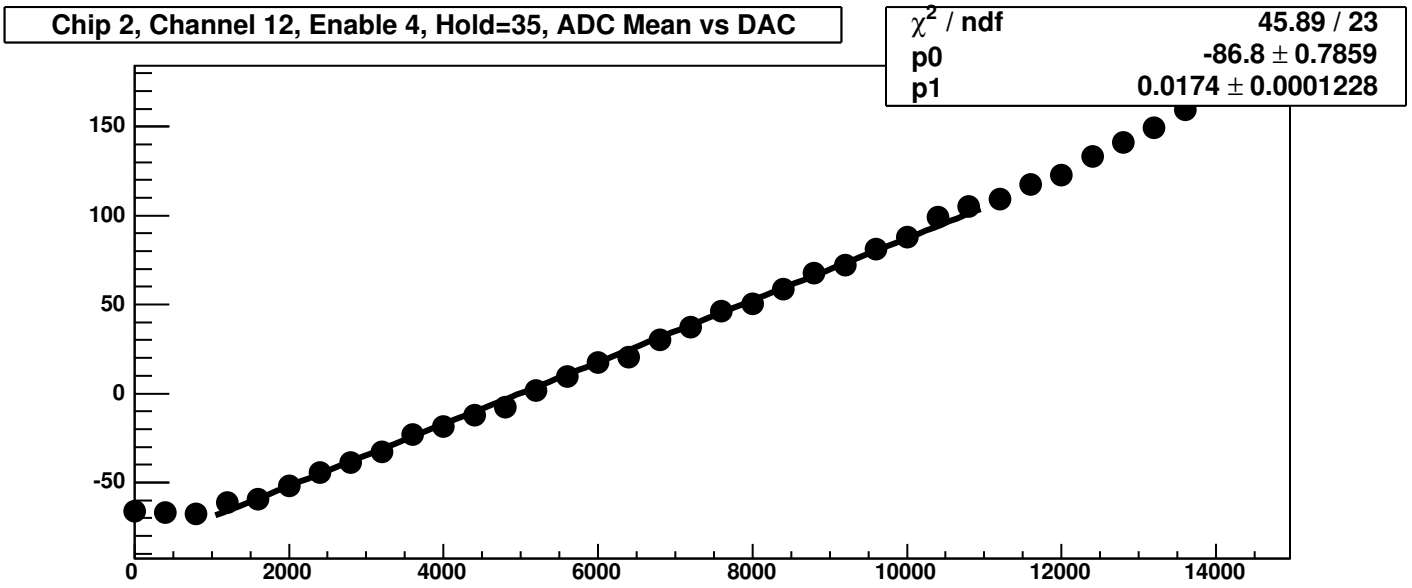

Chip 2, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

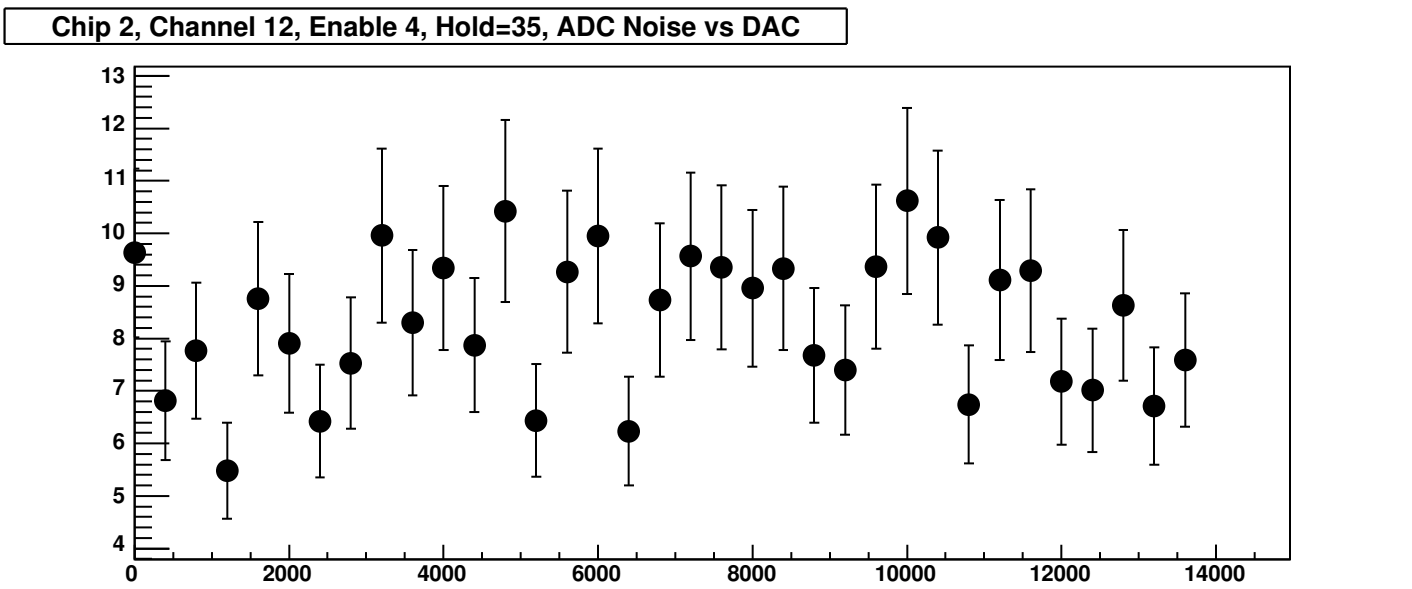

Chip 2, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
0
2000
4000
6000
8000
10000
12000
14000

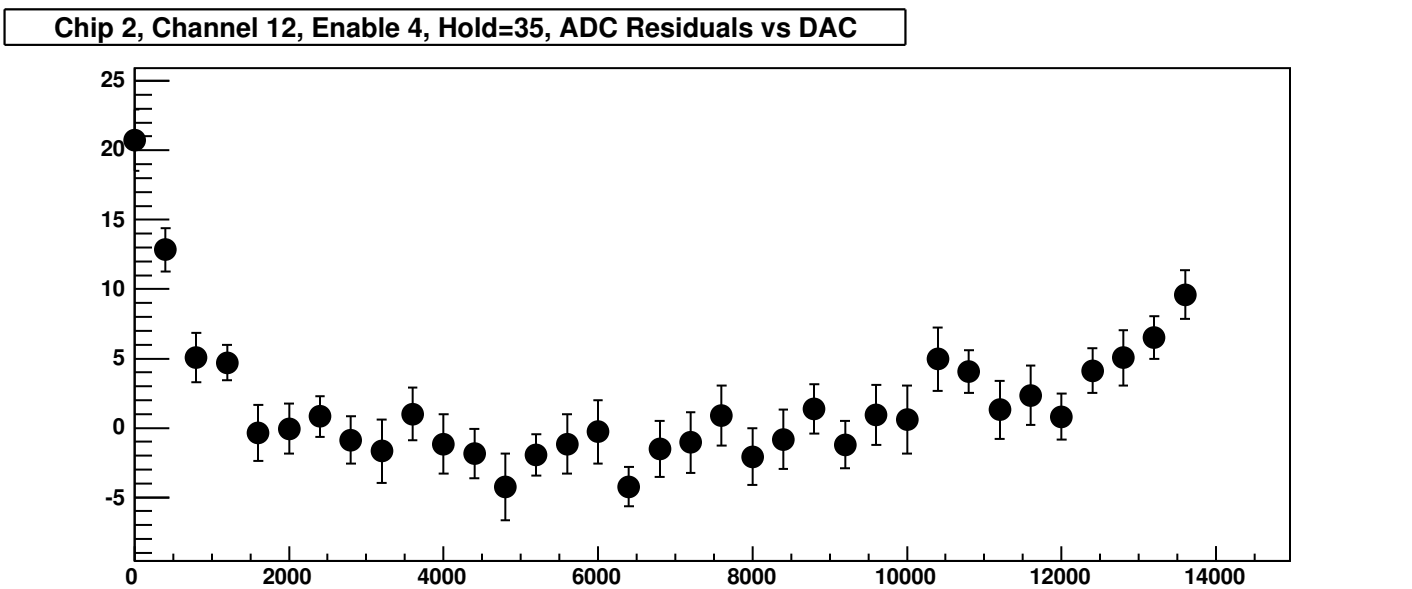

Chip 2, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.44 / 23
p0 -84.41 ± 0.7797
p1 0.007177 ± 0.0001184
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

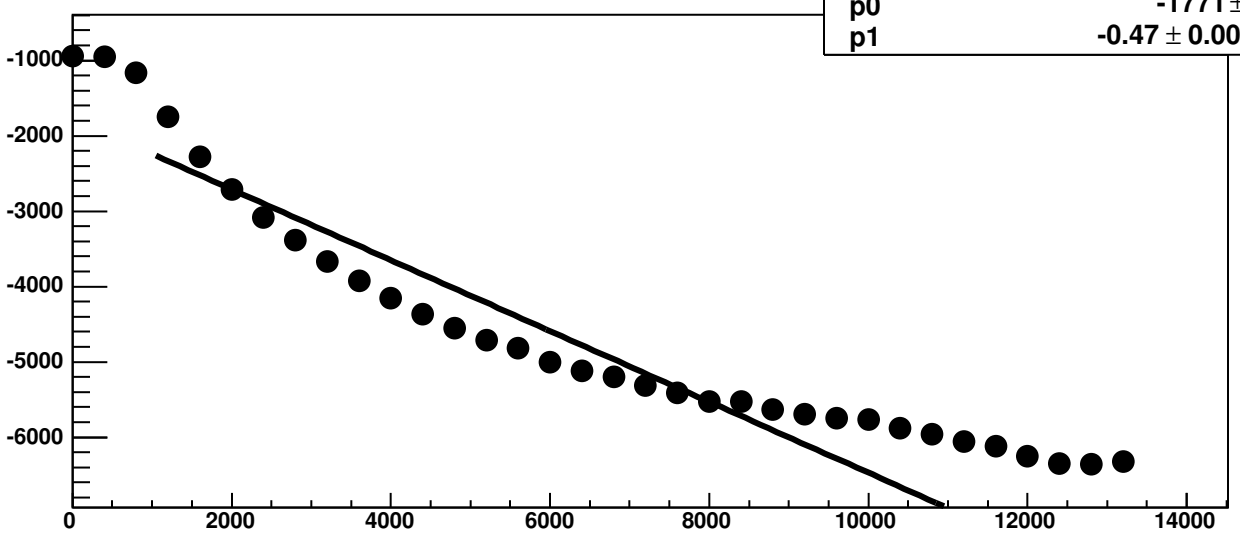

Chip 2, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 45.89 / 23
p0 -86.8 ± 0.7859
p1 0.0174 ± 0.0001228
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

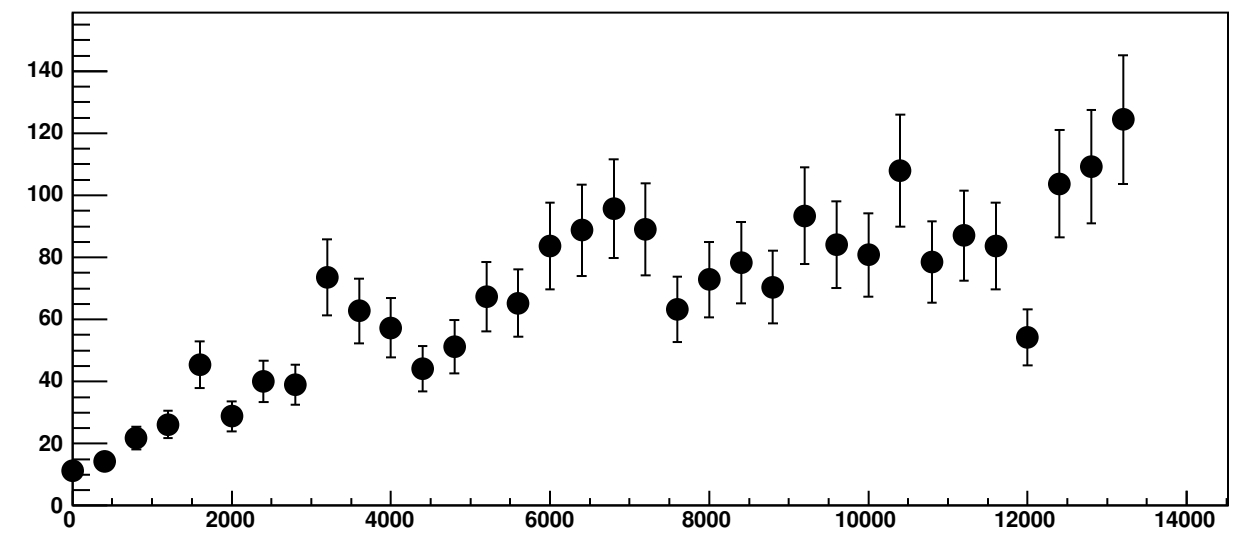

Chip 2, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

Chip 2, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

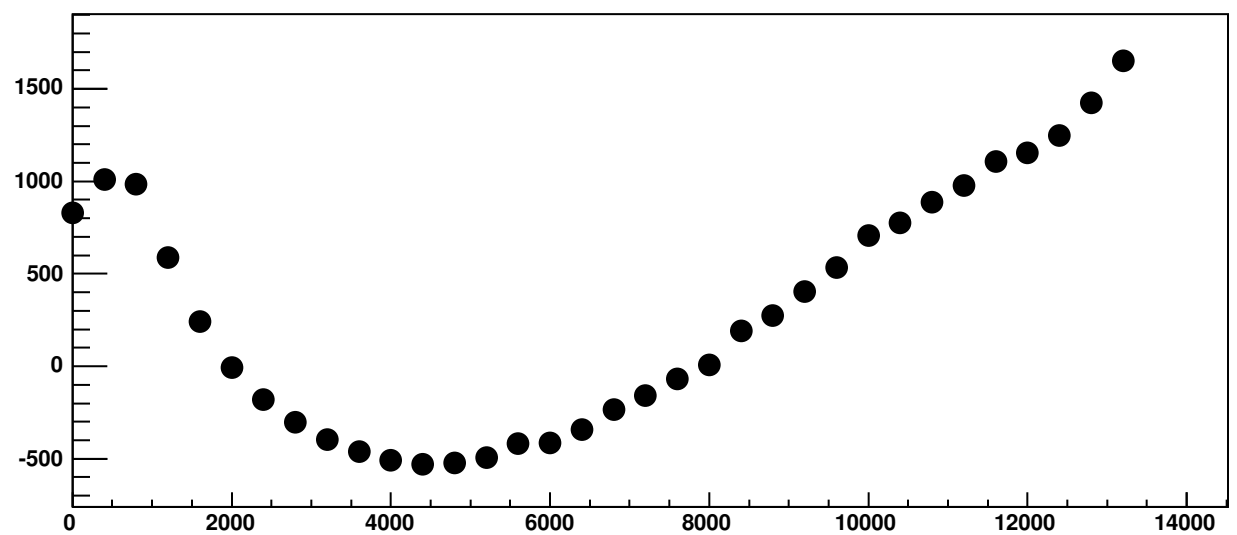
χ^2 / ndf	2.859e+04 / 23
p0	-1771 ± 4.391
p1	-0.47 ± 0.0009029



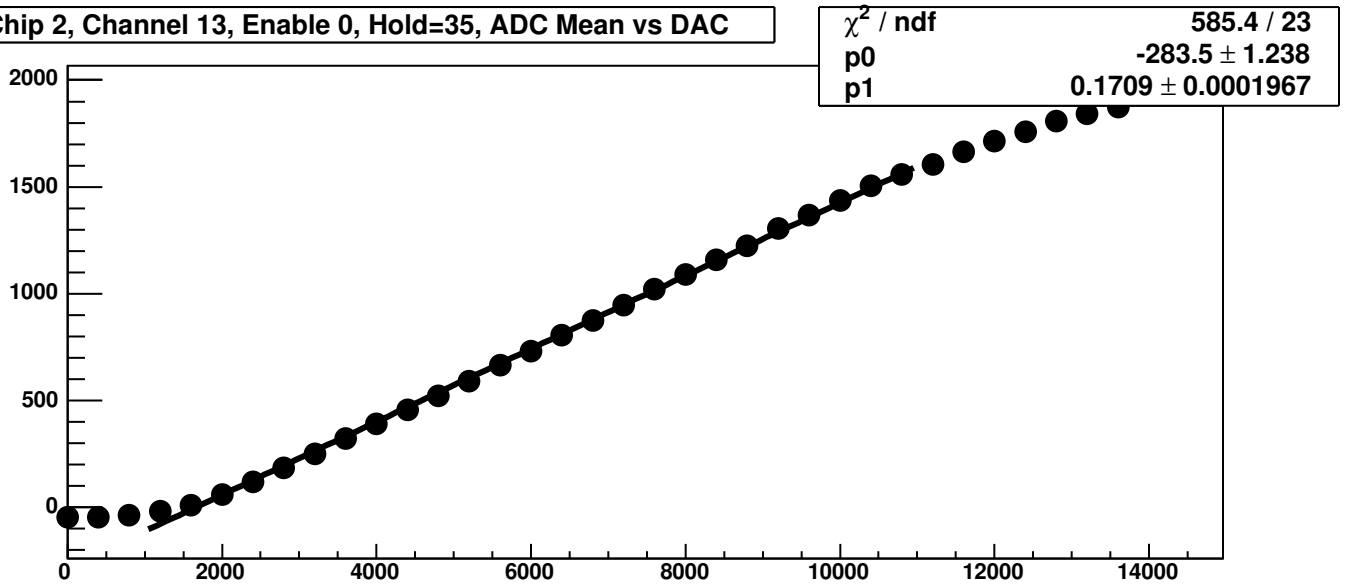
Chip 2, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC



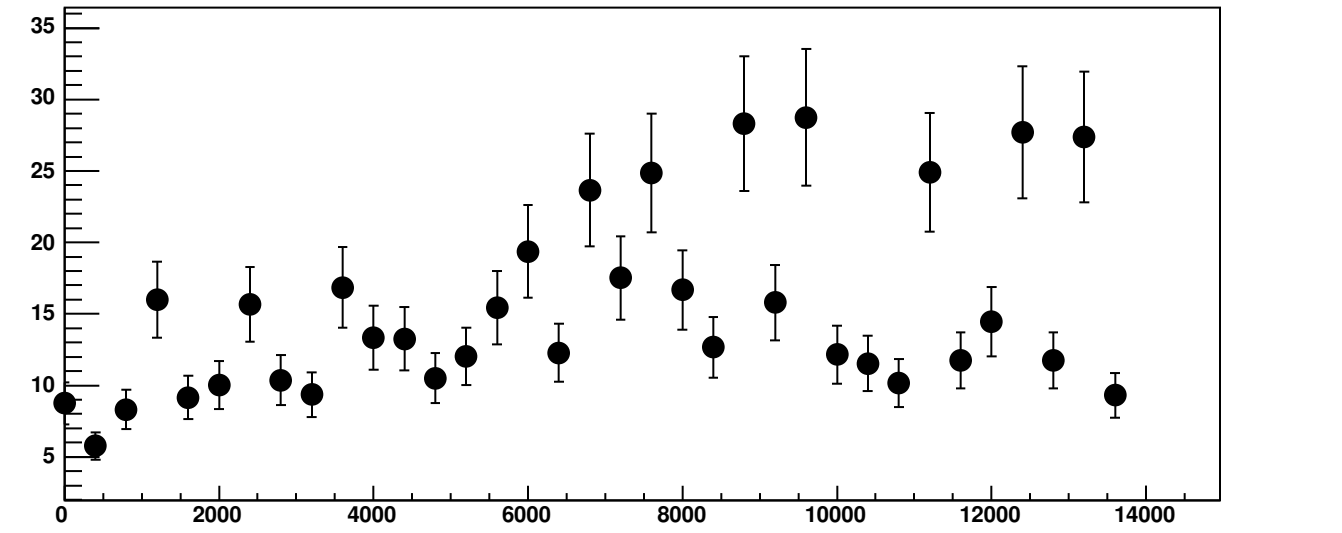
Chip 2, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC



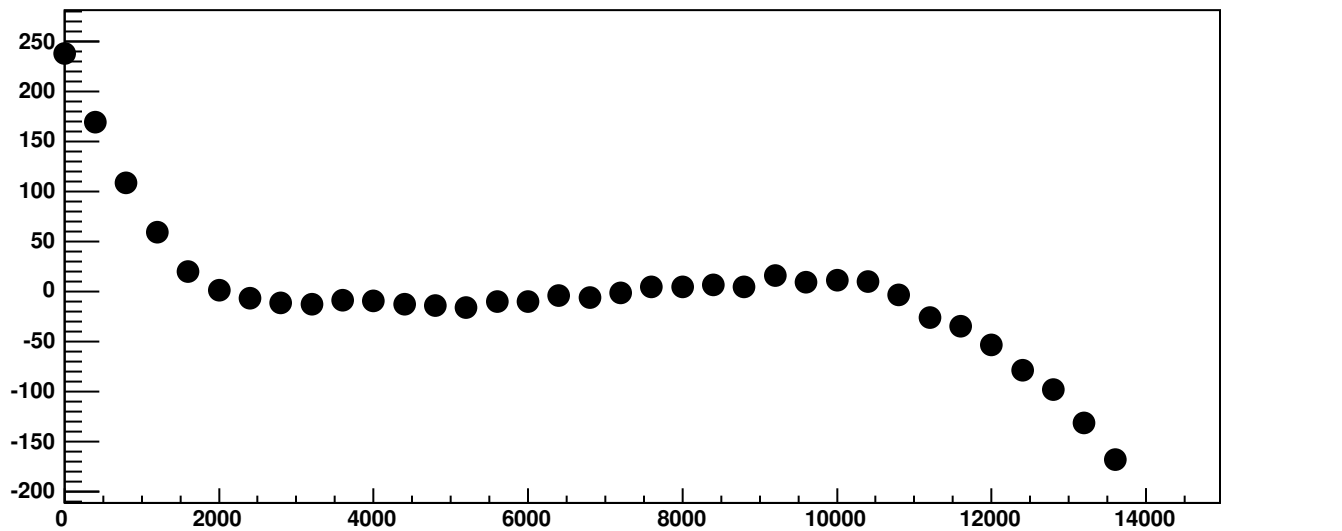
Chip 2, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

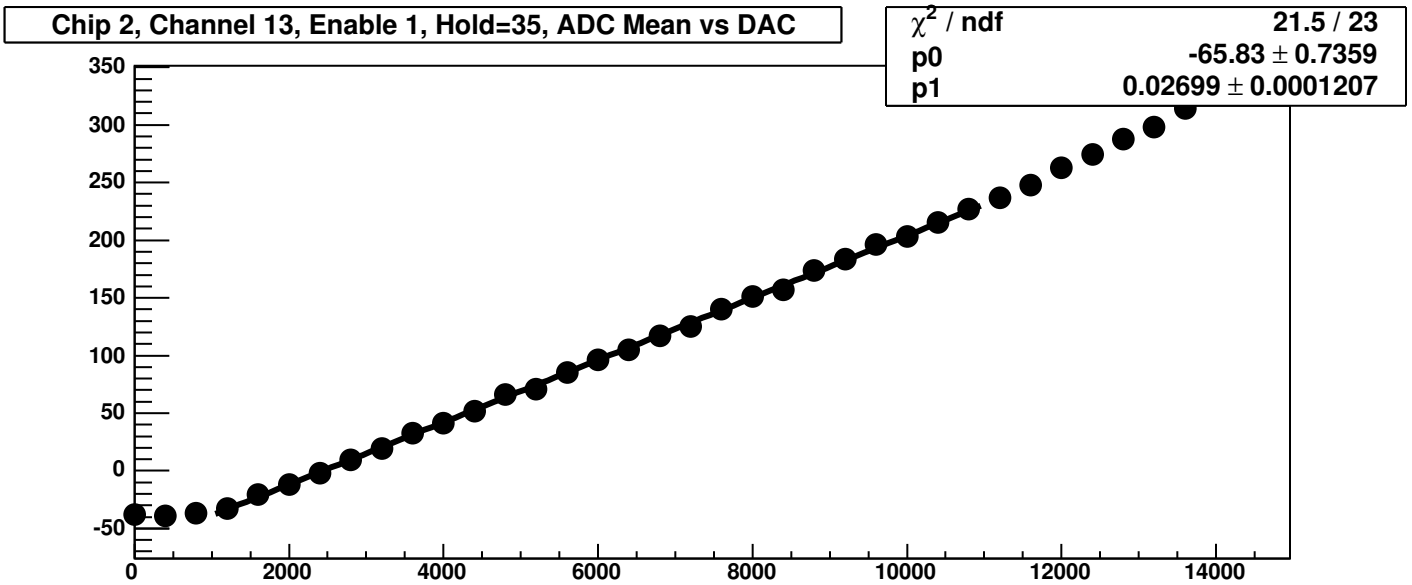


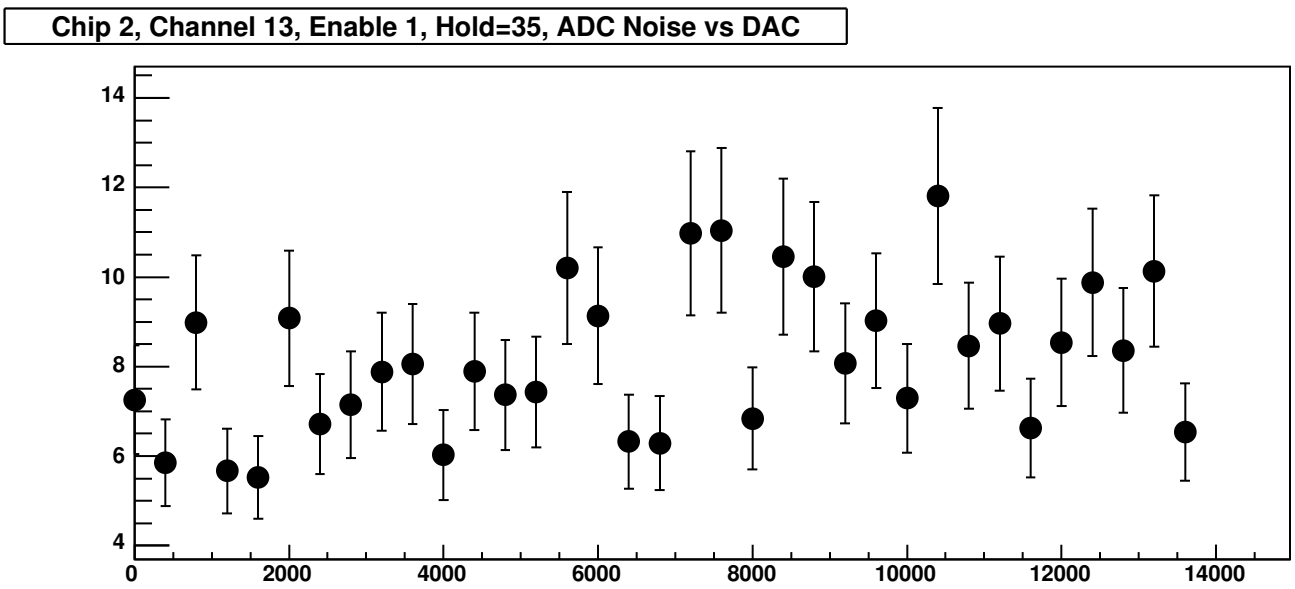
Chip 2, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

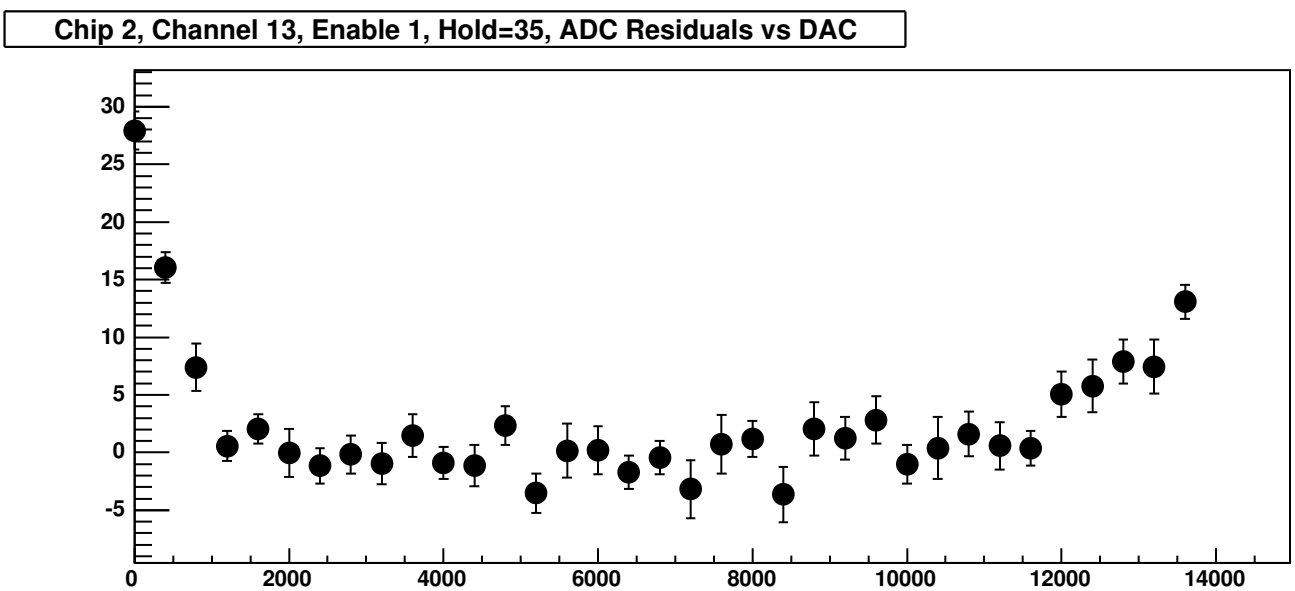


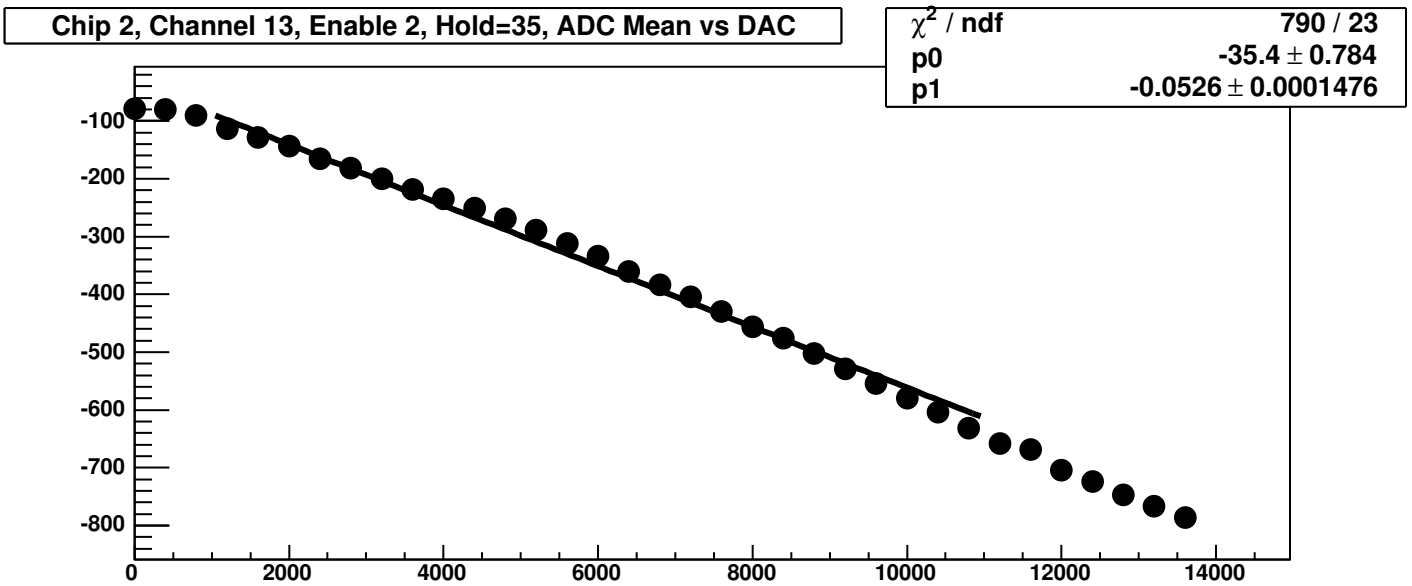
Chip 2, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

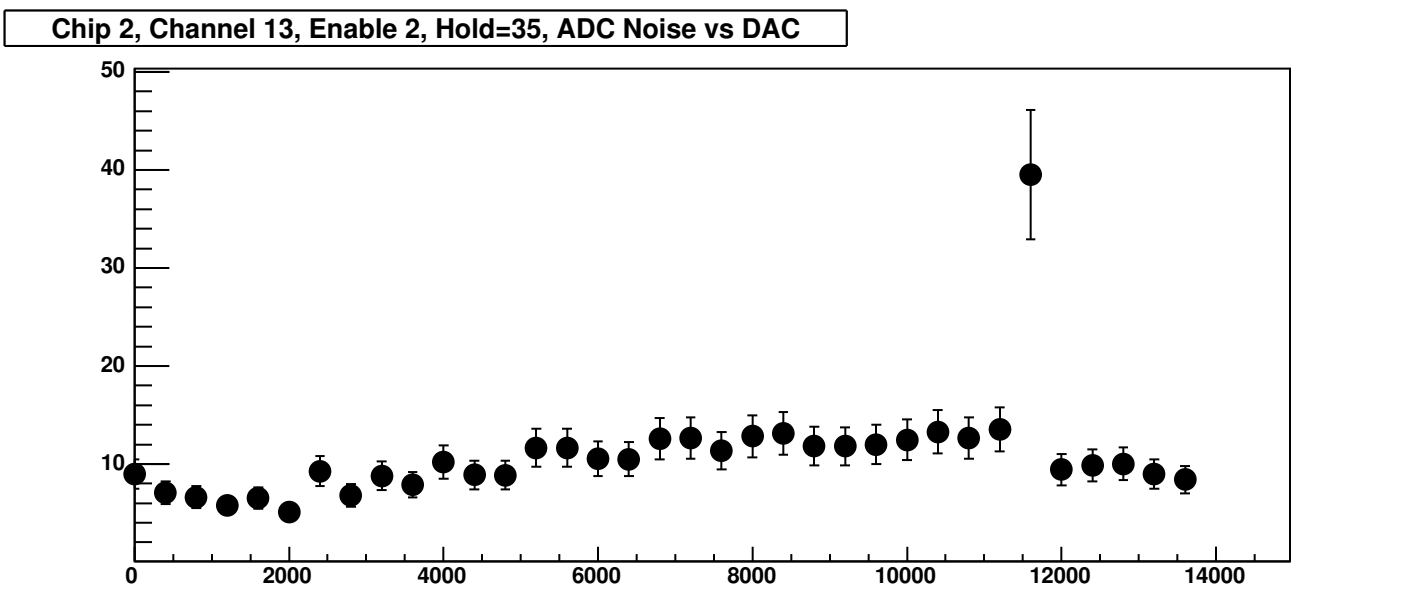


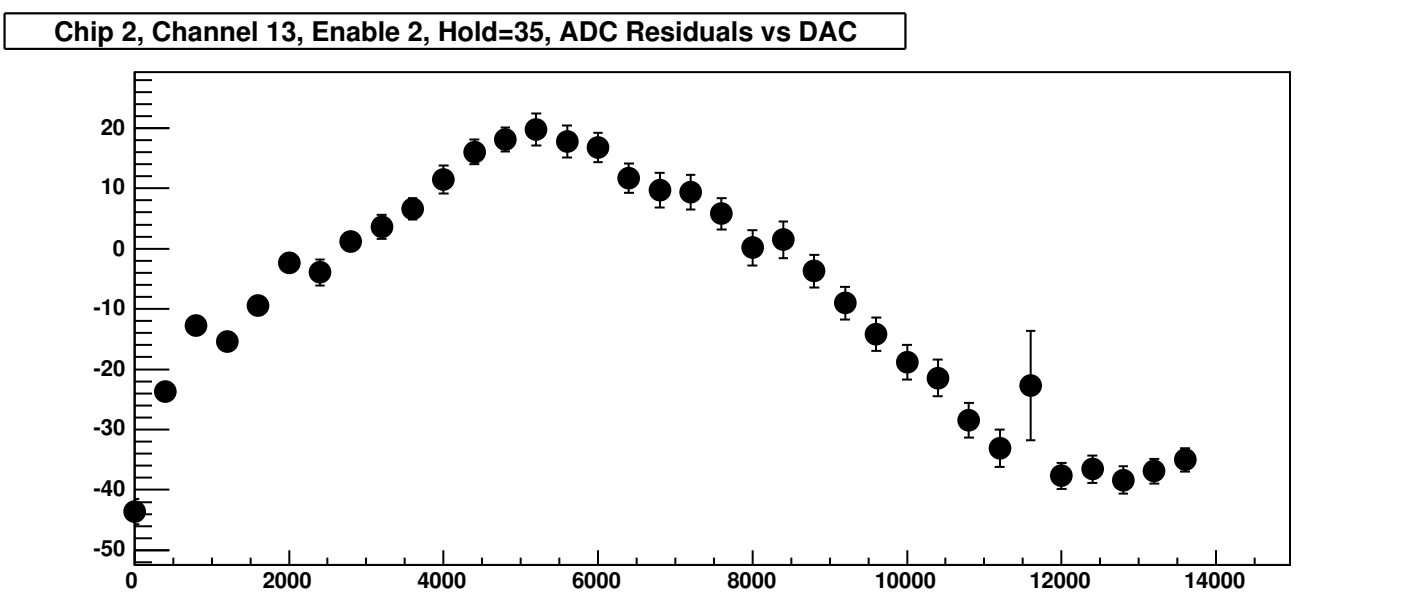

Chip 2, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 21.5 / 23
p0 -65.83 ± 0.7359
p1 0.02699 ± 0.0001207
350
300
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

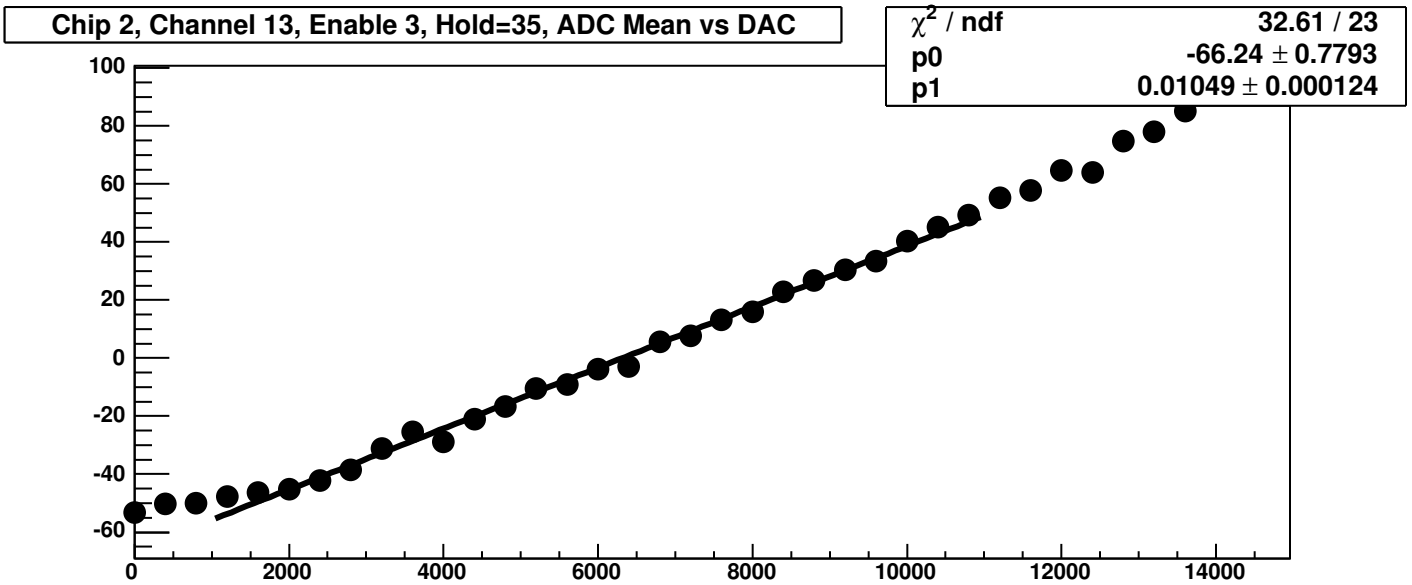

Chip 2, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

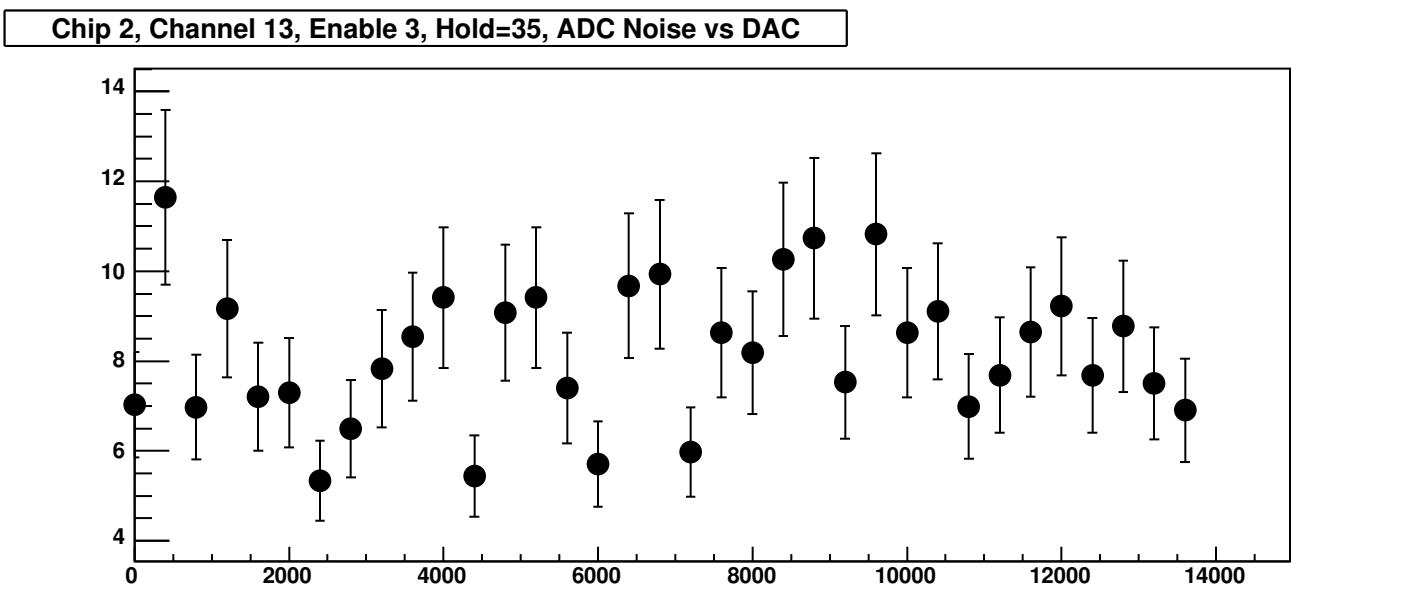

Chip 2, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

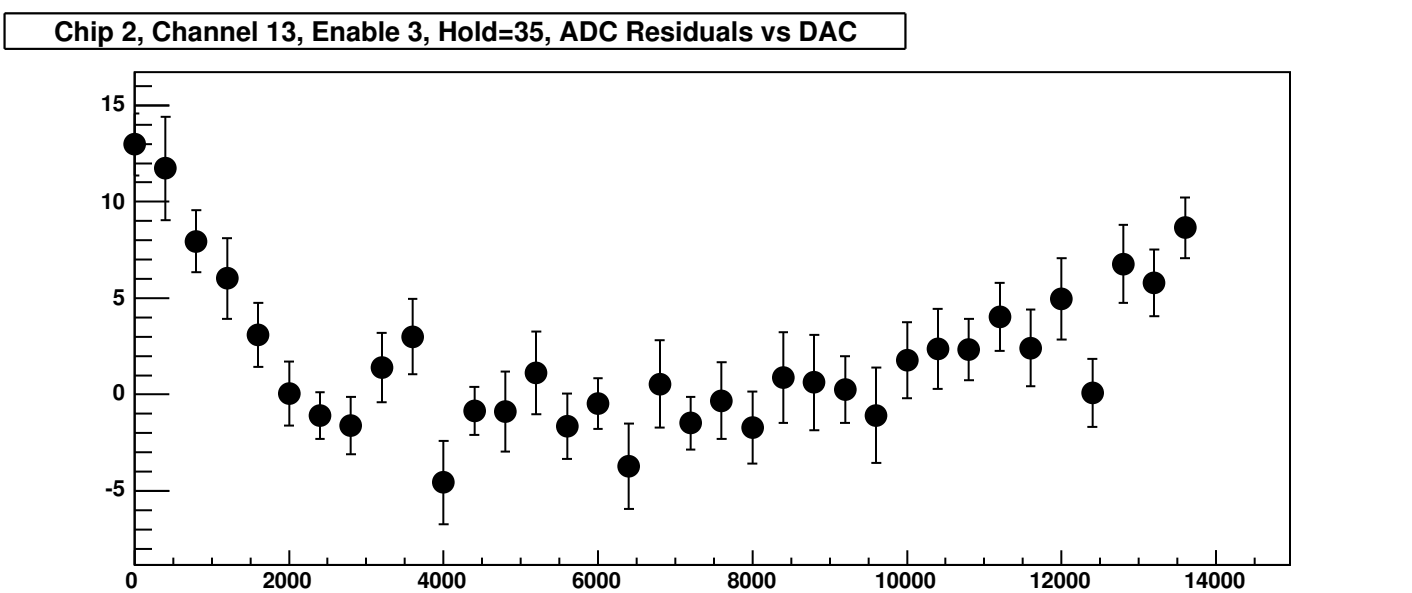

Chip 2, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 790 / 23
p0 -35.4 ± 0.784
p1 -0.0526 ± 0.0001476
-100
-200
-300
-400
-500
-600
-700
-800
0
2000
4000
6000
8000
10000
12000
14000

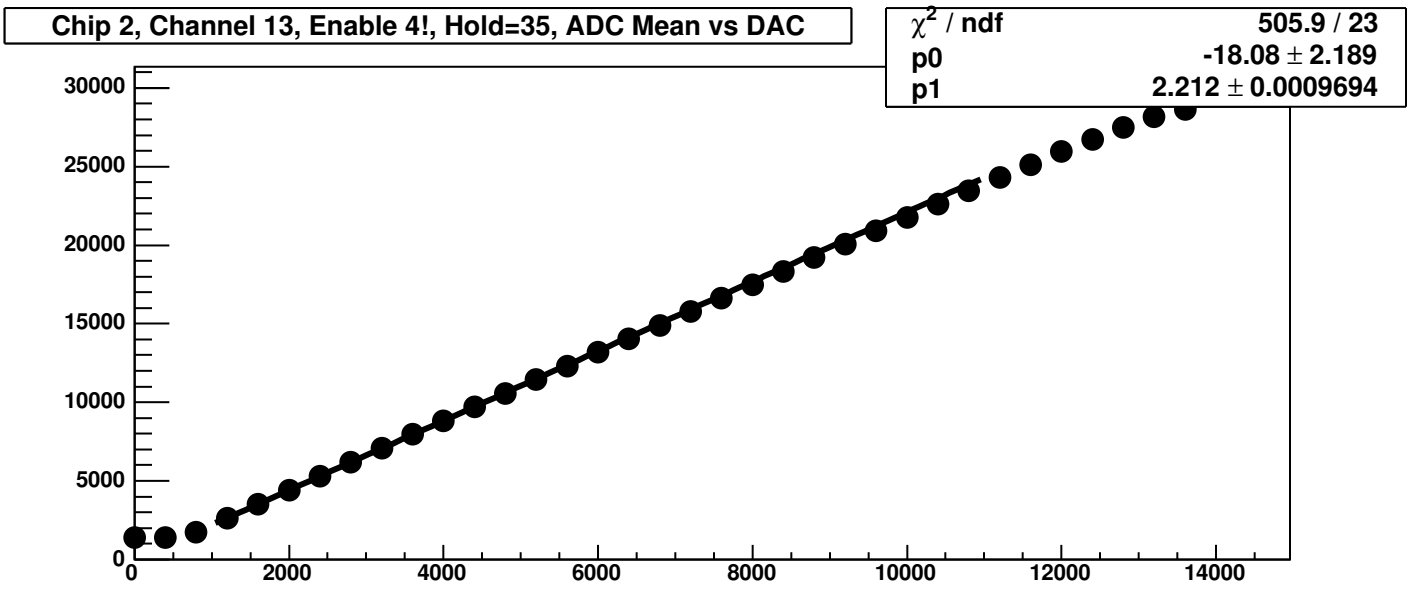

Chip 2, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

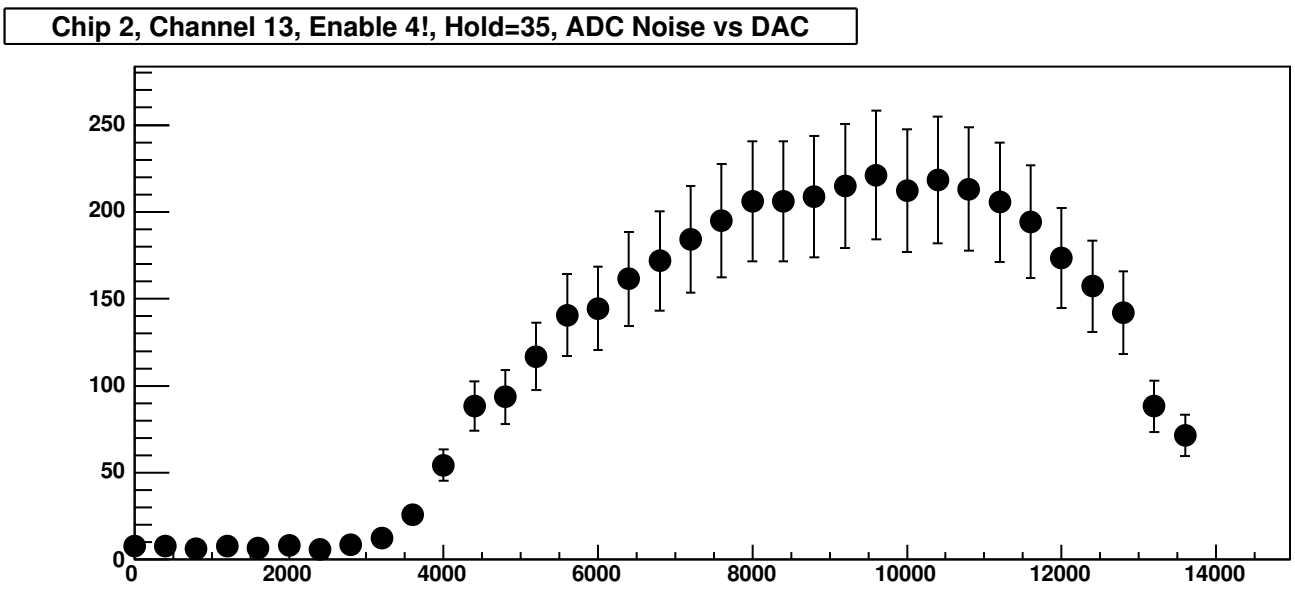

Chip 2, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

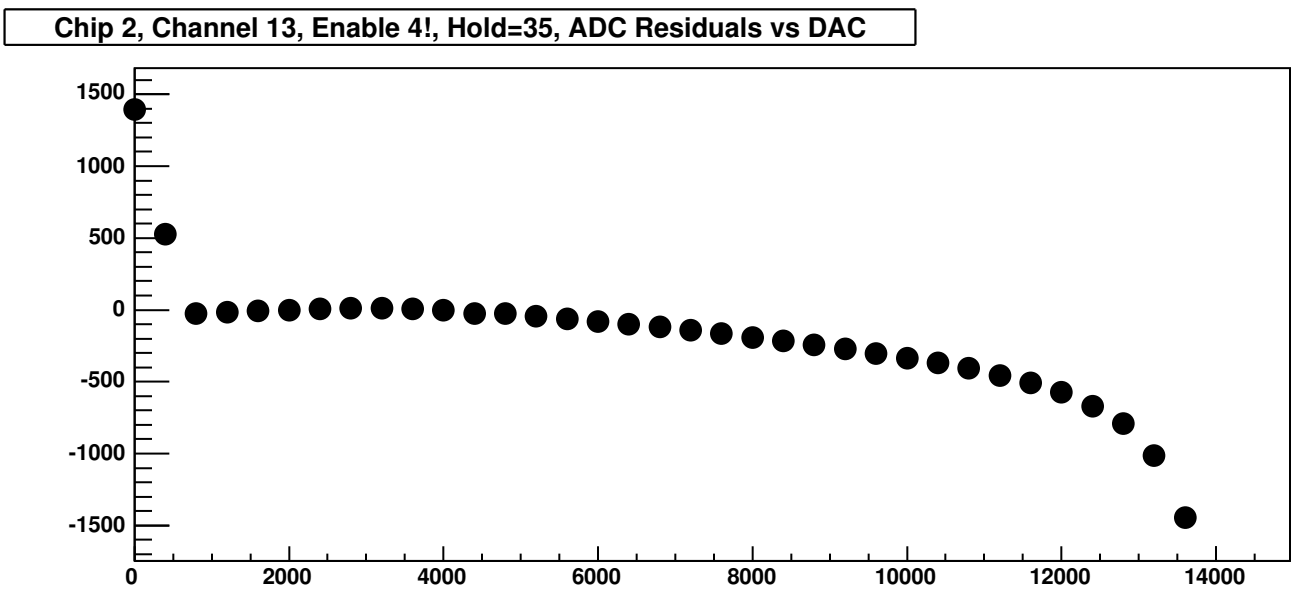

Chip 2, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 32.61 / 23
p0 -66.24 ± 0.7793
p1 0.01049 ± 0.000124
100
80
60
40
20
0
-20
-40
-60
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

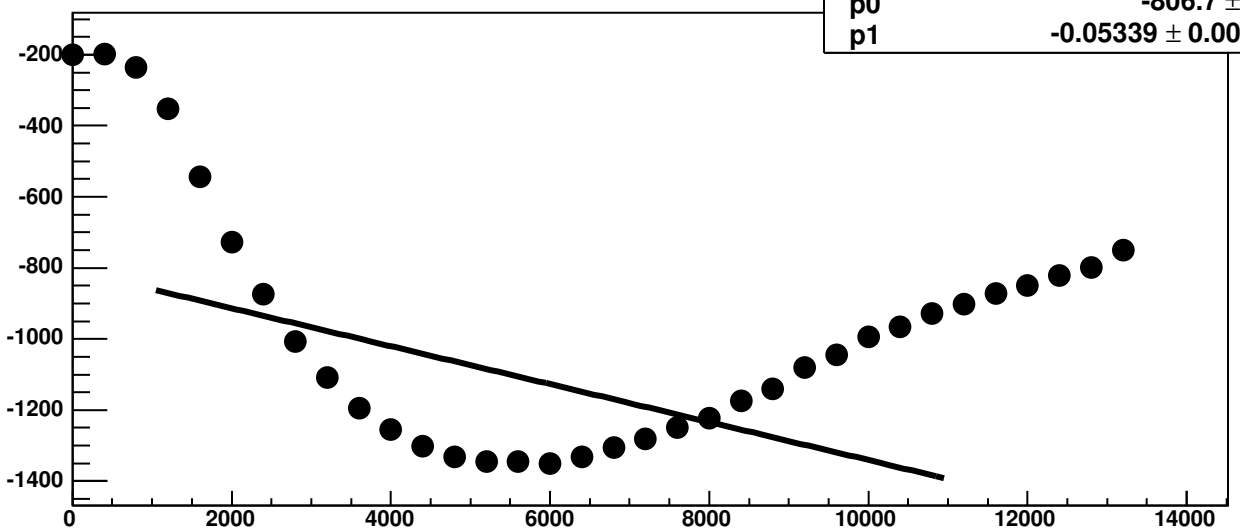

Chip 2, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 505.9 / 23
p0 -18.08 ± 2.189
p1 2.212 ± 0.0009694
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

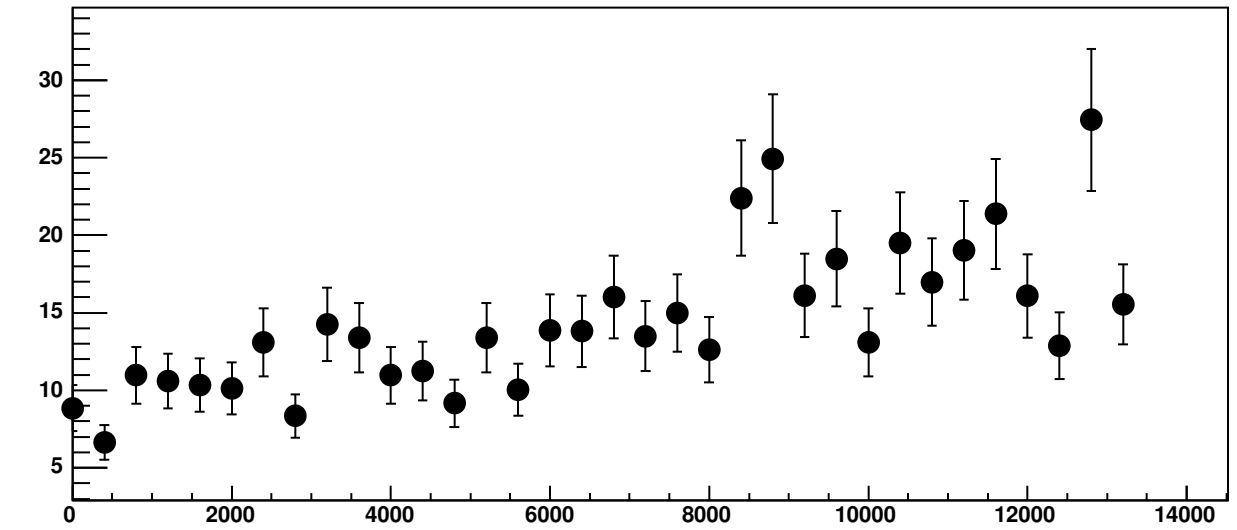

Chip 2, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

Chip 2, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

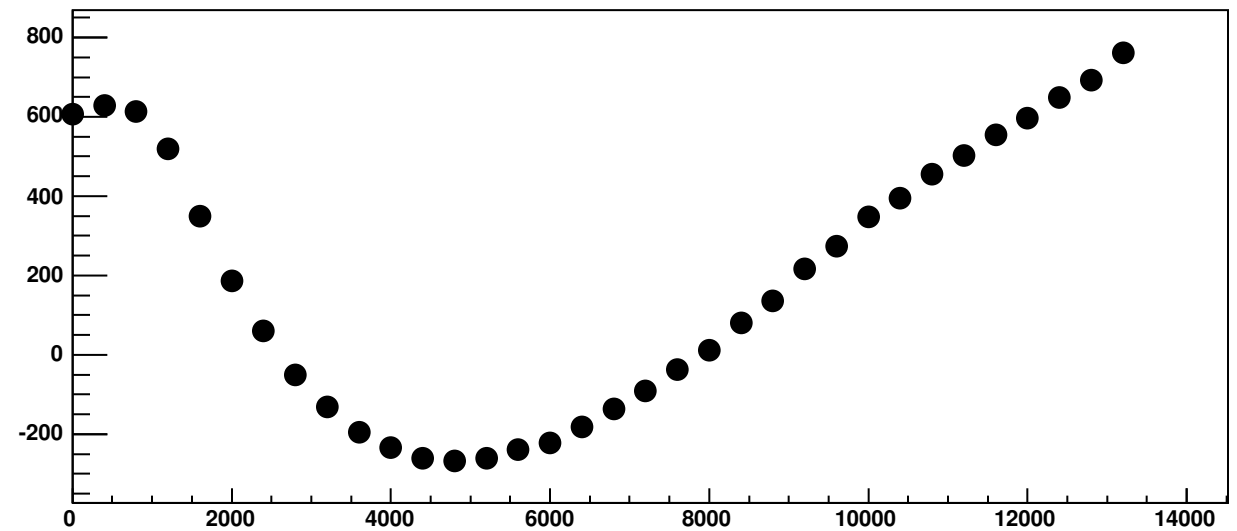
χ^2 / ndf	1.875e+05 / 23
p0	-806.7 ± 1.227
p1	-0.05339 ± 0.0002156

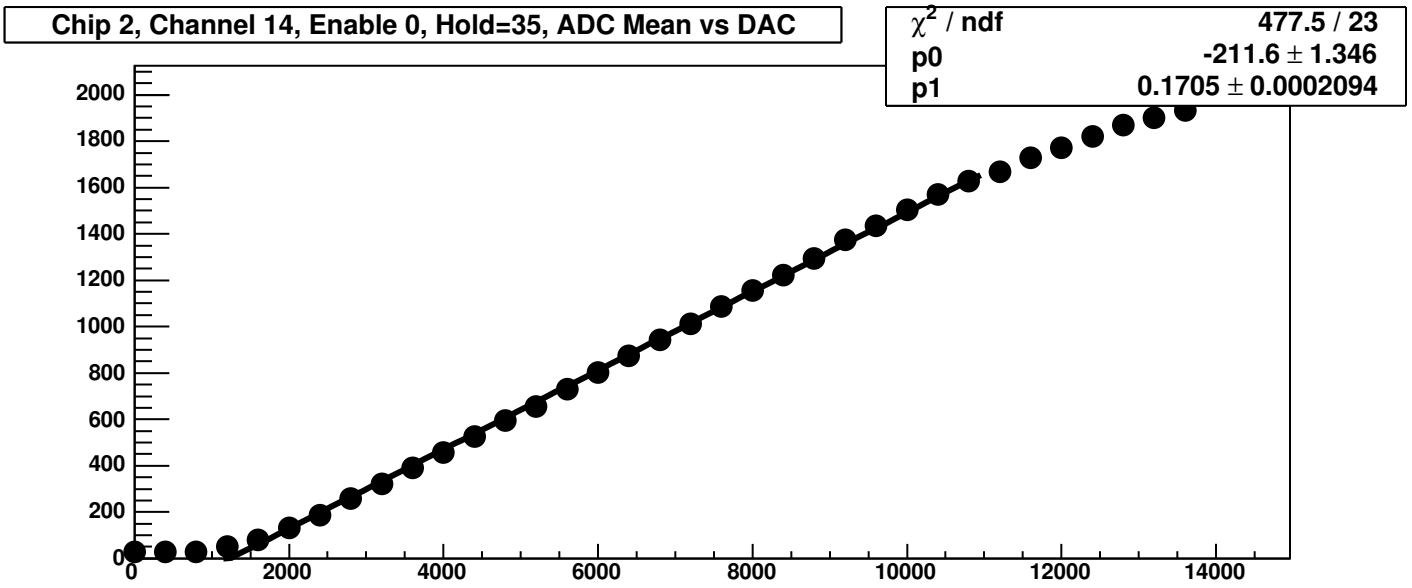


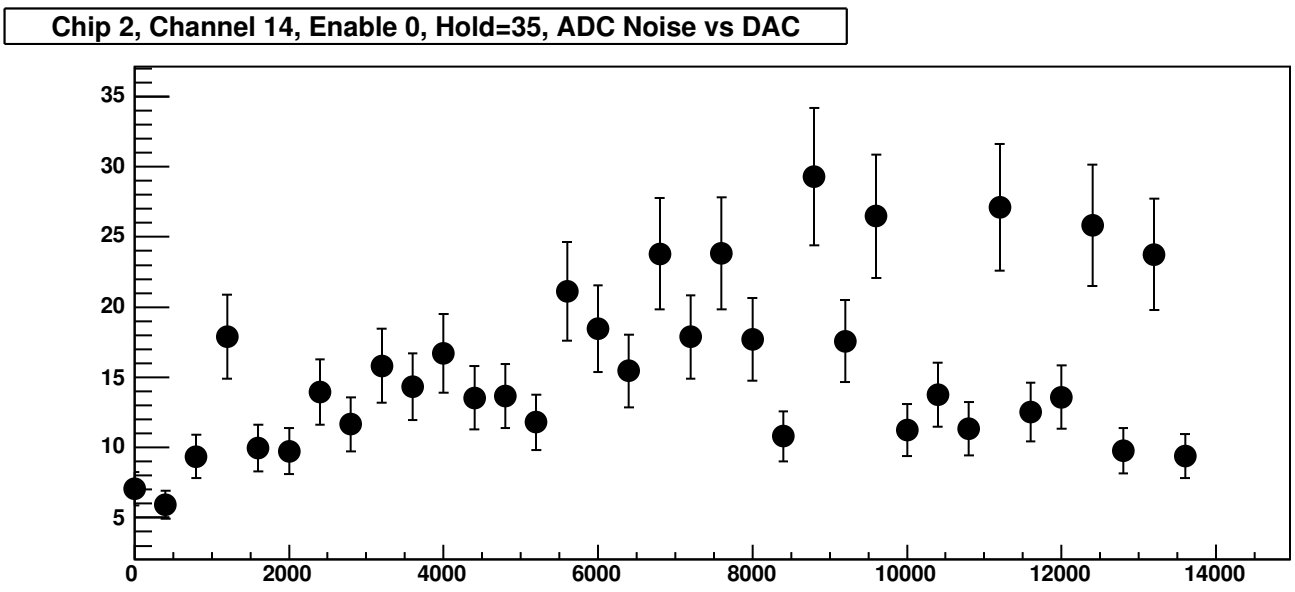
Chip 2, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

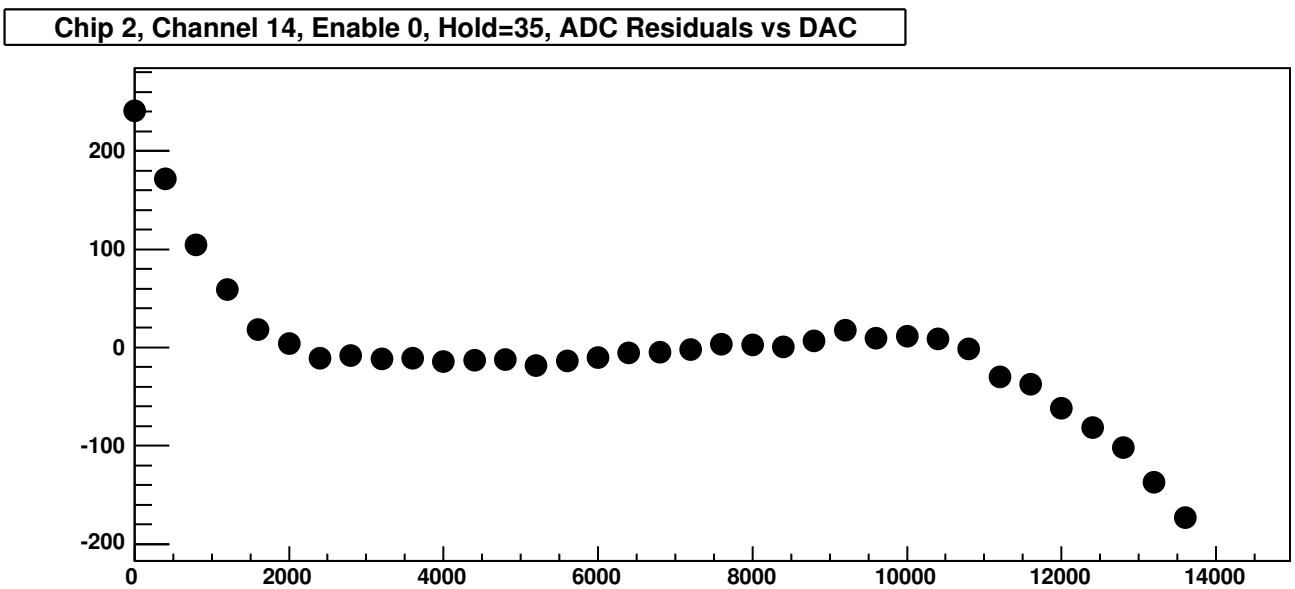


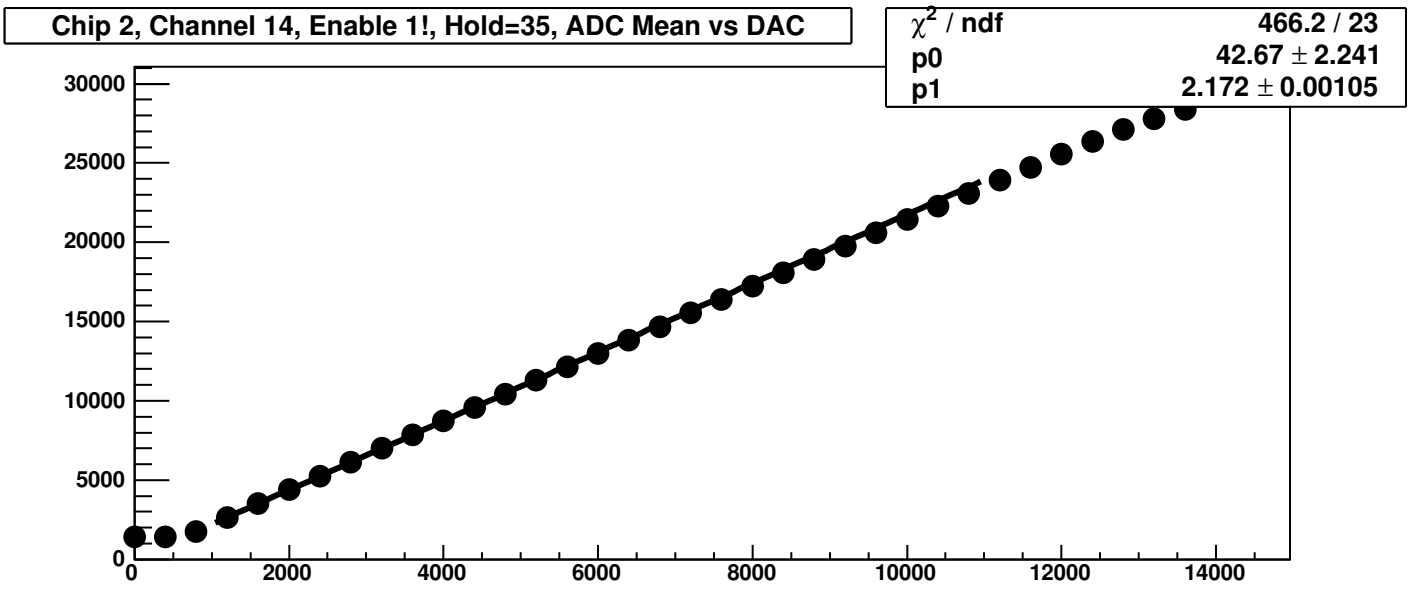
Chip 2, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

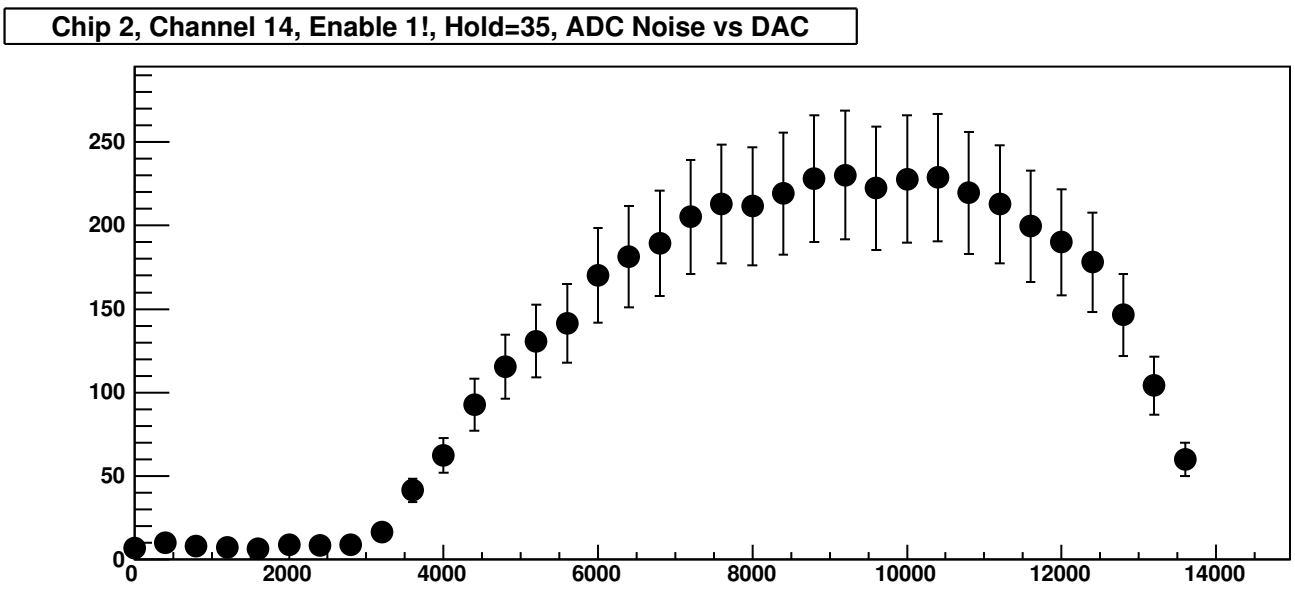


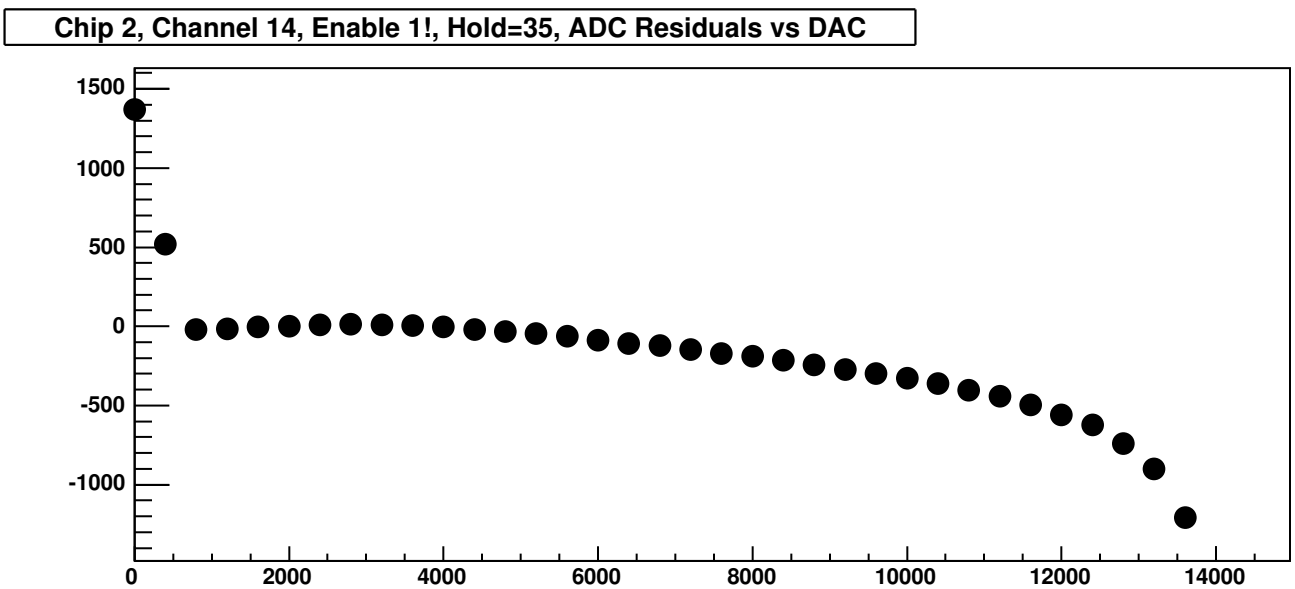

Chip 2, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 477.5 / 23
p0 -211.6 ± 1.346
p1 0.1705 ± 0.0002094
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

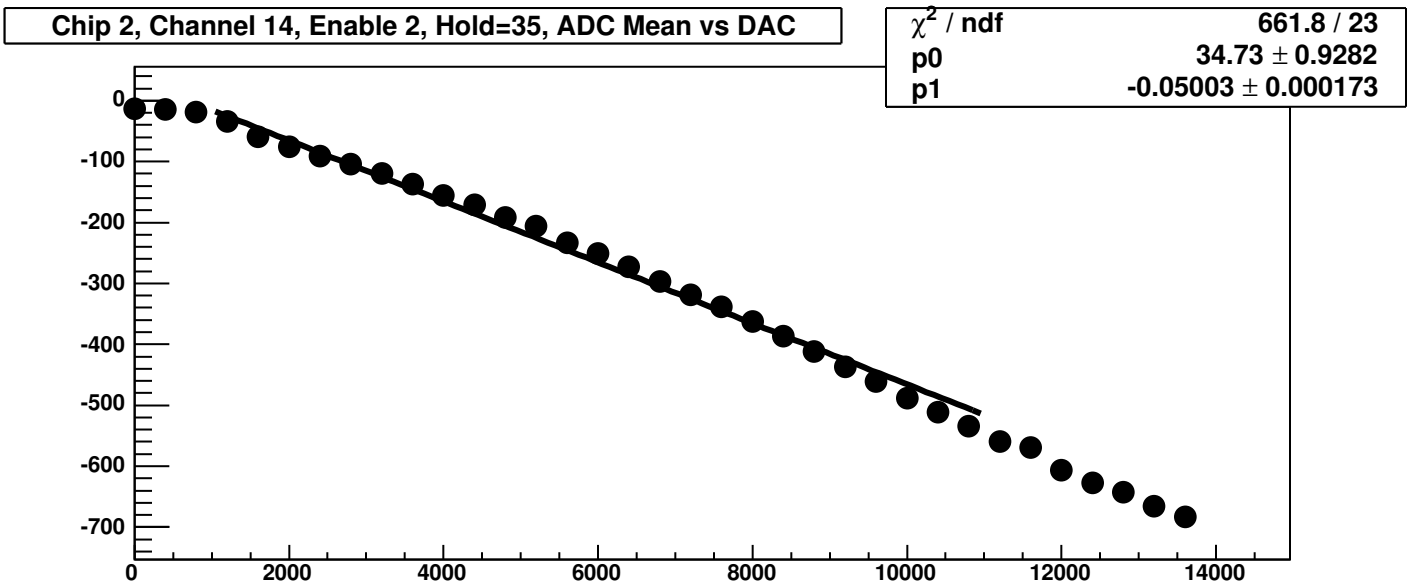

Chip 2, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

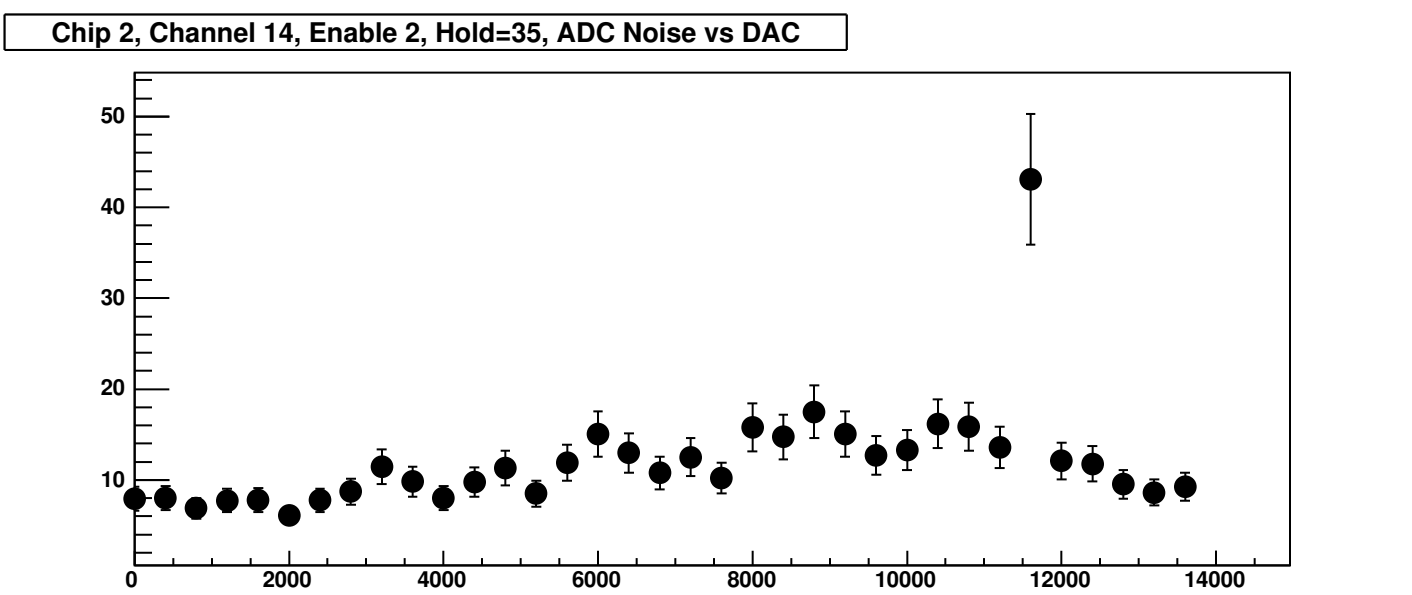

Chip 2, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

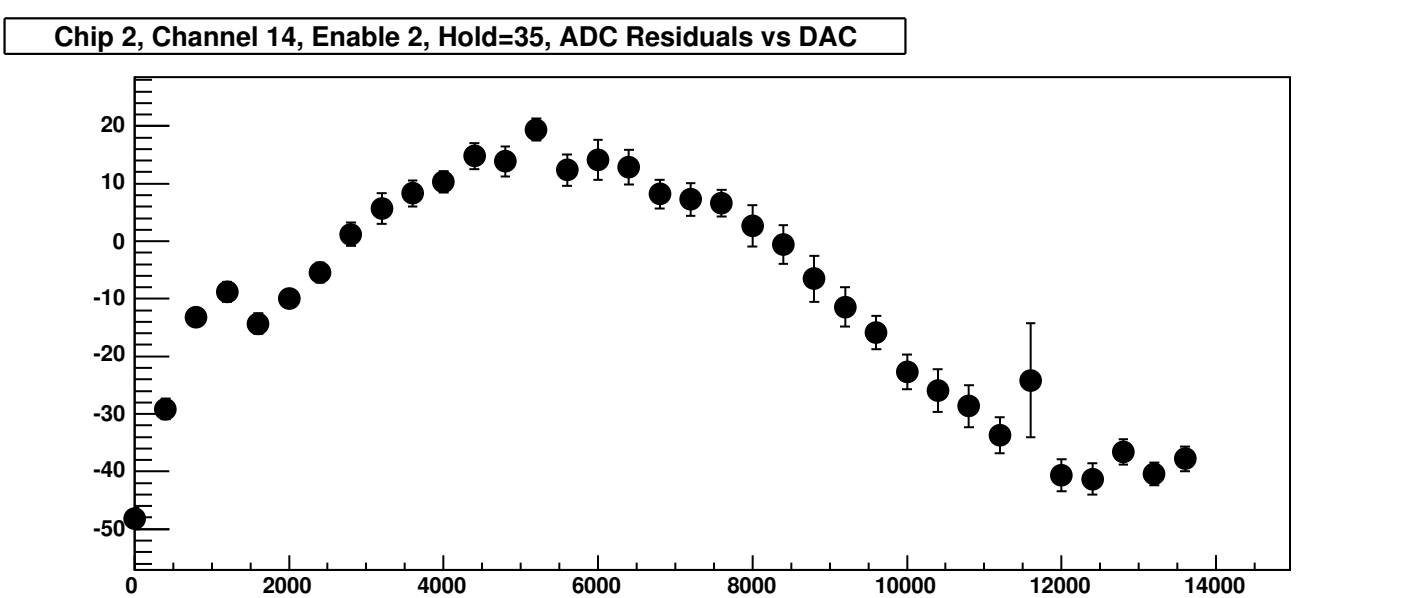

Chip 2, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 466.2 / 23
p0 42.67 ± 2.241
p1 2.172 ± 0.00105
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

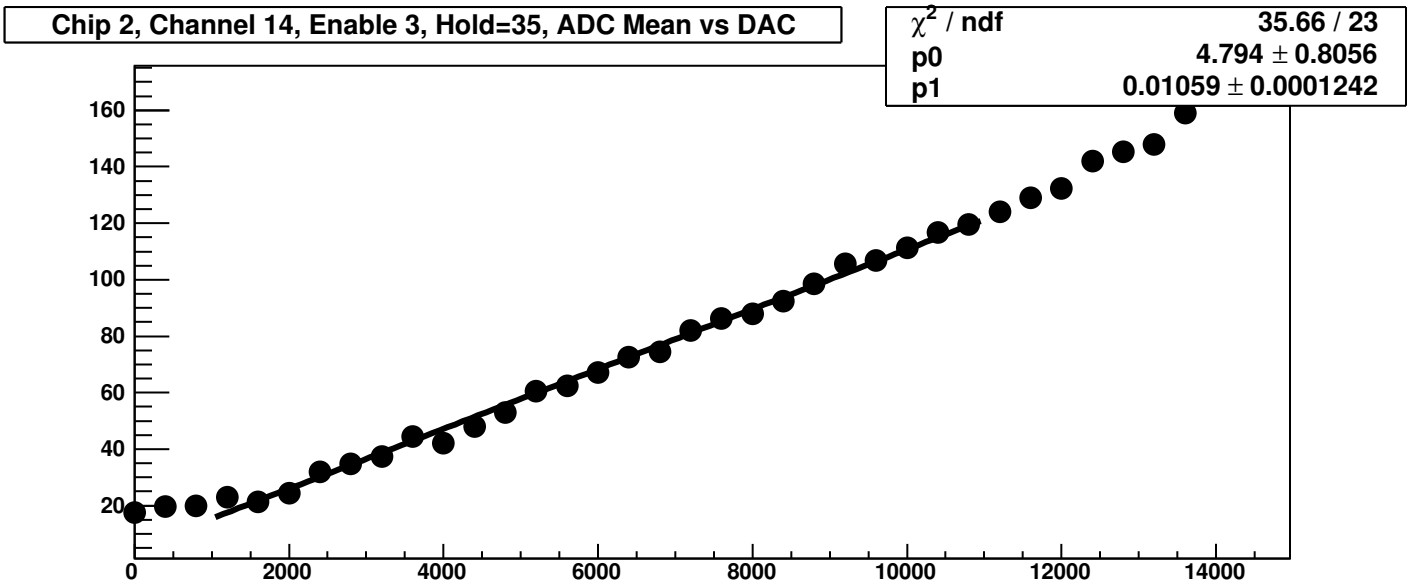

Chip 2, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

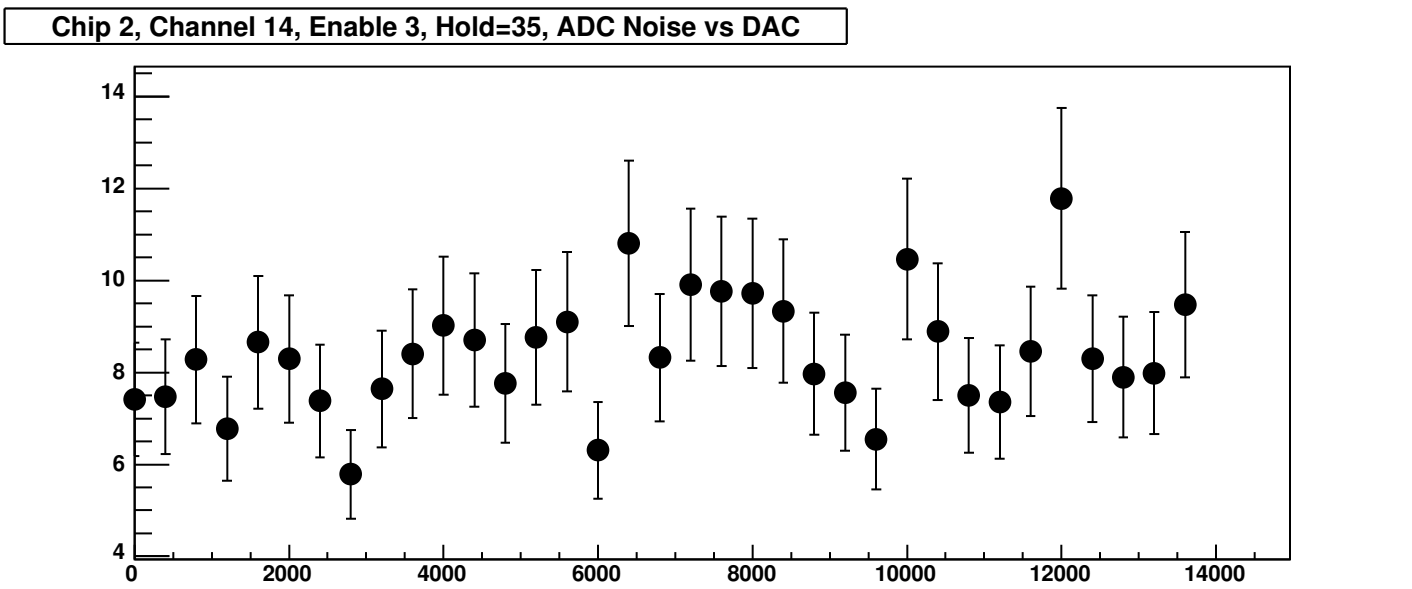

Chip 2, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

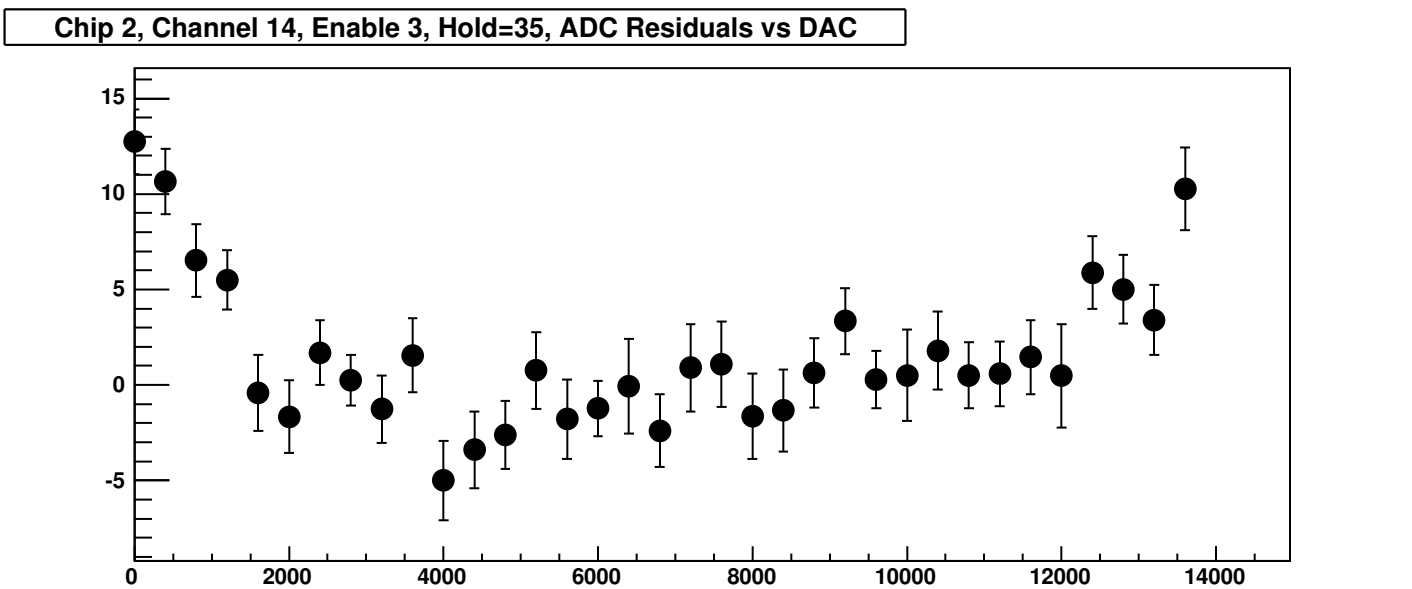

Chip 2, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 661.8 / 23
p0 34.73 ± 0.9282
p1 -0.05003 ± 0.000173
0
-100
-200
-300
-400
-500
-600
-700
0
2000
4000
6000
8000
10000
12000
14000

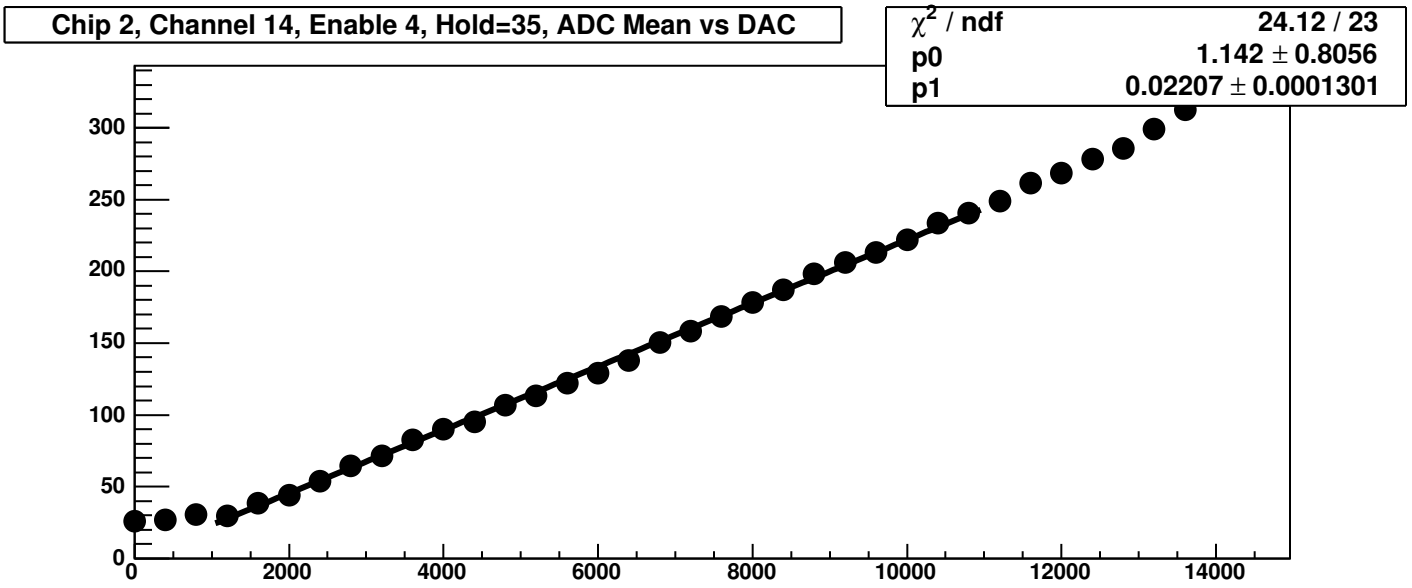

Chip 2, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

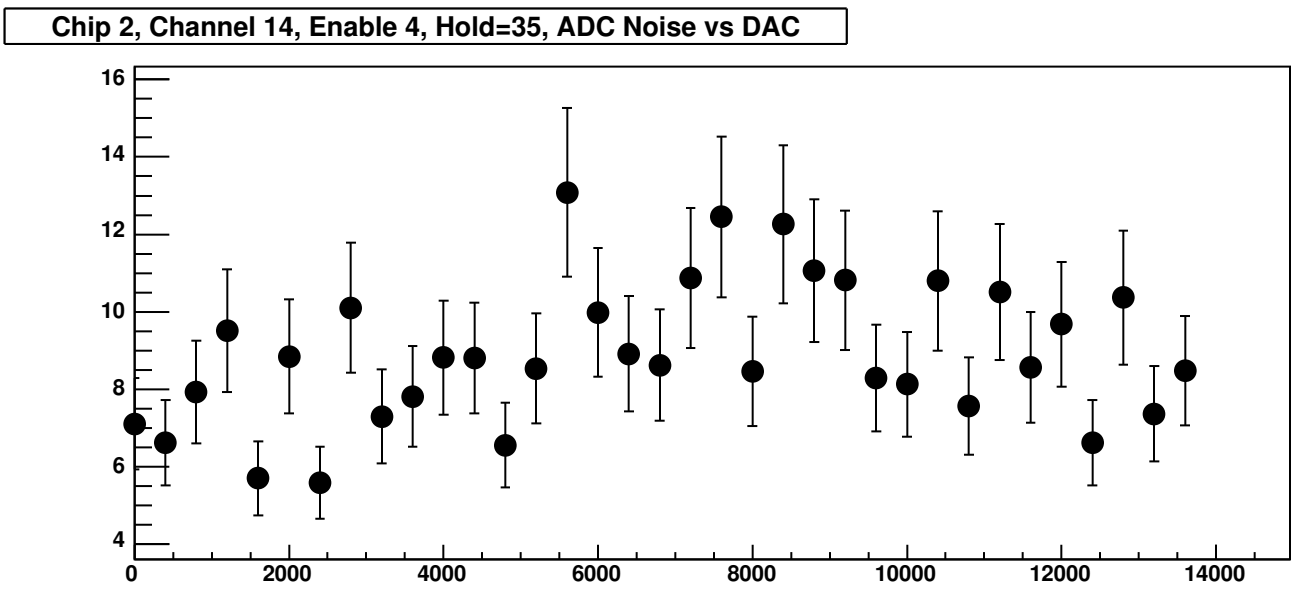

Chip 2, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

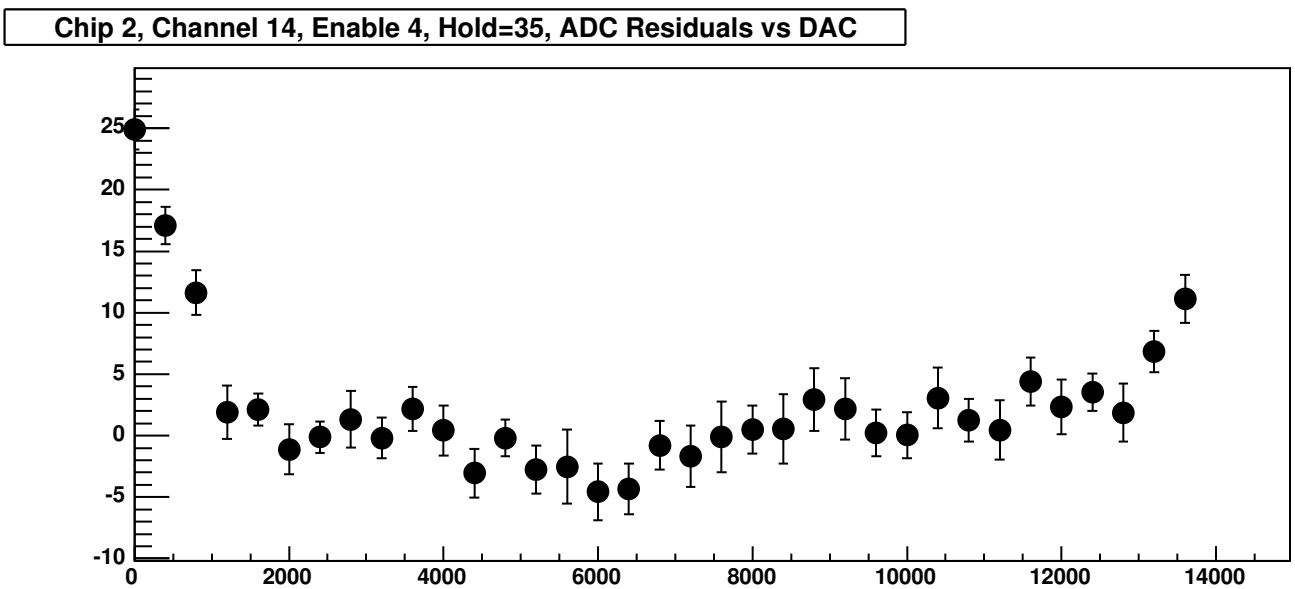

Chip 2, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 35.66 / 23
p0 4.794 ± 0.8056
p1 0.01059 ± 0.0001242
160
140
120
100
80
60
40
20
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

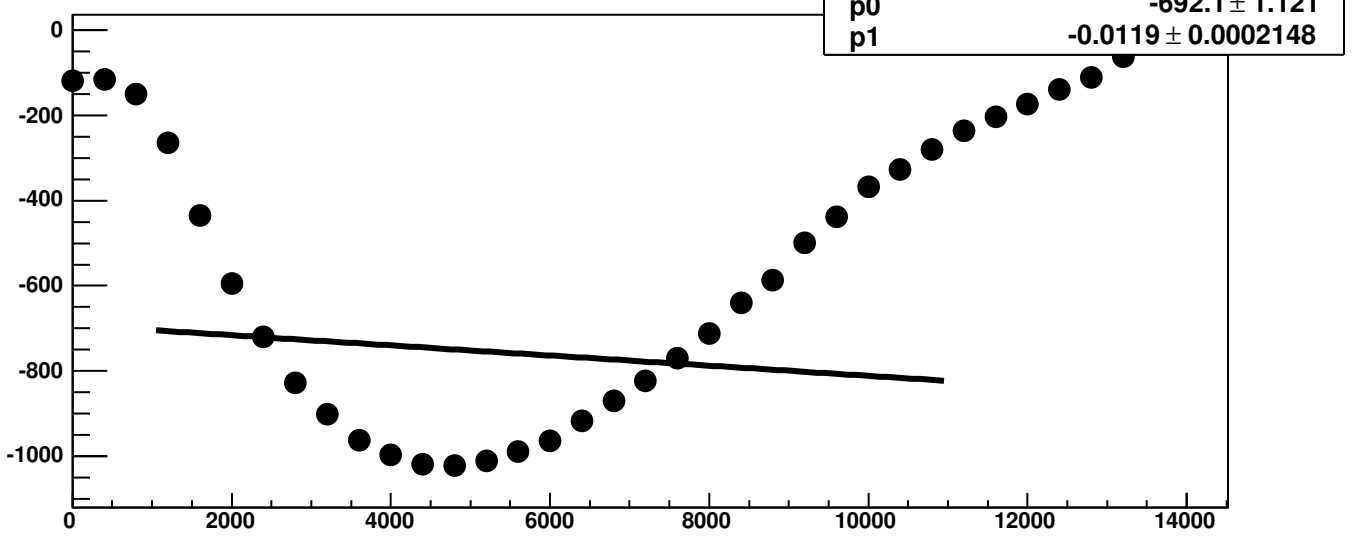

Chip 2, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.12 / 23
p0 1.142 ± 0.8056
p1 0.02207 ± 0.0001301
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

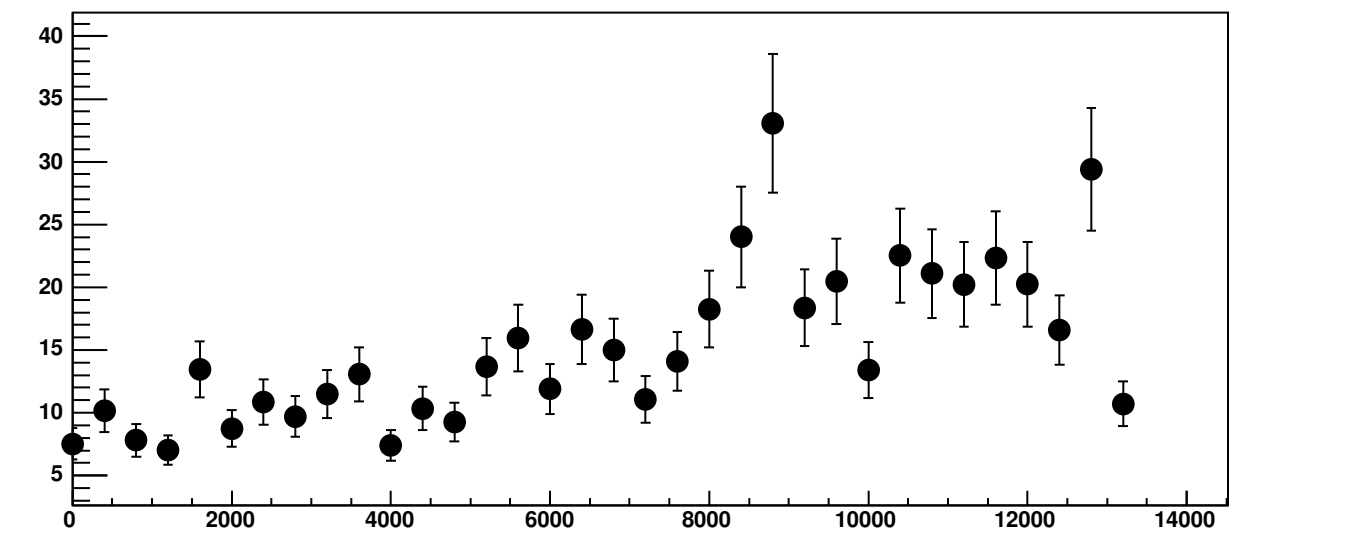

Chip 2, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

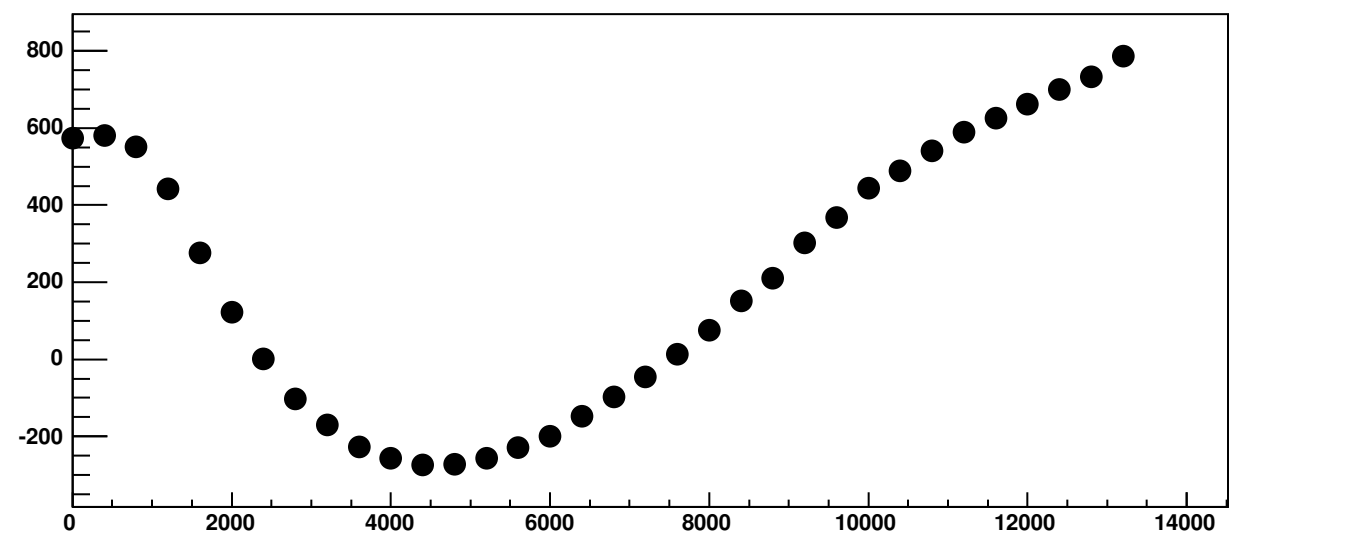
Chip 2, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



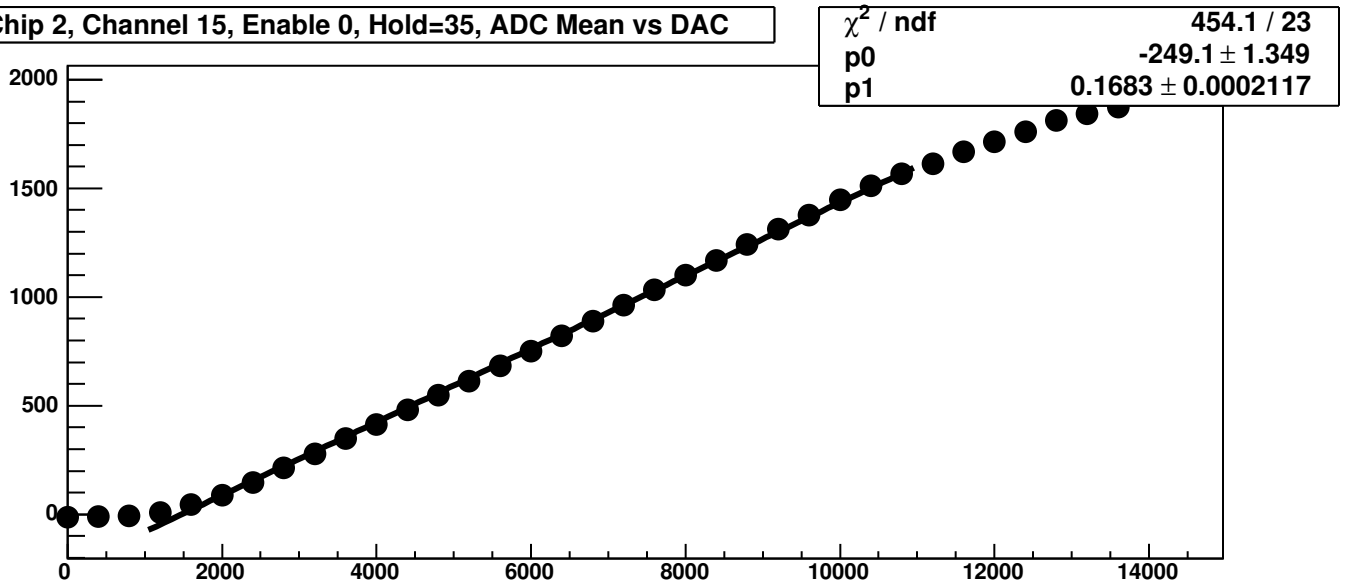
Chip 2, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



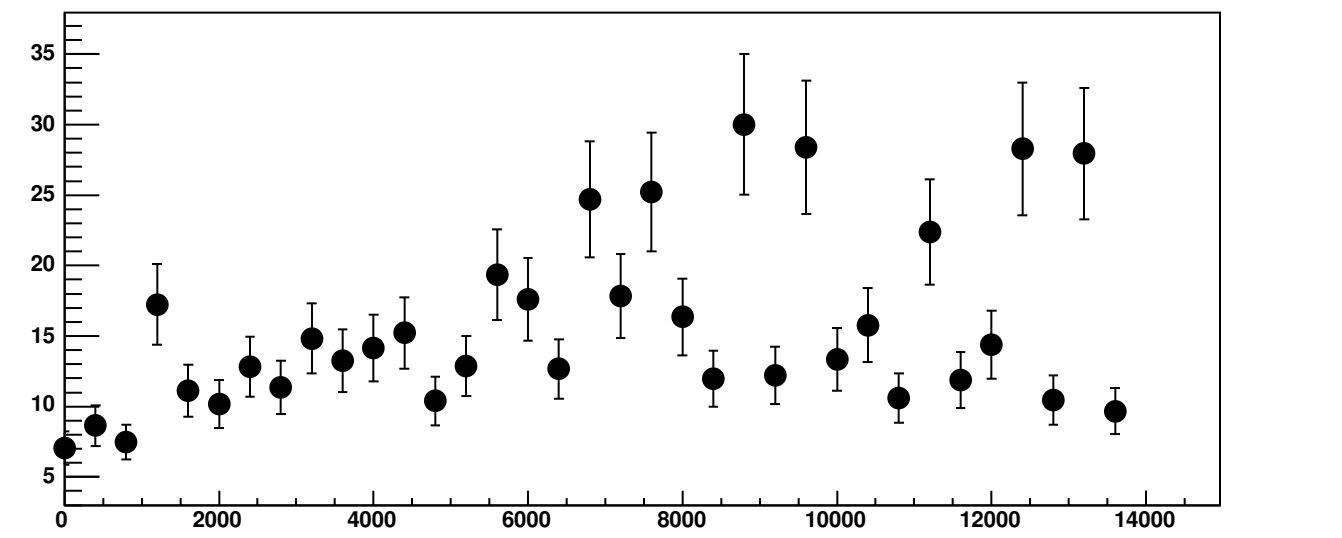
Chip 2, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



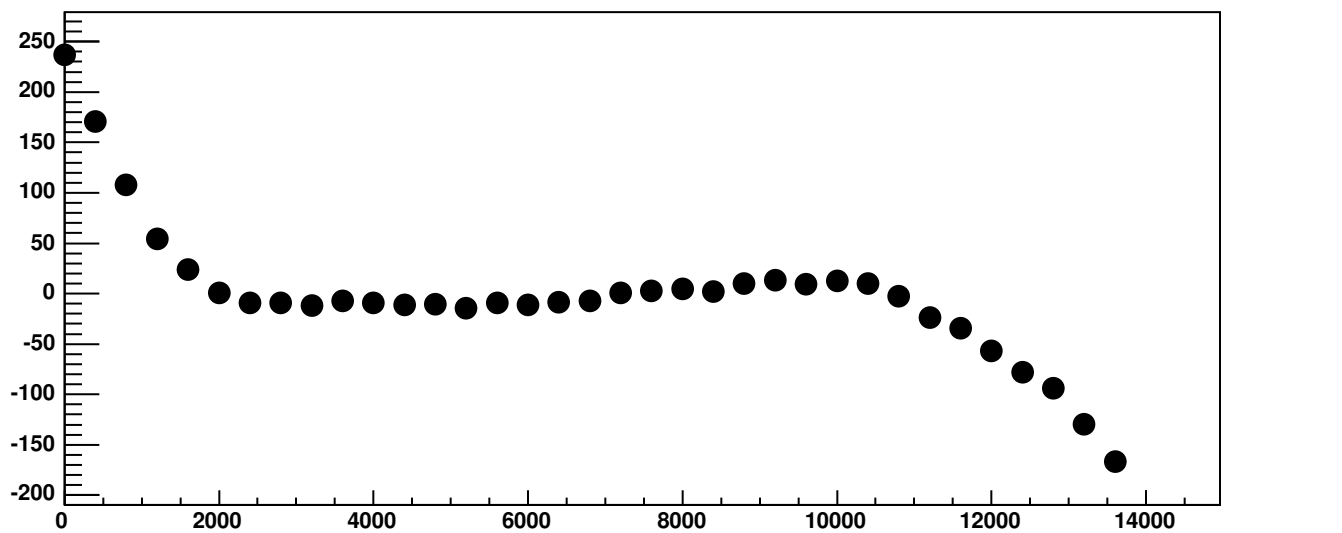
Chip 2, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

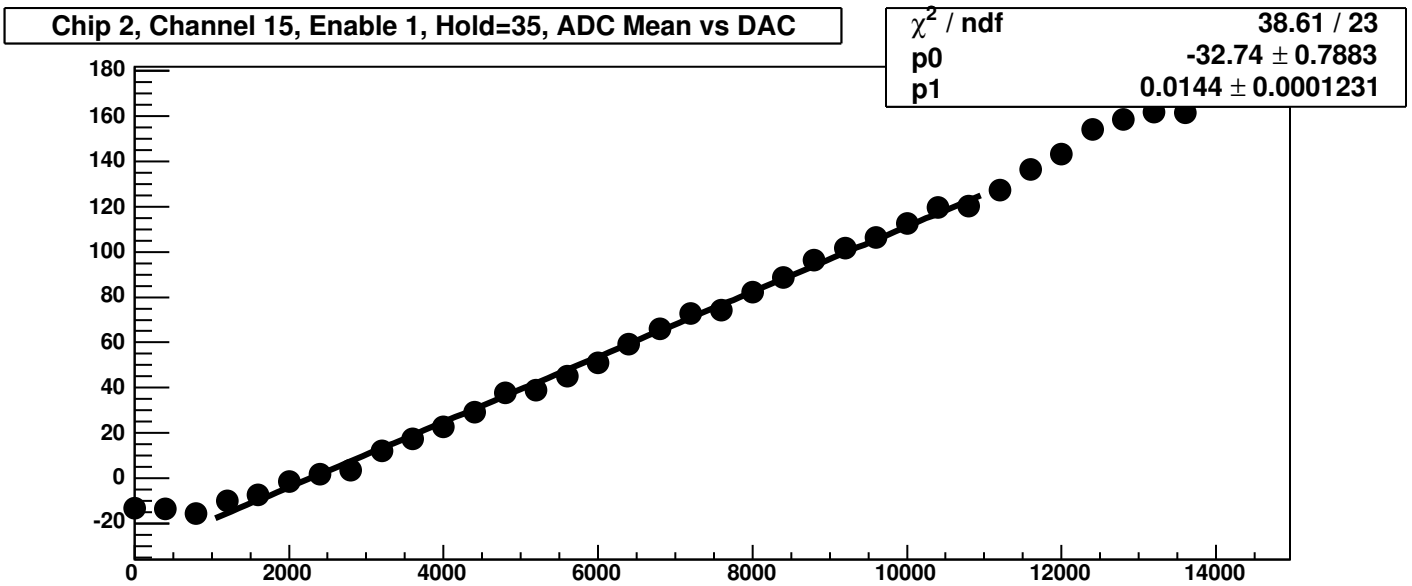


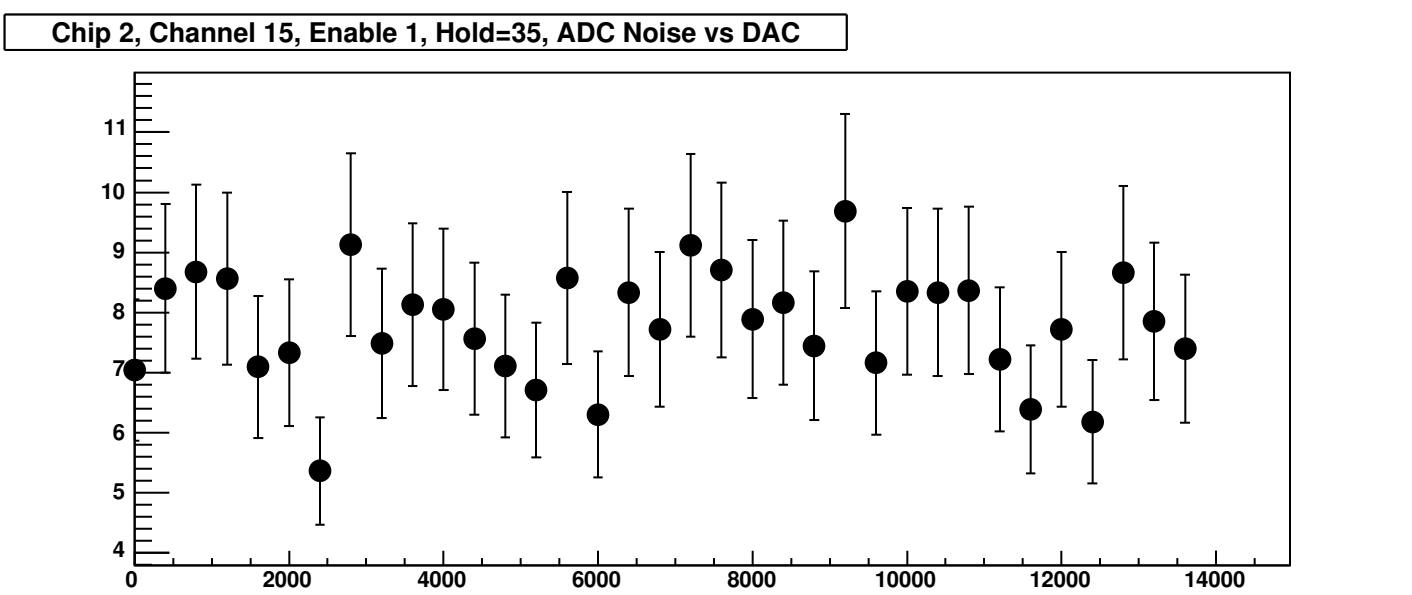
Chip 2, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC

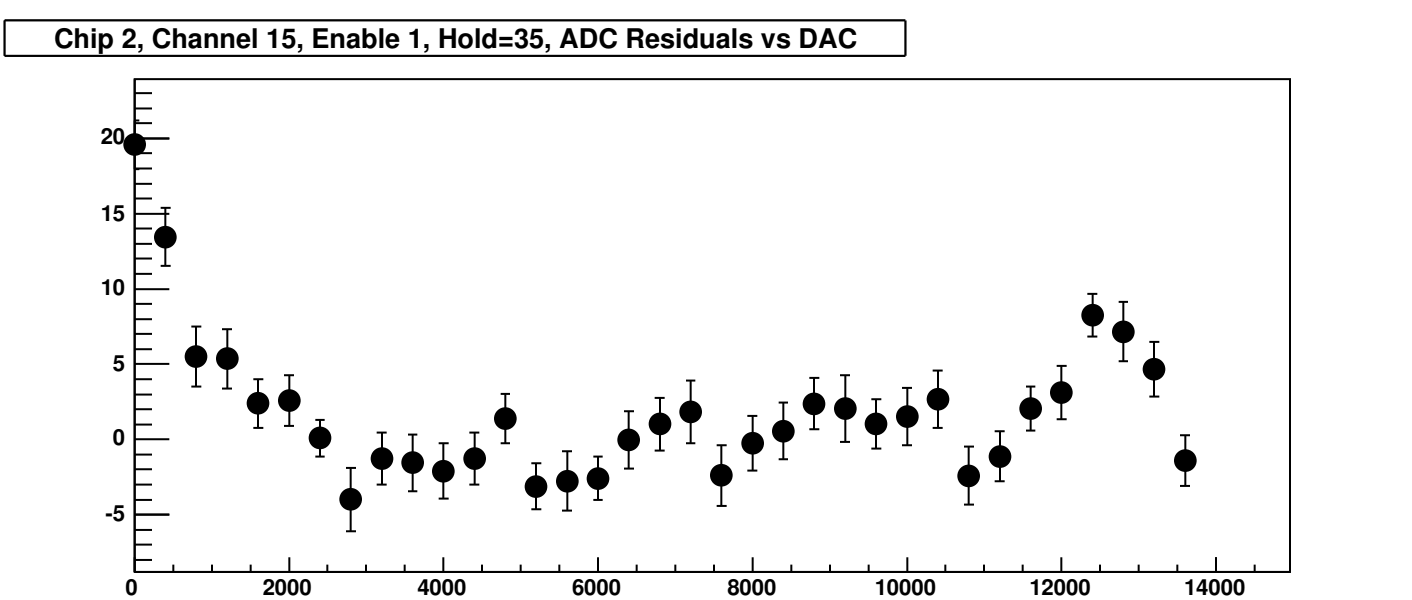


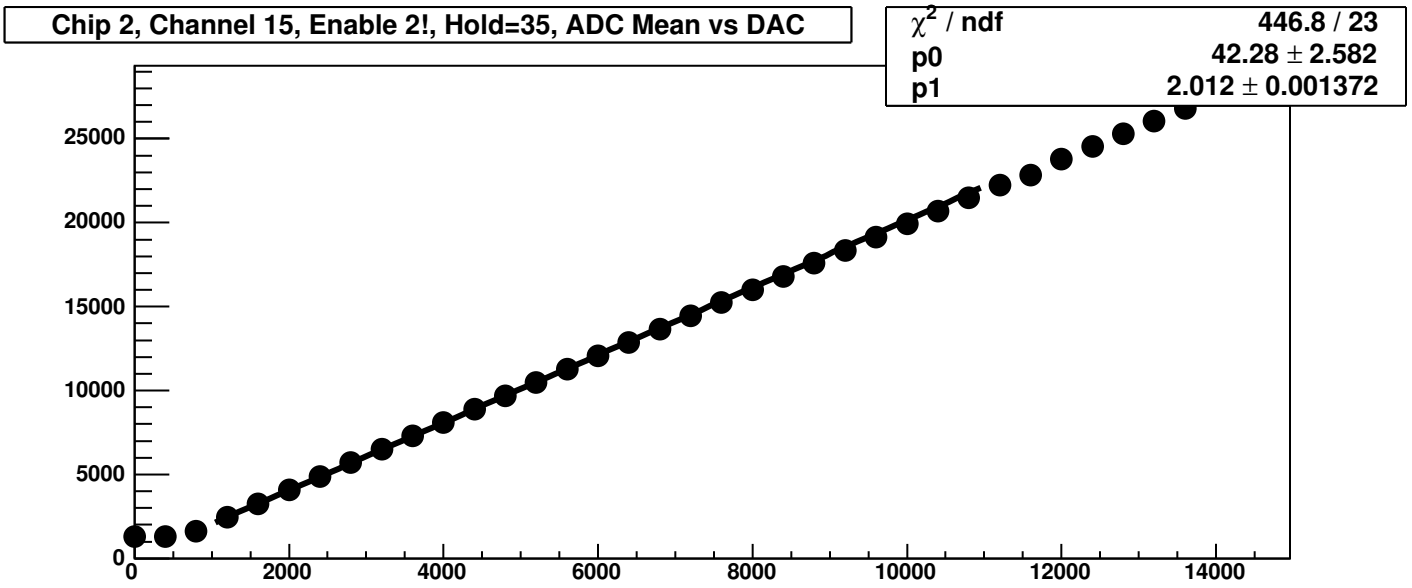
Chip 2, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

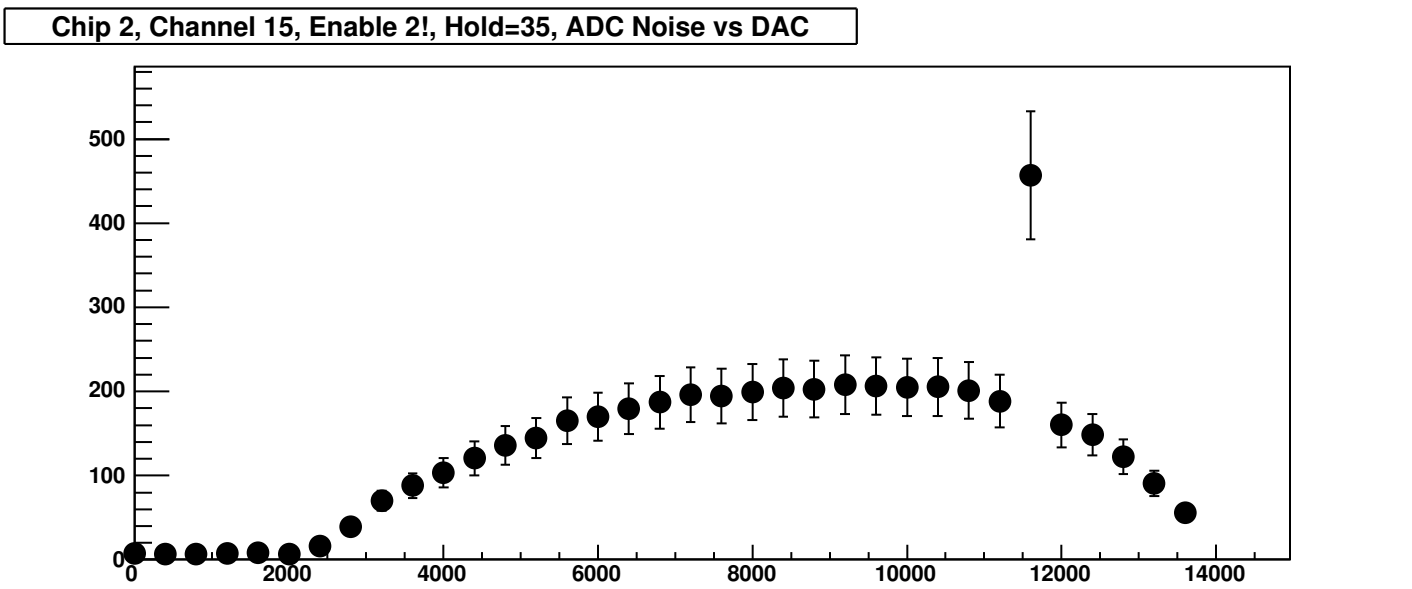


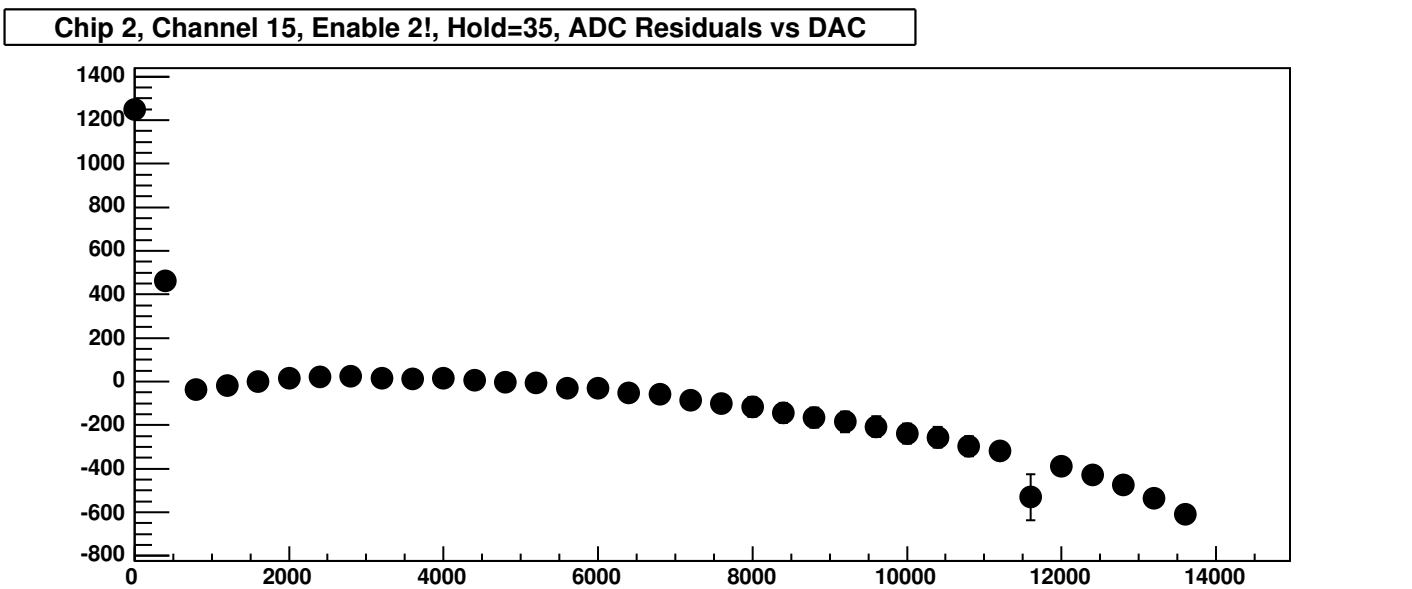

Chip 2, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 38.61 / 23
p0 -32.74 ± 0.7883
p1 0.0144 ± 0.0001231
180
160
140
120
100
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000

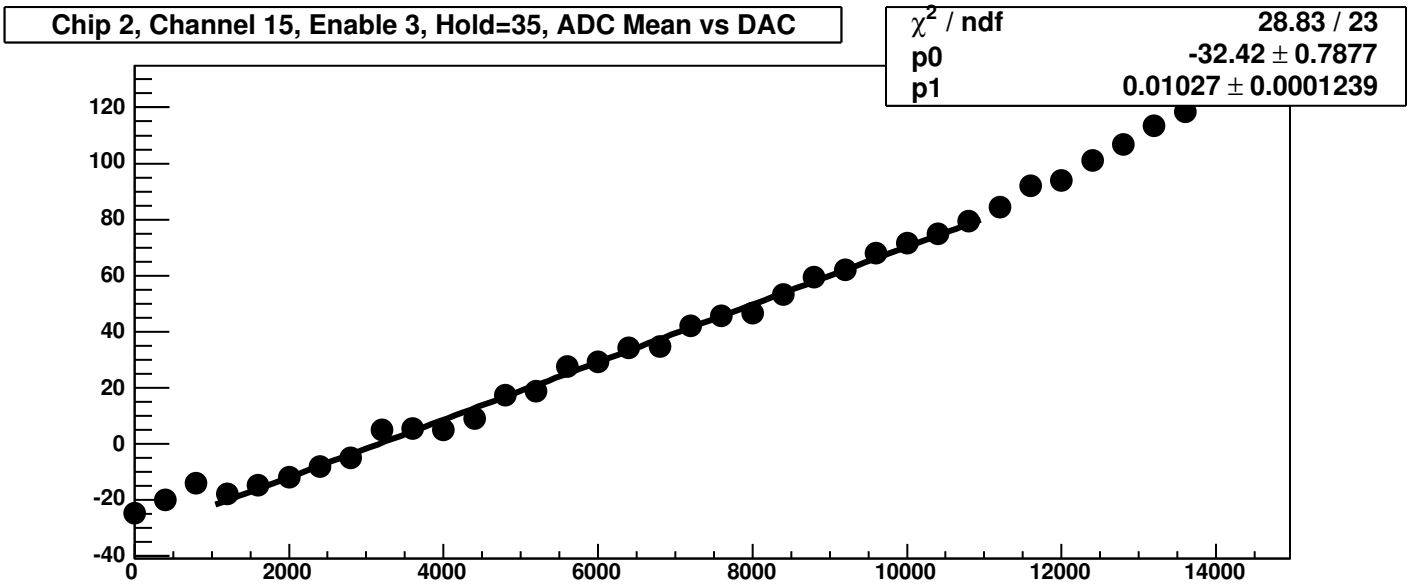

Chip 2, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

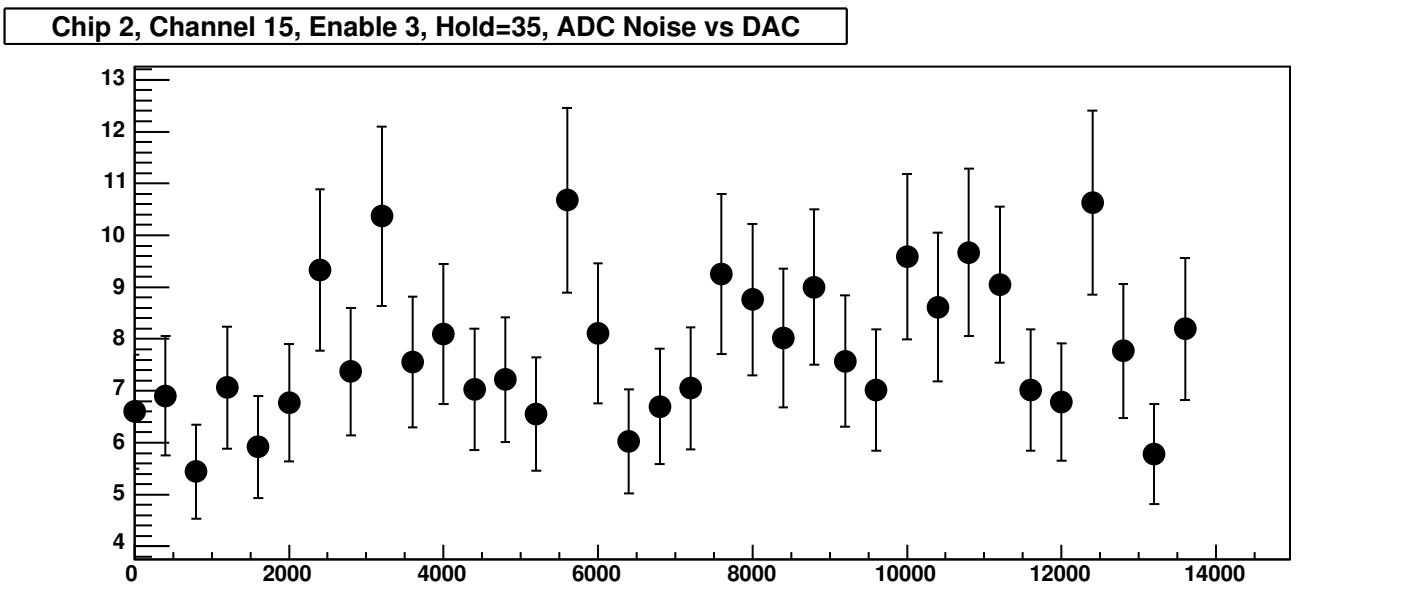

Chip 2, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

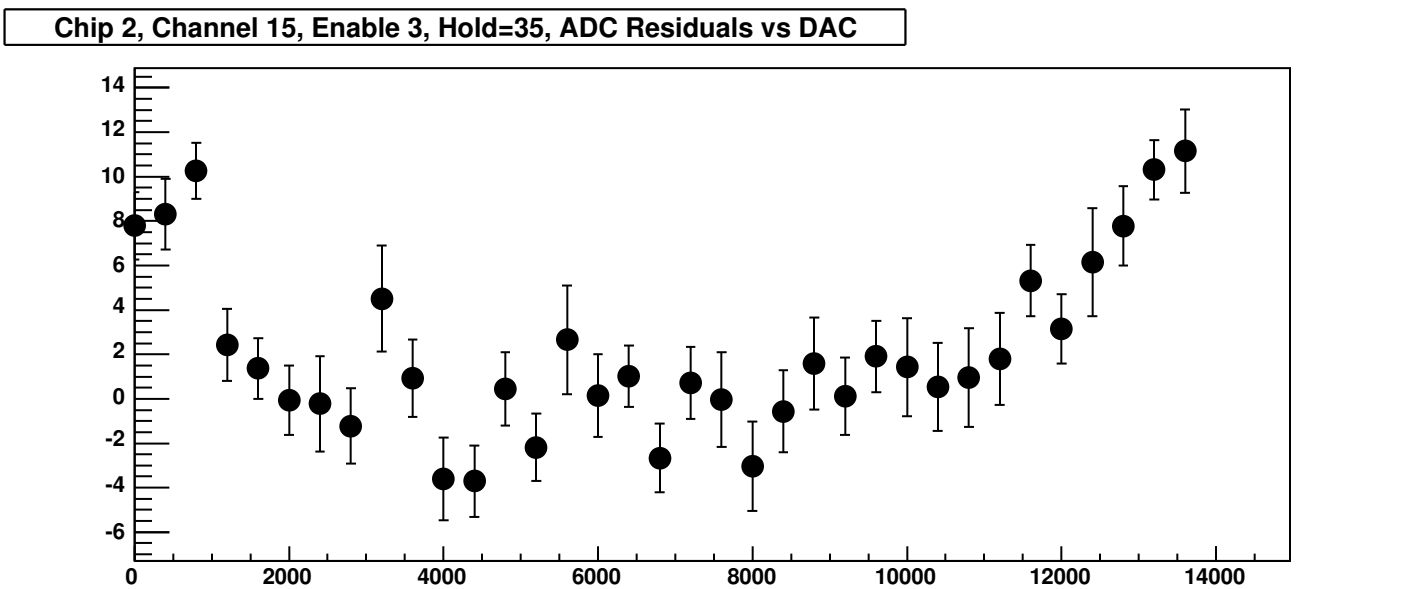

Chip 2, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 446.8 / 23
p0 42.28 ± 2.582
p1 2.012 ± 0.001372
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

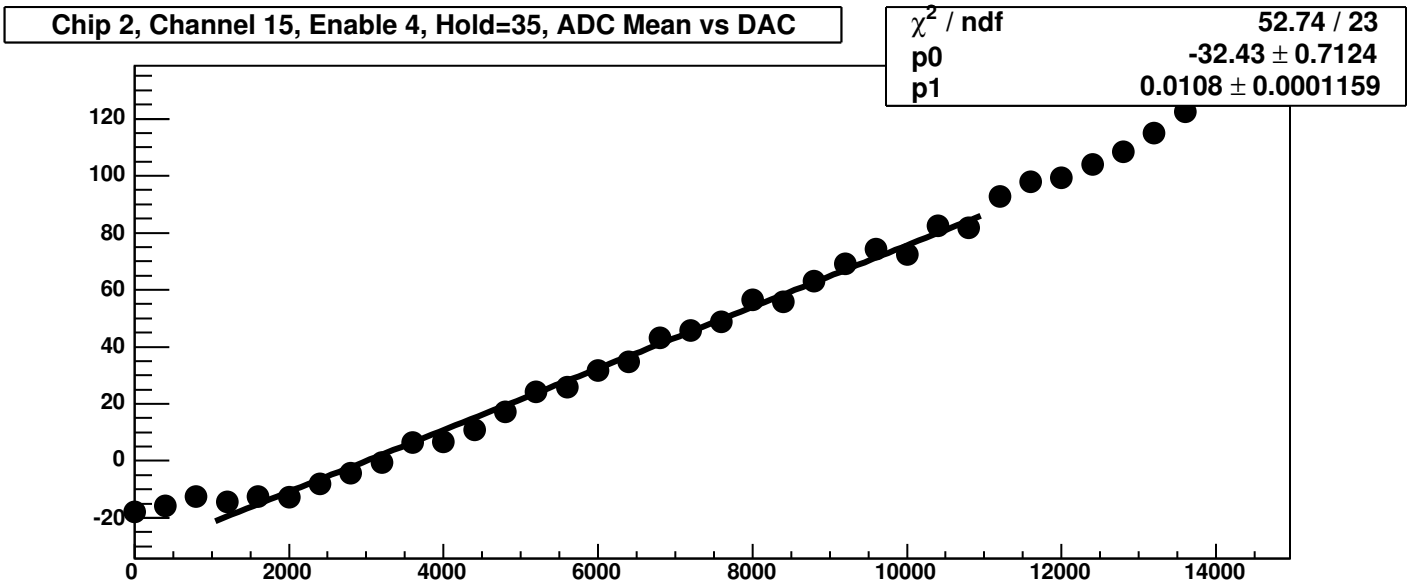

Chip 2, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

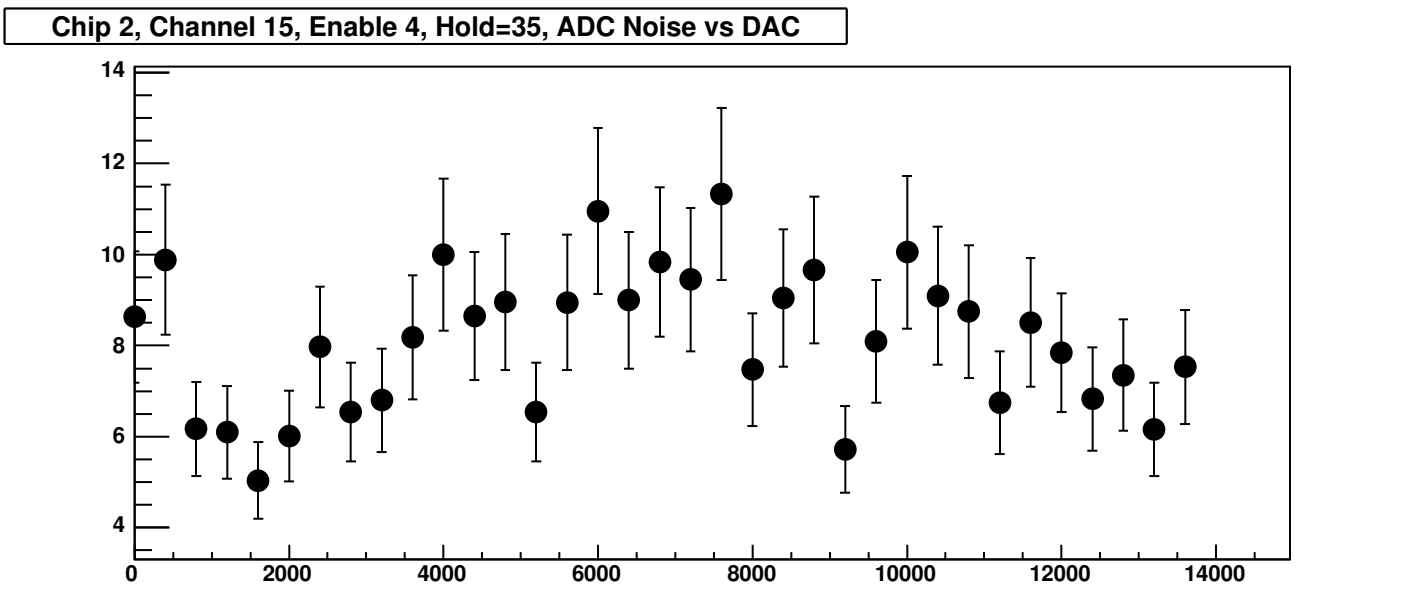

Chip 2, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

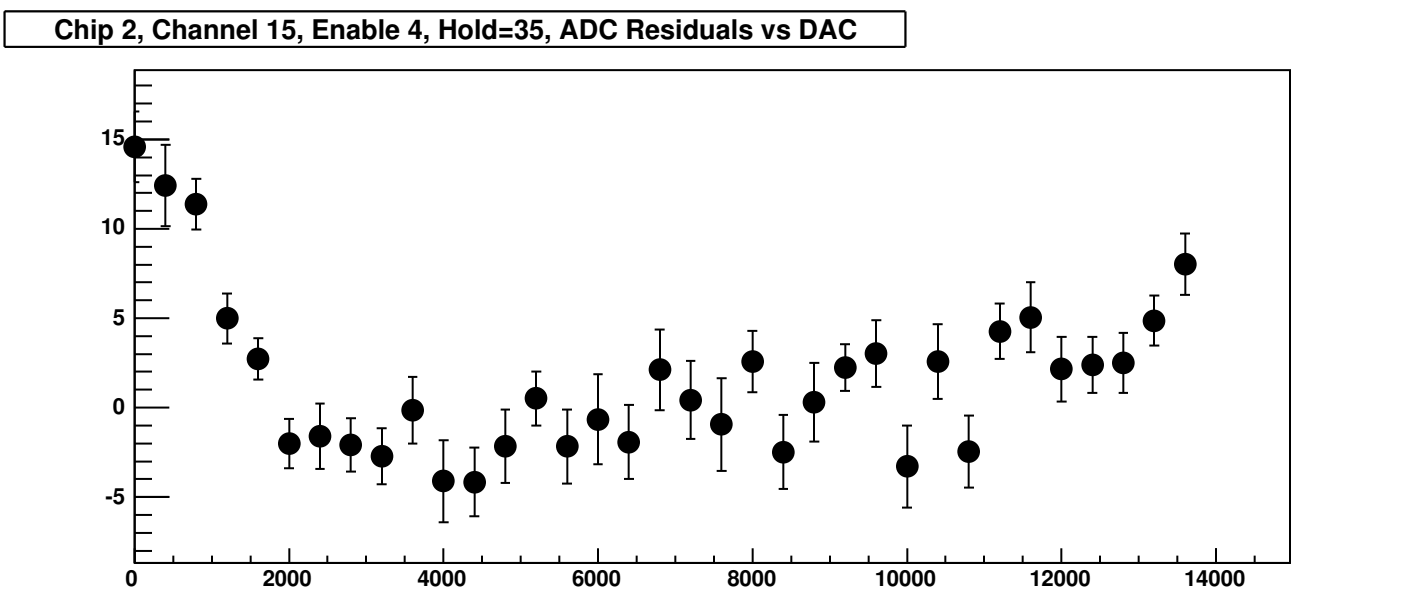

Chip 2, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.83 / 23
p0 -32.42 ± 0.7877
p1 0.01027 ± 0.0001239
120
100
80
60
40
20
0
-20
-40
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

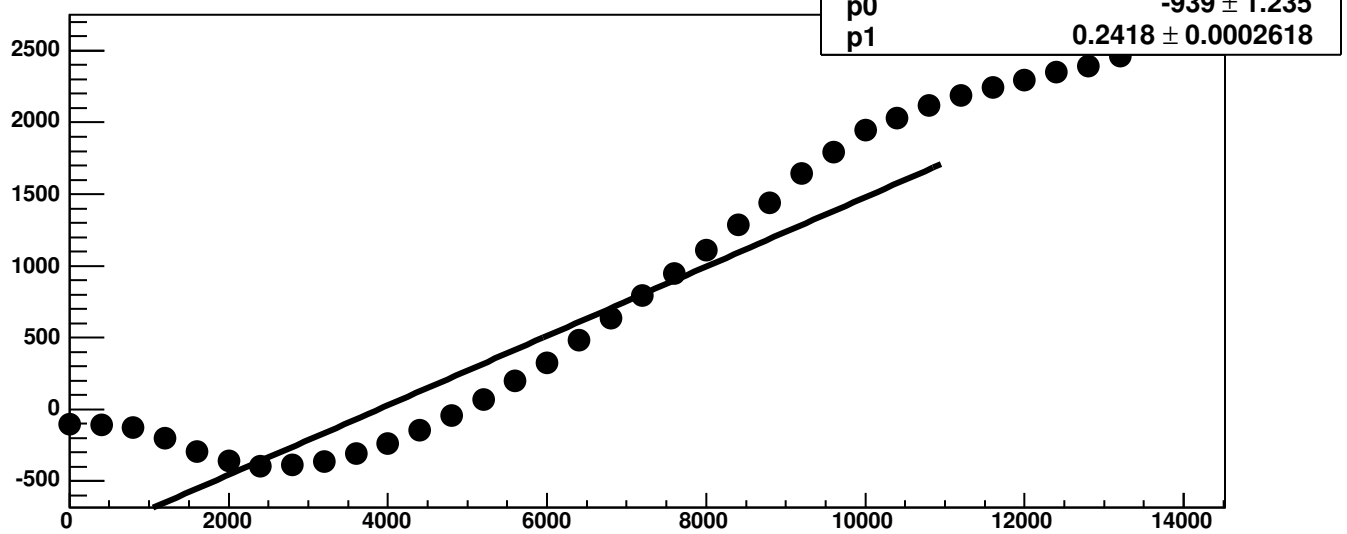

Chip 2, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 52.74 / 23
p0 -32.43 ± 0.7124
p1 0.0108 ± 0.0001159
120
100
80
60
40
20
0
-20
0
2000
4000
6000
8000
10000
12000
14000

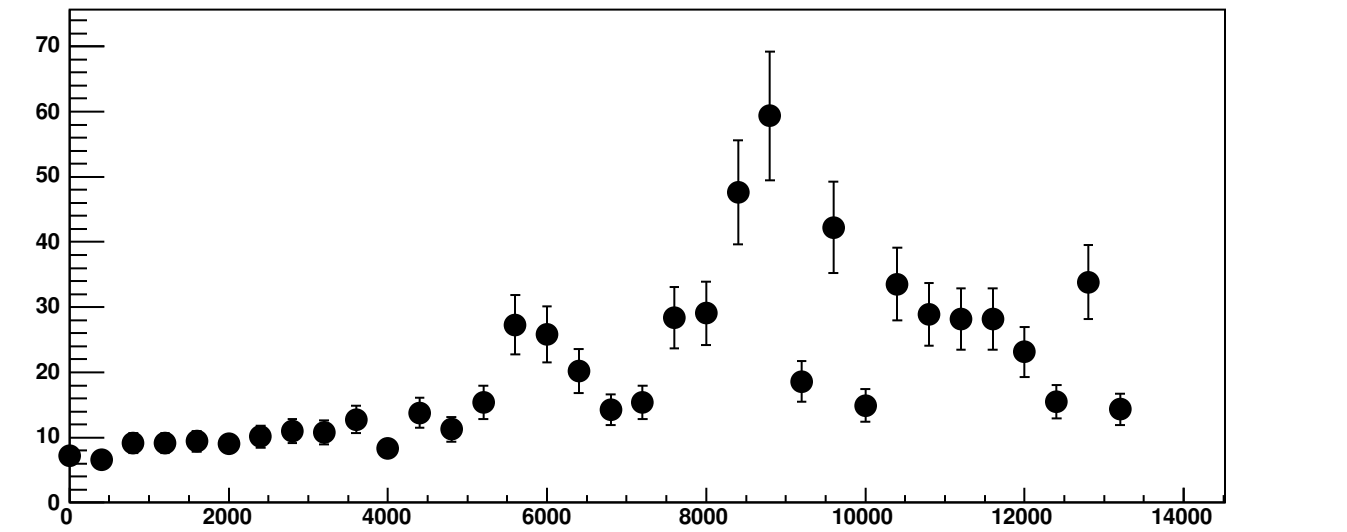

Chip 2, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

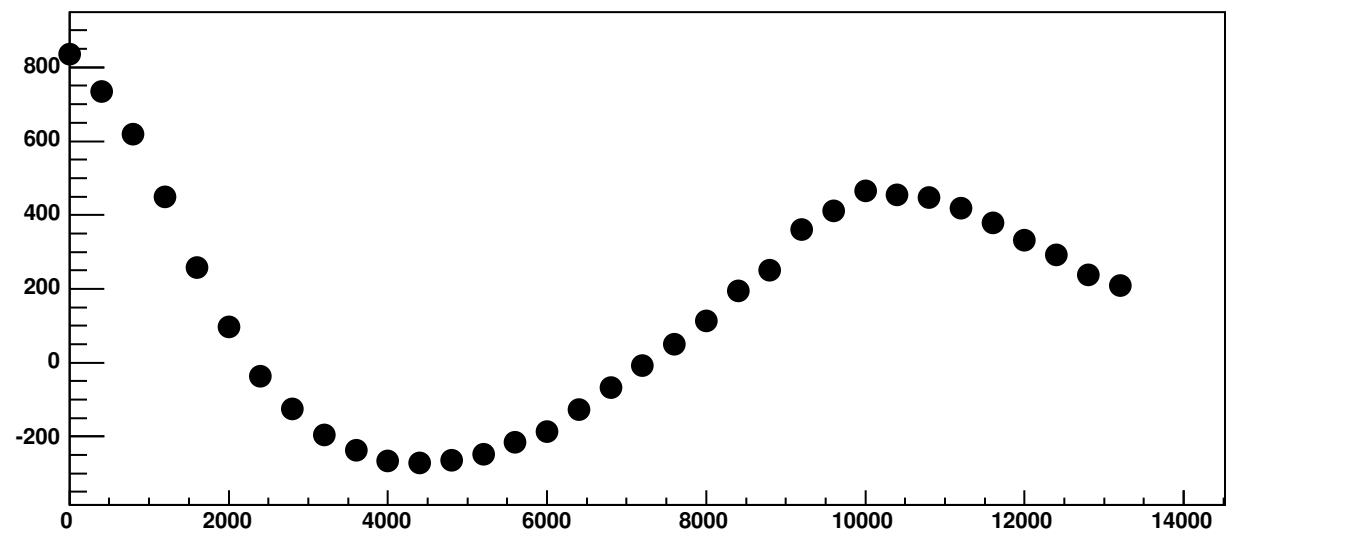
Chip 2, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC

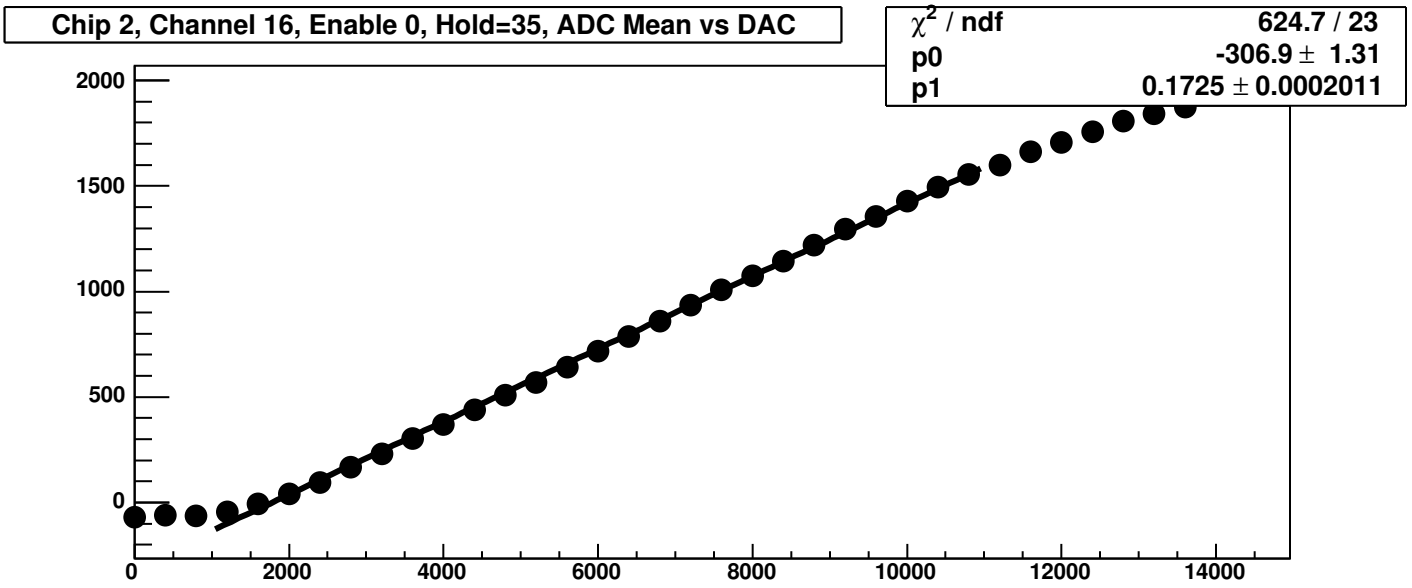


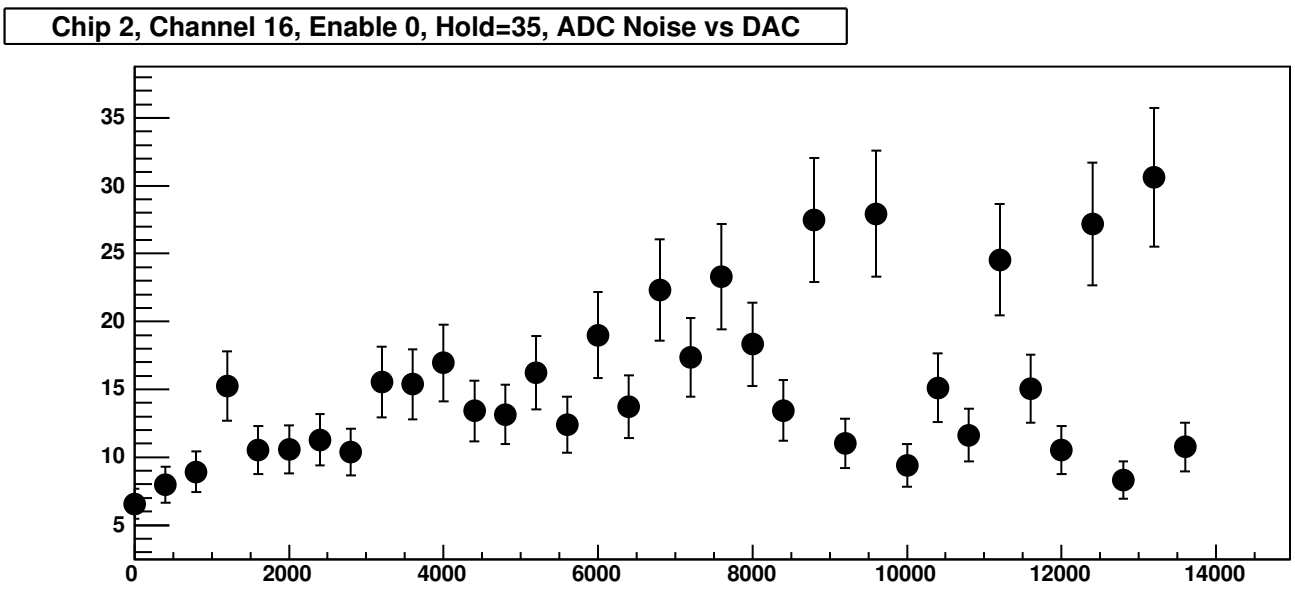
Chip 2, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC

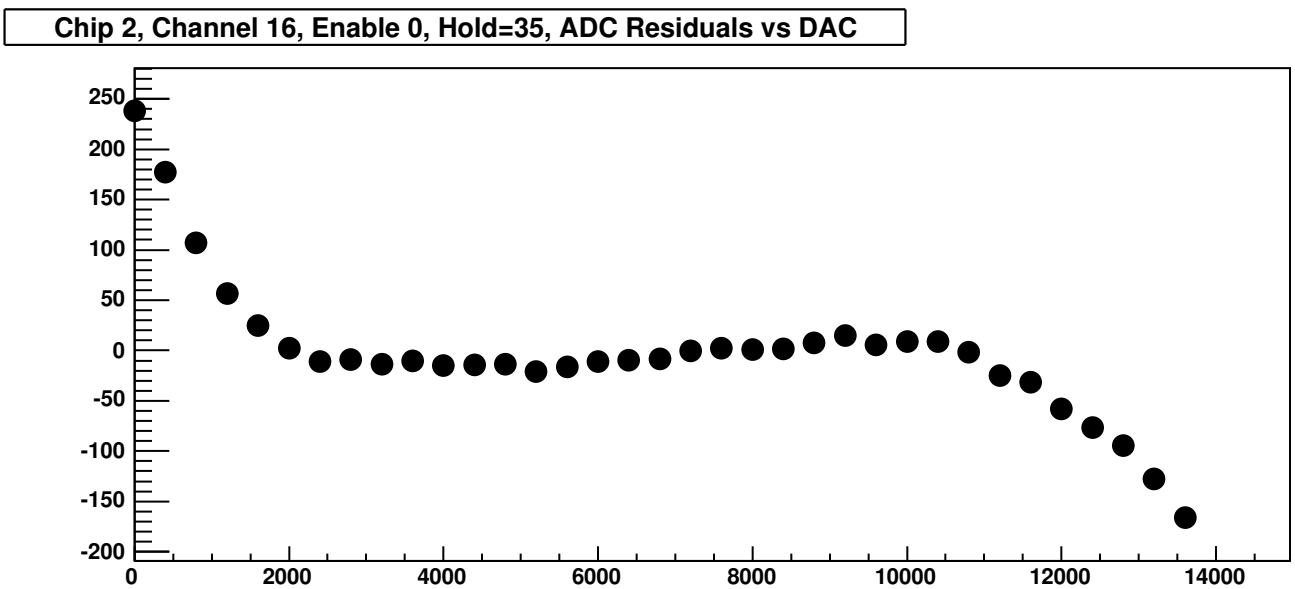


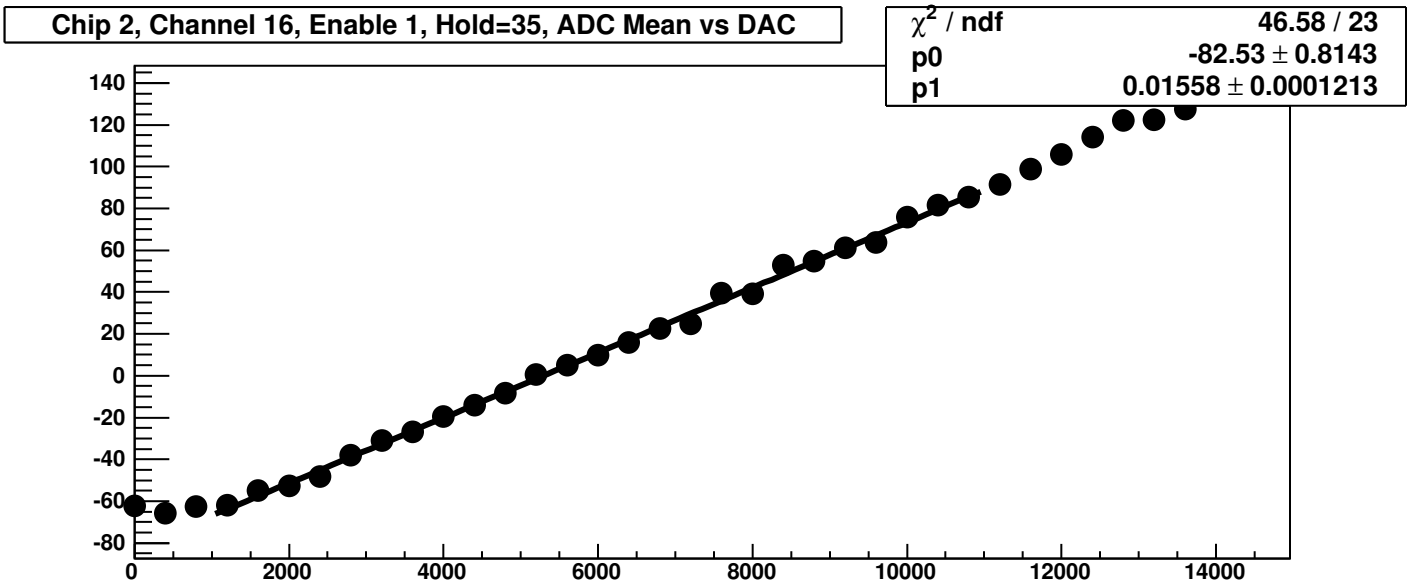
Chip 2, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC

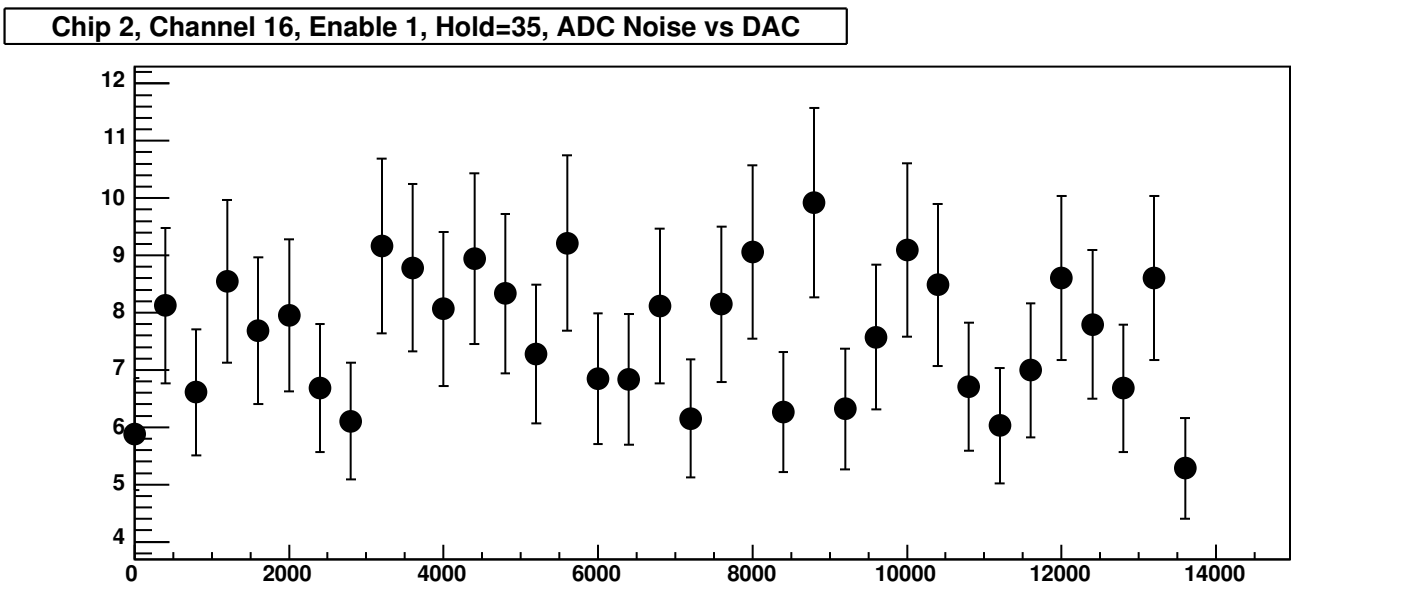


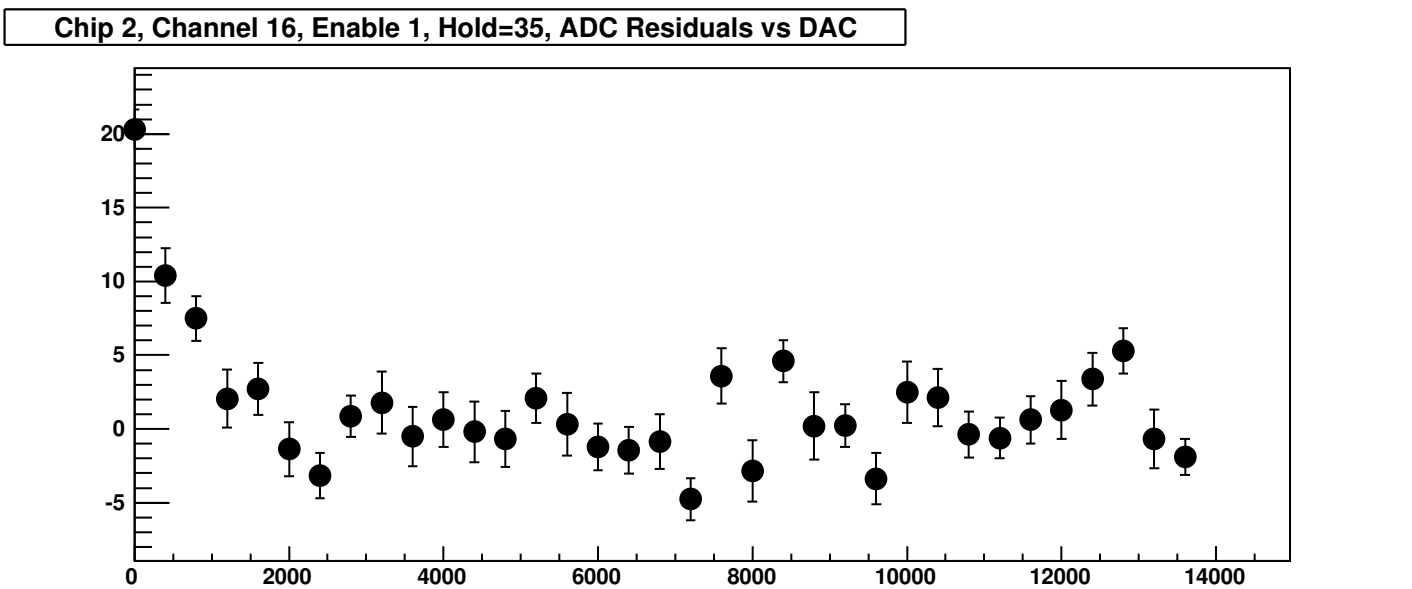

Chip 2, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 624.7 / 23
p0 -306.9 ± 1.31
p1 0.1725 ± 0.0002011
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

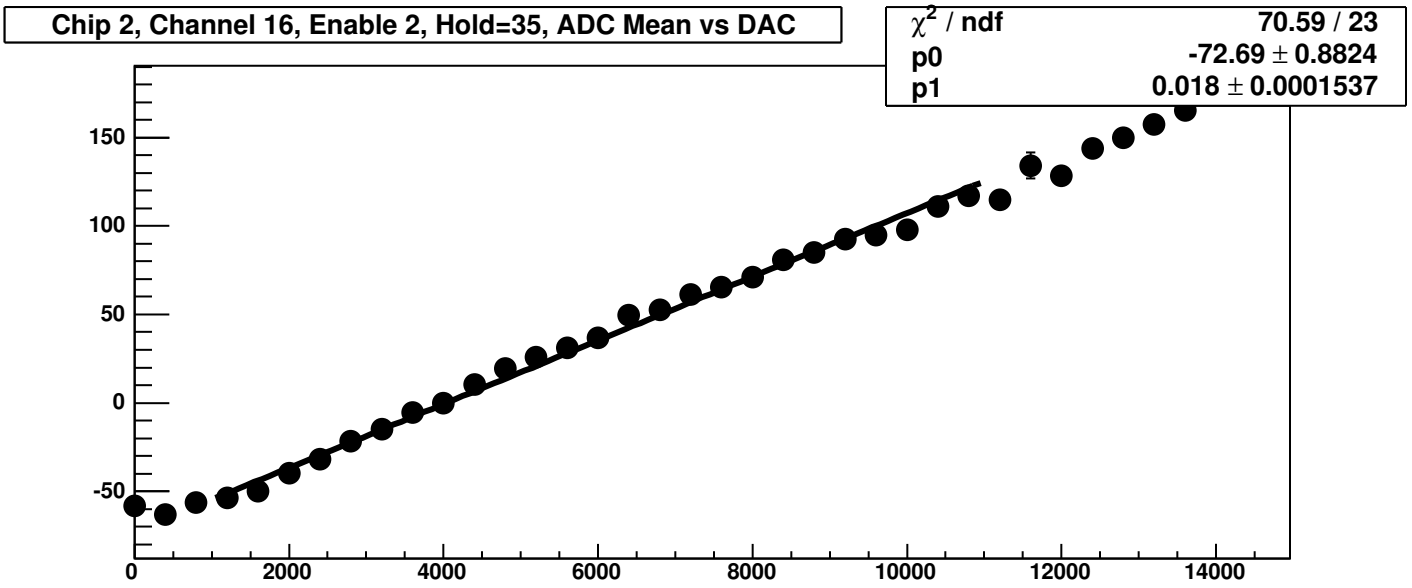

Chip 2, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

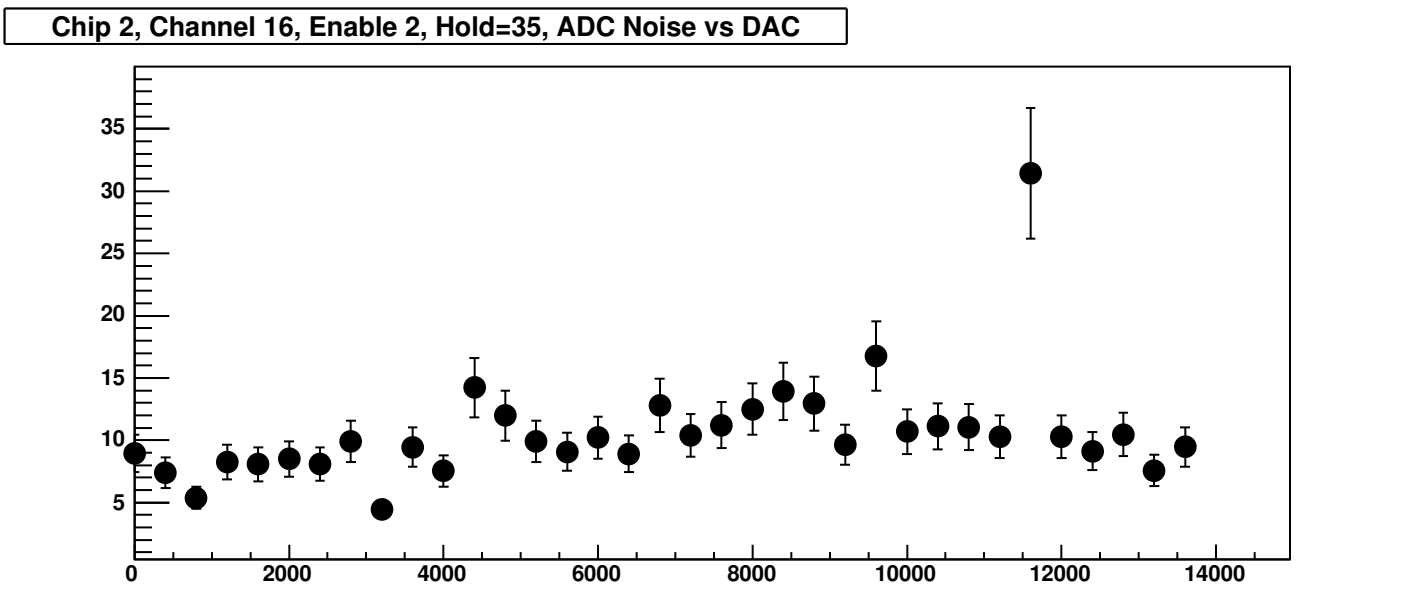

Chip 2, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

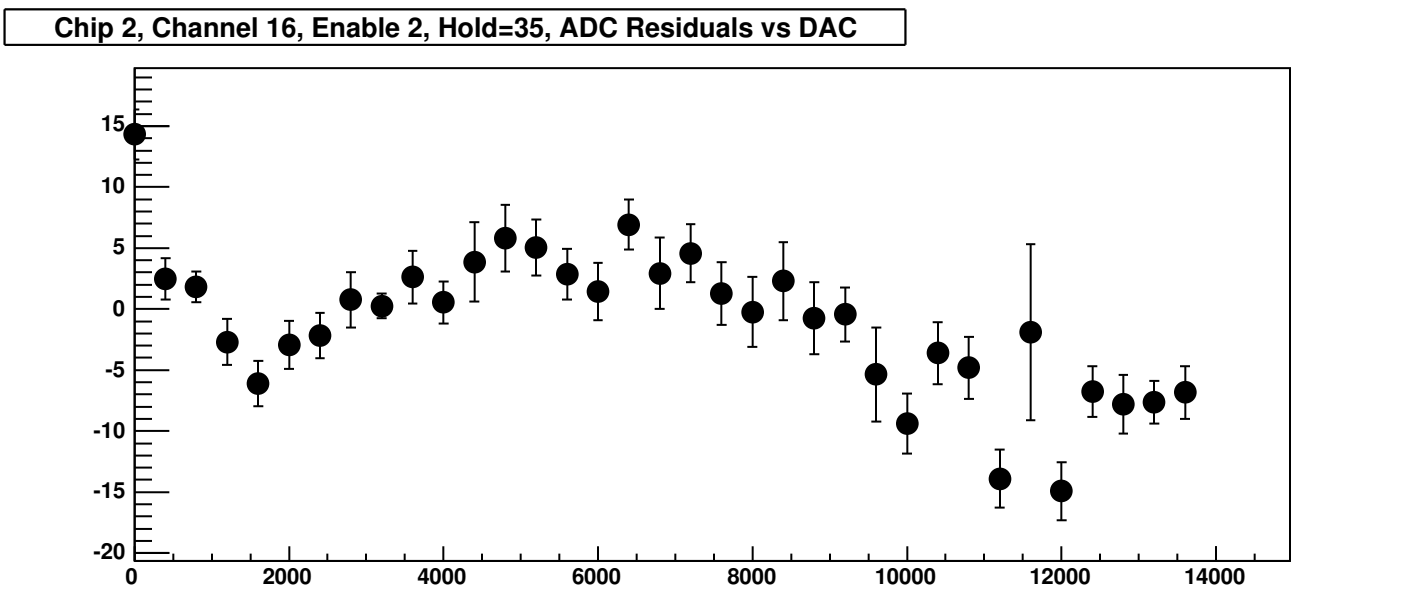

Chip 2, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 46.58 / 23
p0 -82.53 ± 0.8143
p1 0.01558 ± 0.0001213
140
120
100
80
60
40
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

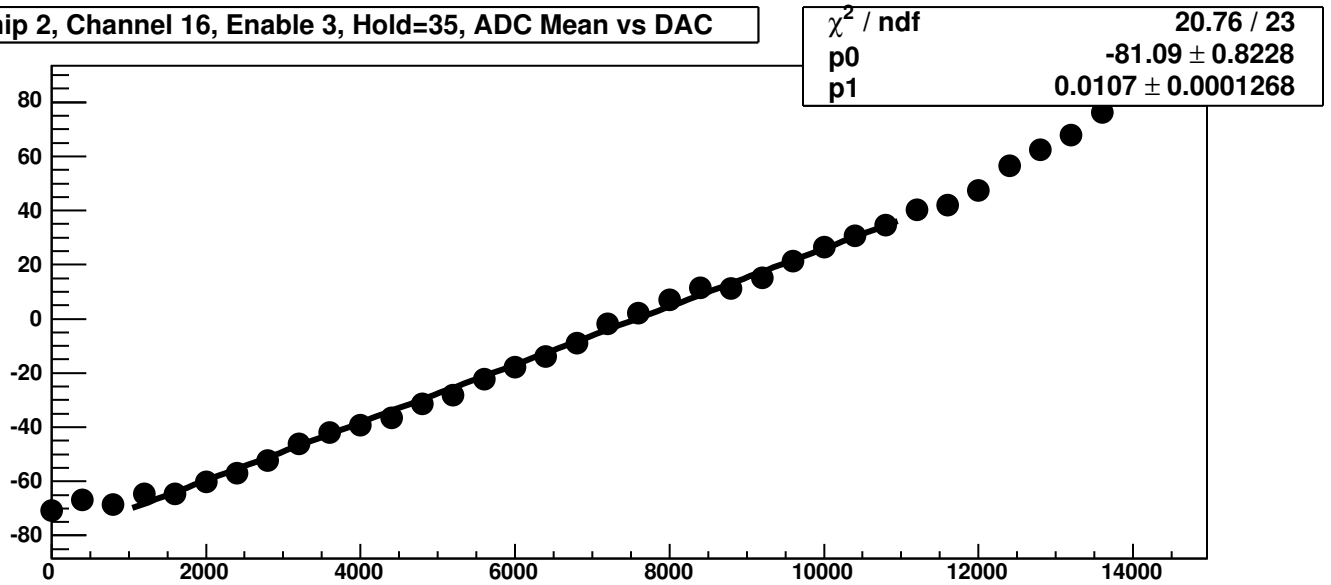

Chip 2, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 70.59 / 23
p0 -72.69 ± 0.8824
p1 0.018 ± 0.0001537
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

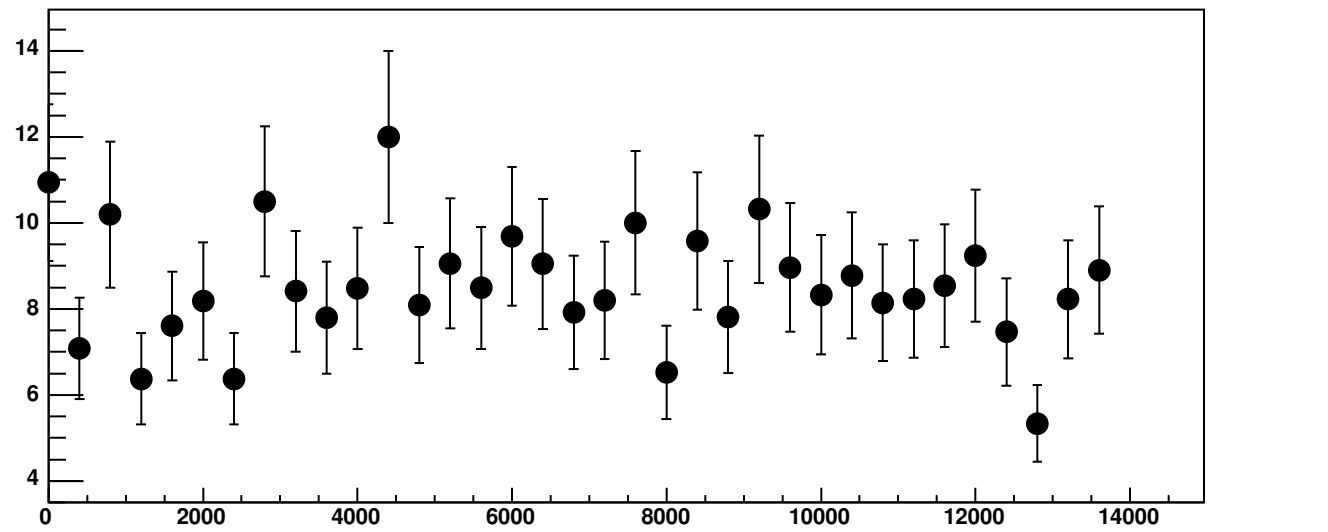

Chip 2, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 2, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

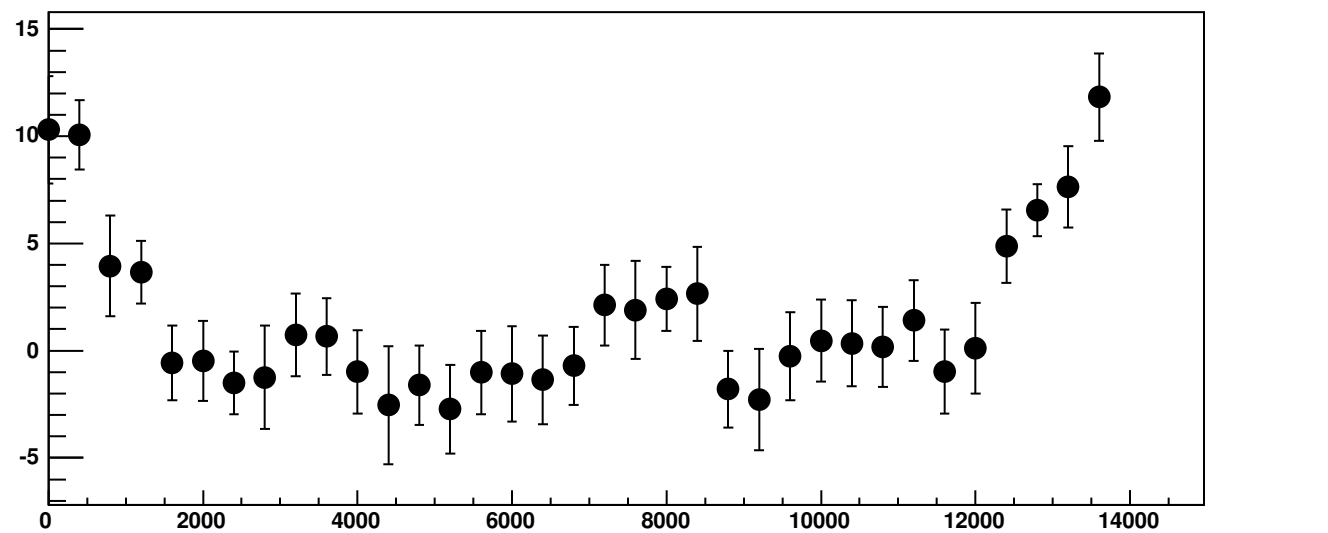
Chip 2, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

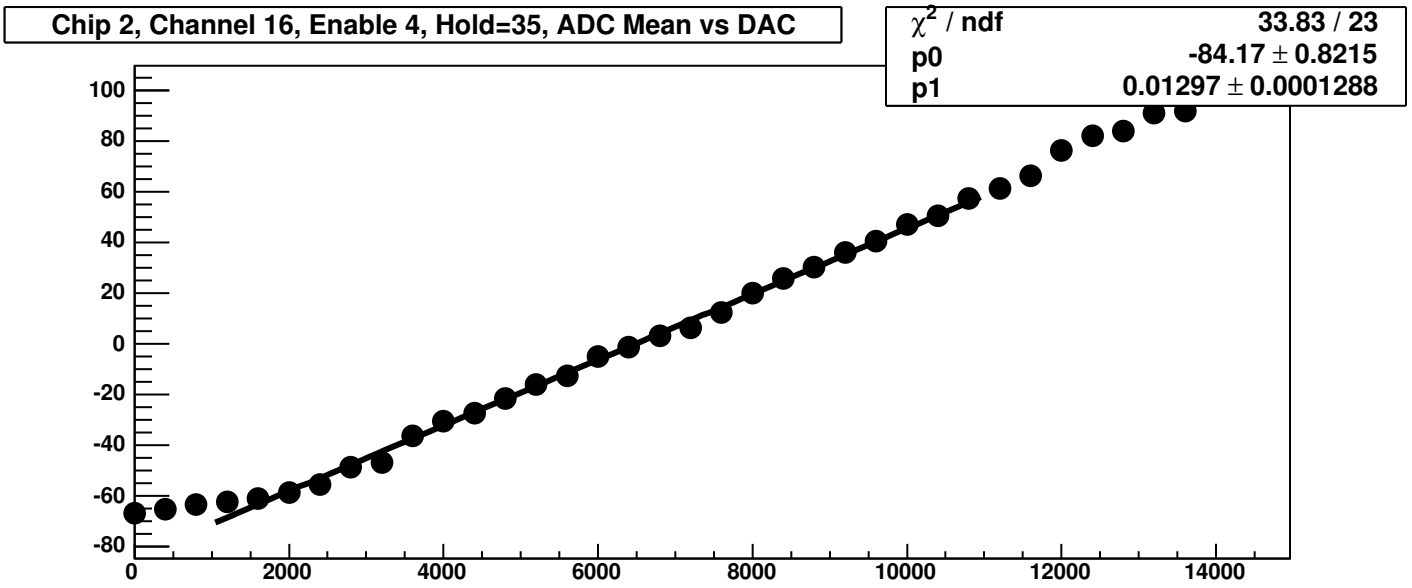


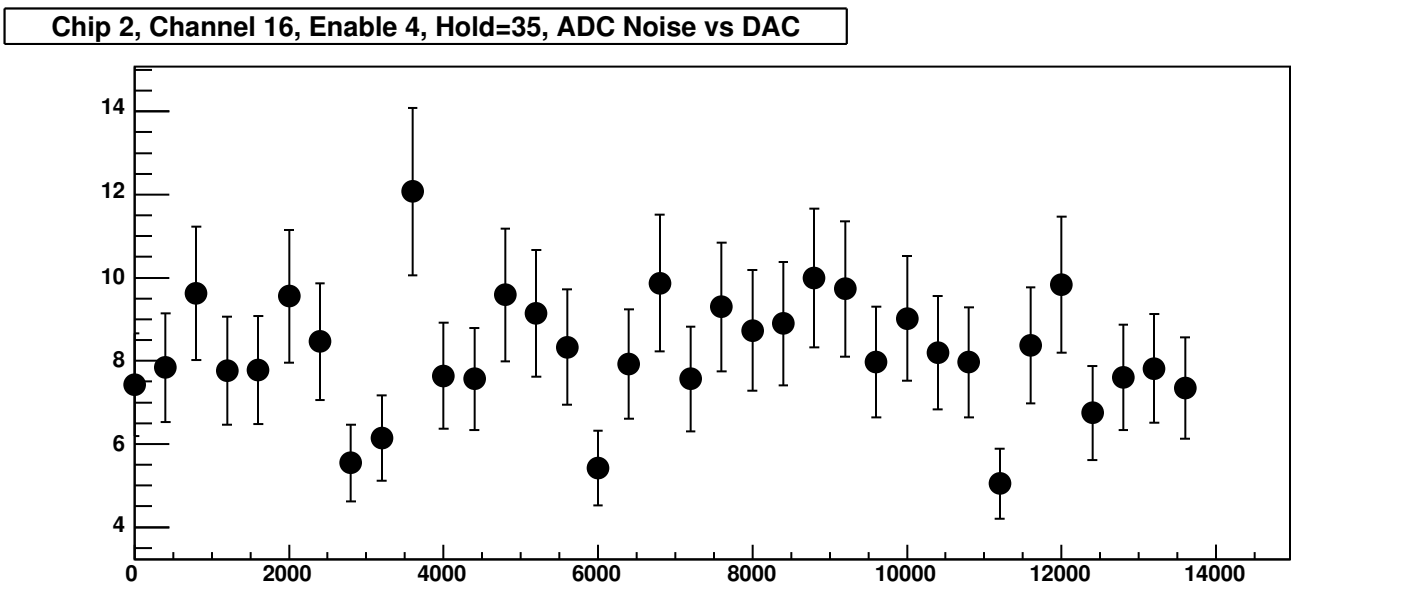
Chip 2, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC

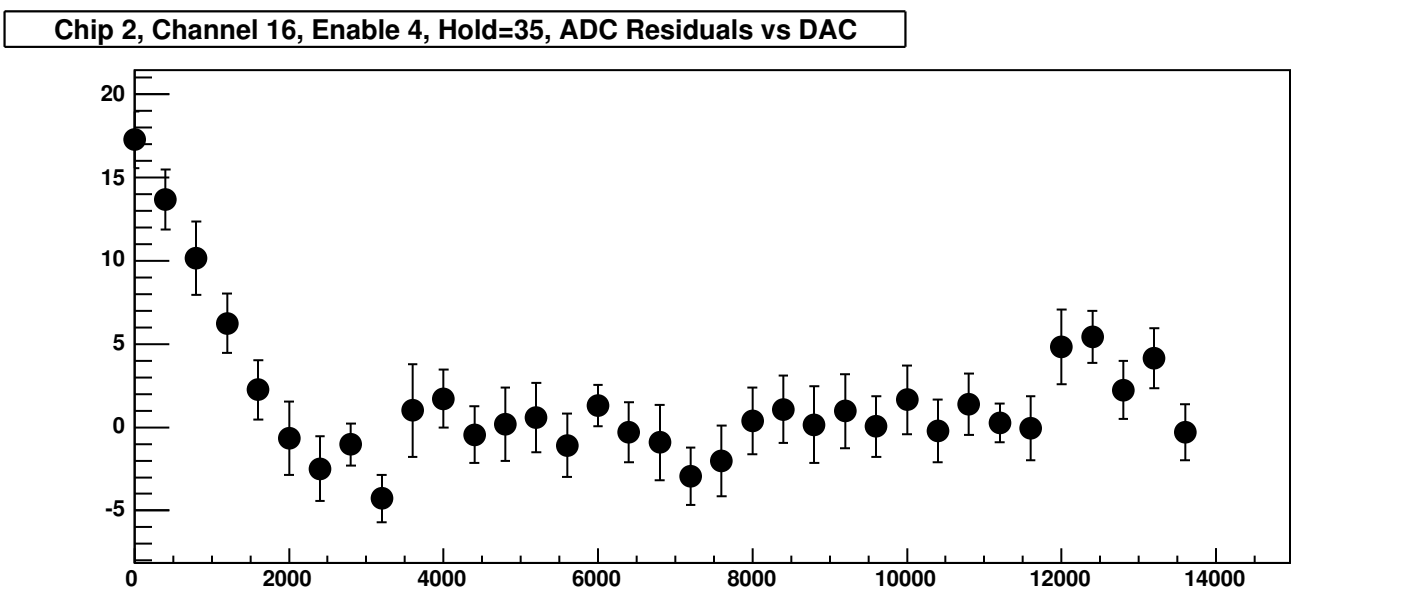


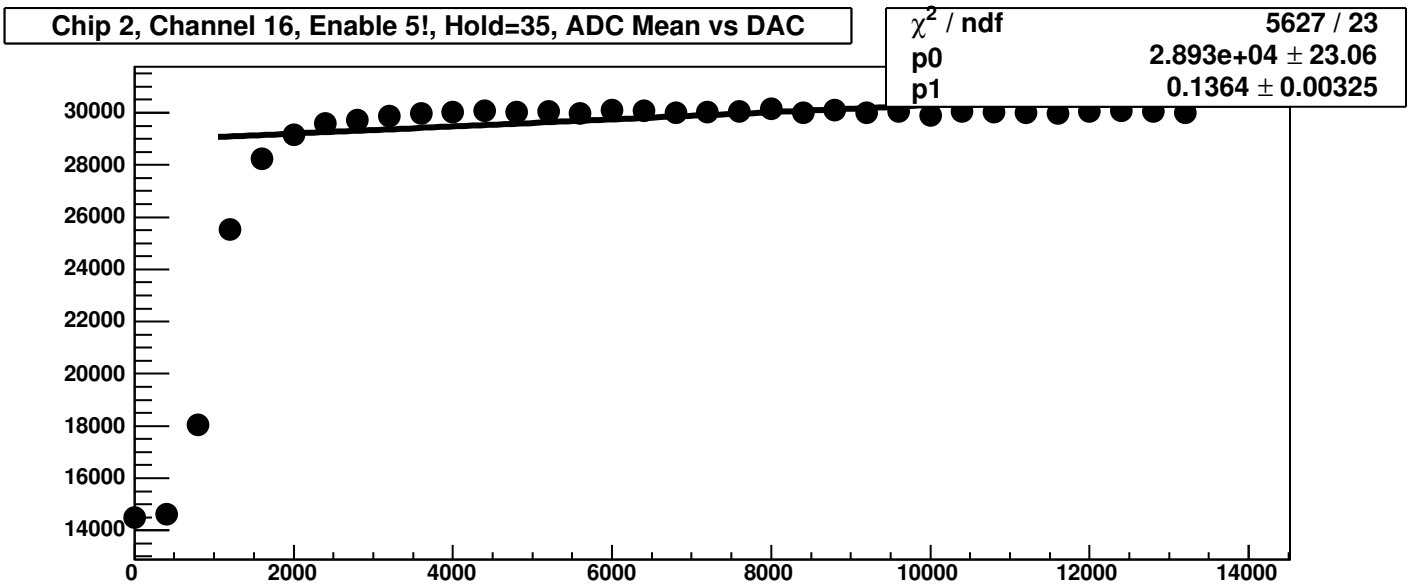
Chip 2, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

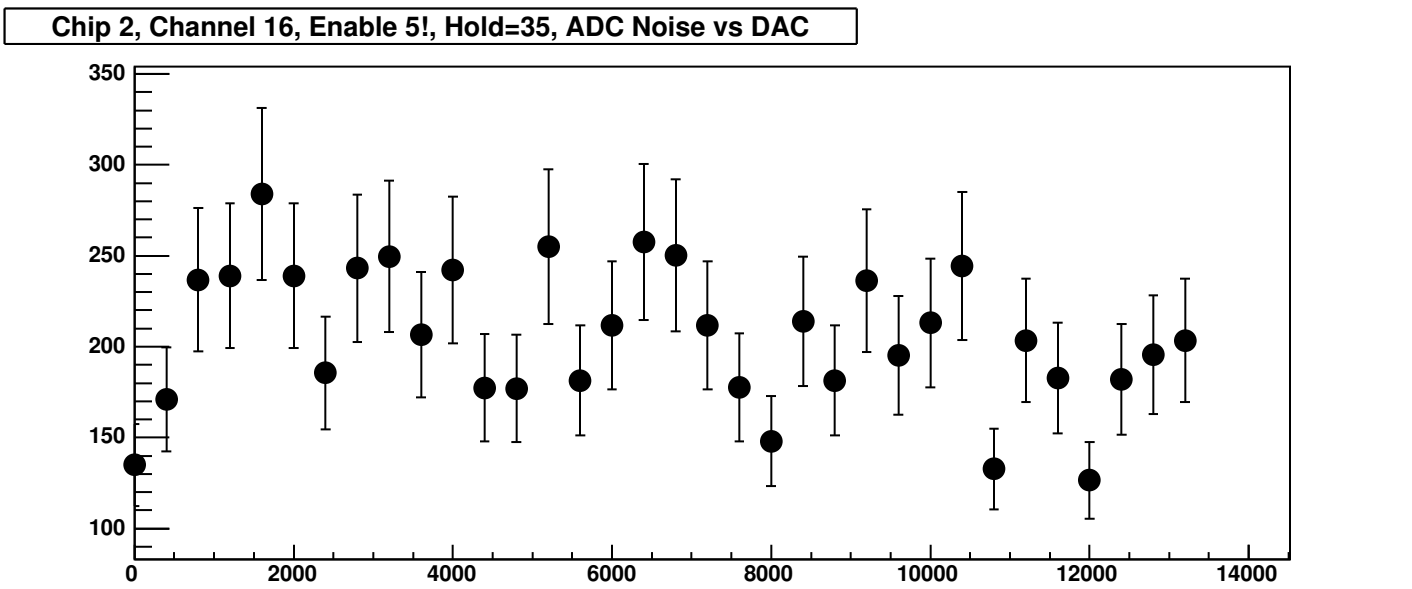


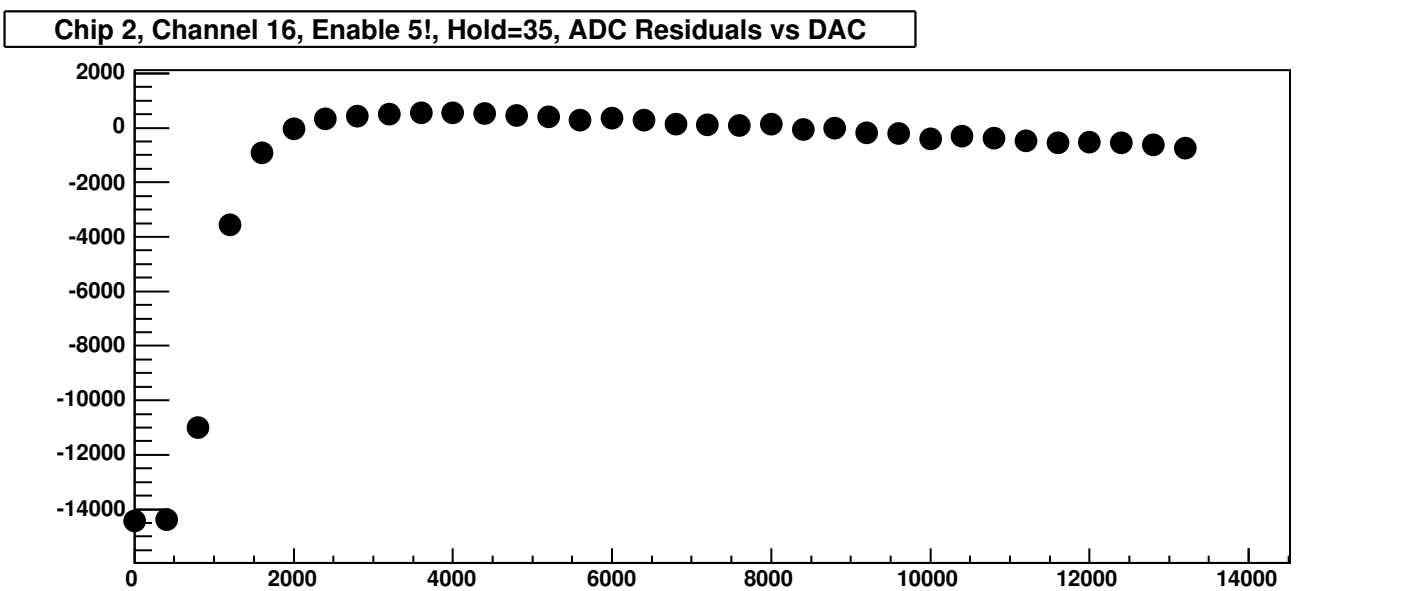

Chip 2, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 33.83 / 23
p0 -84.17 ± 0.8215
p1 0.01297 ± 0.0001288
100
80
60
40
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000

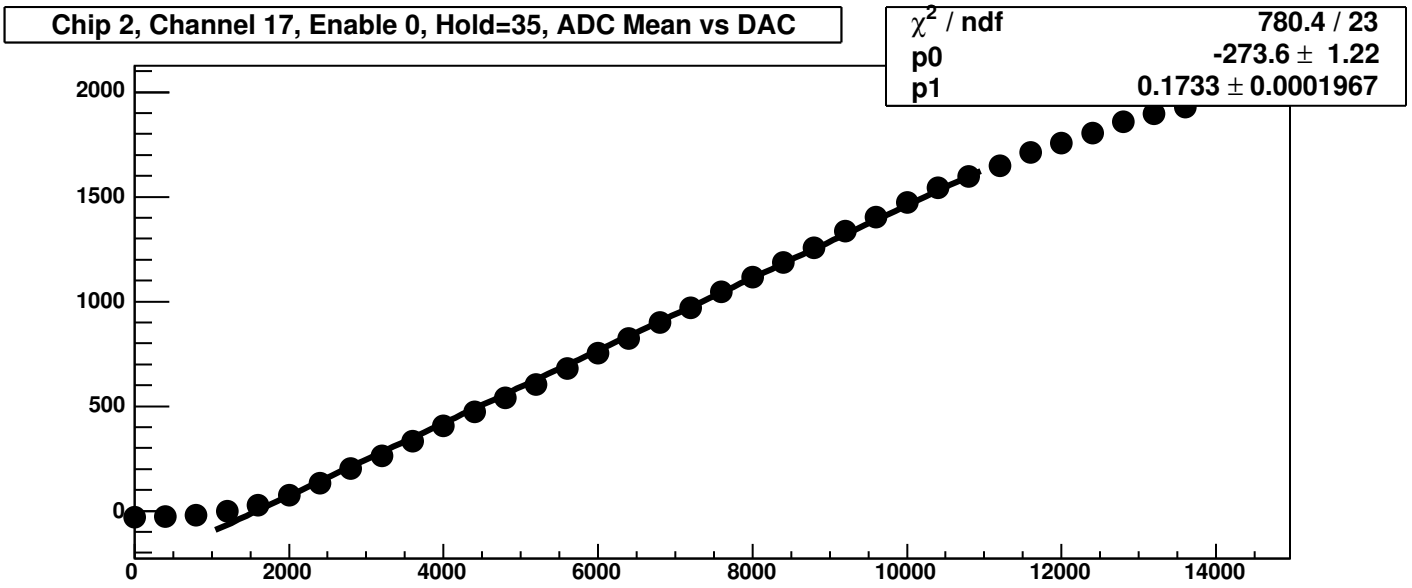

Chip 2, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

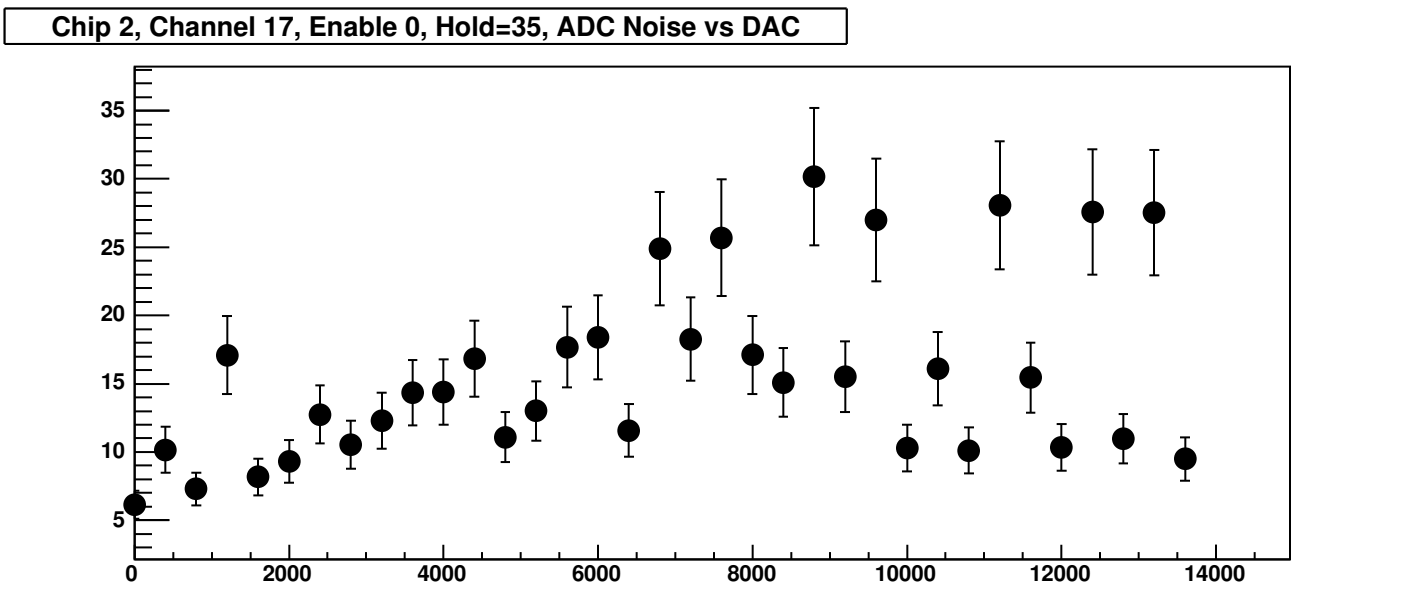

Chip 2, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

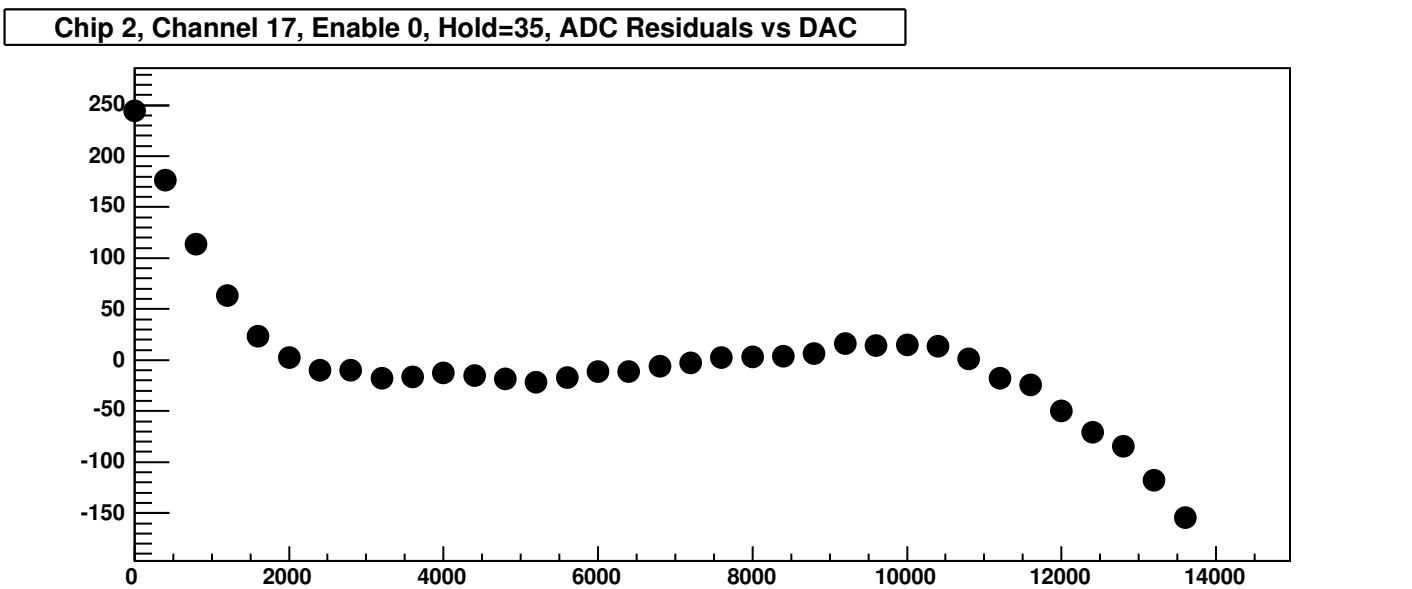

Chip 2, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 5627 / 23
p0 2.893e+04 ± 23.06
p1 0.1364 ± 0.00325
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

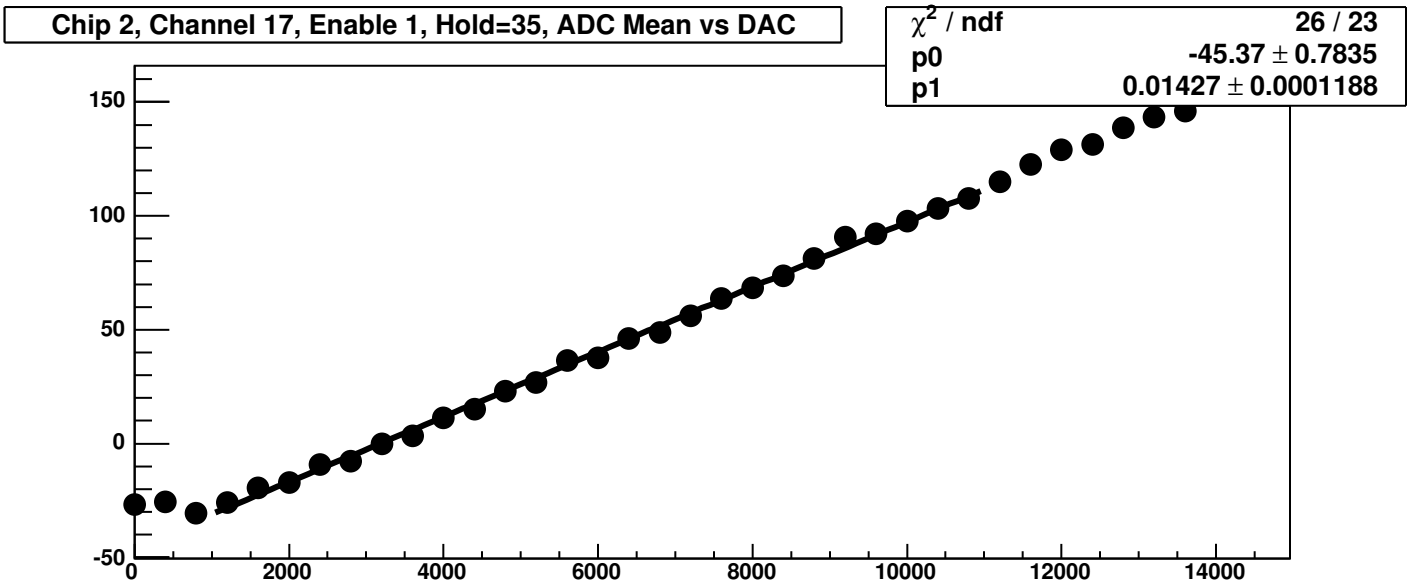

Chip 2, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

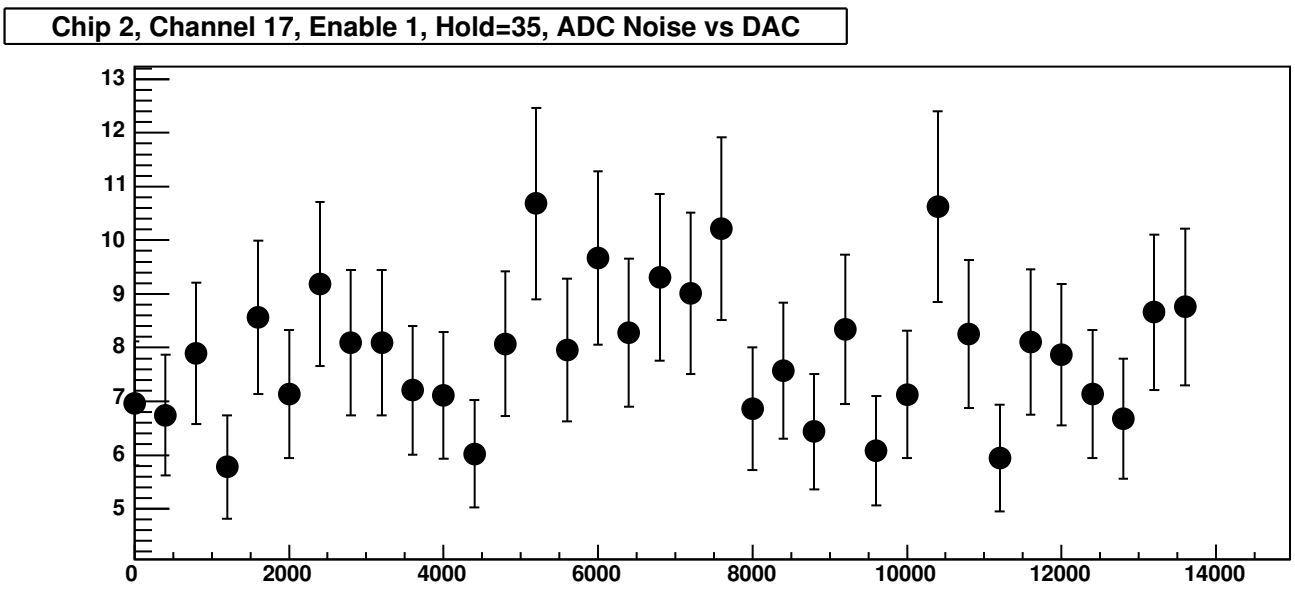

Chip 2, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

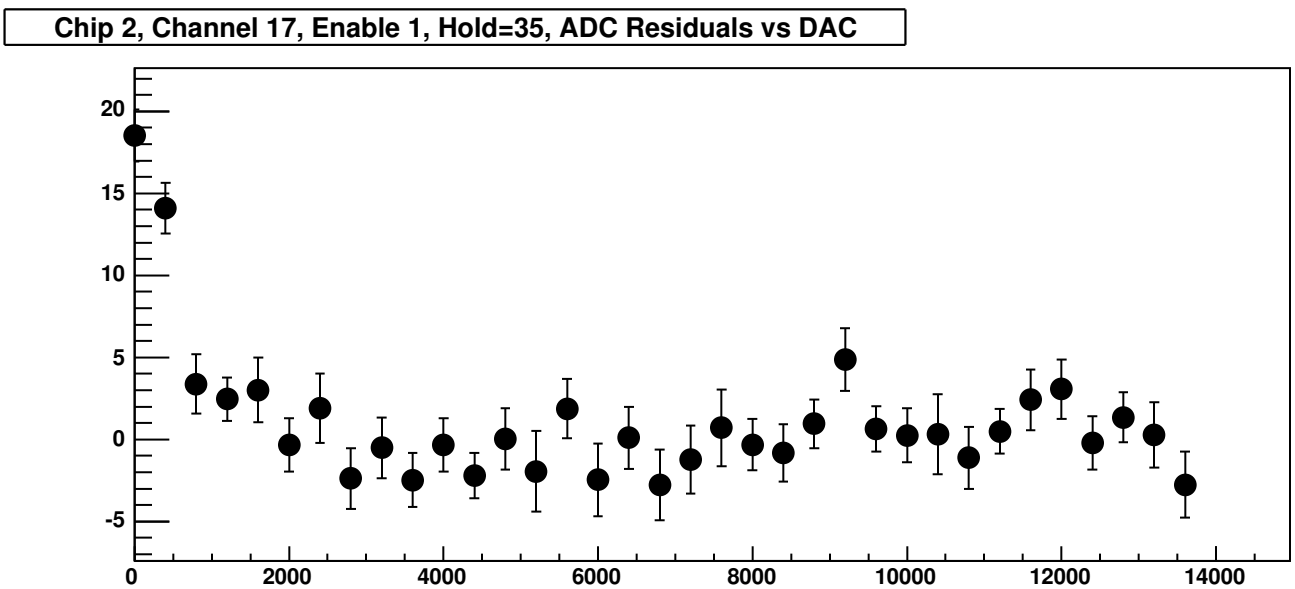

Chip 2, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 780.4 / 23
p0 -273.6 ± 1.22
p1 0.1733 ± 0.0001967
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

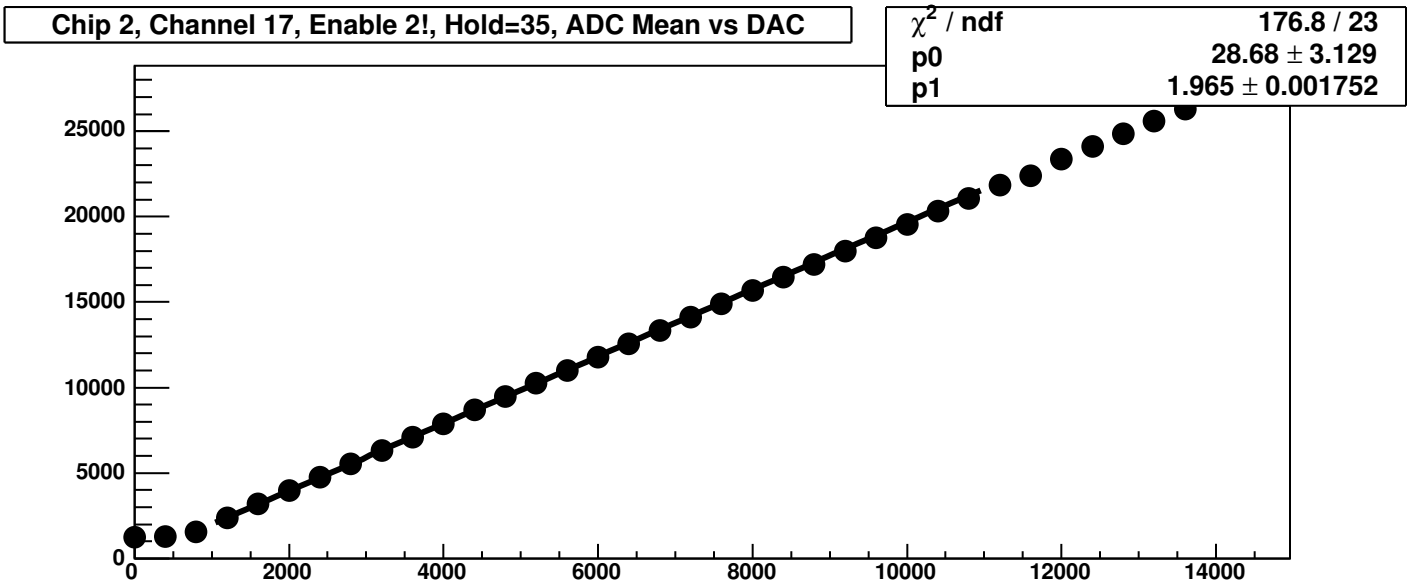

Chip 2, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

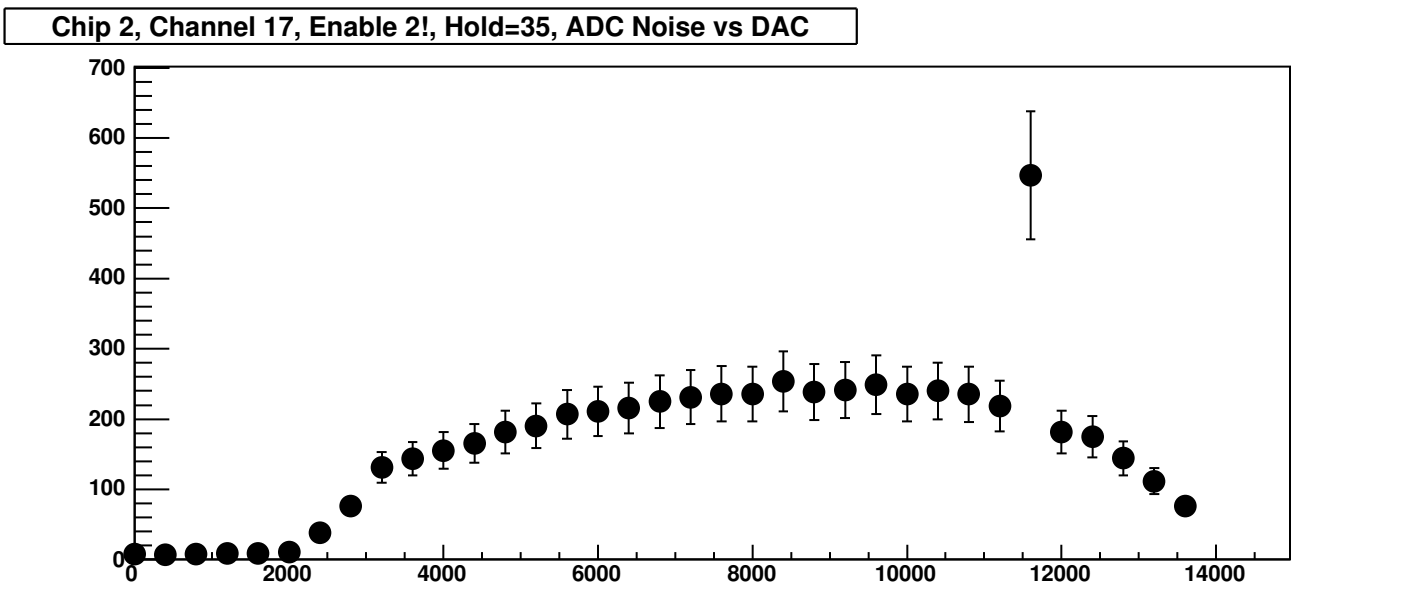

Chip 2, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

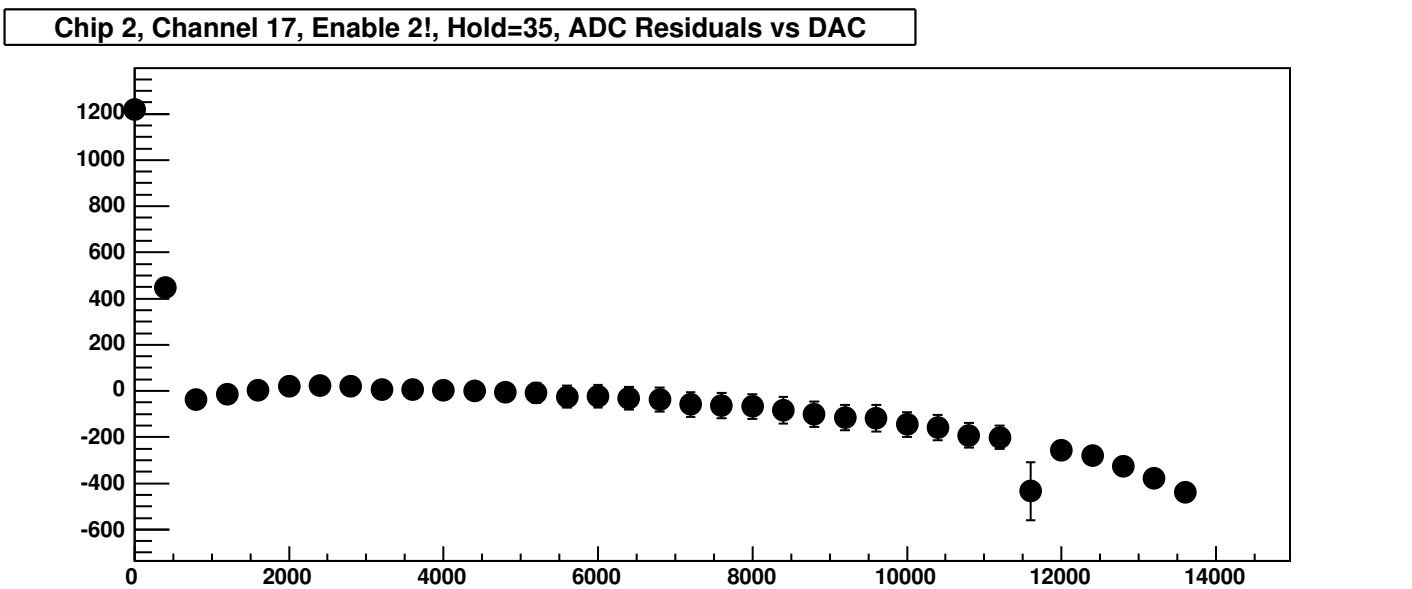

Chip 2, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 26 / 23
p0 -45.37 ± 0.7835
p1 0.01427 ± 0.0001188
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

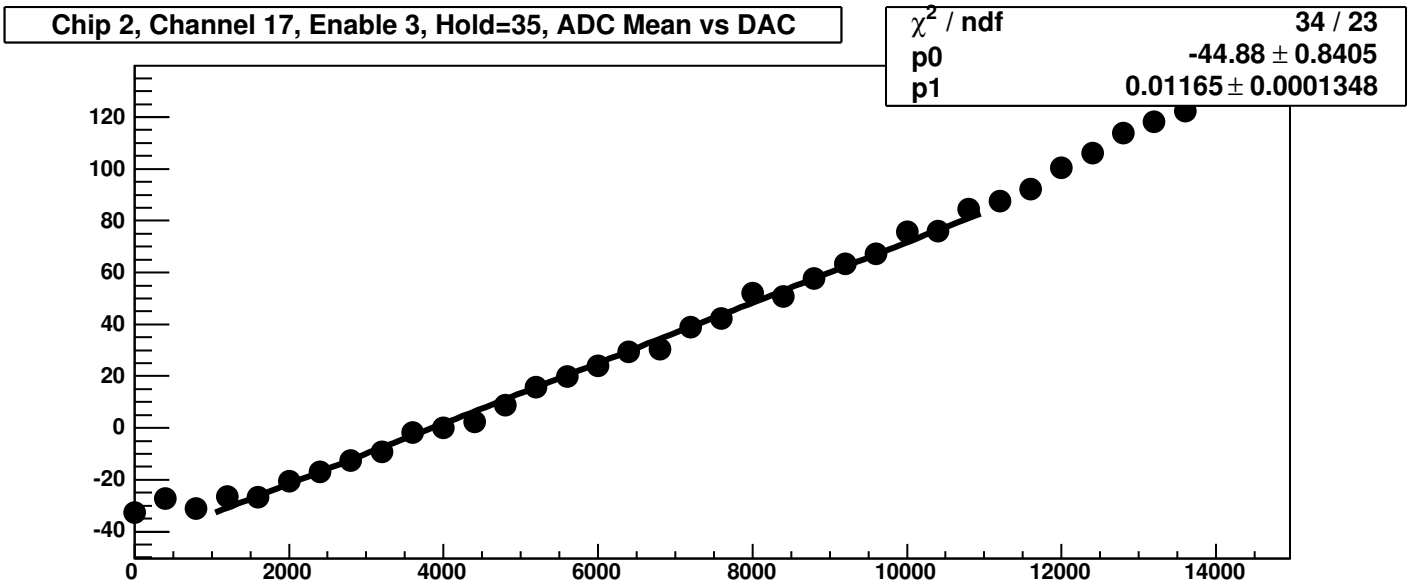

Chip 2, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

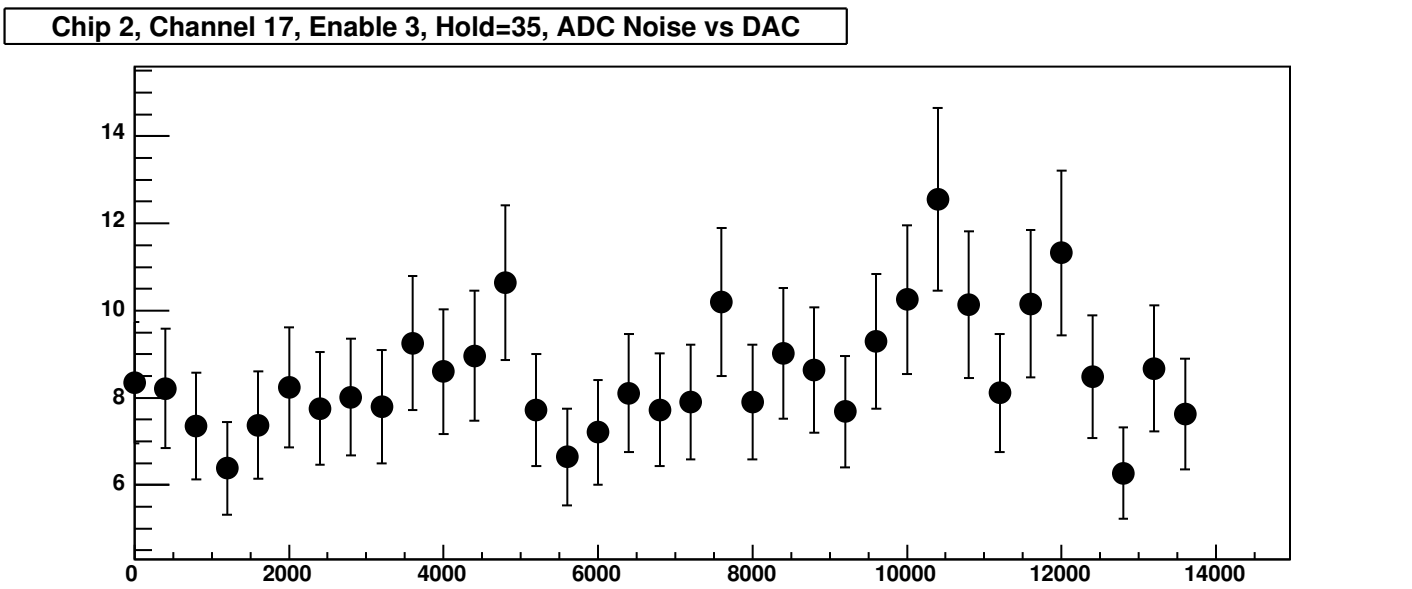

Chip 2, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

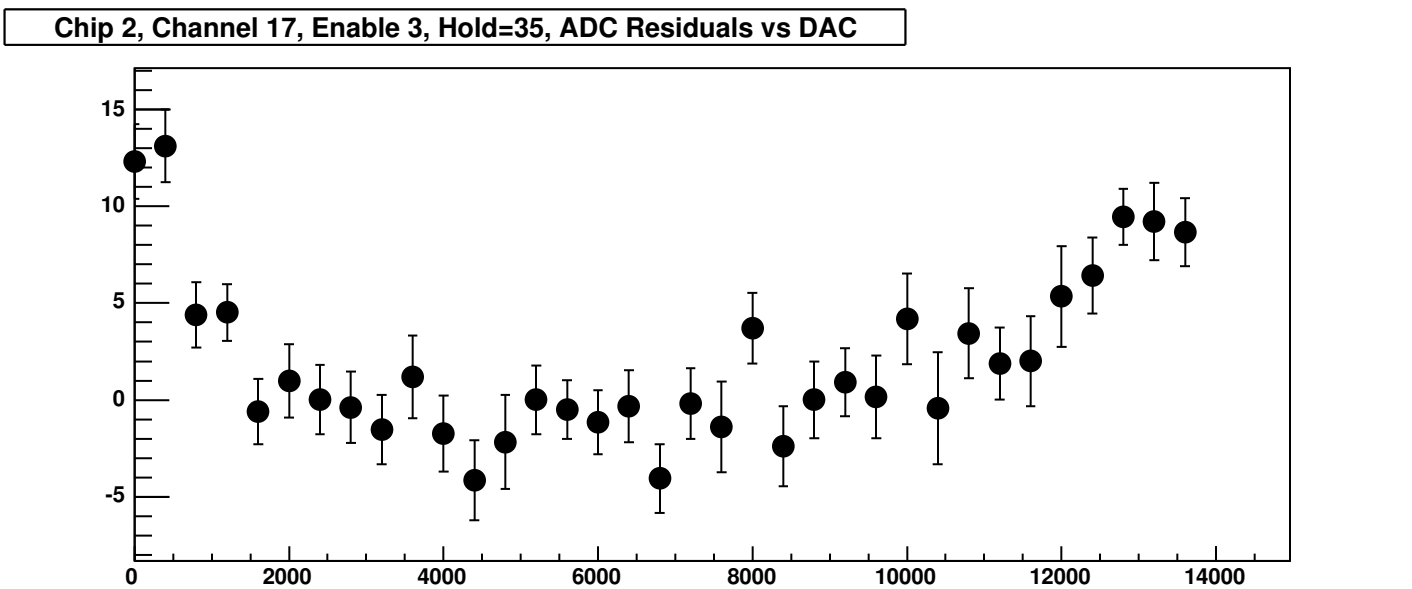

Chip 2, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 176.8 / 23
p0 28.68 ± 3.129
p1 1.965 ± 0.001752
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

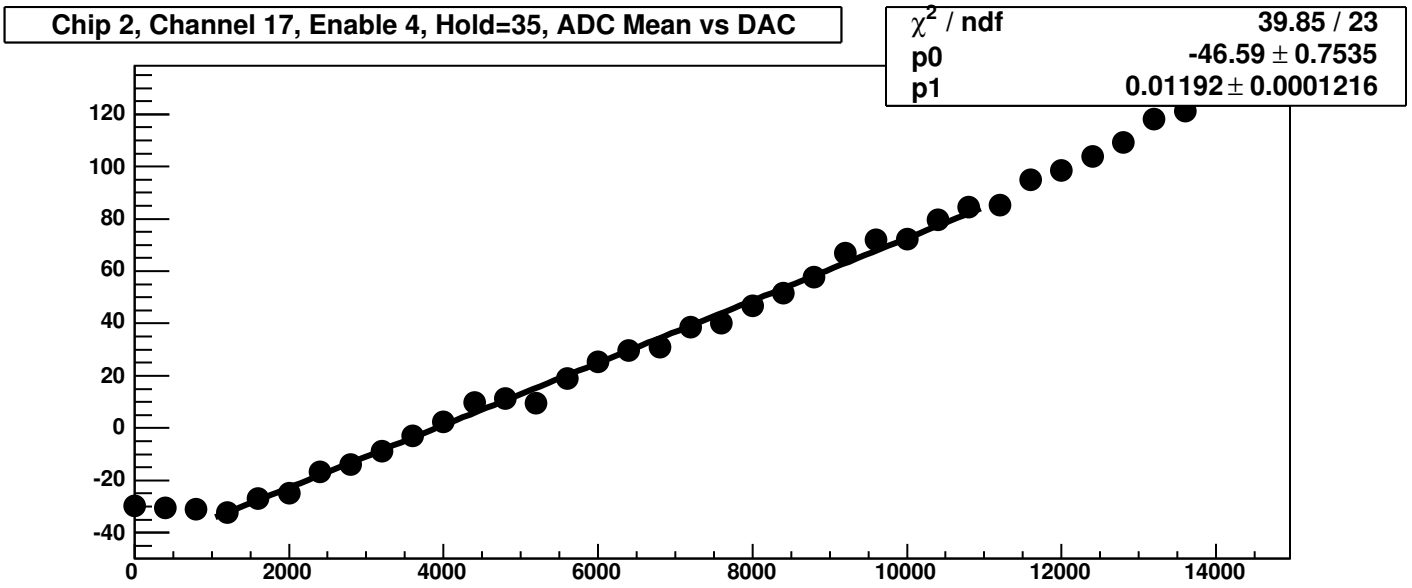

Chip 2, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

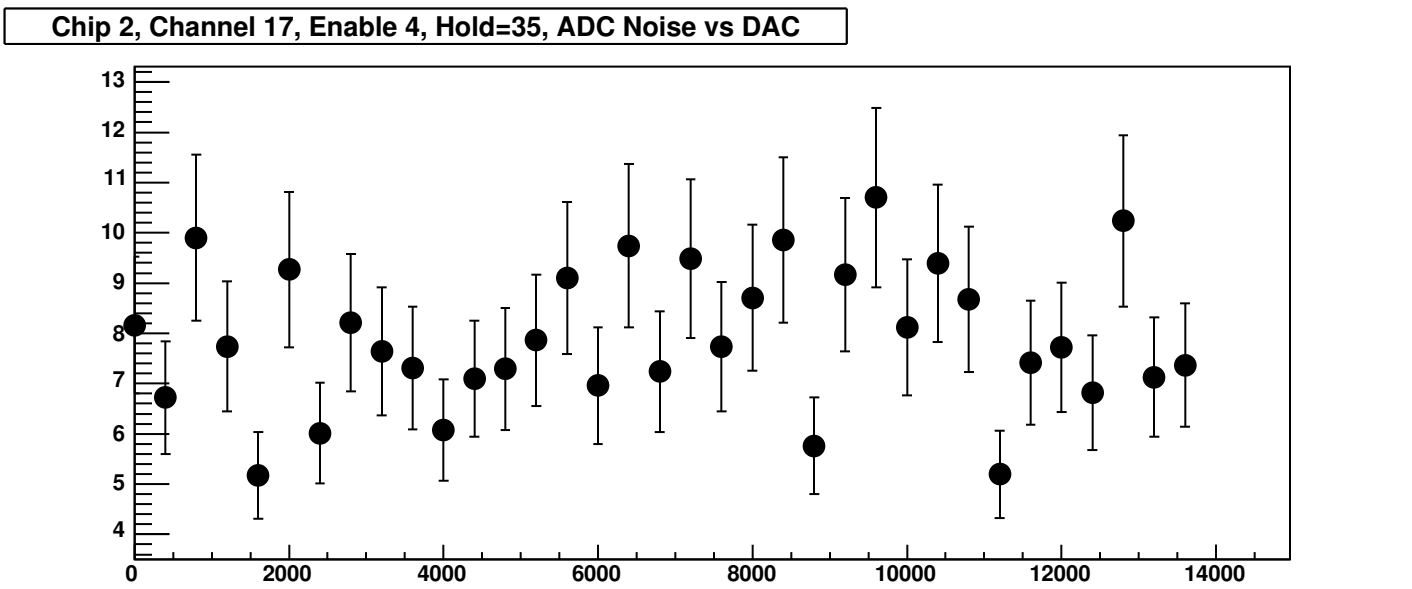

Chip 2, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

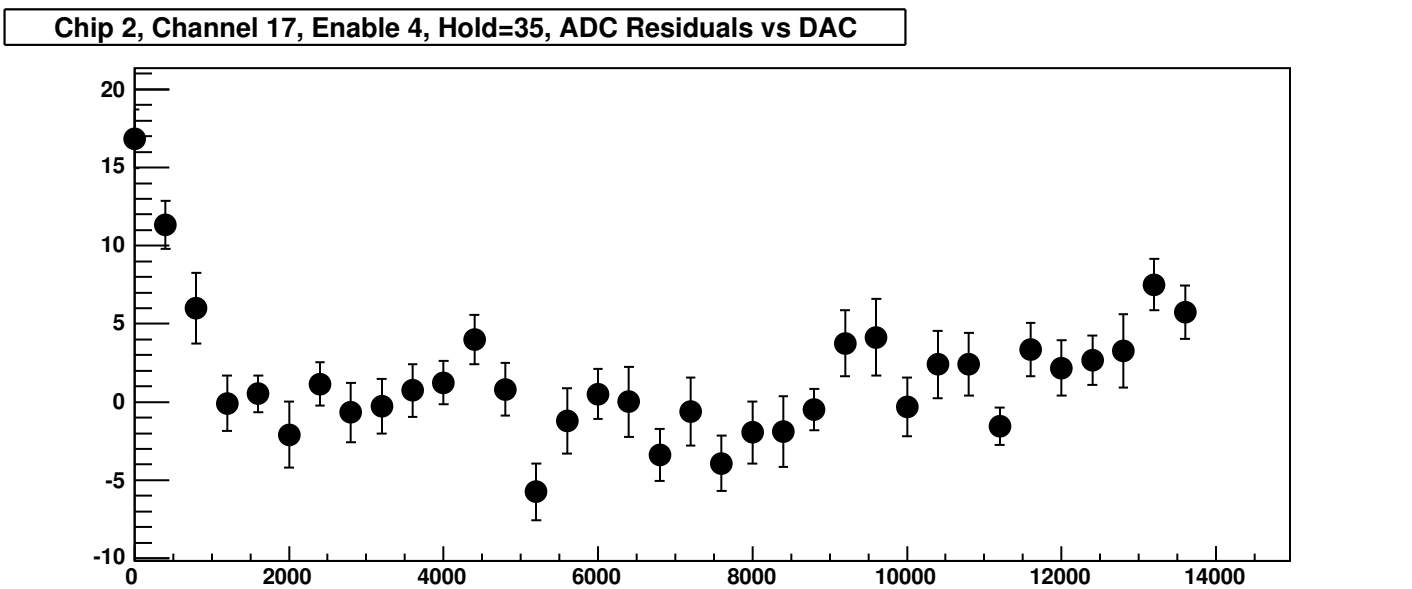

Chip 2, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 34 / 23
p0 -44.88 ± 0.8405
p1 0.01165 ± 0.0001348
120
100
80
60
40
20
0
-20
-40
0
2000
4000
6000
8000
10000
12000
14000

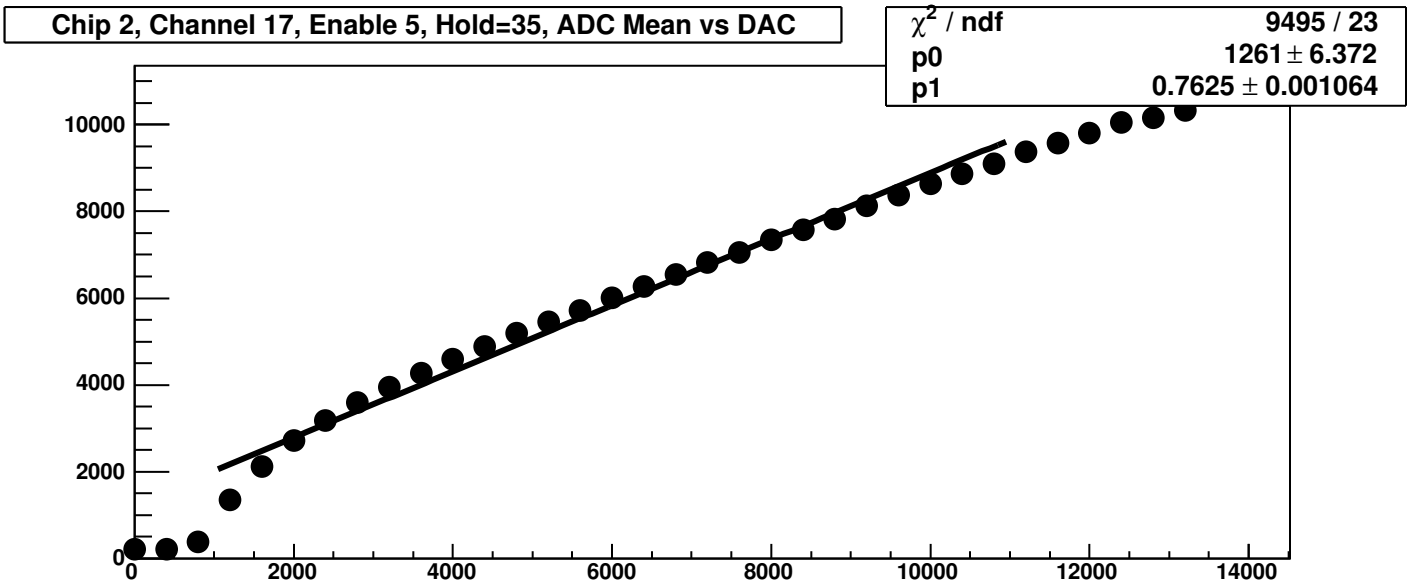

Chip 2, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

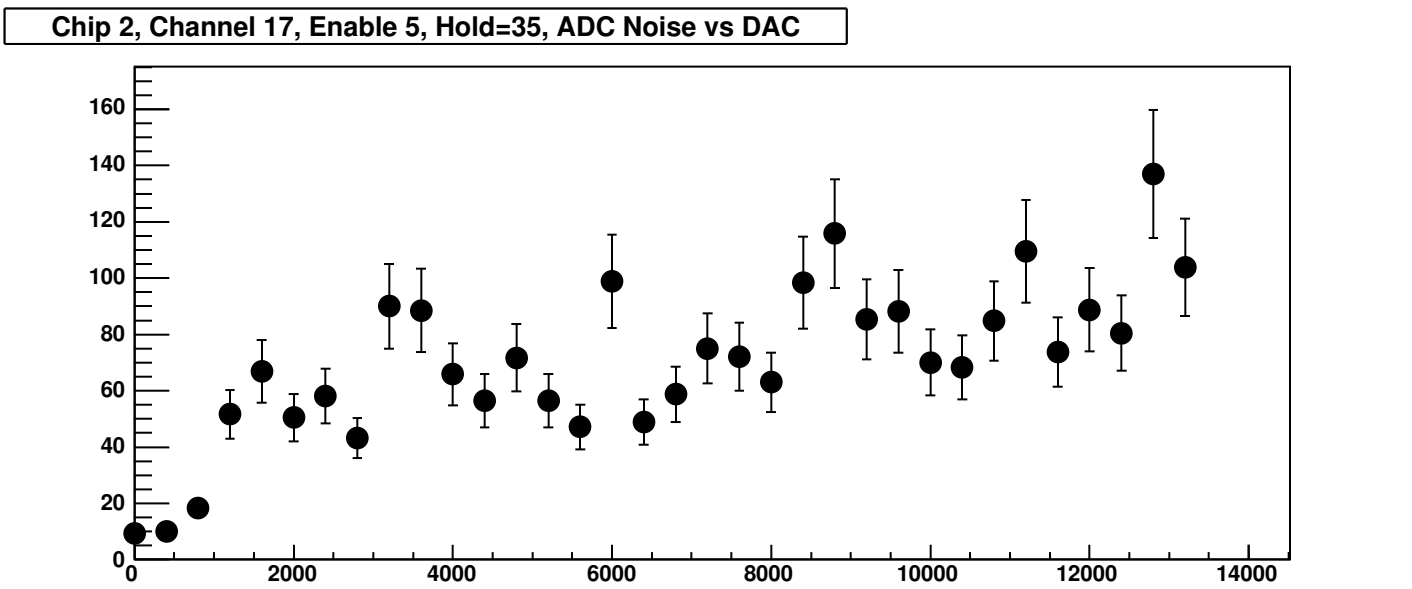

Chip 2, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

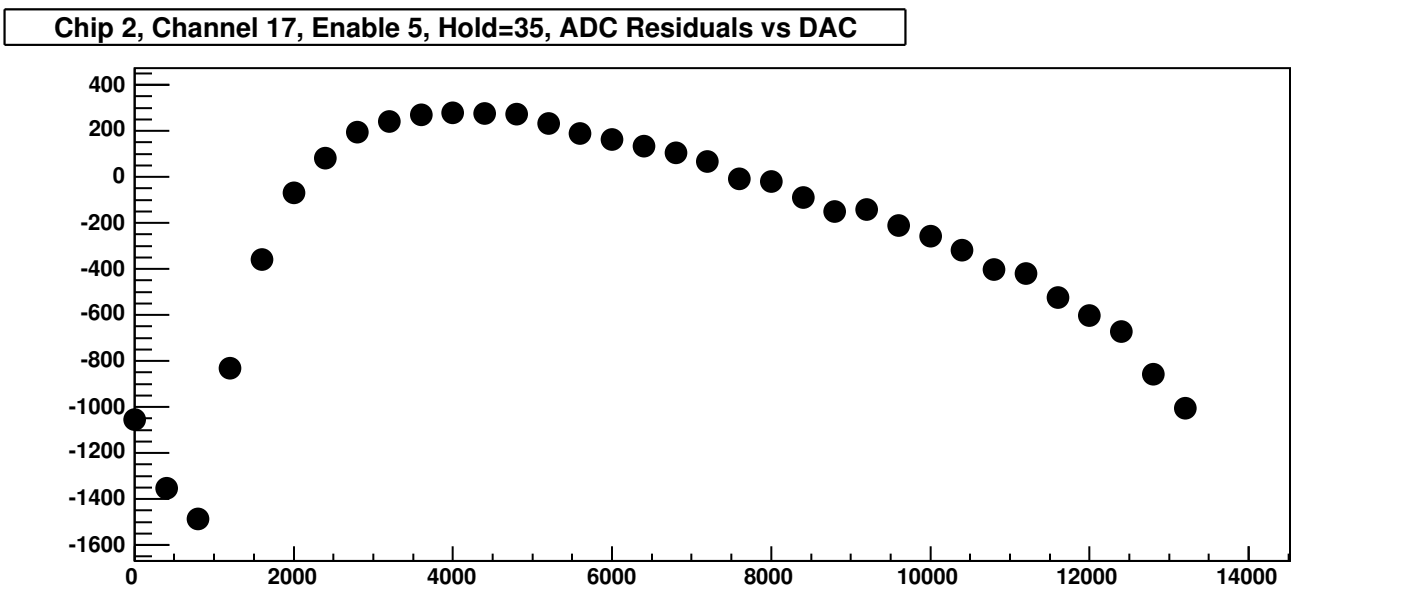

Chip 2, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 39.85 / 23
p0 -46.59 ± 0.7535
p1 0.01192 ± 0.0001216
120
100
80
60
40
20
0
-20
-40
0
2000
4000
6000
8000
10000
12000
14000

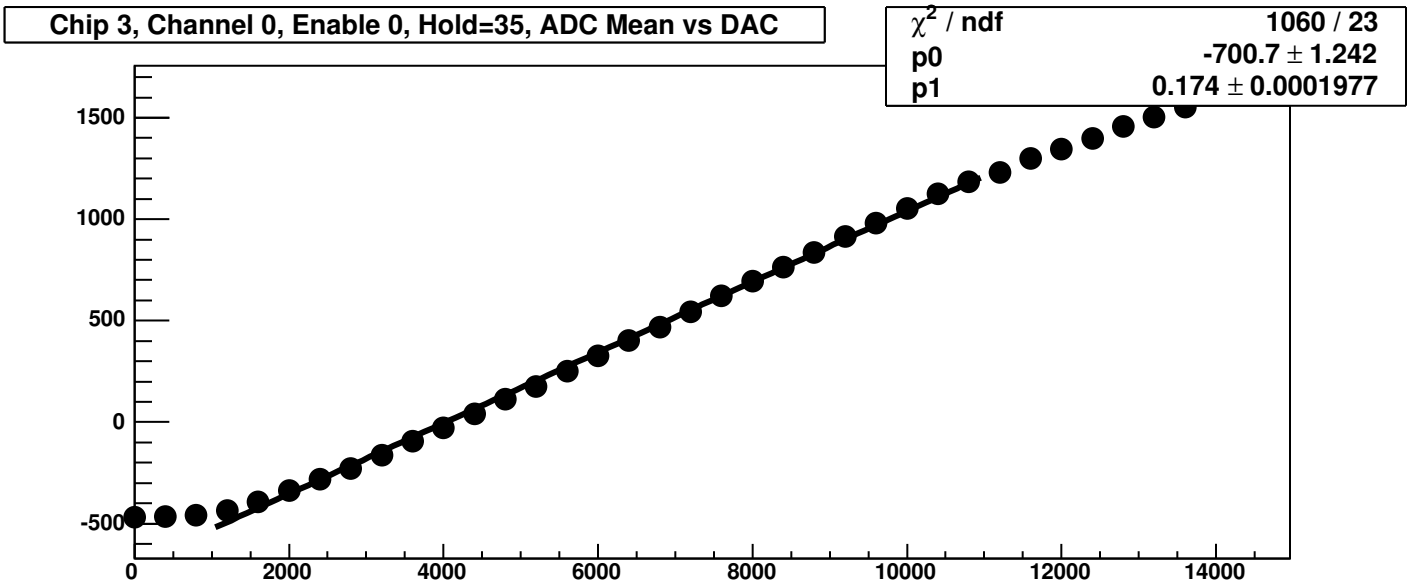

Chip 2, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

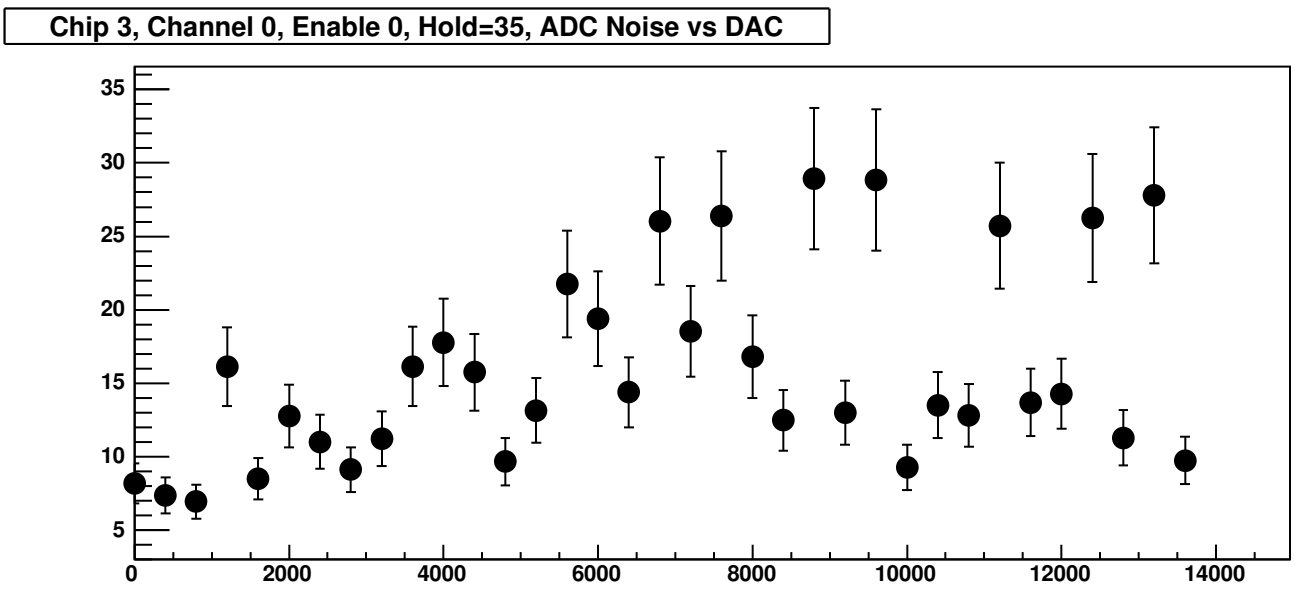

Chip 2, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

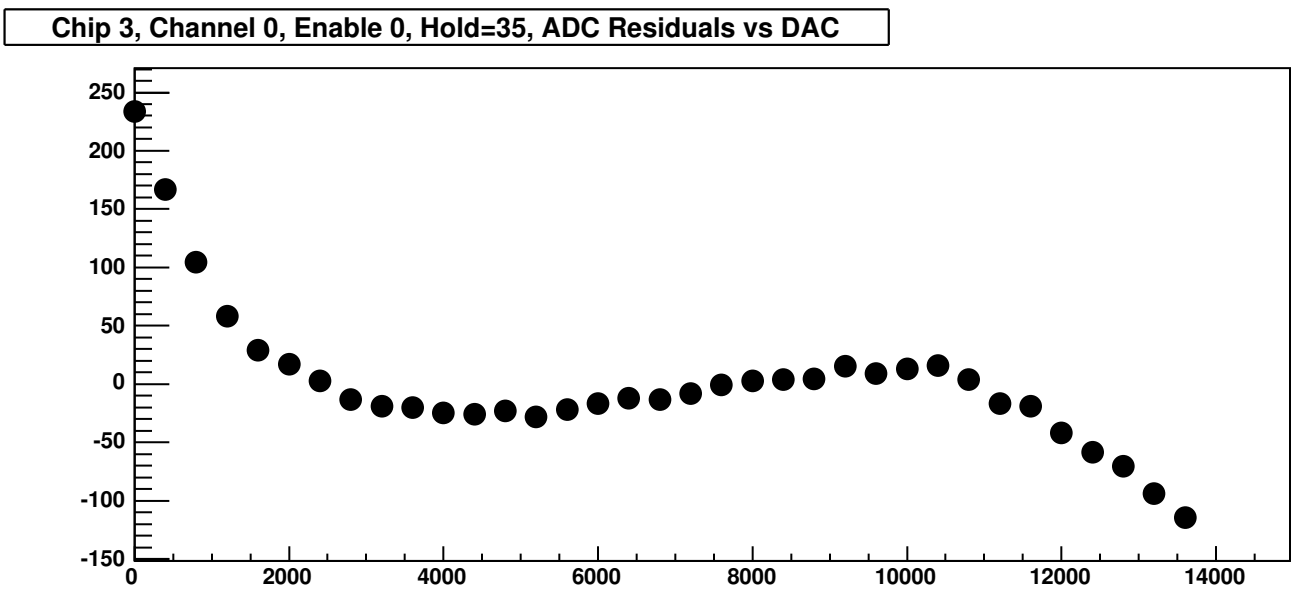

Chip 2, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 9495 / 23
p0 1261 ± 6.372
p1 0.7625 ± 0.001064
0
2000
4000
6000
8000
10000
12000
14000

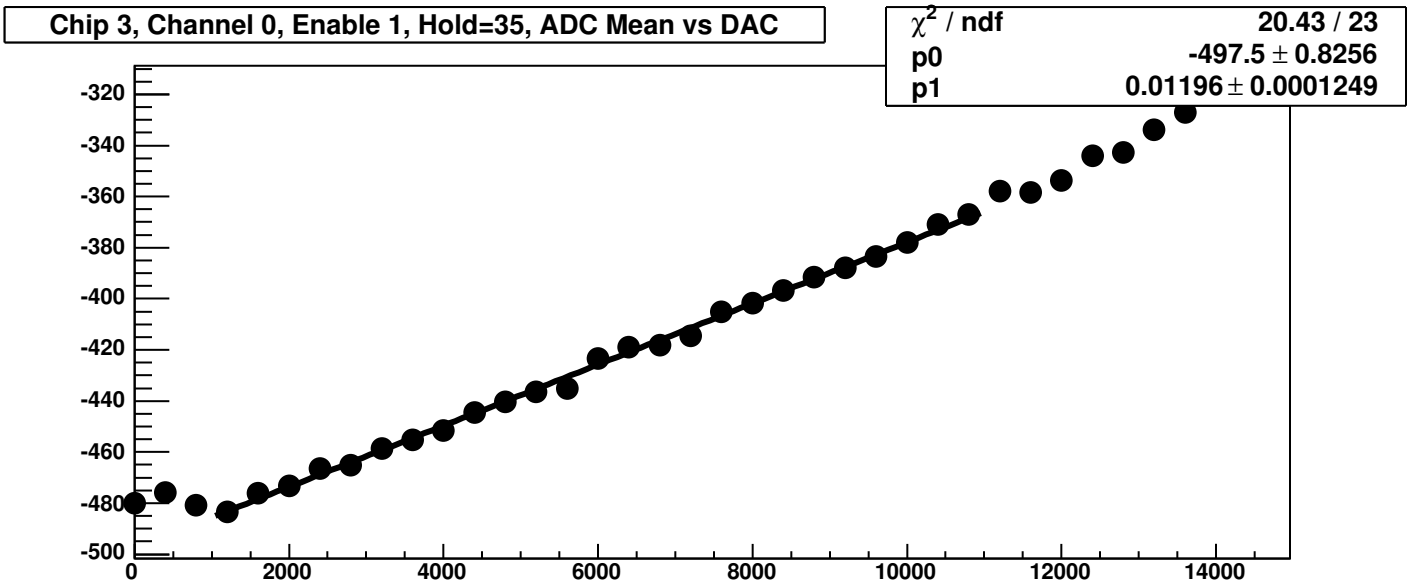

Chip 2, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
0
20
40
60
80
100
120
140
160
2000
4000
6000
8000
10000
12000
14000

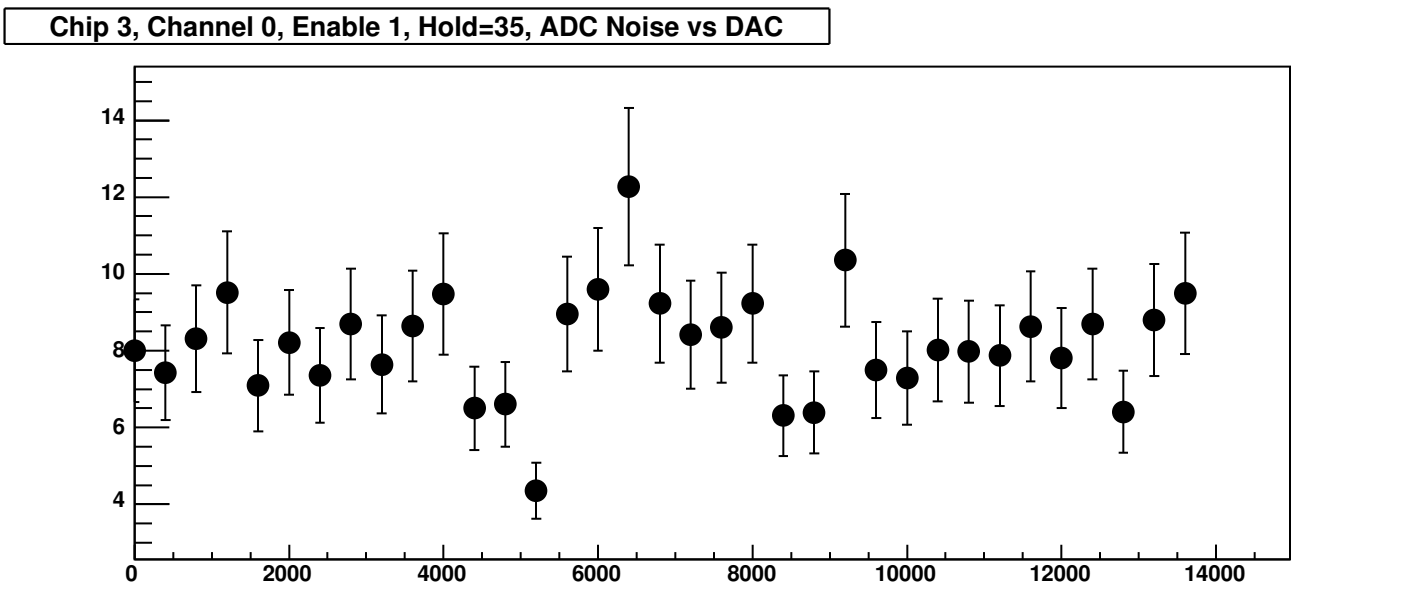

Chip 2, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
2000
4000
6000
8000
10000
12000
14000

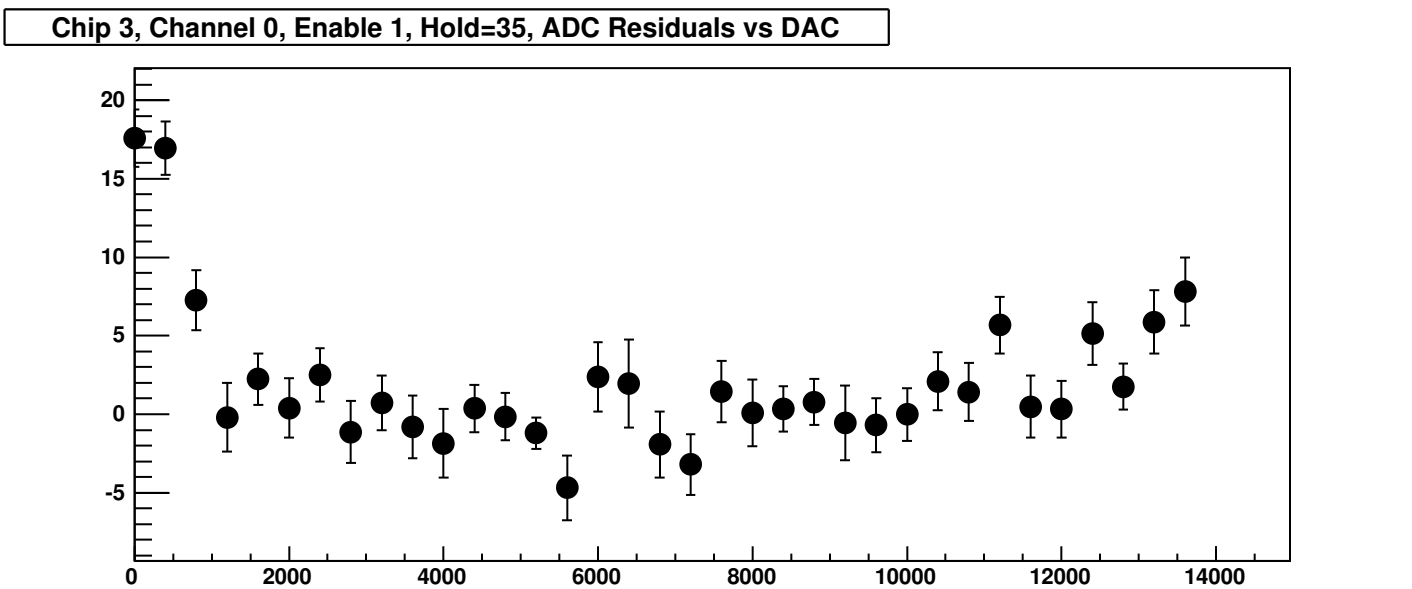

Chip 3, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1060 / 23
p0 -700.7 ± 1.242
p1 0.174 ± 0.0001977
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

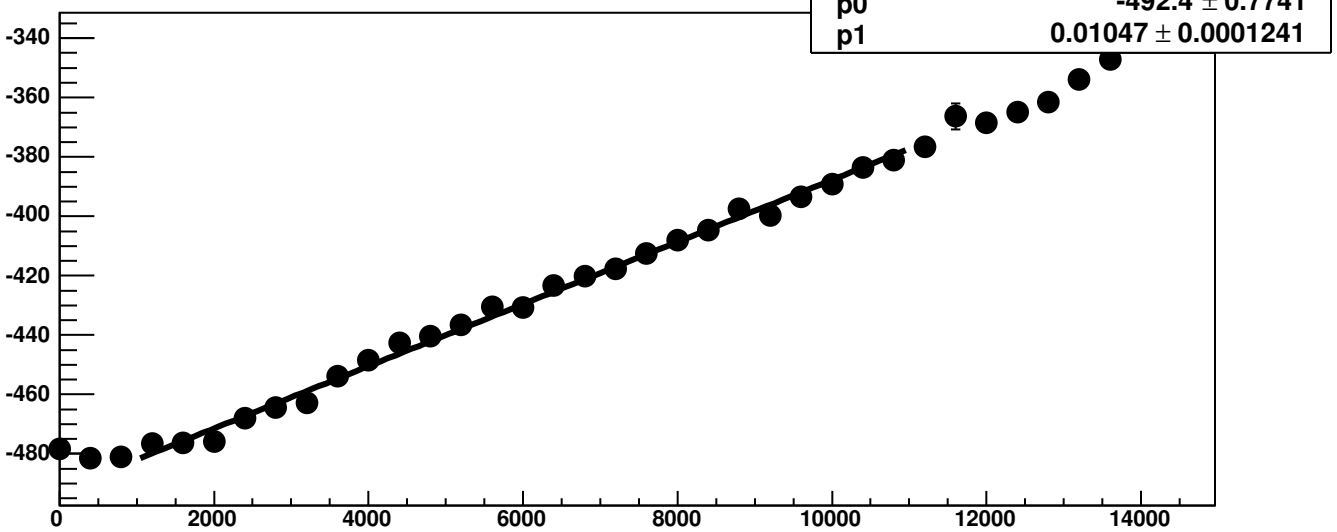

Chip 3, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 20.43 / 23
p0 -497.5 ± 0.8256
p1 0.01196 ± 0.0001249
-320
-340
-360
-380
-400
-420
-440
-460
-480
-500
0
2000
4000
6000
8000
10000
12000
14000

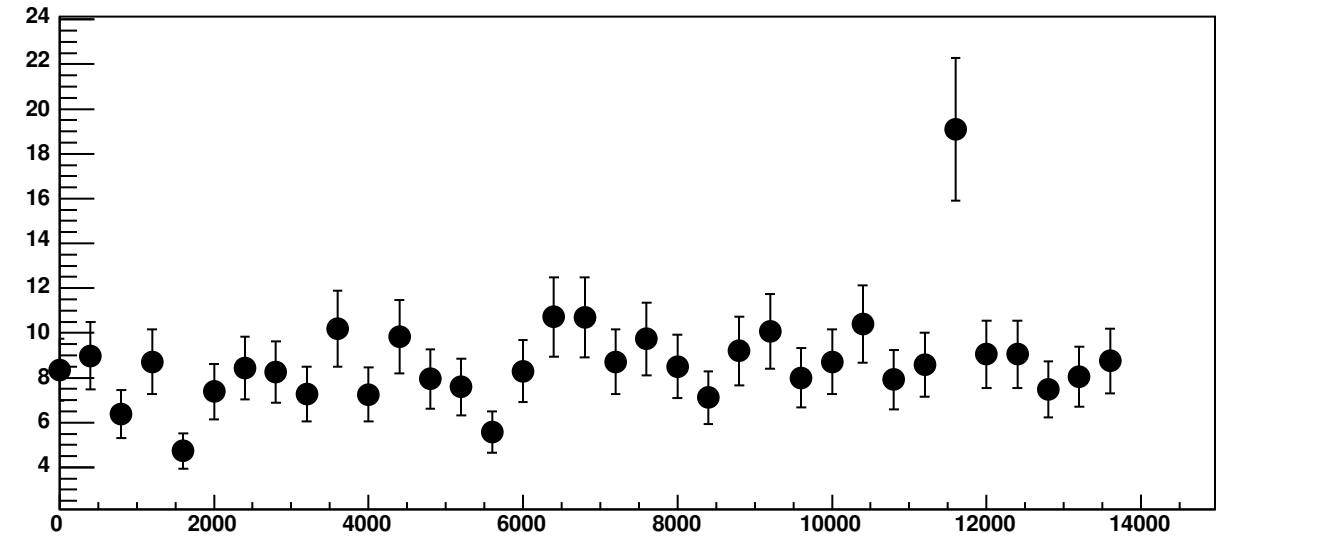

Chip 3, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

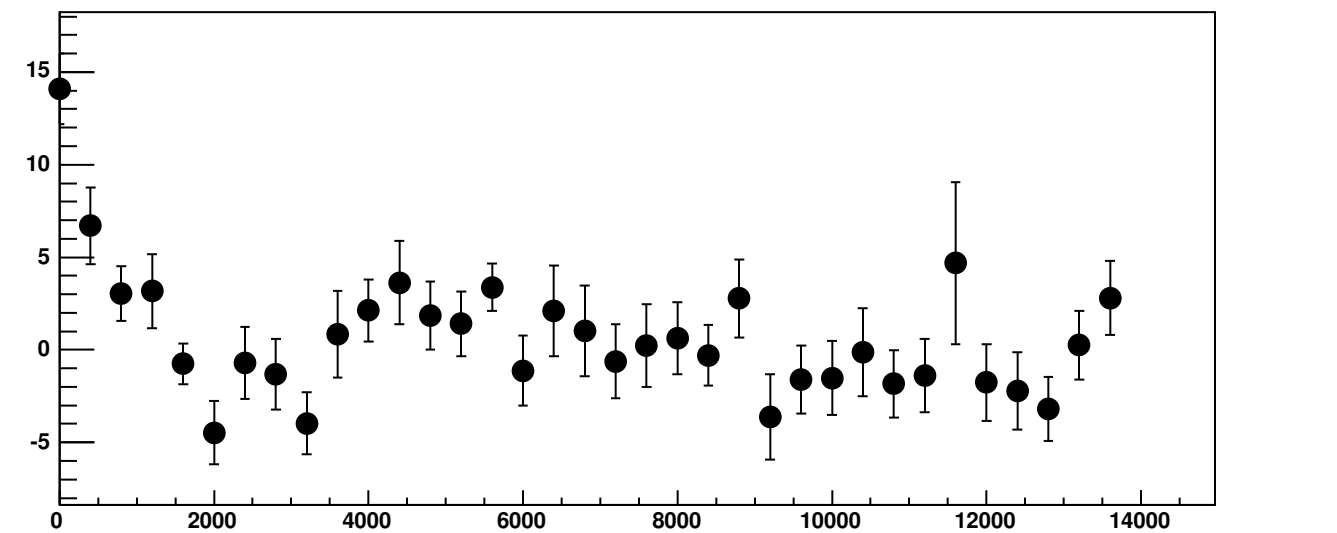
Chip 3, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

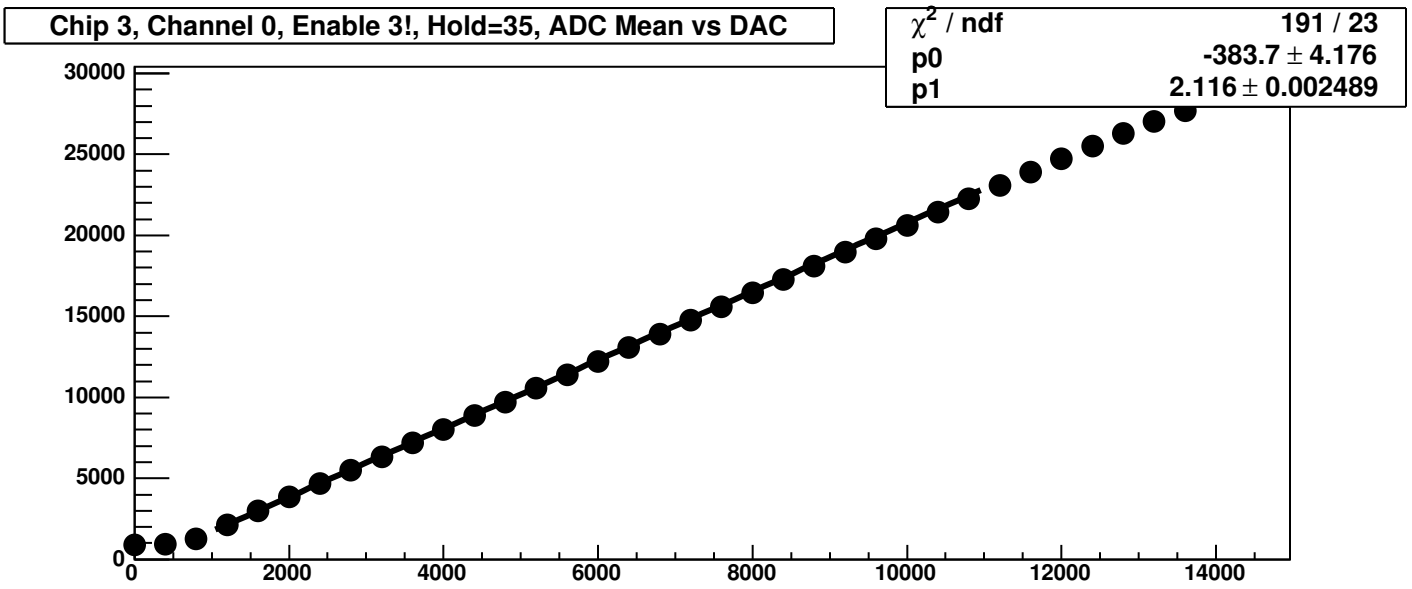


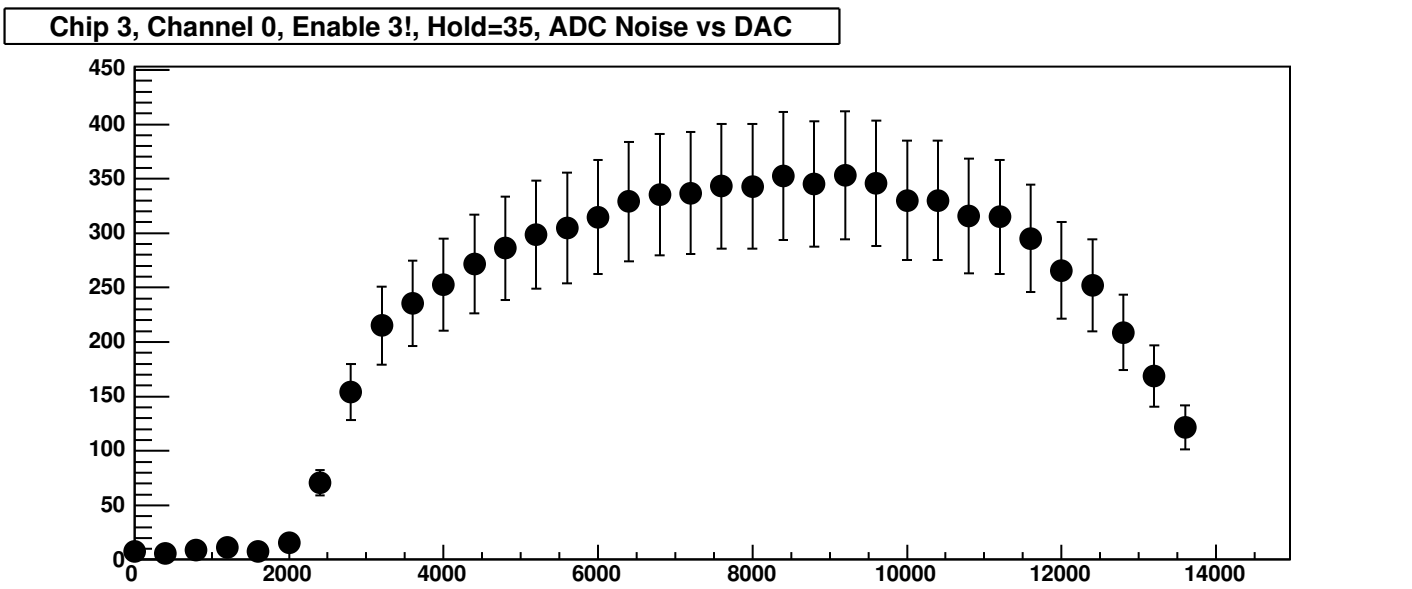
Chip 3, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC

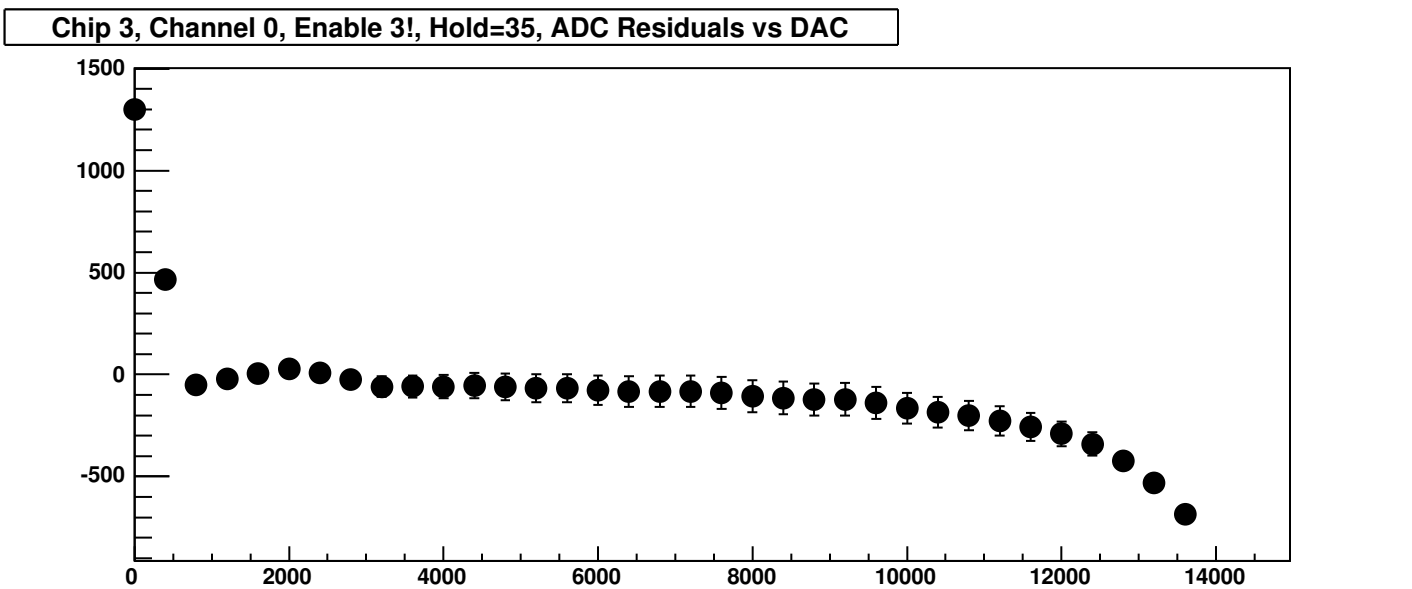


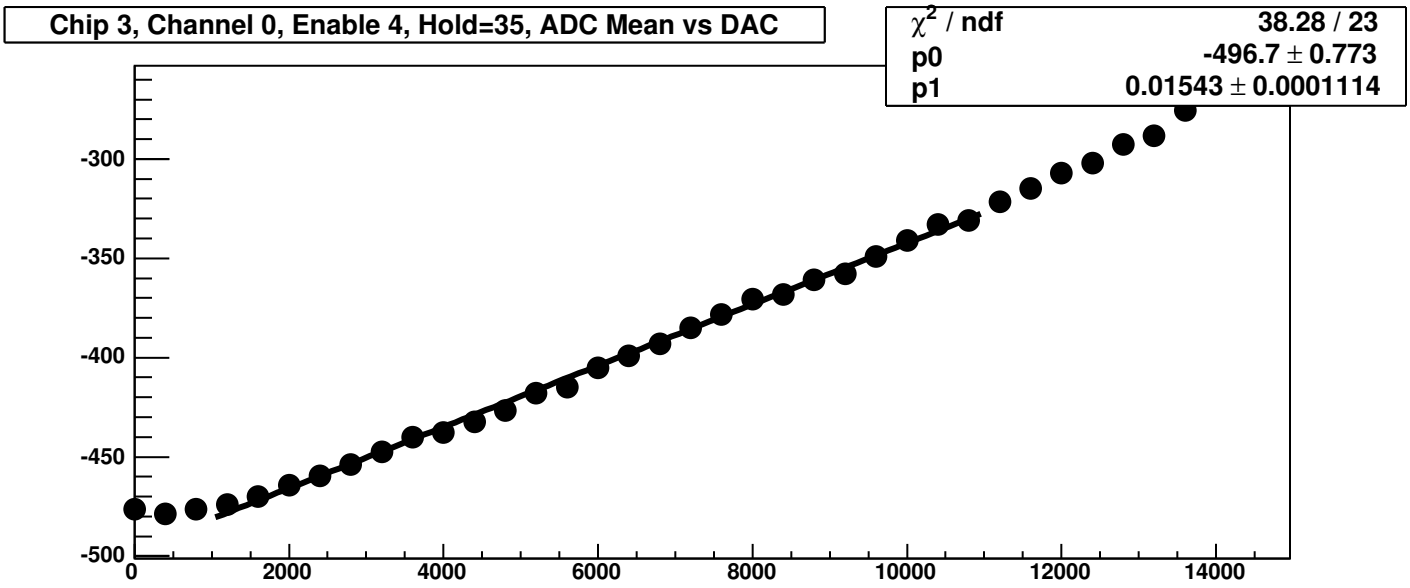
Chip 3, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

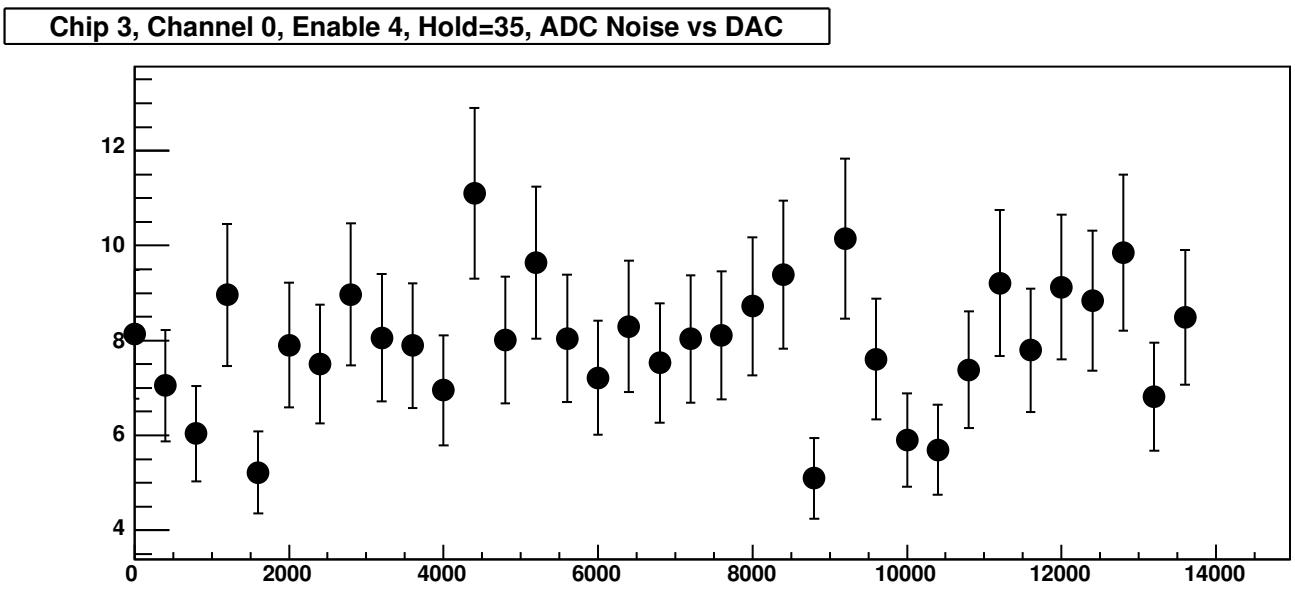


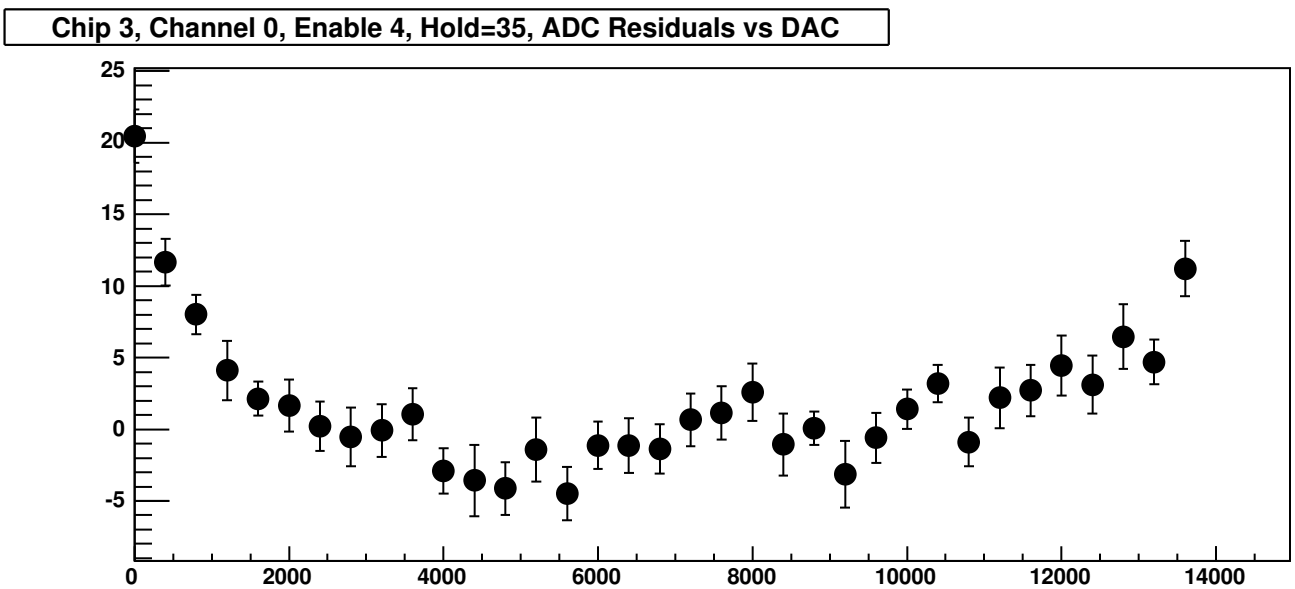

Chip 3, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 191 / 23
p0 -383.7 ± 4.176
p1 2.116 ± 0.002489
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

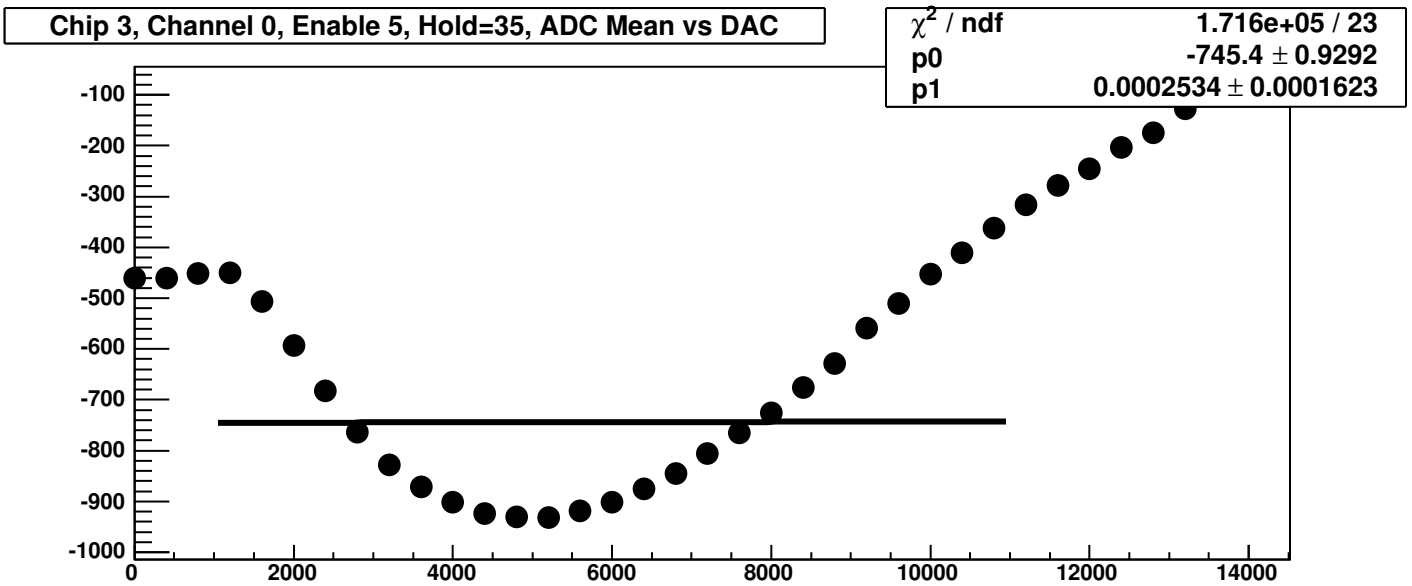

Chip 3, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

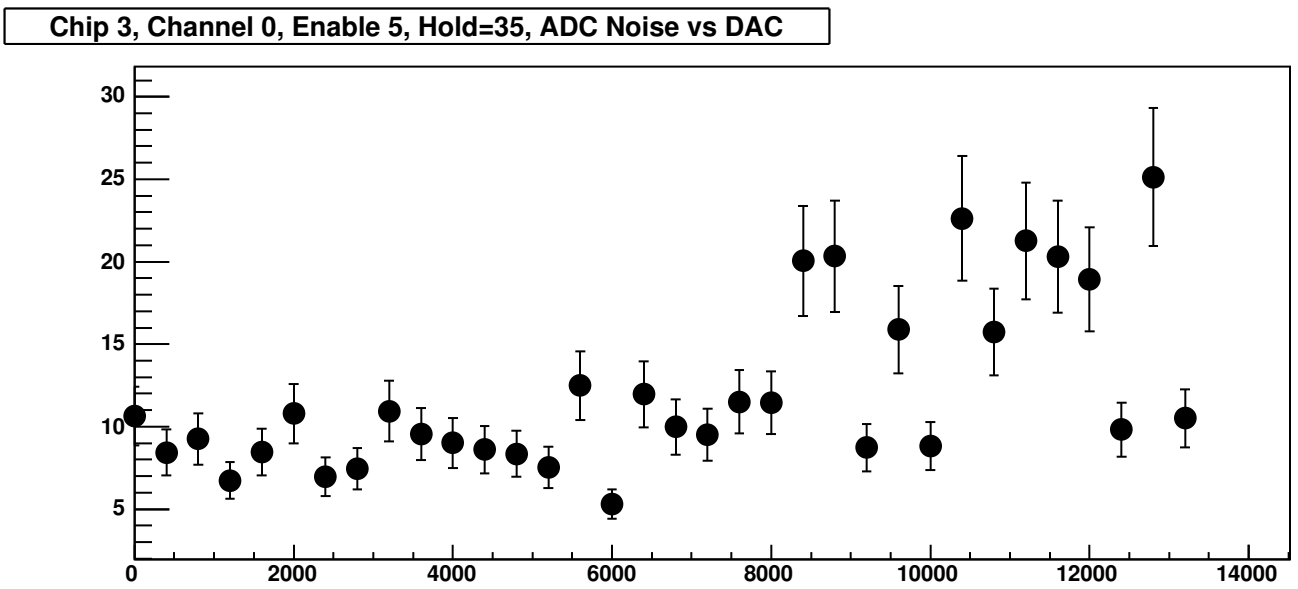

Chip 3, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

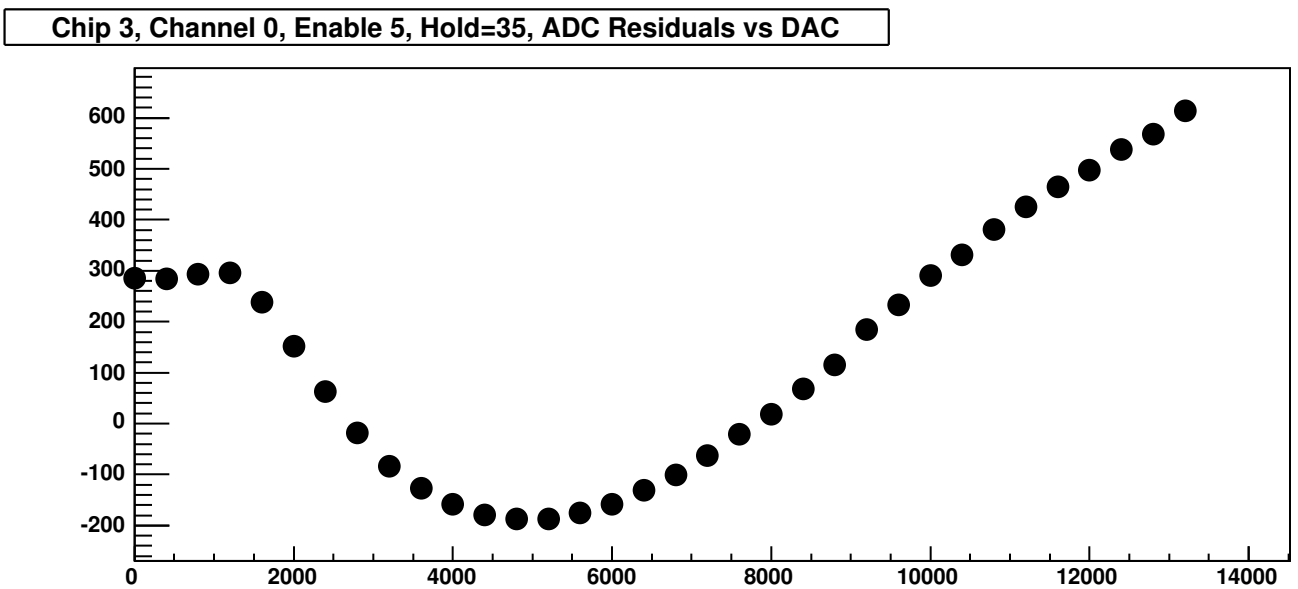

Chip 3, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 38.28 / 23
p0 -496.7 ± 0.773
p1 0.01543 ± 0.0001114
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

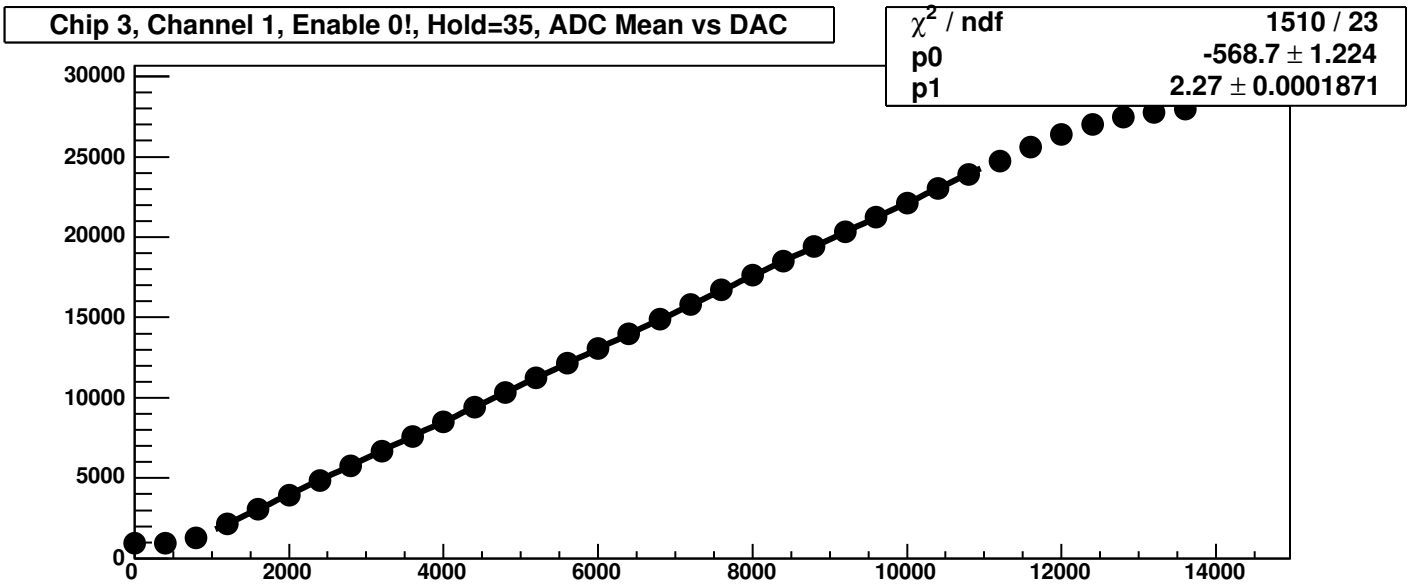

Chip 3, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

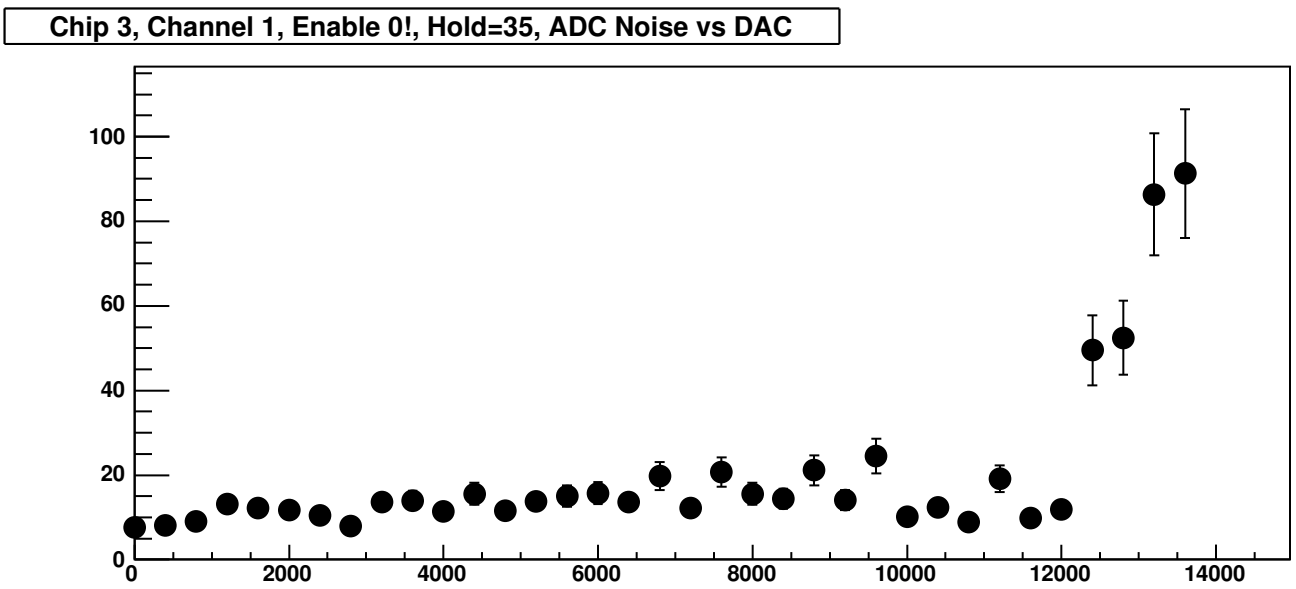

Chip 3, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

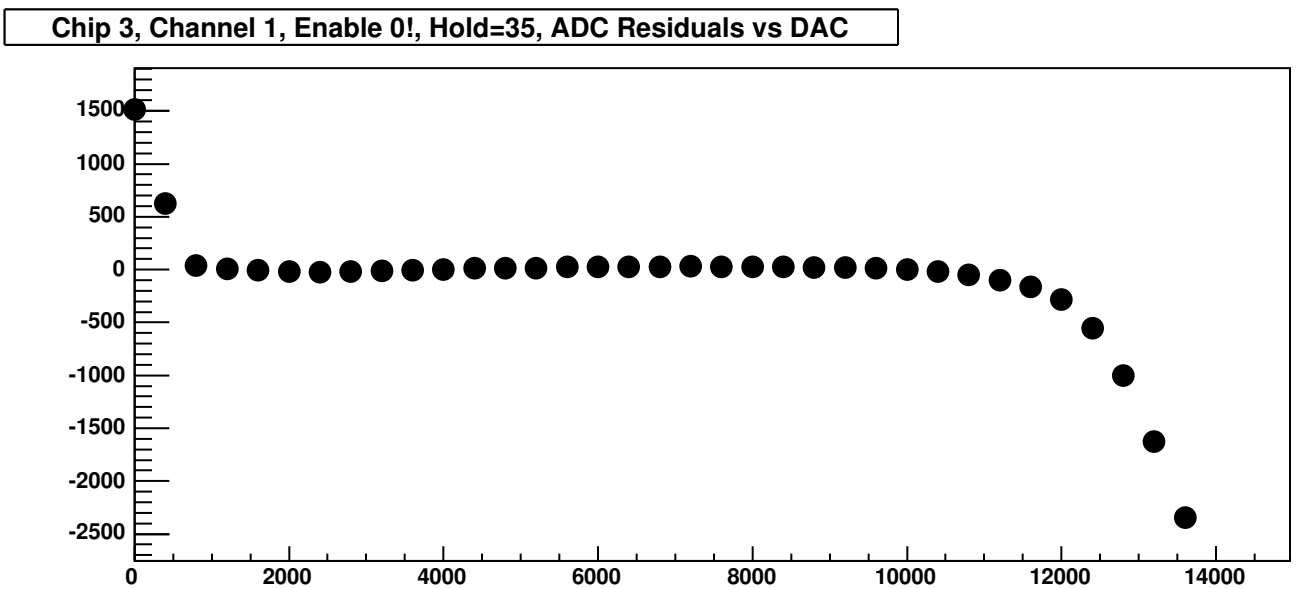

Chip 3, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.716e+05 / 23
p0 -745.4 ± 0.9292
p1 0.0002534 ± 0.0001623
-100
-200
-300
-400
-500
-600
-700
-800
-900
-1000
0
2000
4000
6000
8000
10000
12000
14000

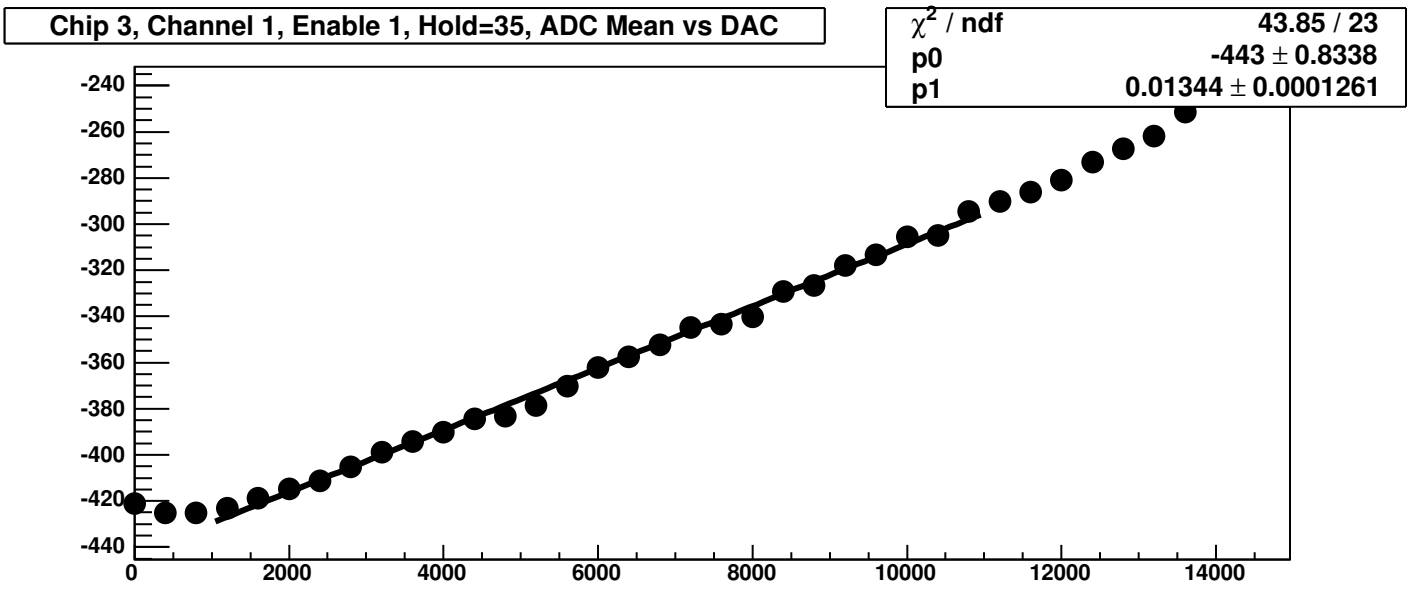

Chip 3, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

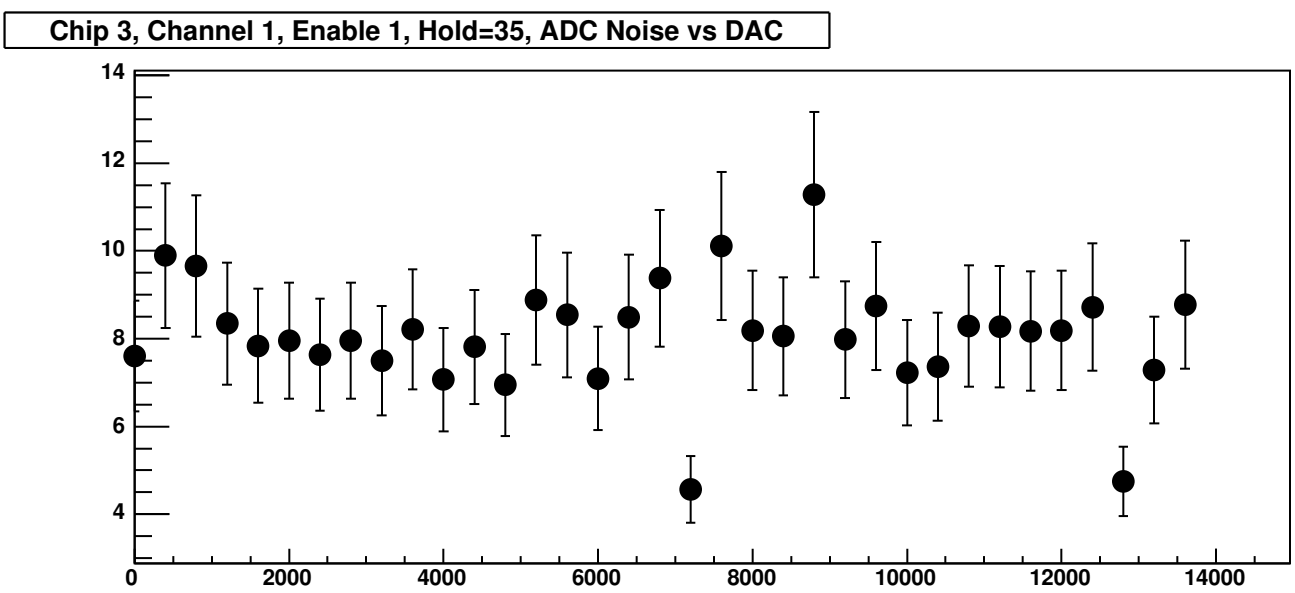

Chip 3, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

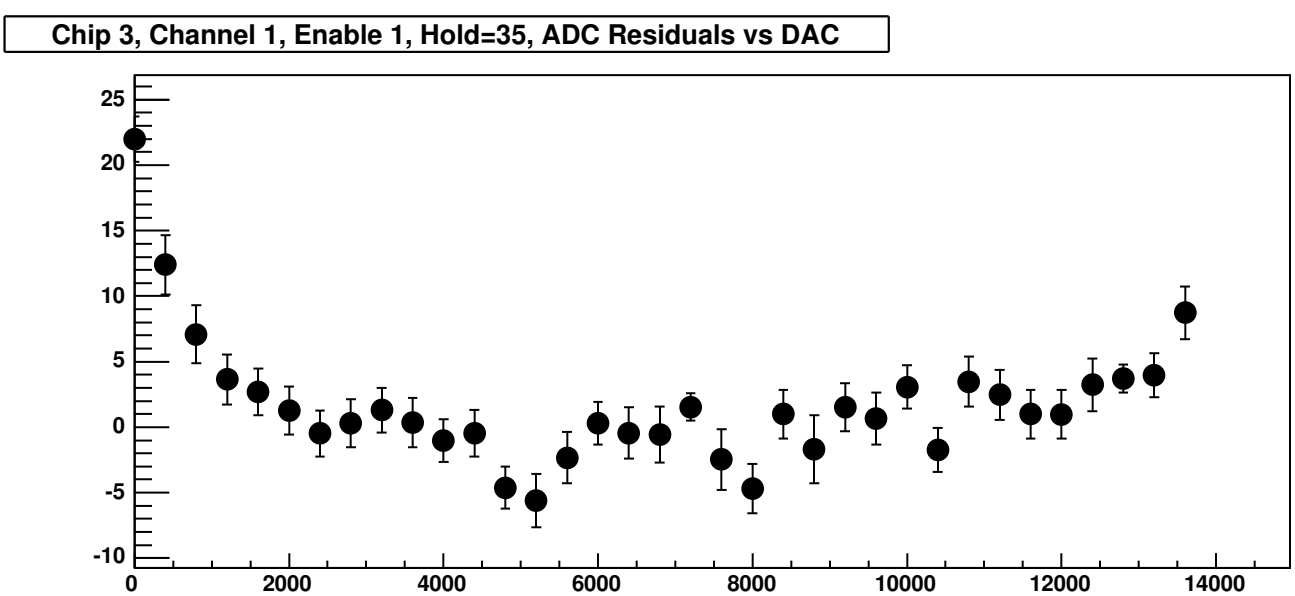

Chip 3, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1510 / 23
p0 -568.7 ± 1.224
p1 2.27 ± 0.0001871
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

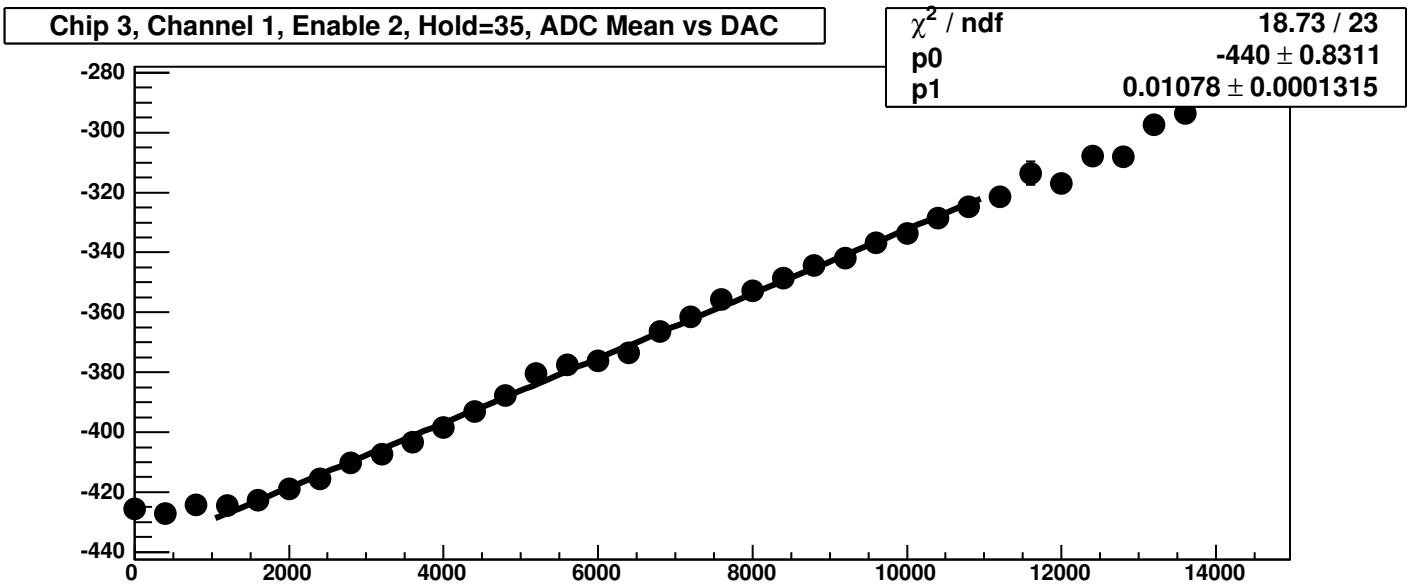

Chip 3, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

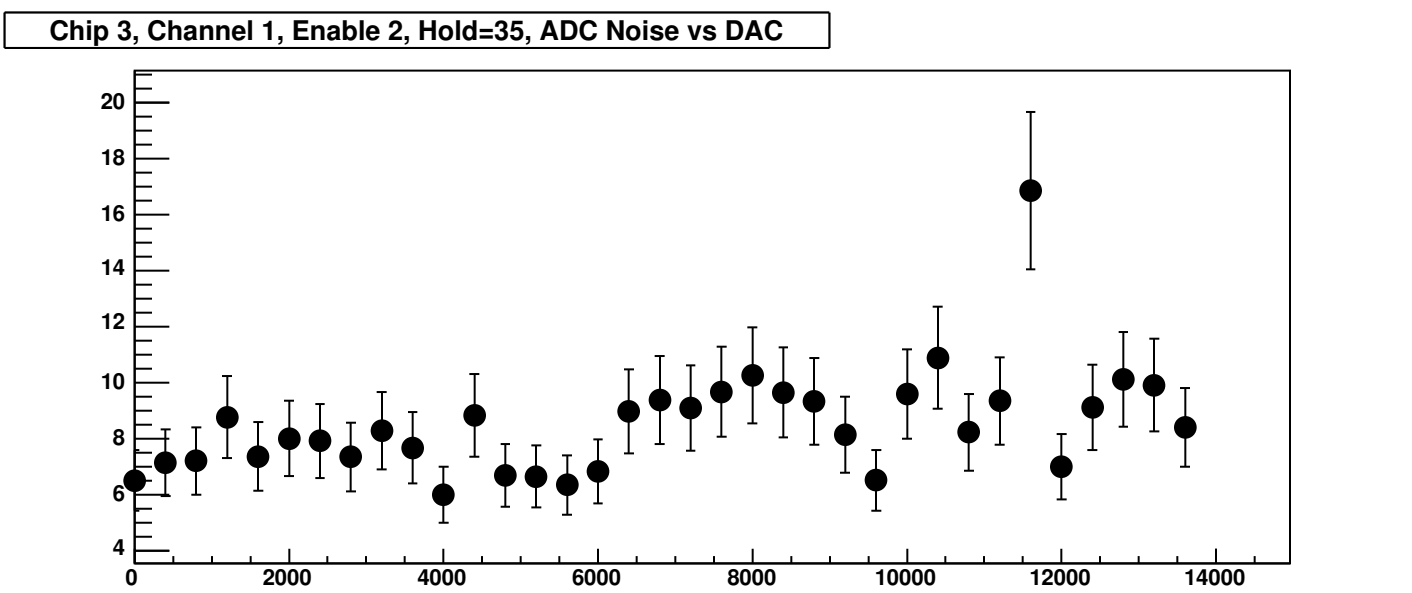

Chip 3, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

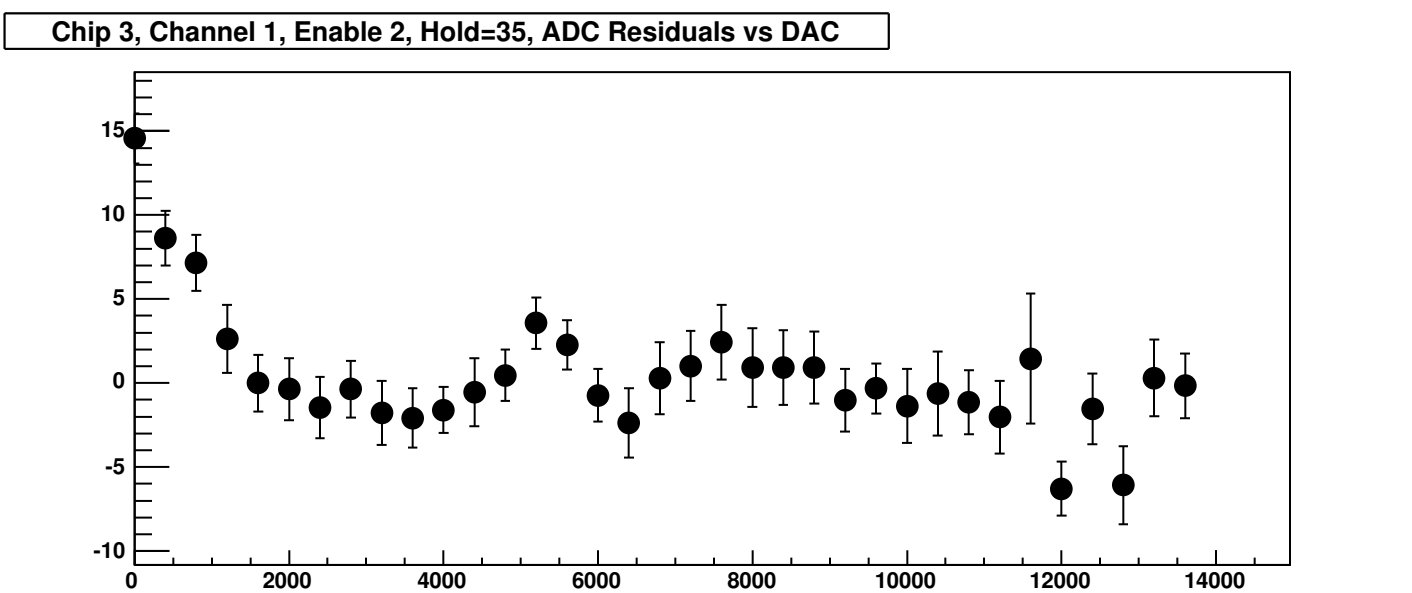

Chip 3, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 43.85 / 23
p0 -443 ± 0.8338
p1 0.01344 ± 0.0001261
-240
-260
-280
-300
-320
-340
-360
-380
-400
-420
-440
0
2000
4000
6000
8000
10000
12000
14000

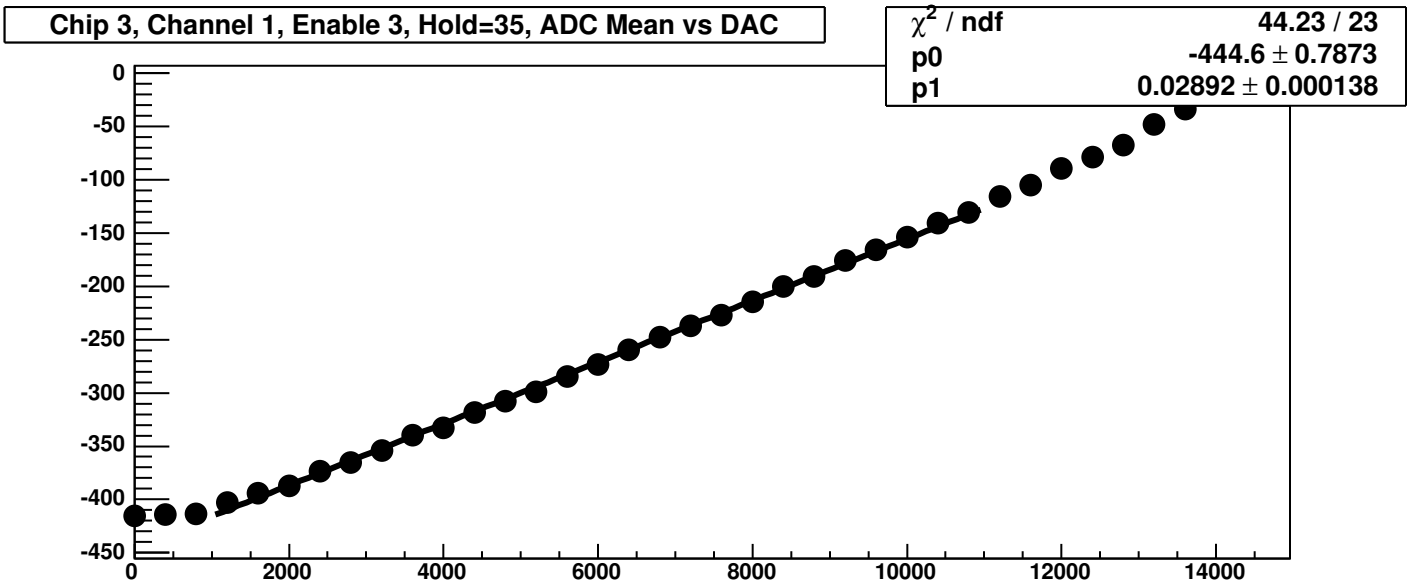

Chip 3, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

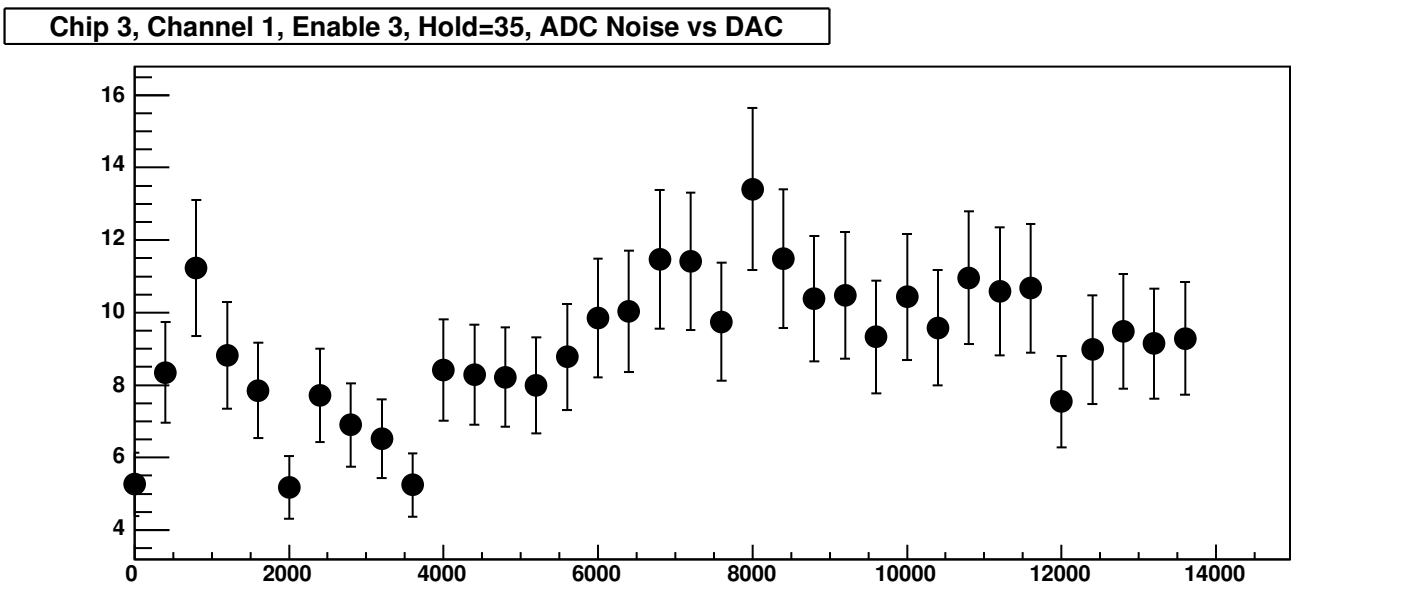

Chip 3, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

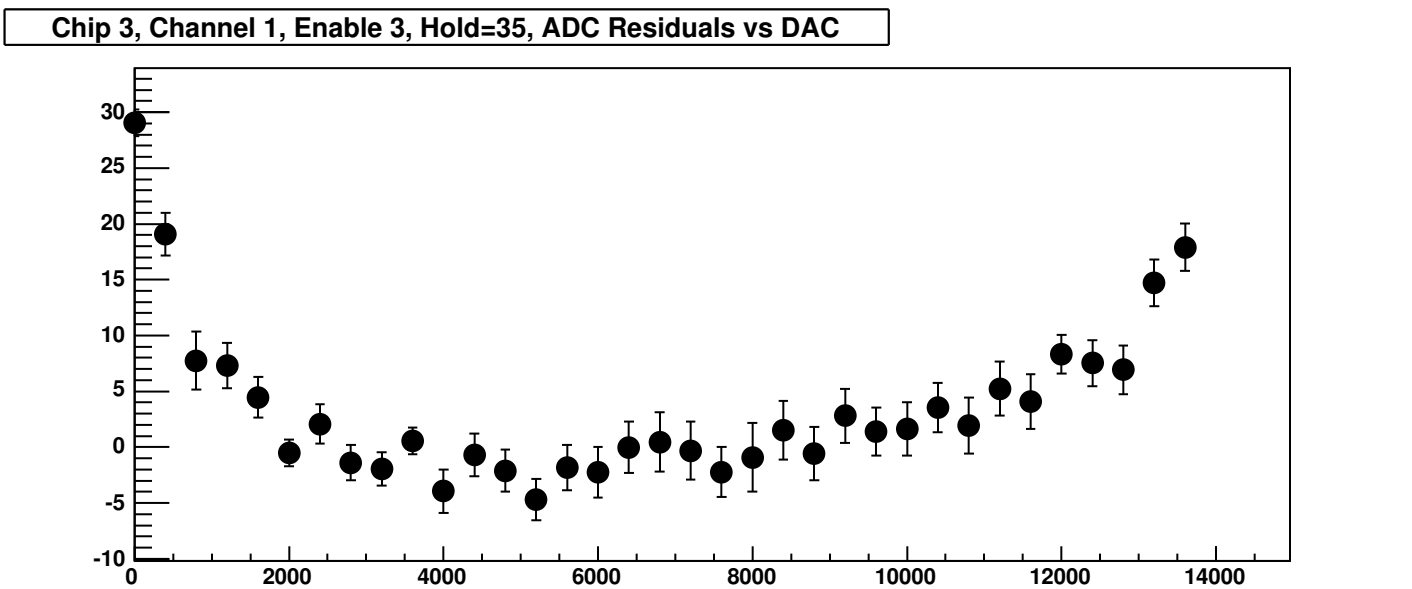

Chip 3, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 18.73 / 23
p0 -440 ± 0.8311
p1 0.01078 ± 0.0001315
-280
-300
-320
-340
-360
-380
-400
-420
-440
0
2000
4000
6000
8000
10000
12000
14000

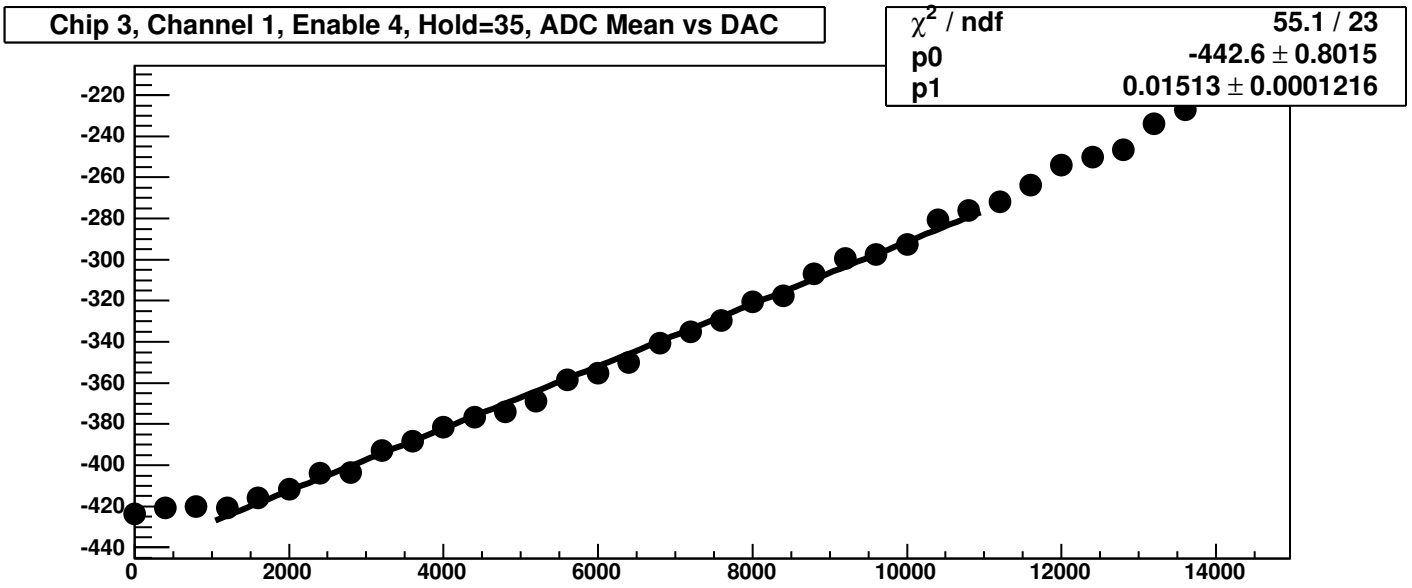

Chip 3, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

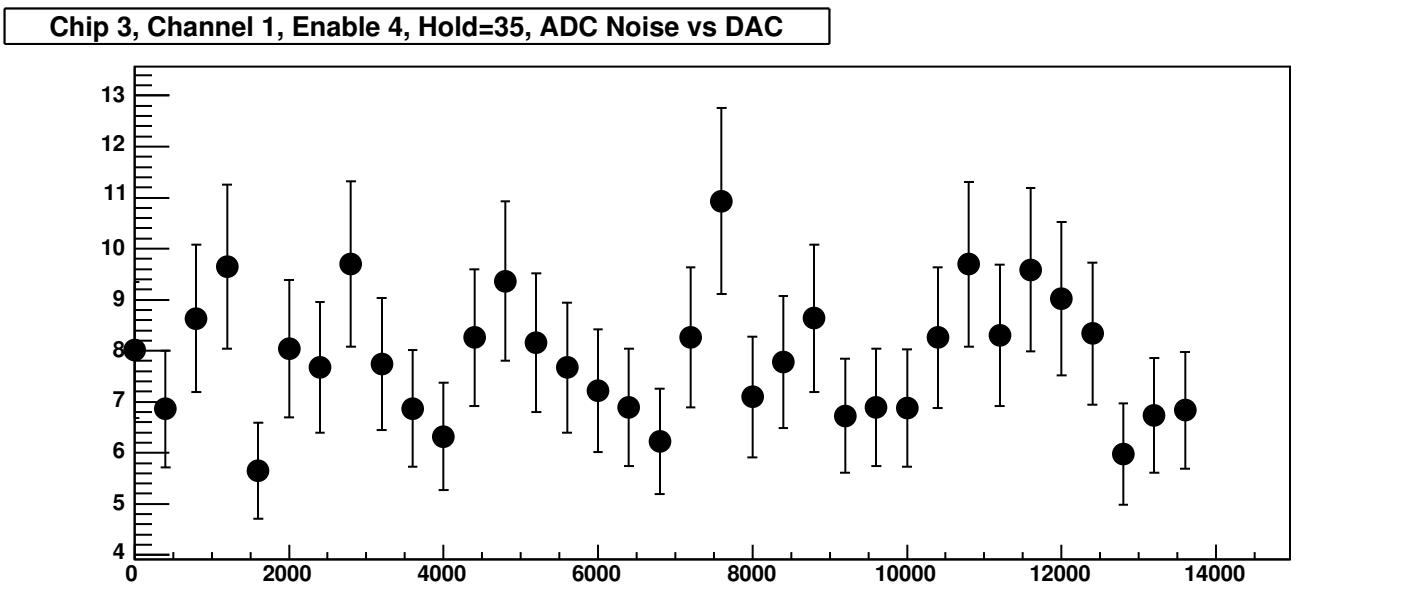

Chip 3, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

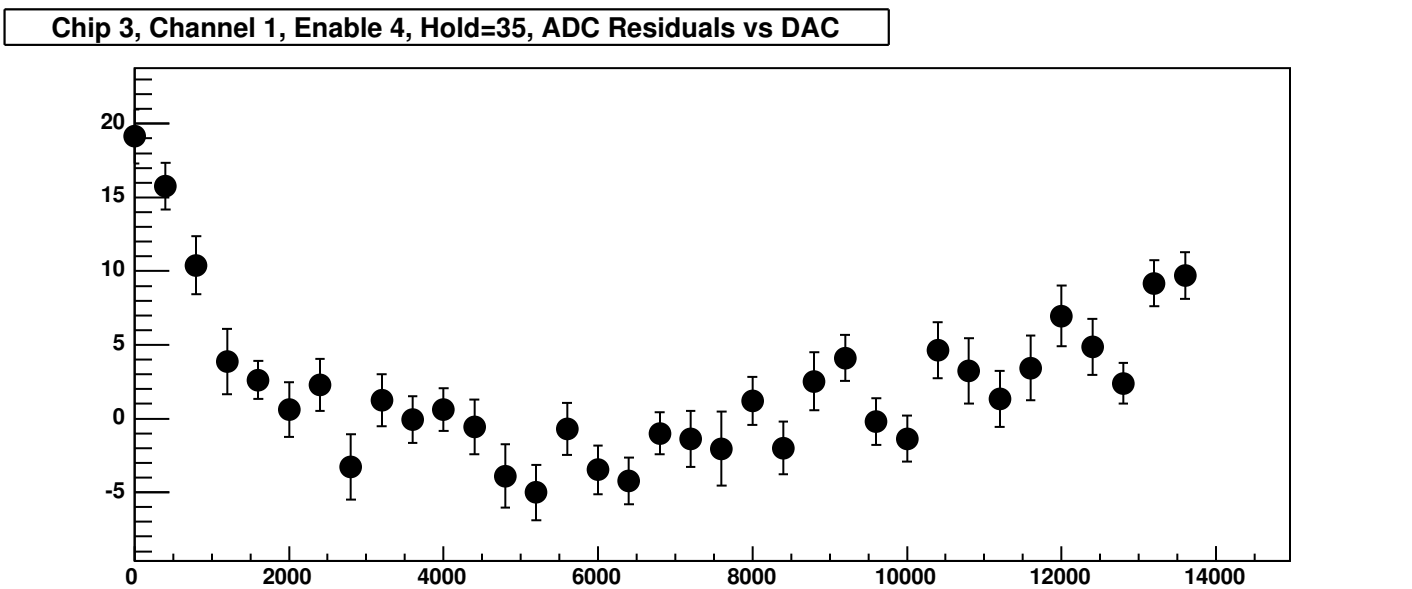

Chip 3, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 44.23 / 23
p0 -444.6 ± 0.7873
p1 0.02892 ± 0.000138
0
-50
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

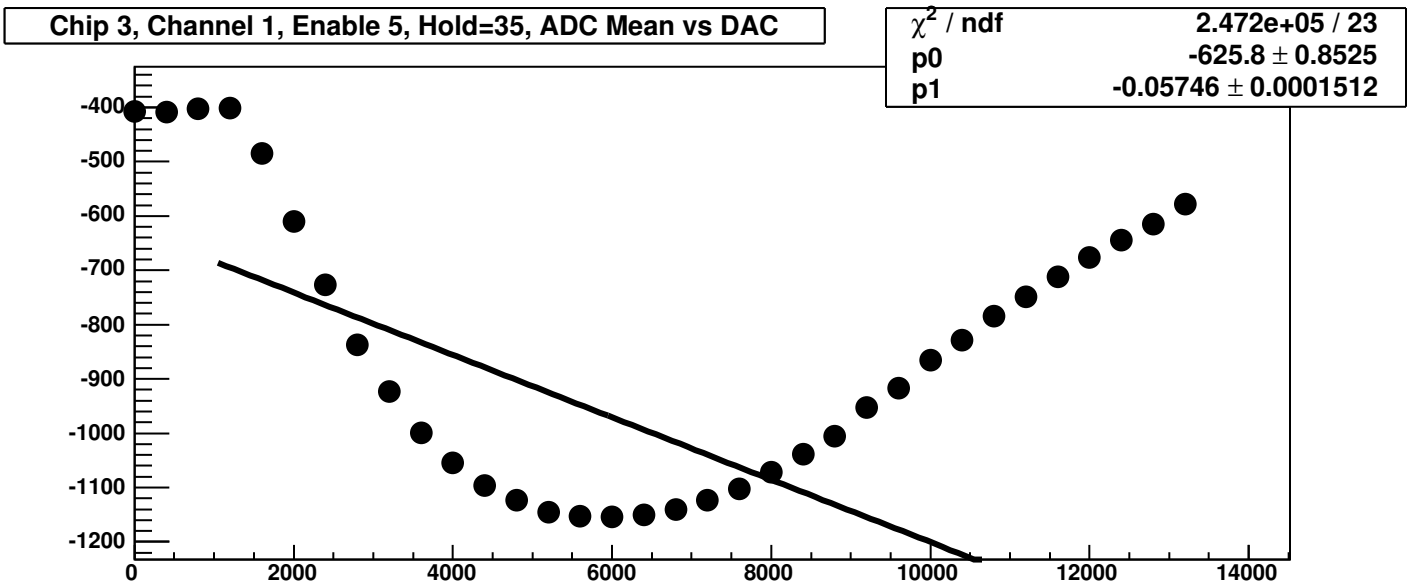

Chip 3, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

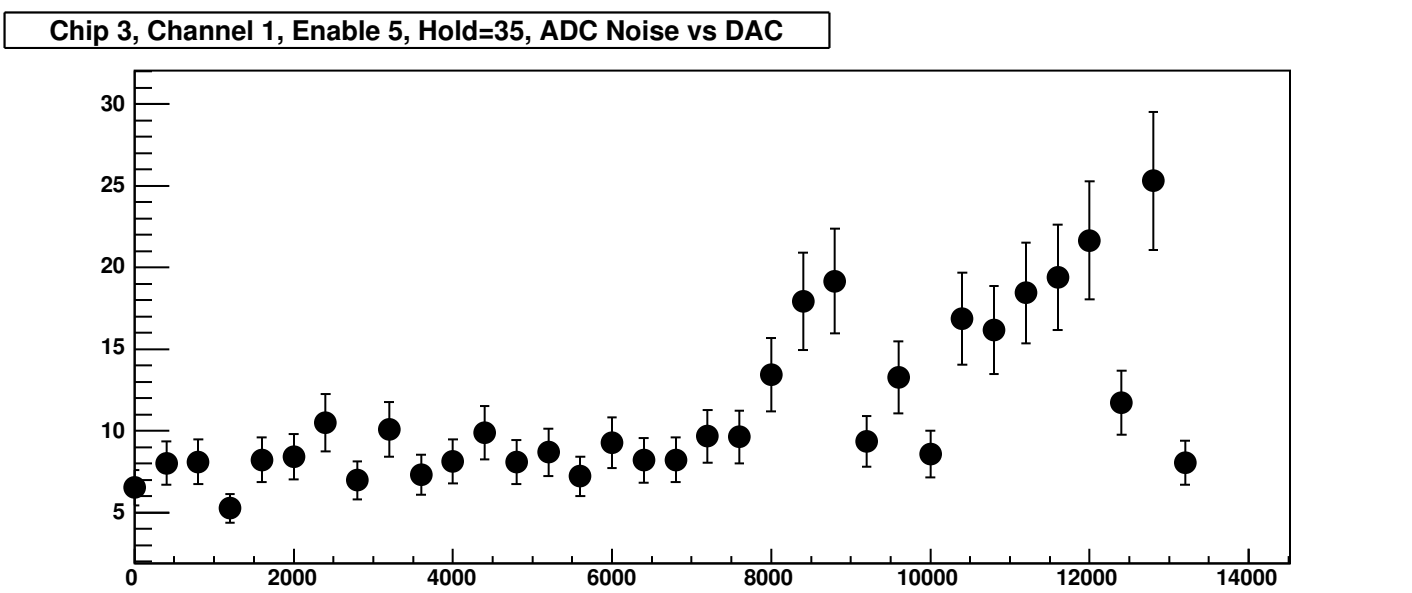

Chip 3, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

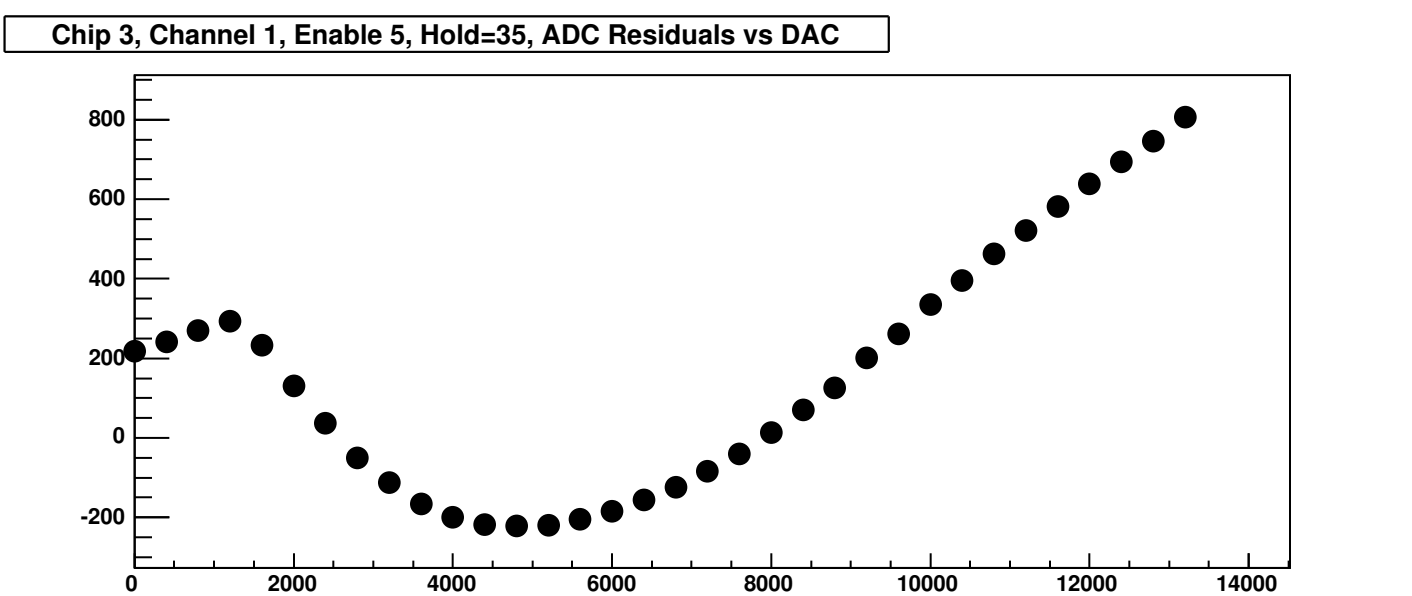

Chip 3, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 55.1 / 23
p0 -442.6 ± 0.8015
p1 0.01513 ± 0.0001216
-220
-240
-260
-280
-300
-320
-340
-360
-380
-400
-420
-440
0
2000
4000
6000
8000
10000
12000
14000

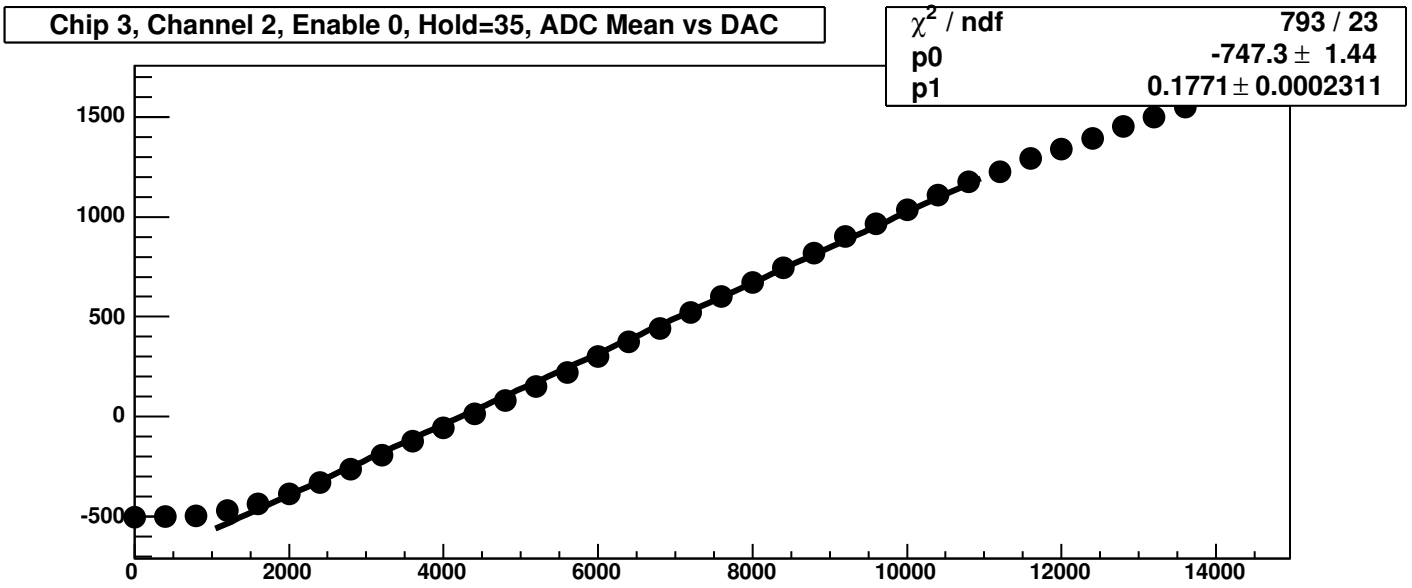

Chip 3, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

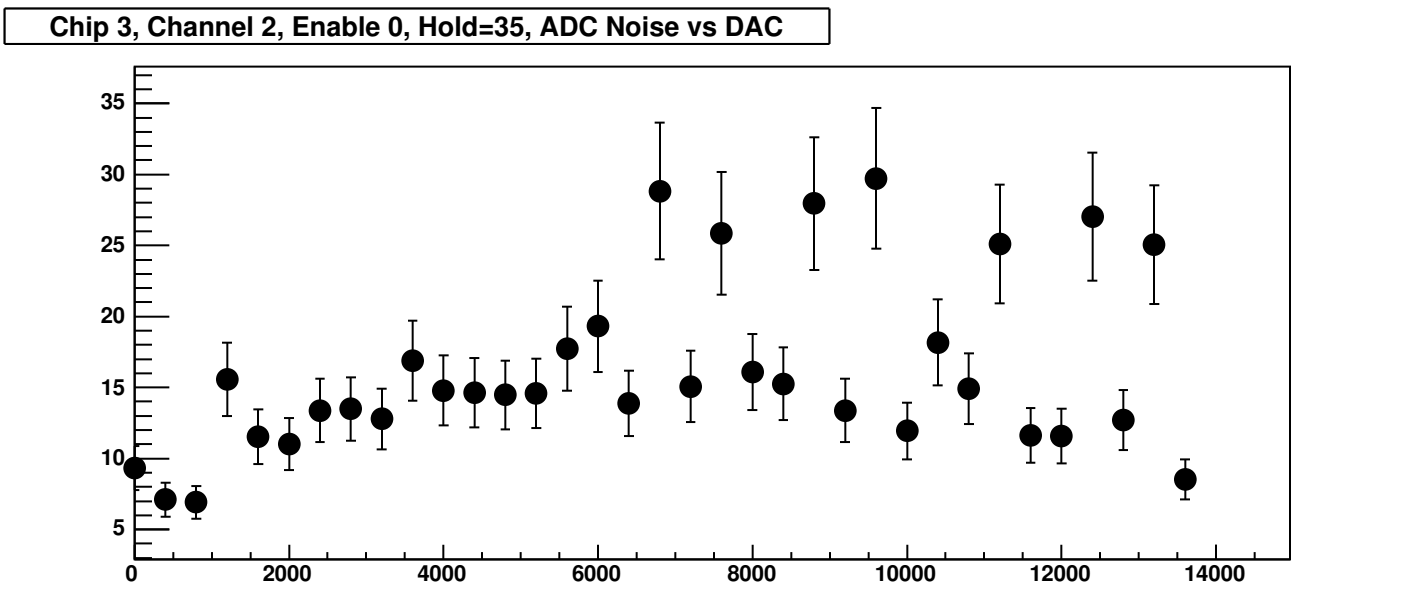

Chip 3, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

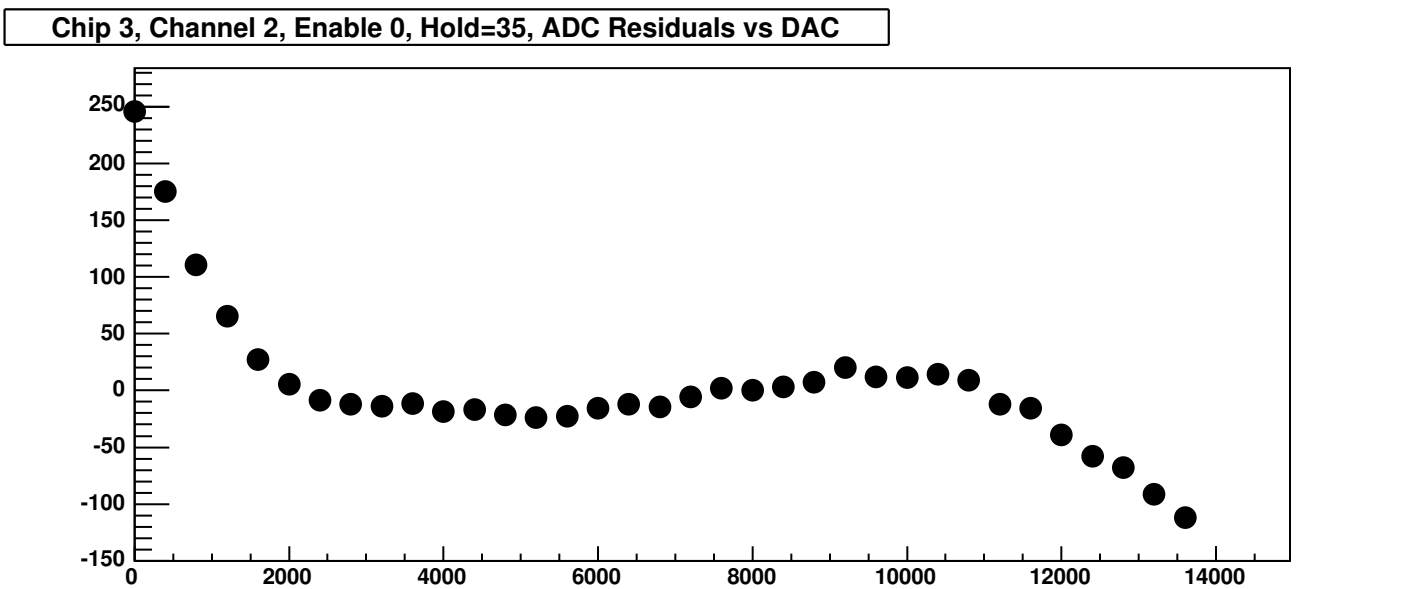

Chip 3, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2.472e+05 / 23
p0 -625.8 ± 0.8525
p1 -0.05746 ± 0.0001512
-400
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
2000
4000
6000
8000
10000
12000
14000

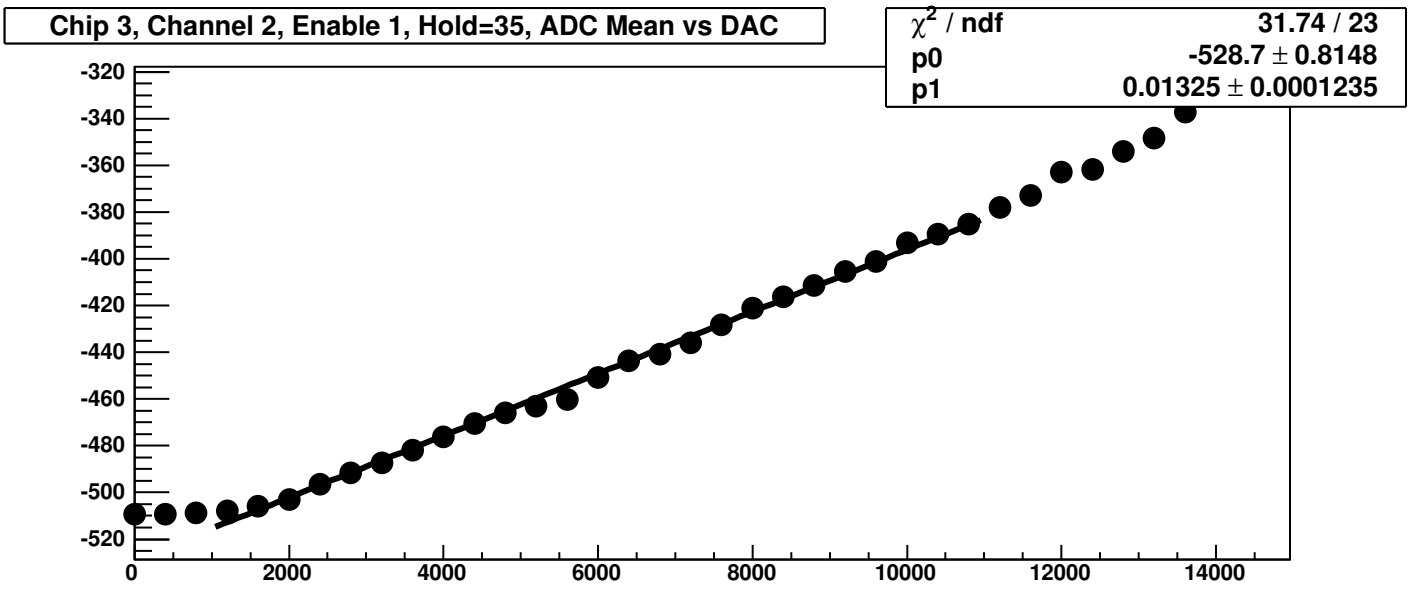

Chip 3, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

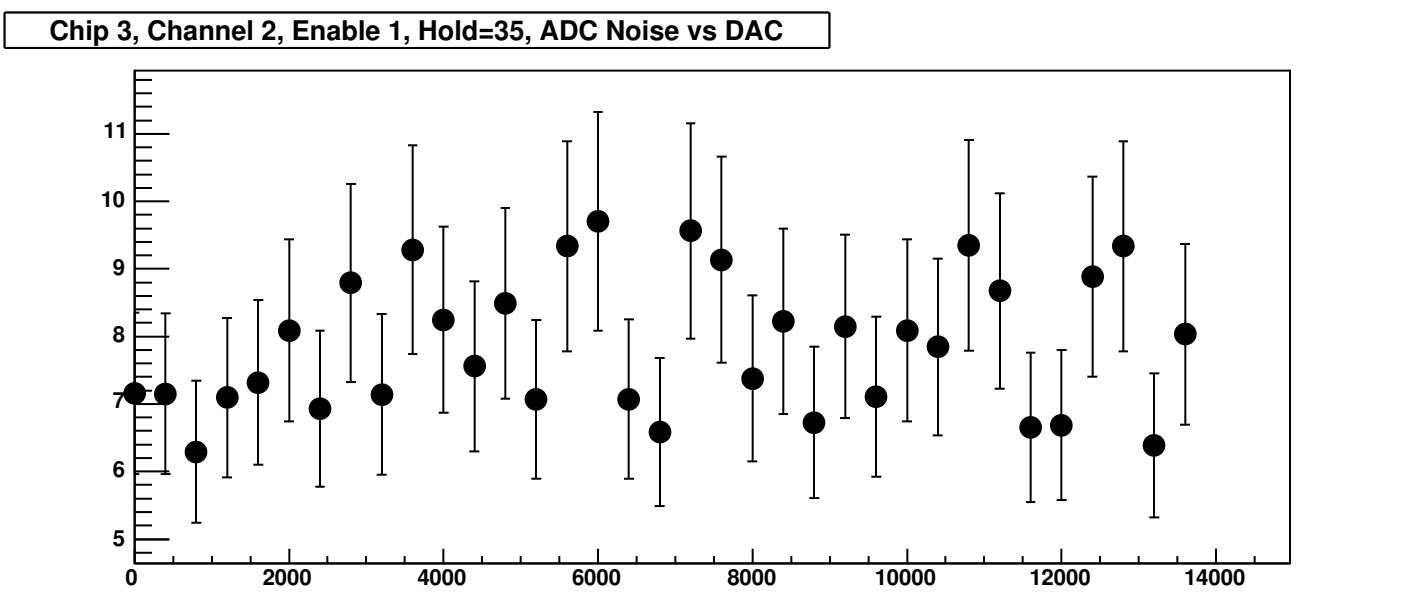

Chip 3, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

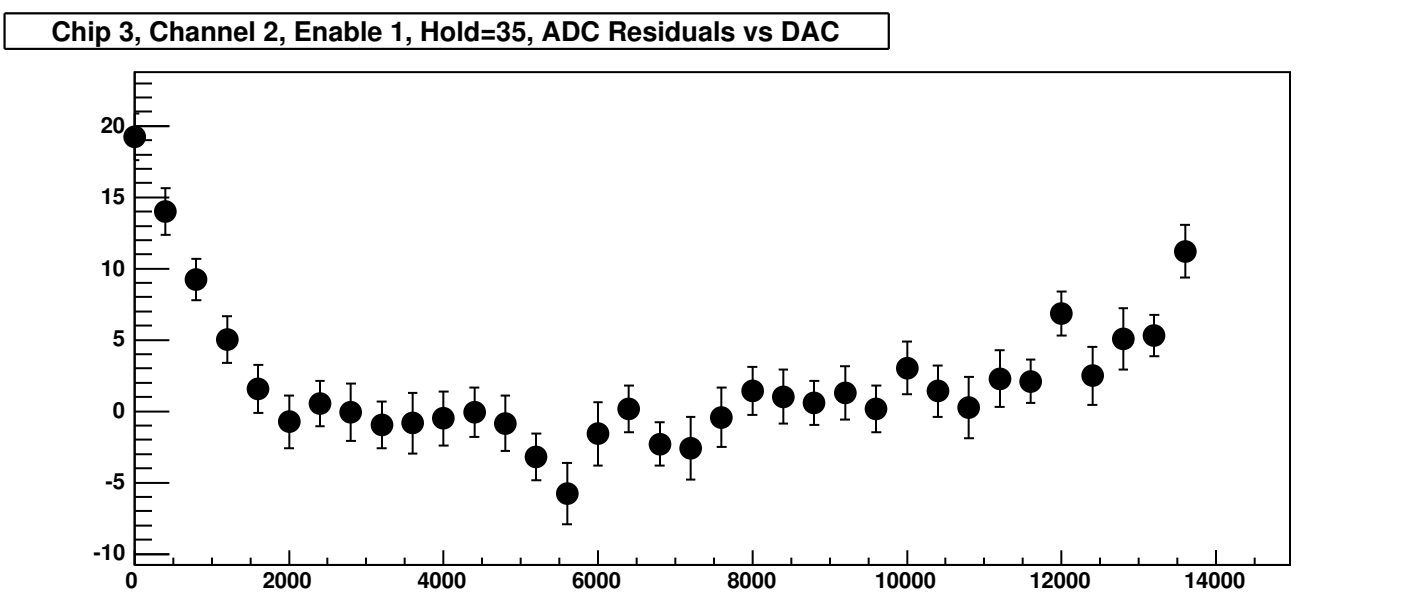

Chip 3, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 793 / 23
p0 -747.3 ± 1.44
p1 0.1771 ± 0.0002311
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

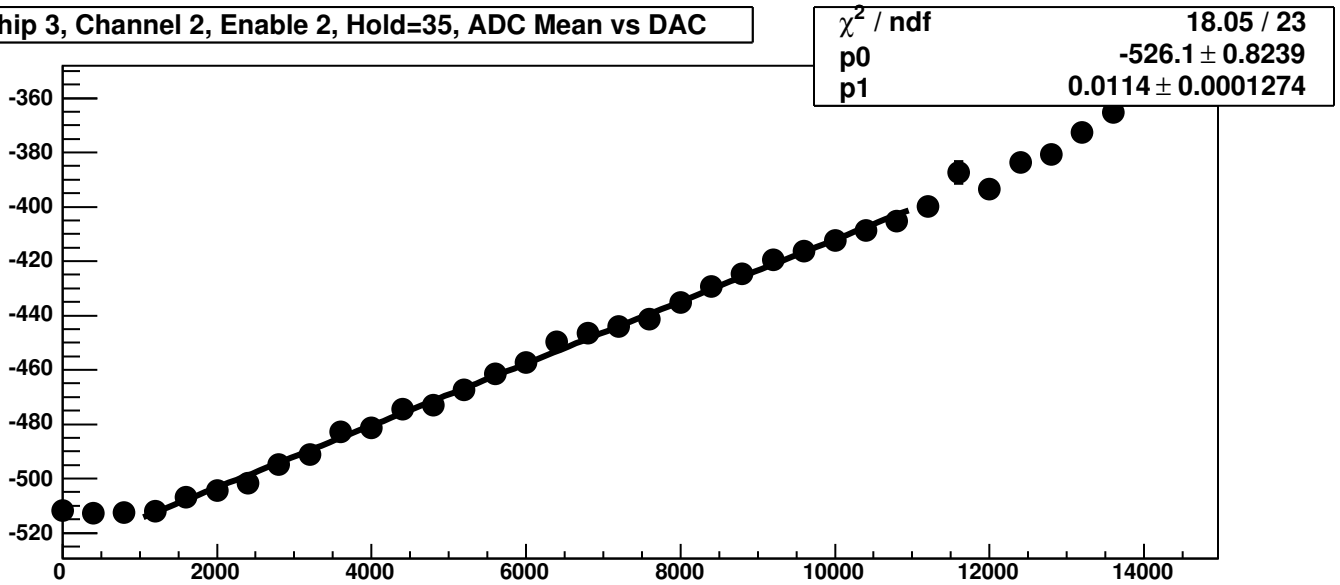

Chip 3, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.74 / 23
p0 -528.7 ± 0.8148
p1 0.01325 ± 0.0001235
-320
-340
-360
-380
-400
-420
-440
-460
-480
-500
-520
0
2000
4000
6000
8000
10000
12000
14000

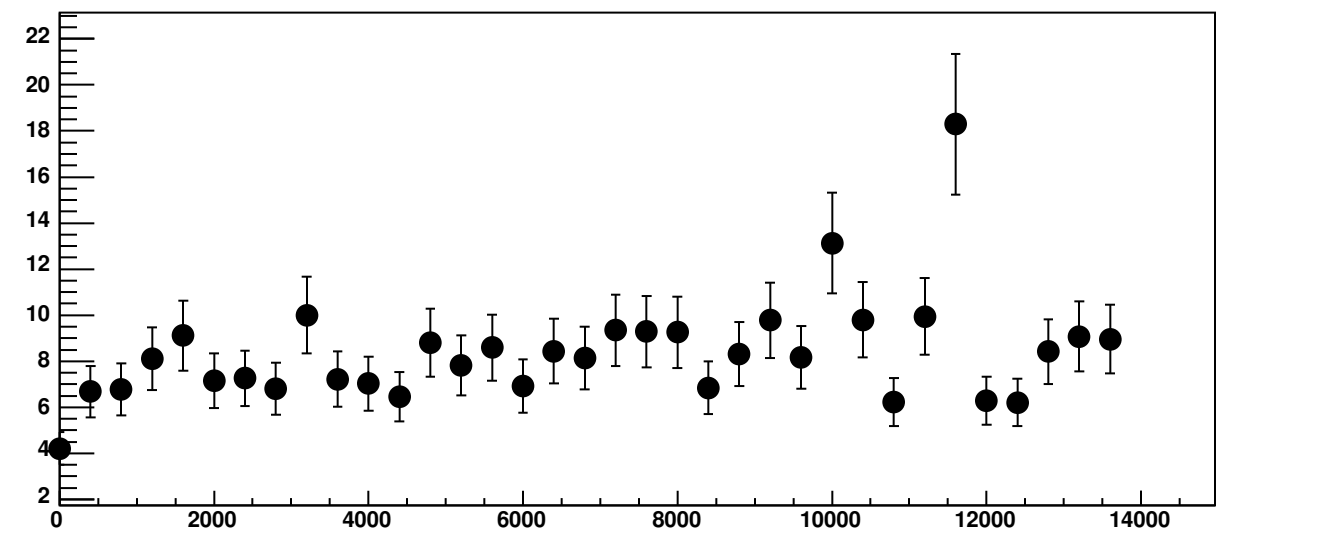

Chip 3, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

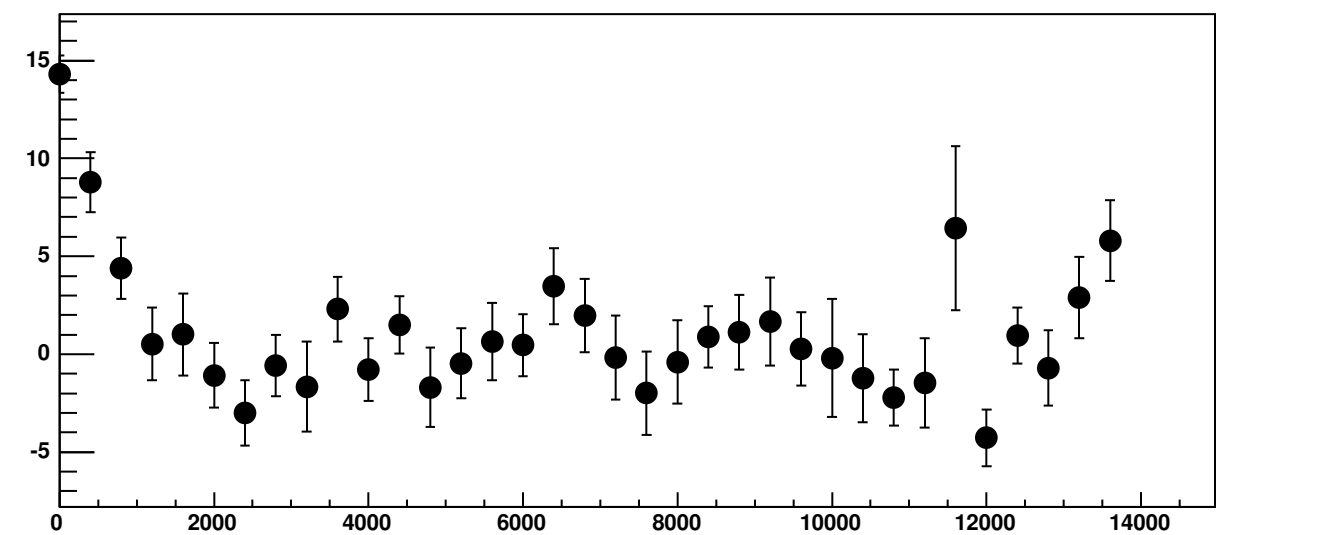
Chip 3, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

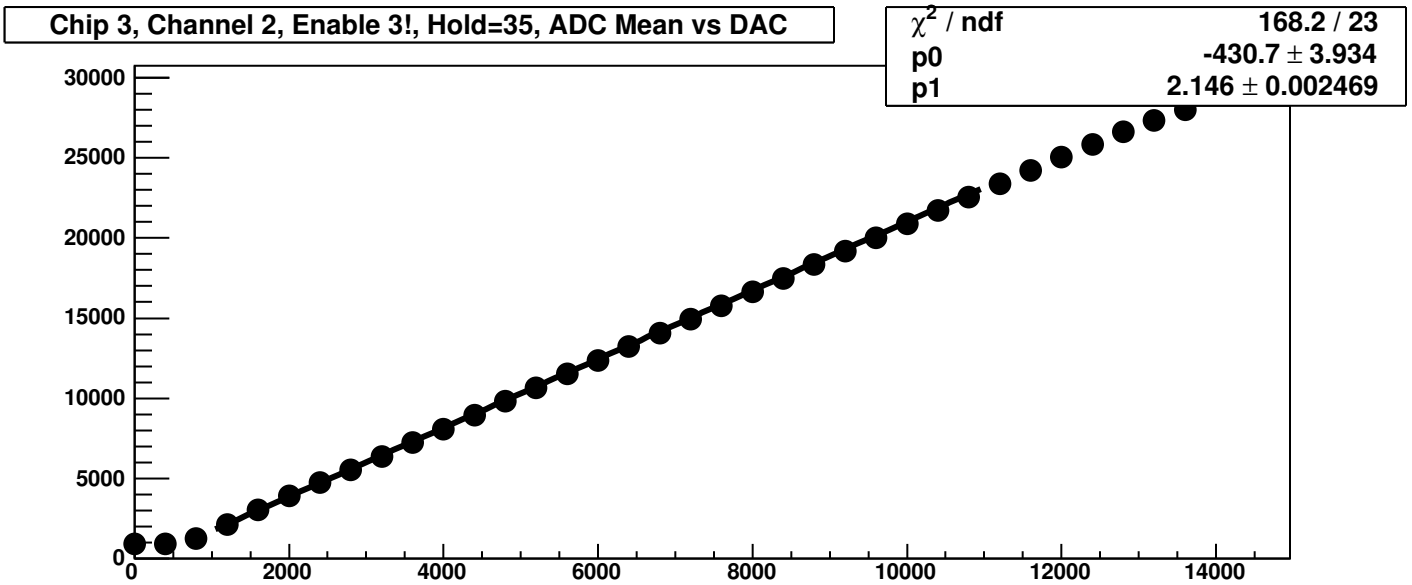


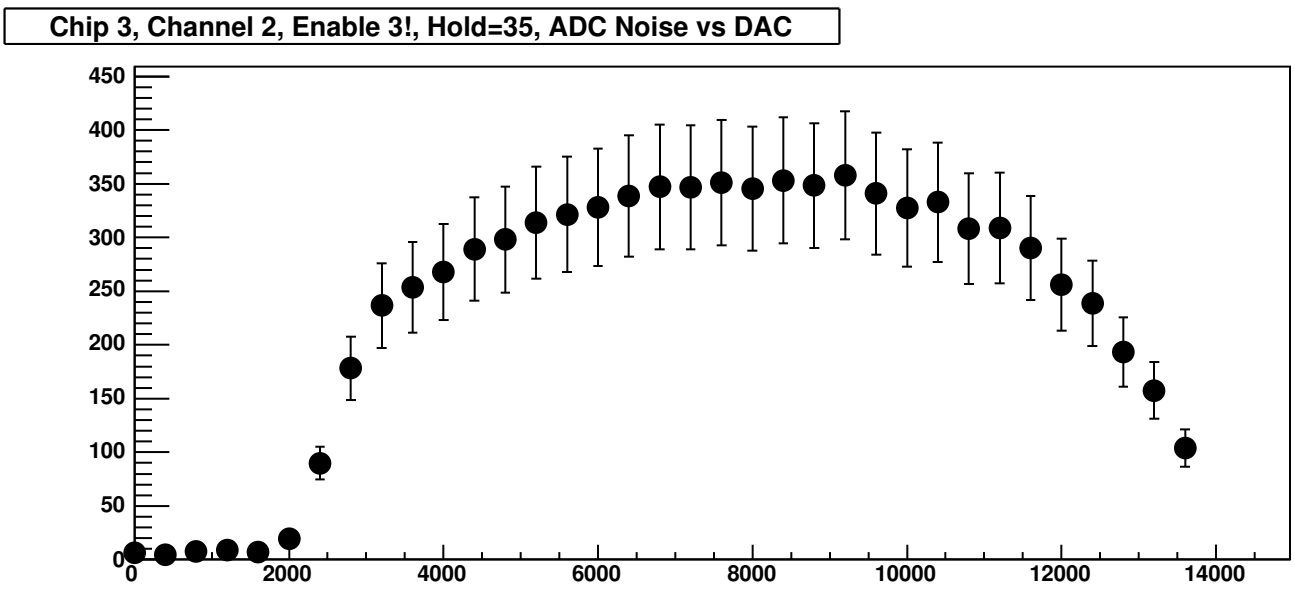
Chip 3, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

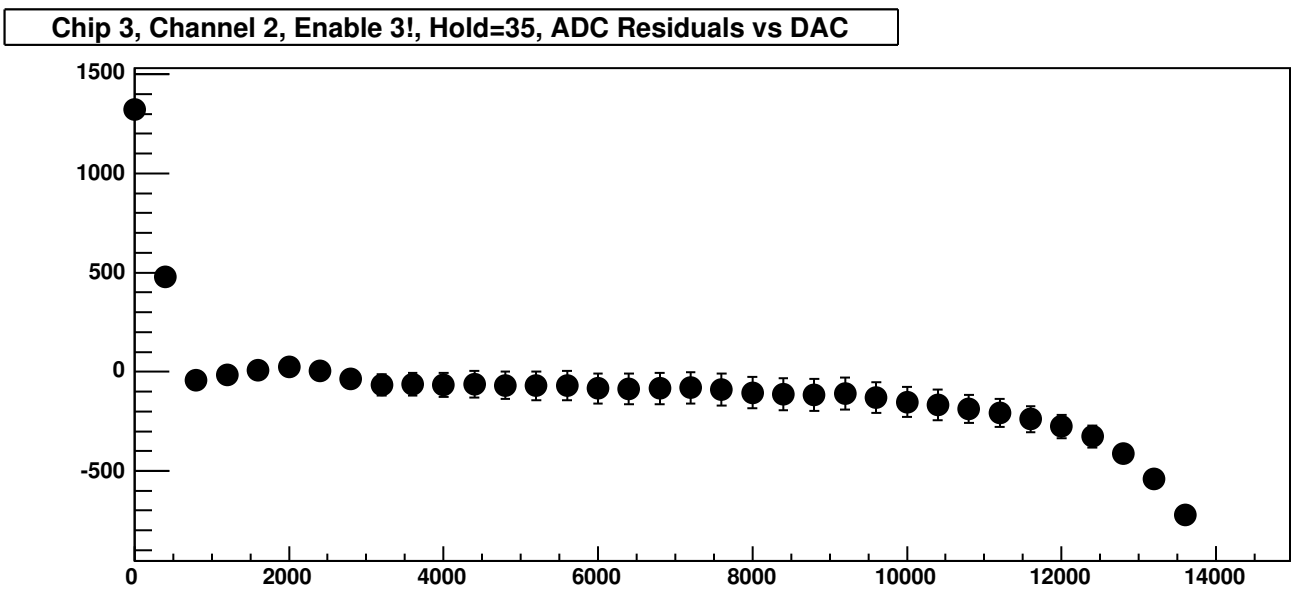


Chip 3, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

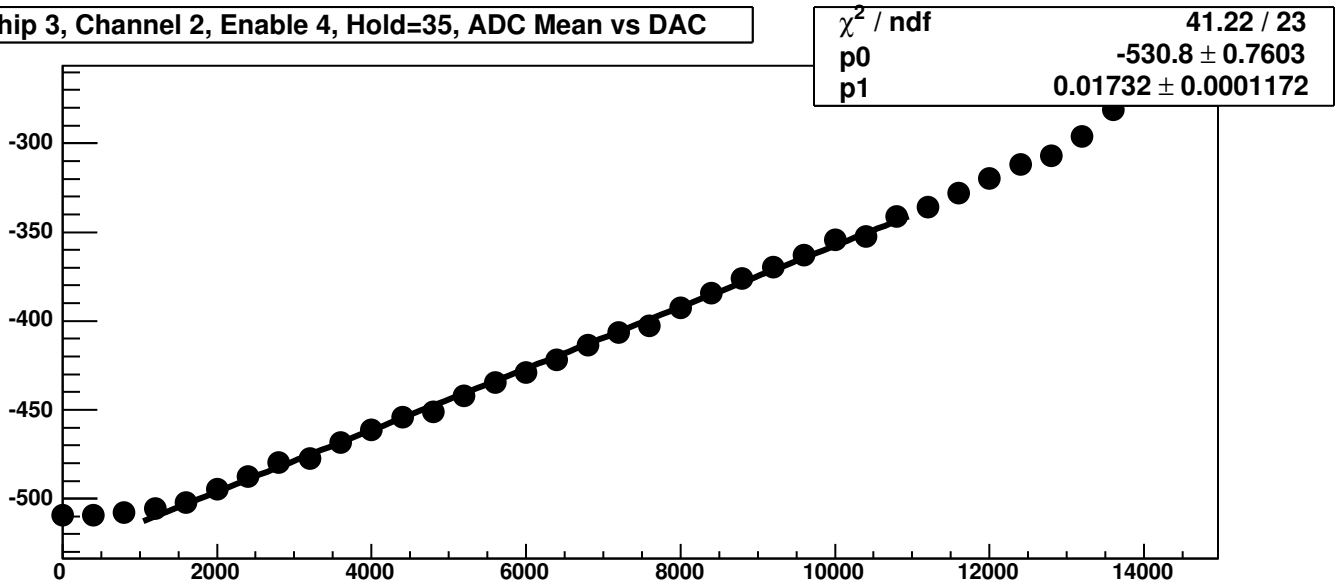



Chip 3, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 168.2 / 23
p0 -430.7 ± 3.934
p1 2.146 ± 0.002469
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

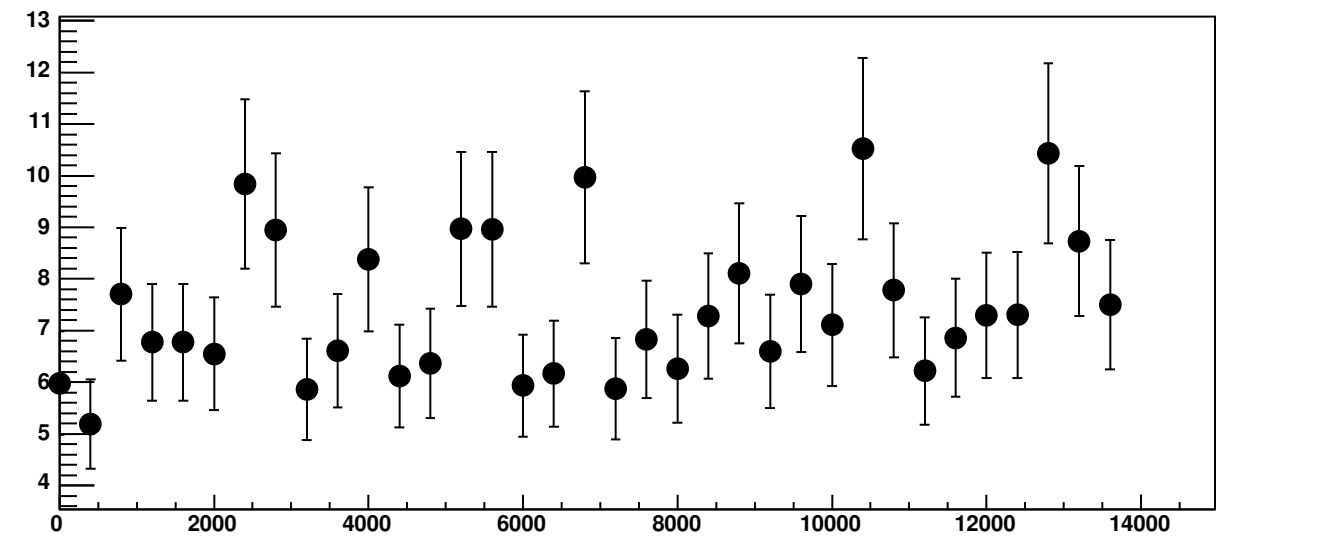

Chip 3, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

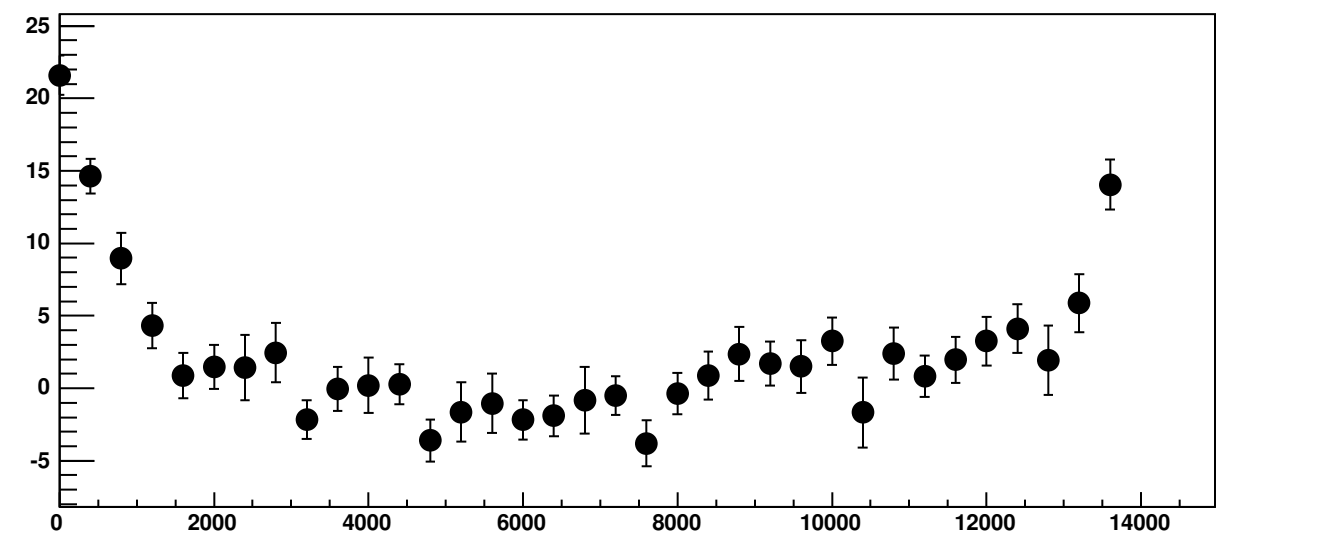
Chip 3, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC



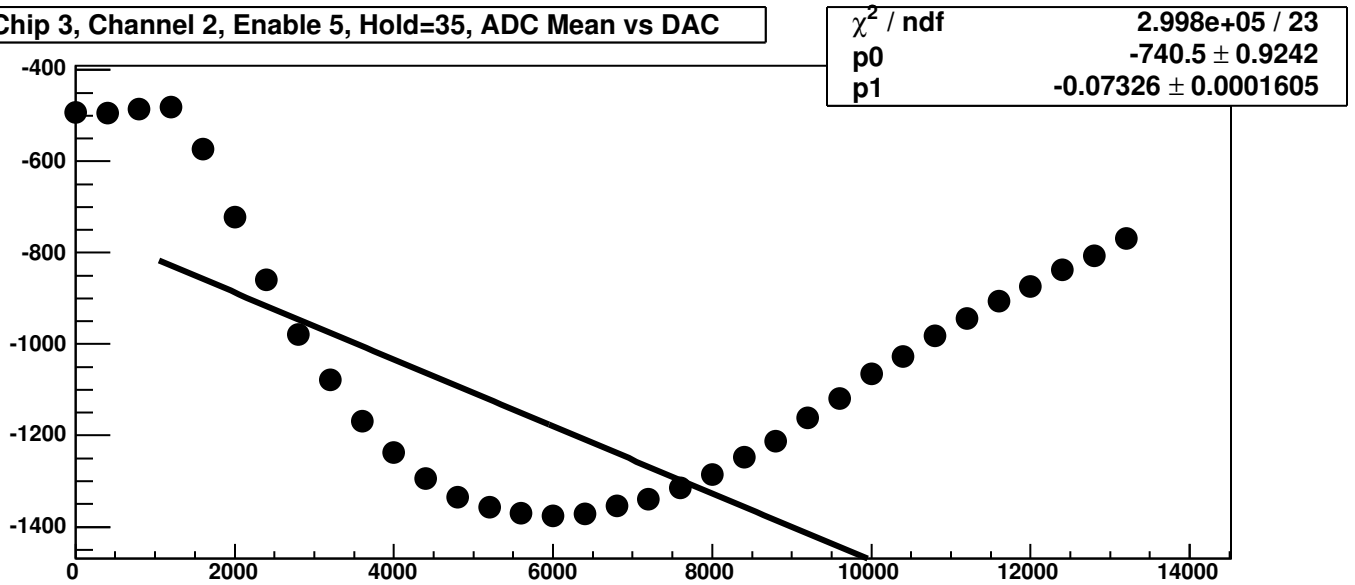
Chip 3, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC



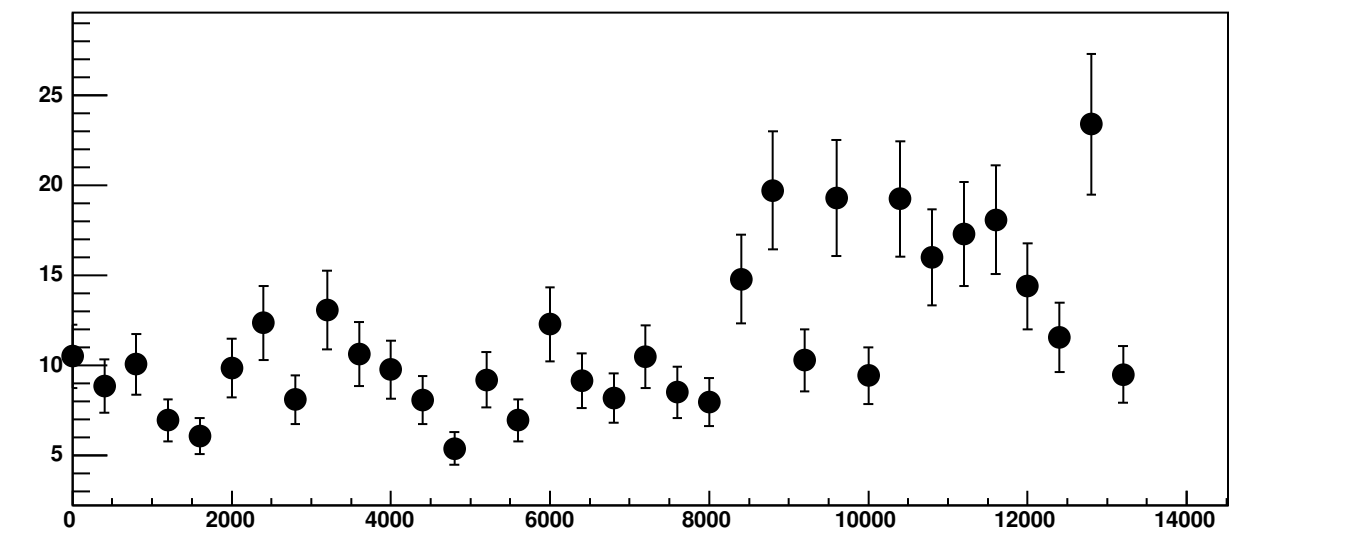
Chip 3, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC



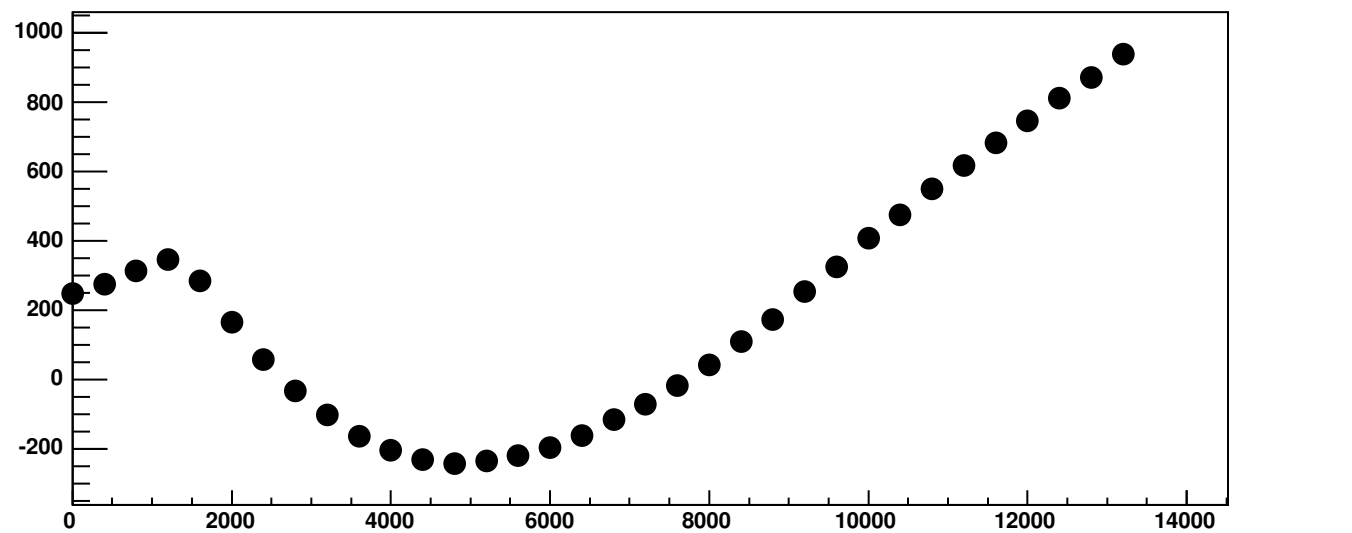
Chip 3, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



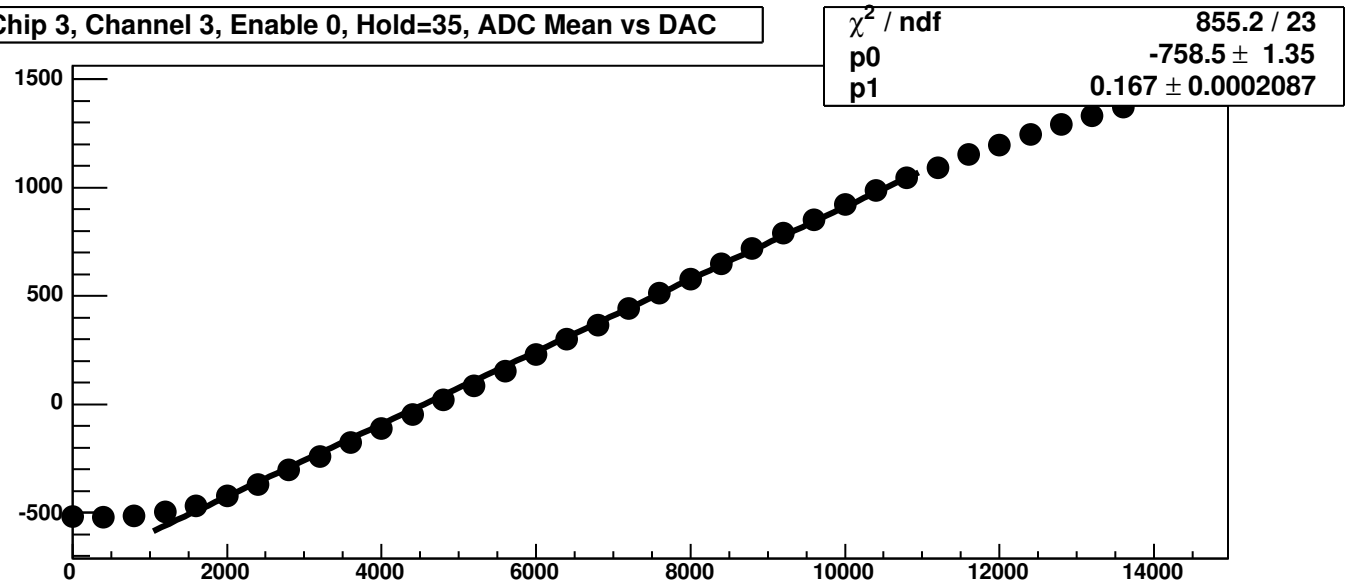
Chip 3, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



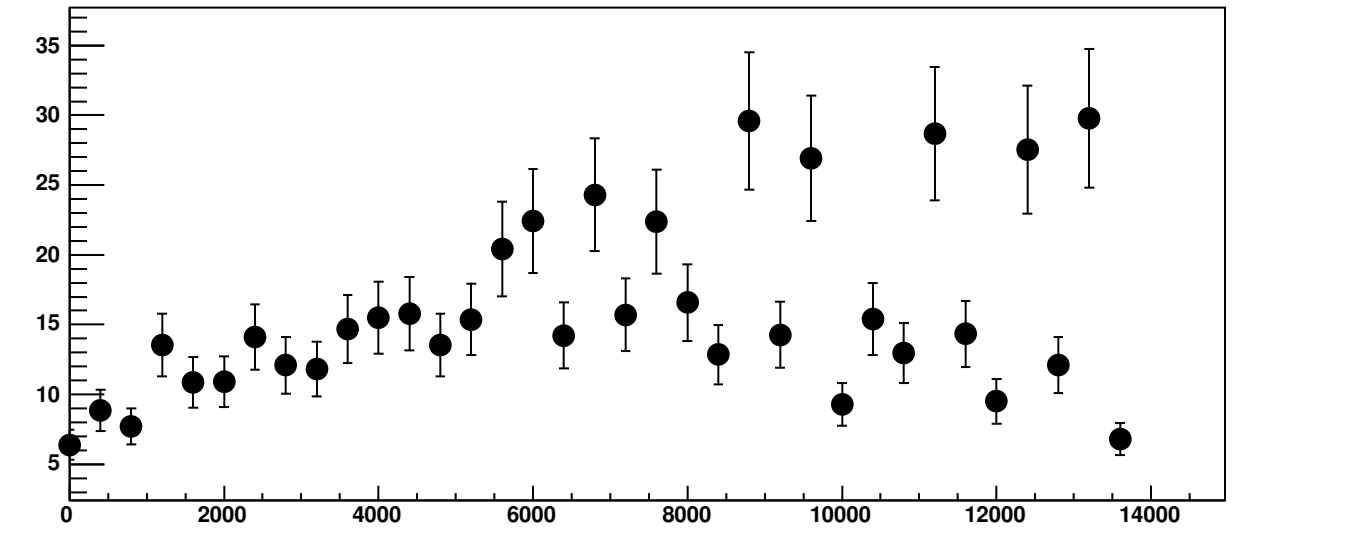
Chip 3, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



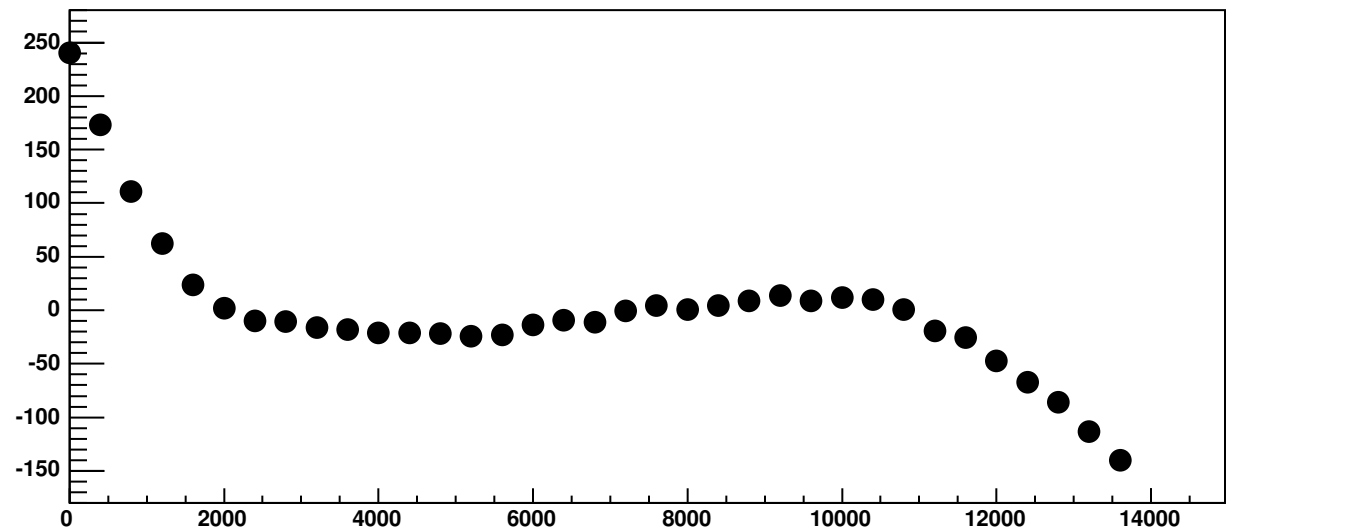
Chip 3, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

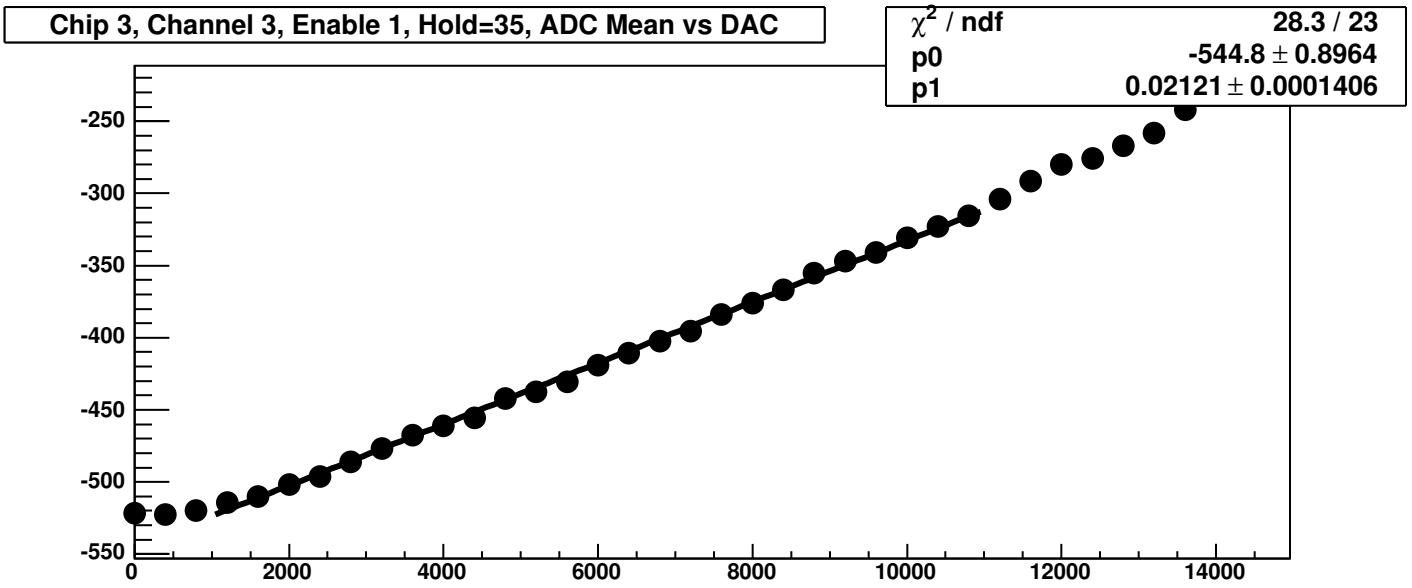


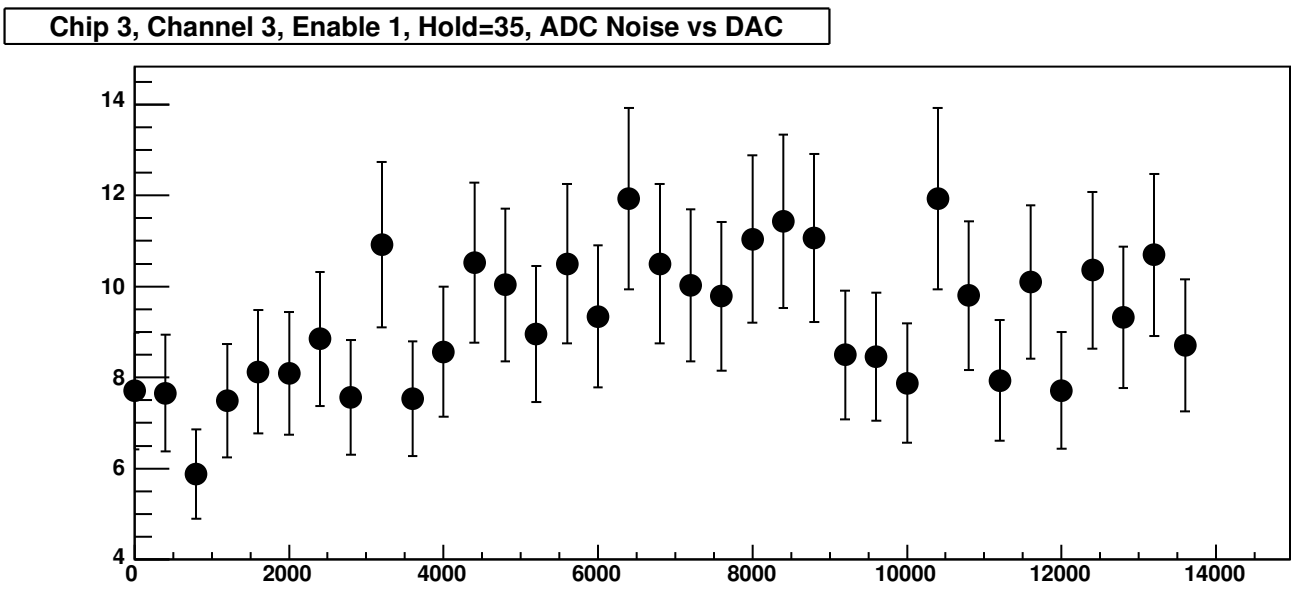
Chip 3, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

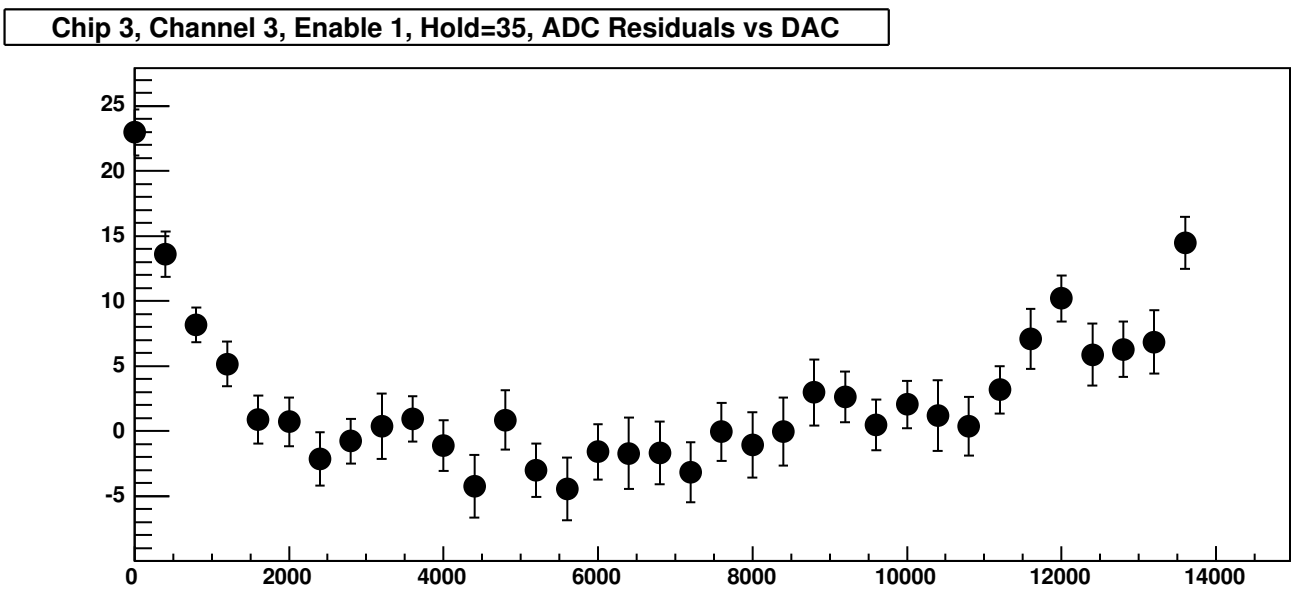


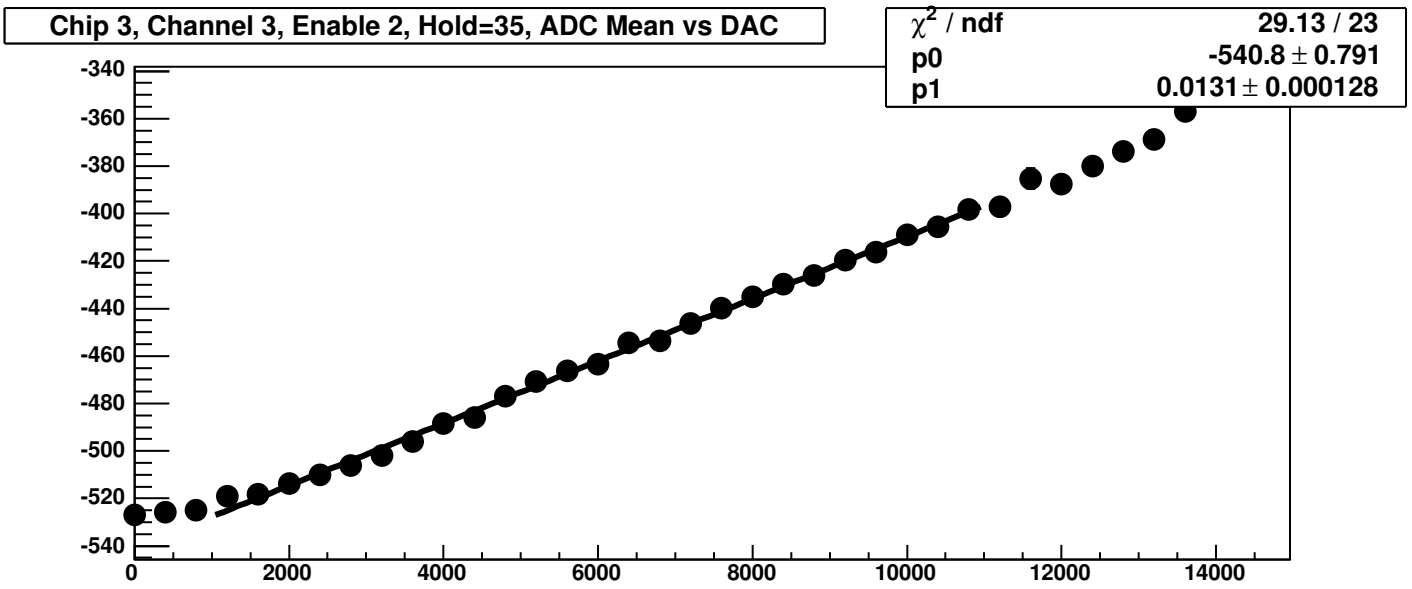
Chip 3, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

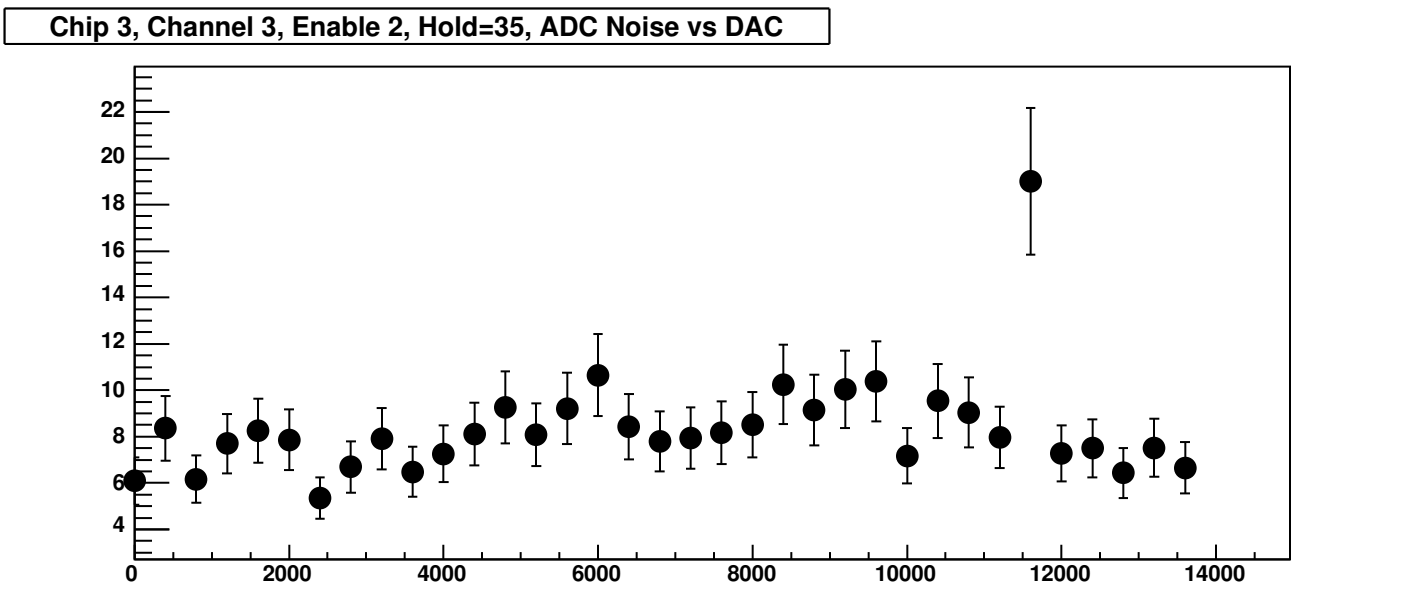


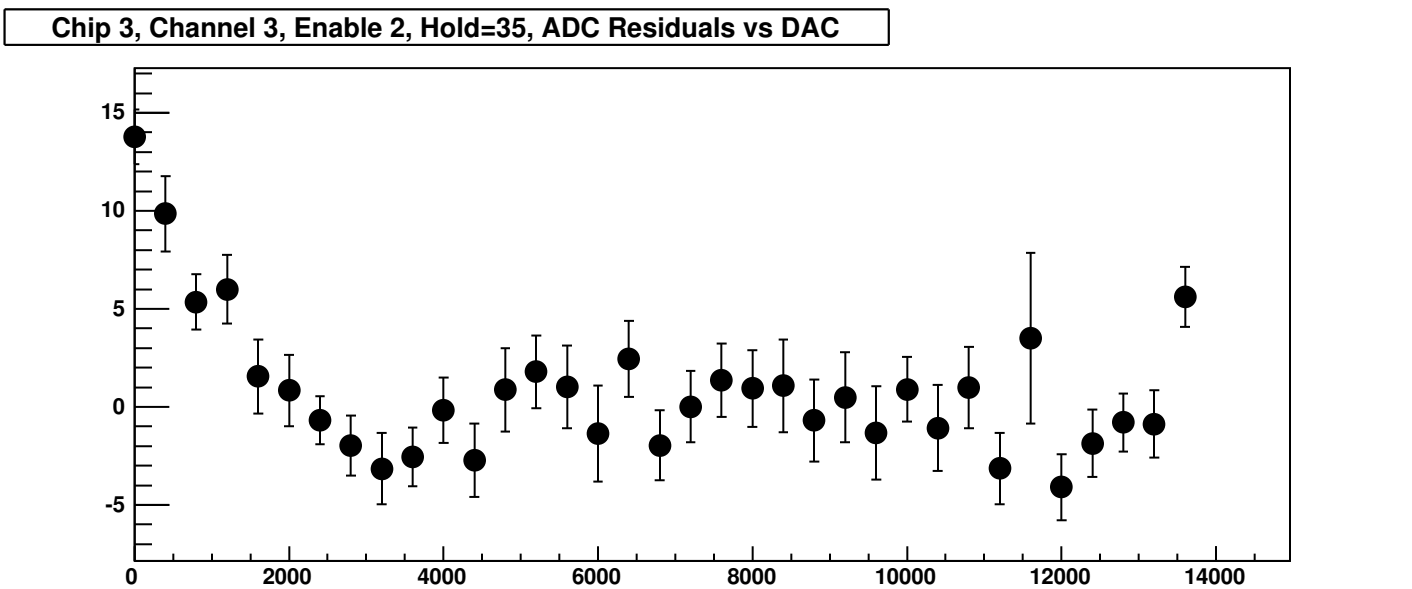

Chip 3, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 28.3 / 23
p0 -544.8 ± 0.8964
p1 0.02121 ± 0.0001406
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

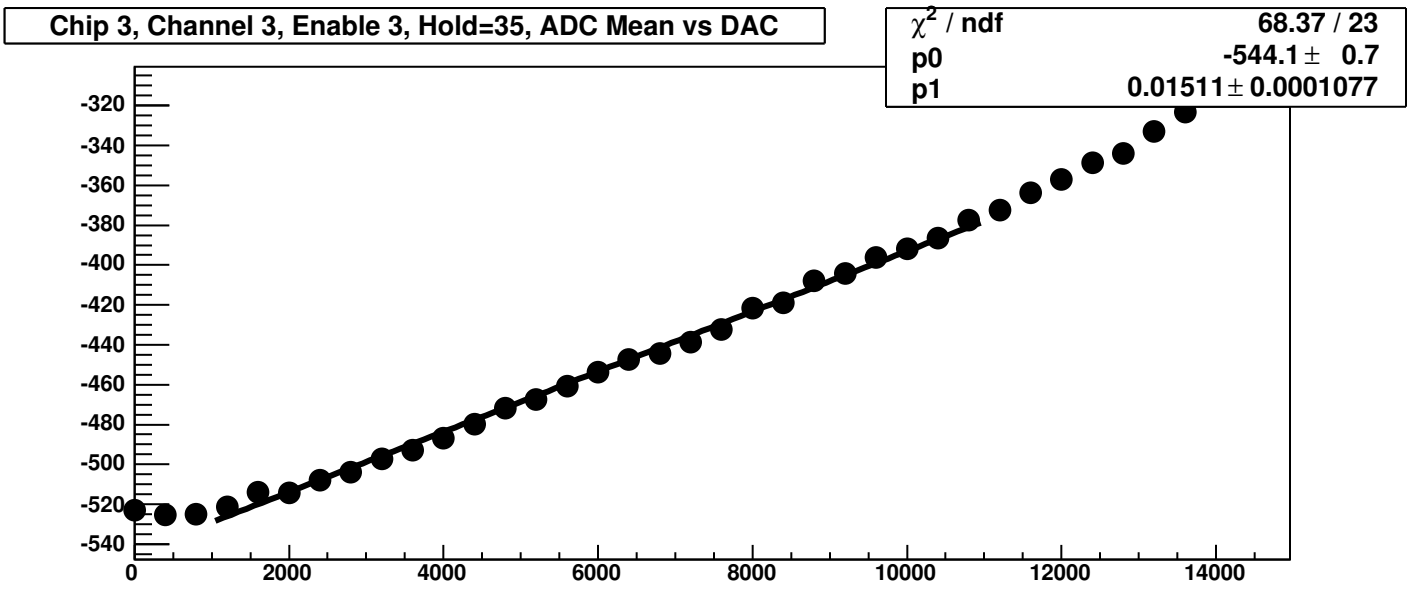

Chip 3, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

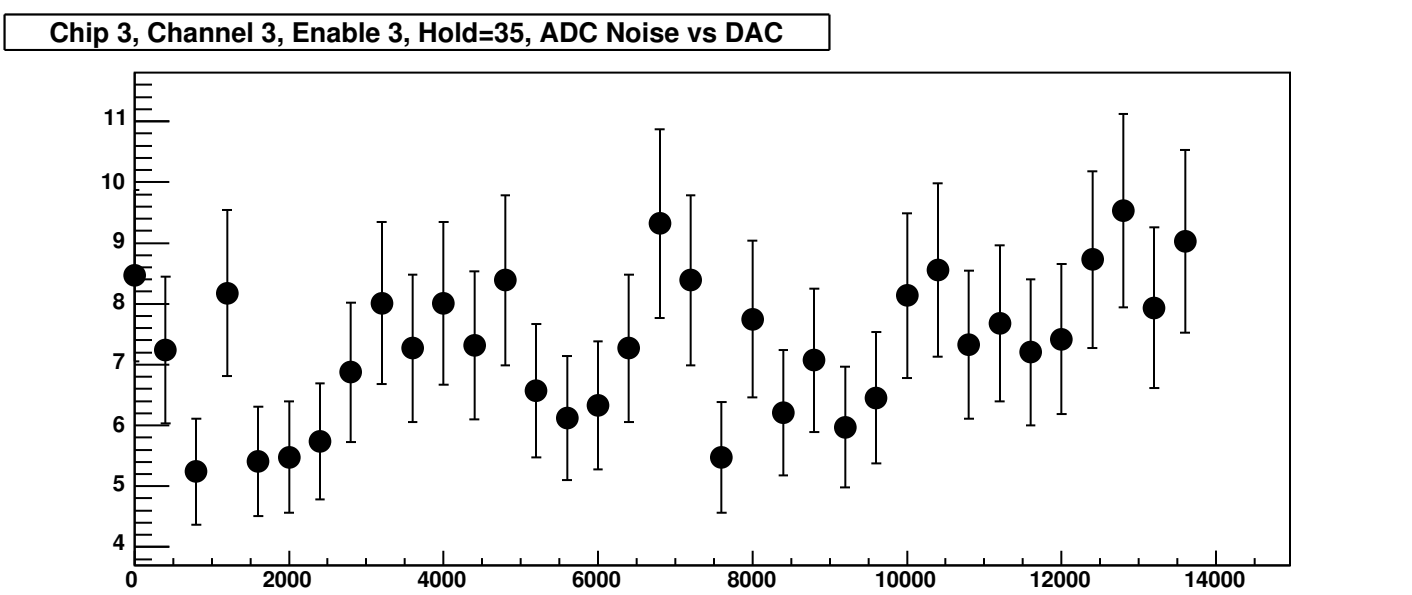

Chip 3, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

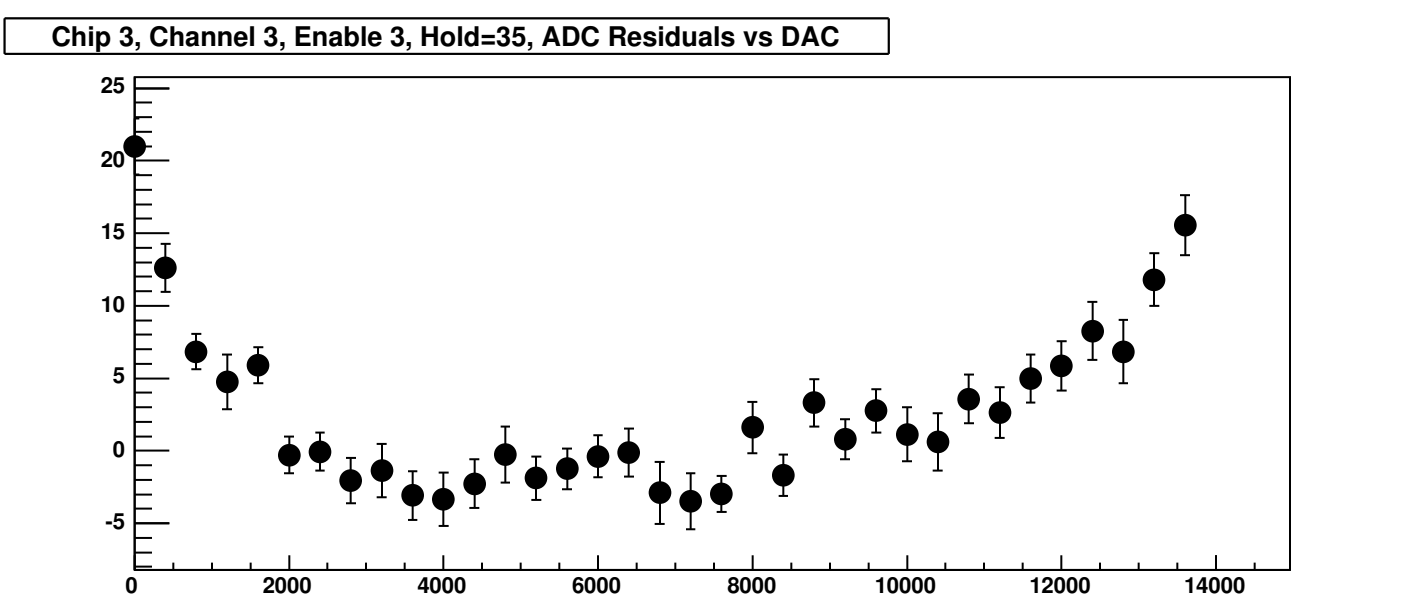

Chip 3, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 29.13 / 23
p0 -540.8 ± 0.791
p1 0.0131 ± 0.000128
-340
-360
-380
-400
-420
-440
-460
-480
-500
-520
-540
0
2000
4000
6000
8000
10000
12000
14000

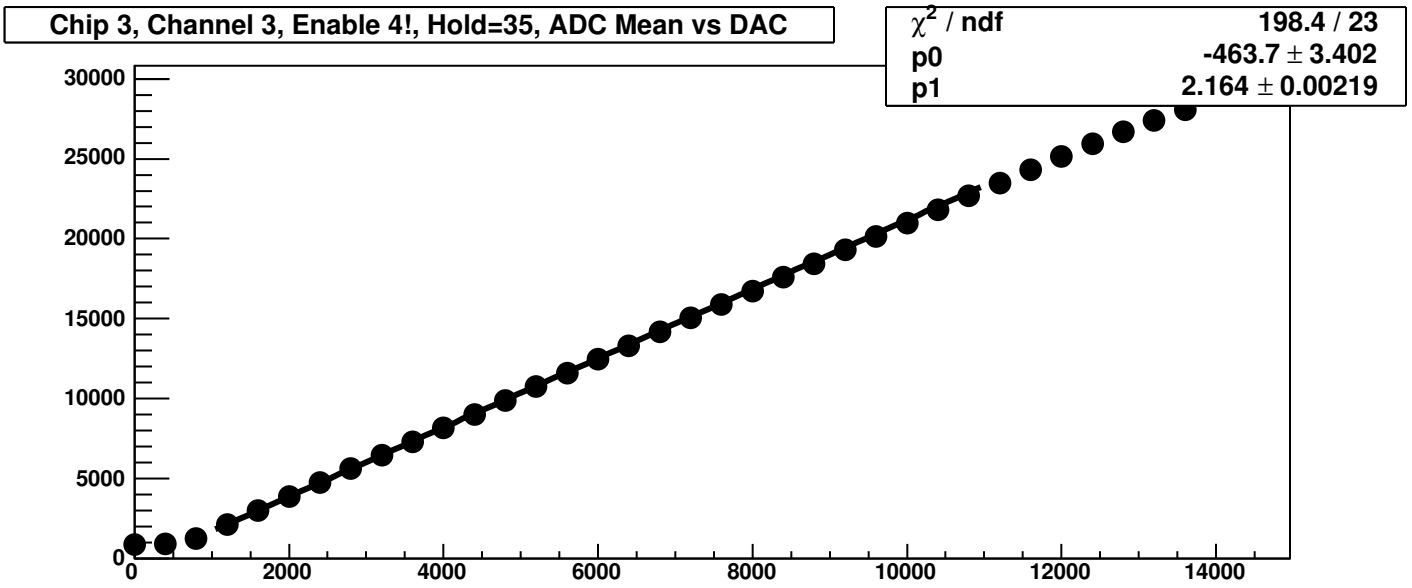

Chip 3, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

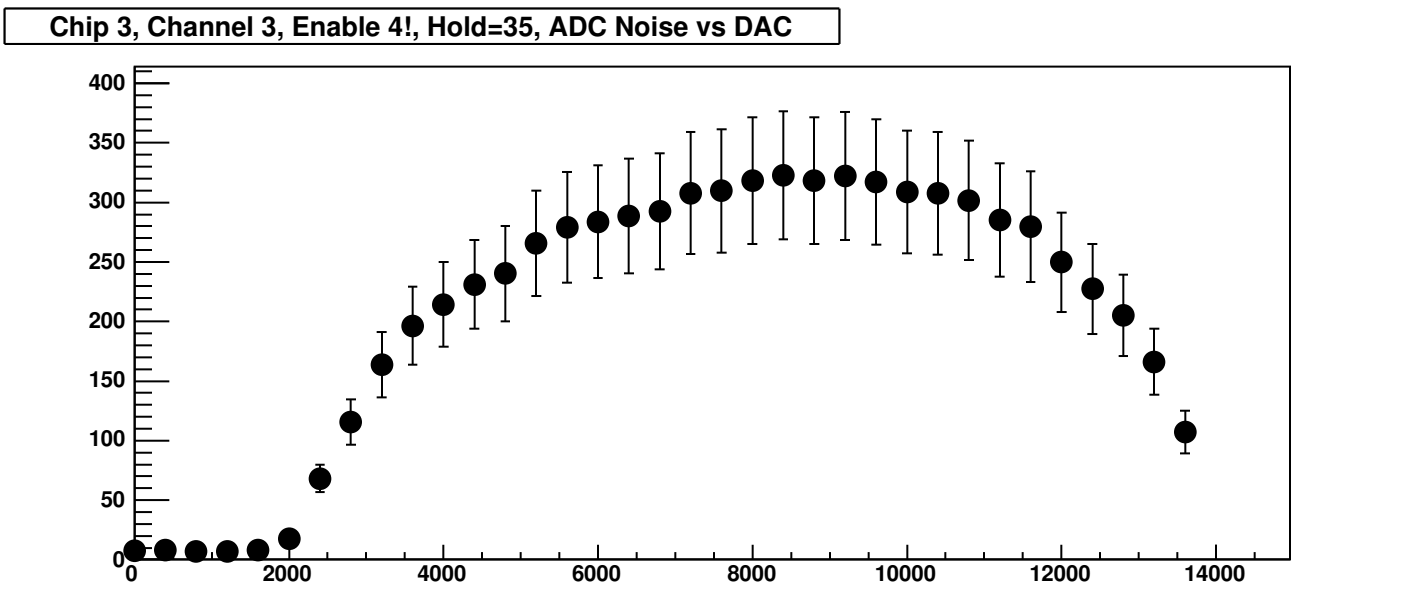

Chip 3, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

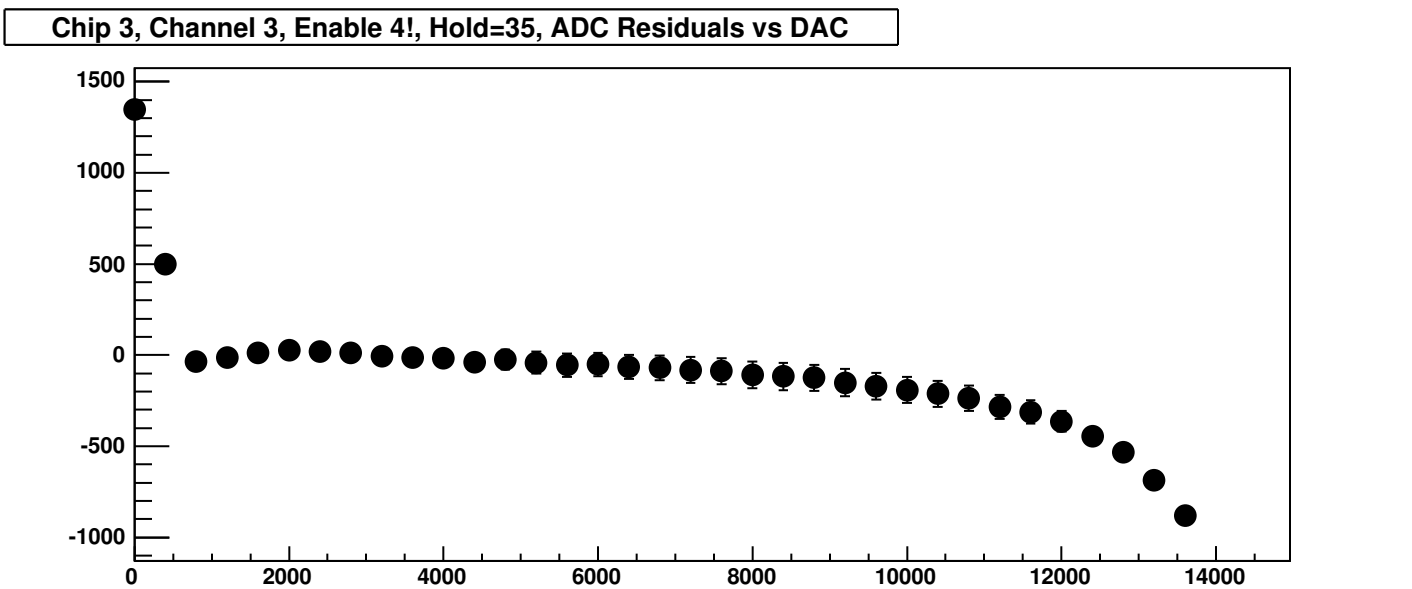

Chip 3, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 68.37 / 23
p0 -544.1 ± 0.7
p1 0.01511 ± 0.0001077
-320
-340
-360
-380
-400
-420
-440
-460
-480
-500
-520
-540
0
2000
4000
6000
8000
10000
12000
14000

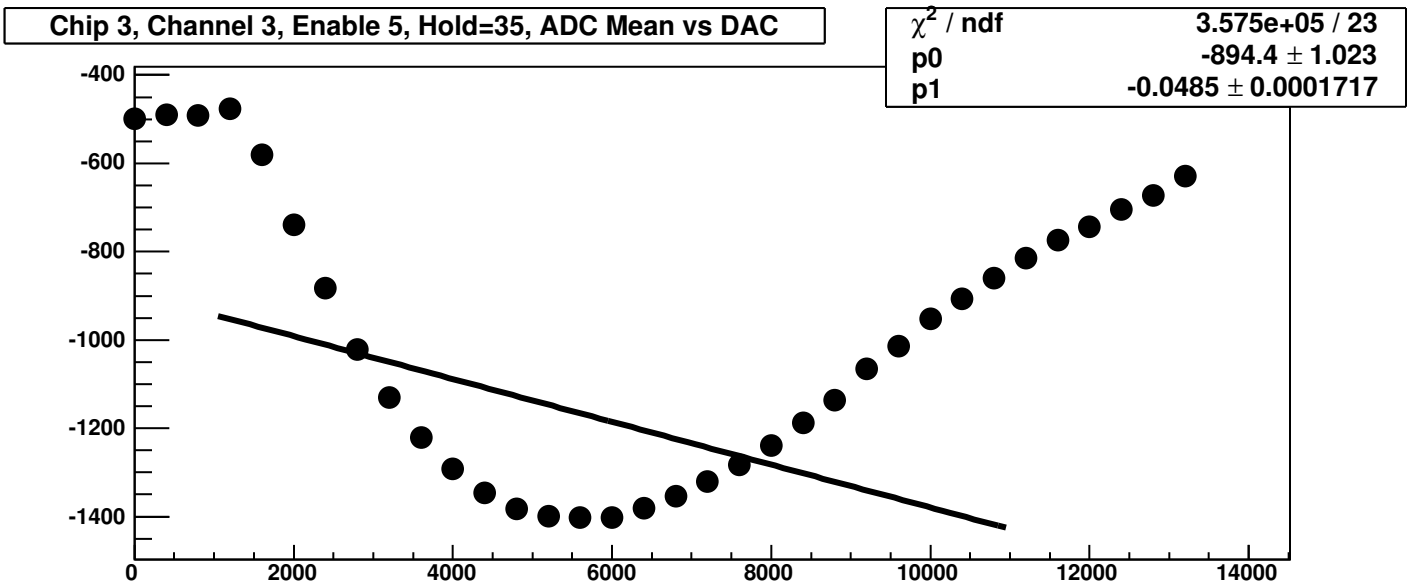

Chip 3, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

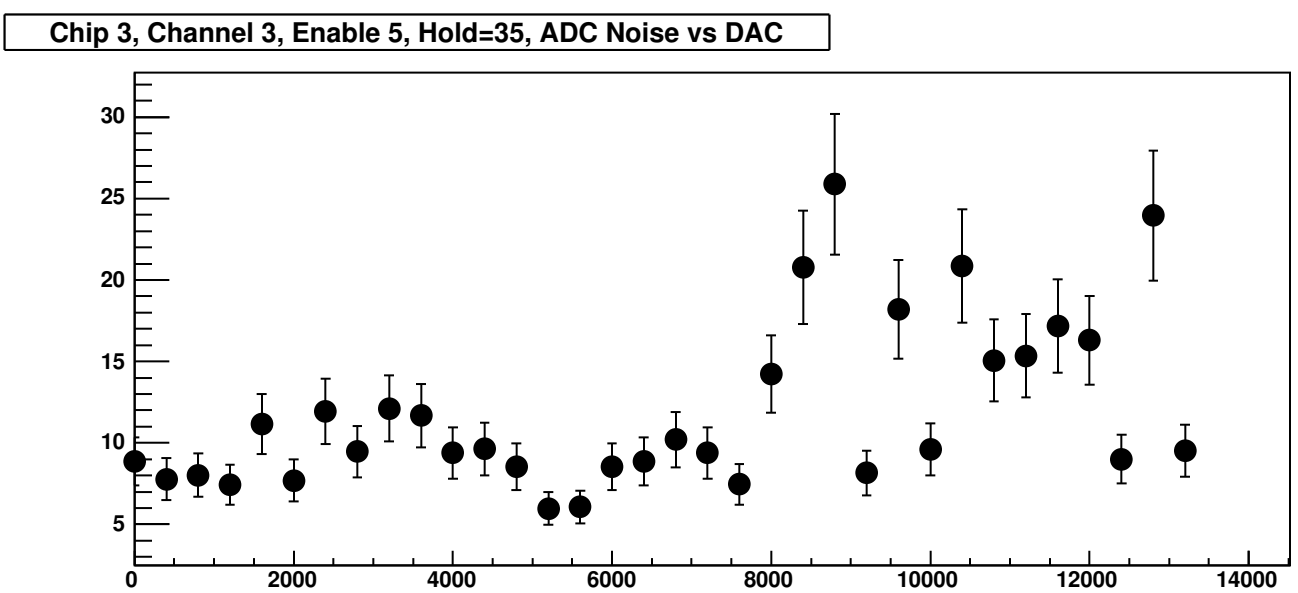

Chip 3, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

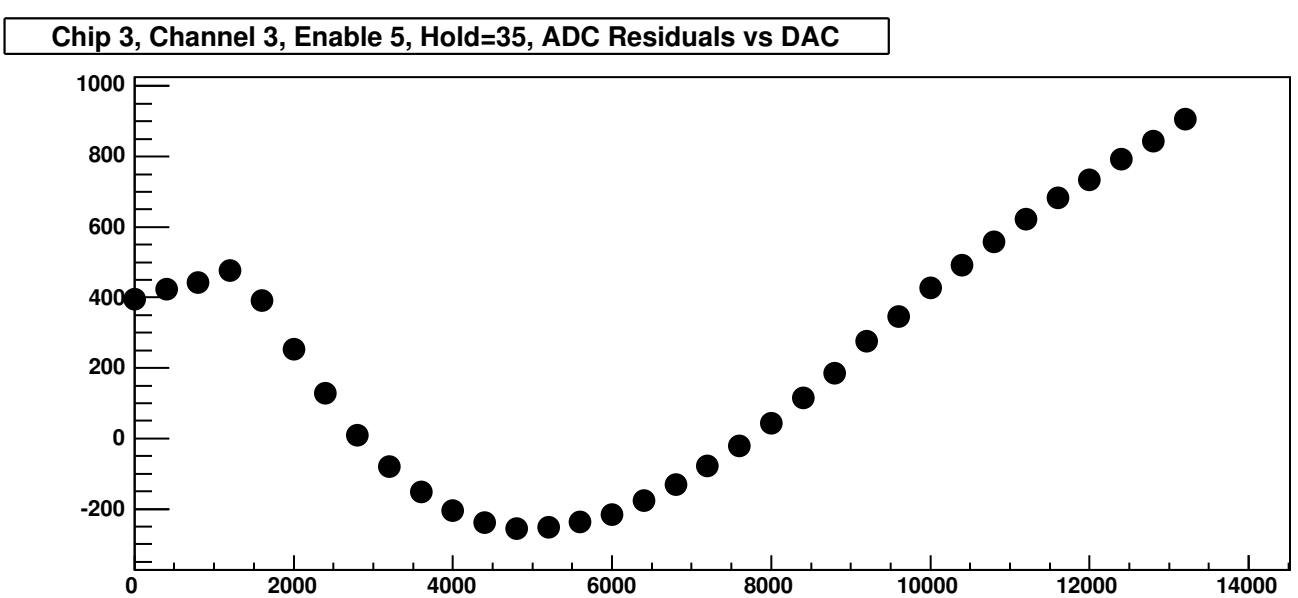

Chip 3, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ^2 / ndf 198.4 / 23
p0 -463.7 ± 3.402
p1 2.164 ± 0.00219
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

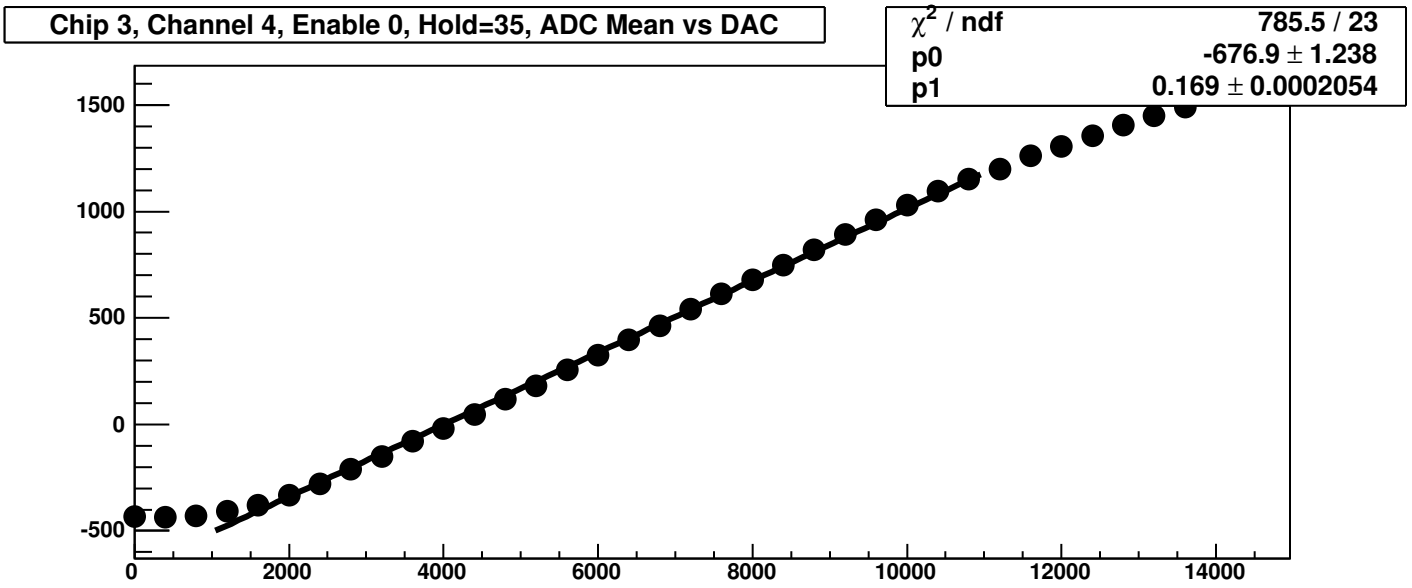

Chip 3, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

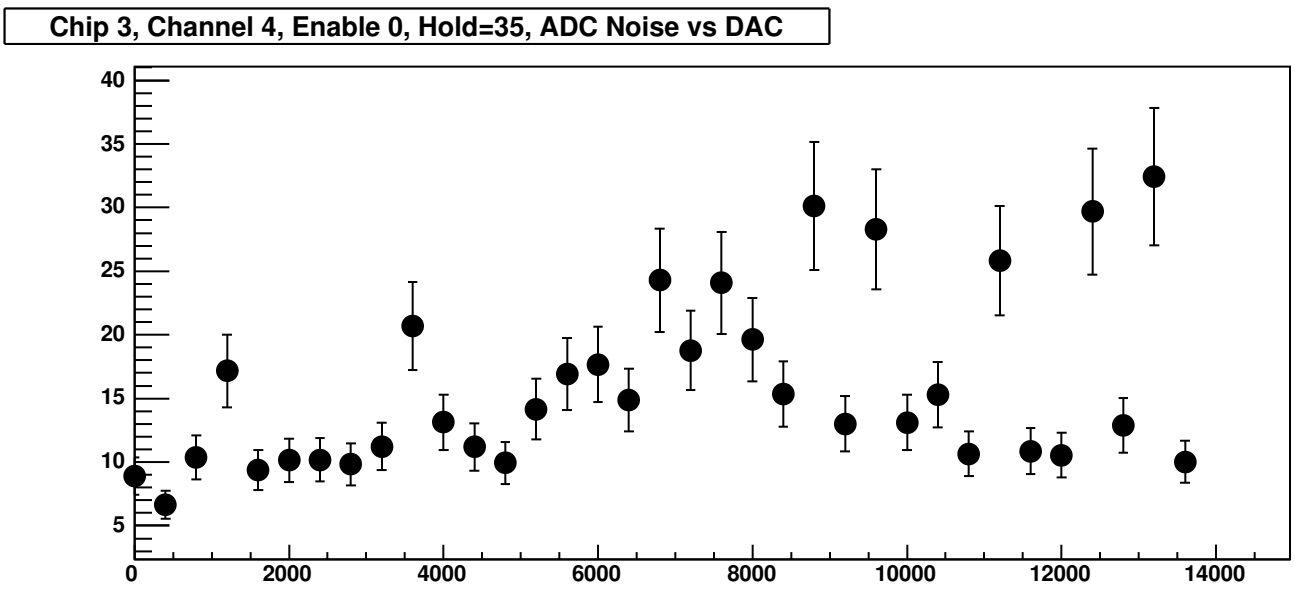

Chip 3, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

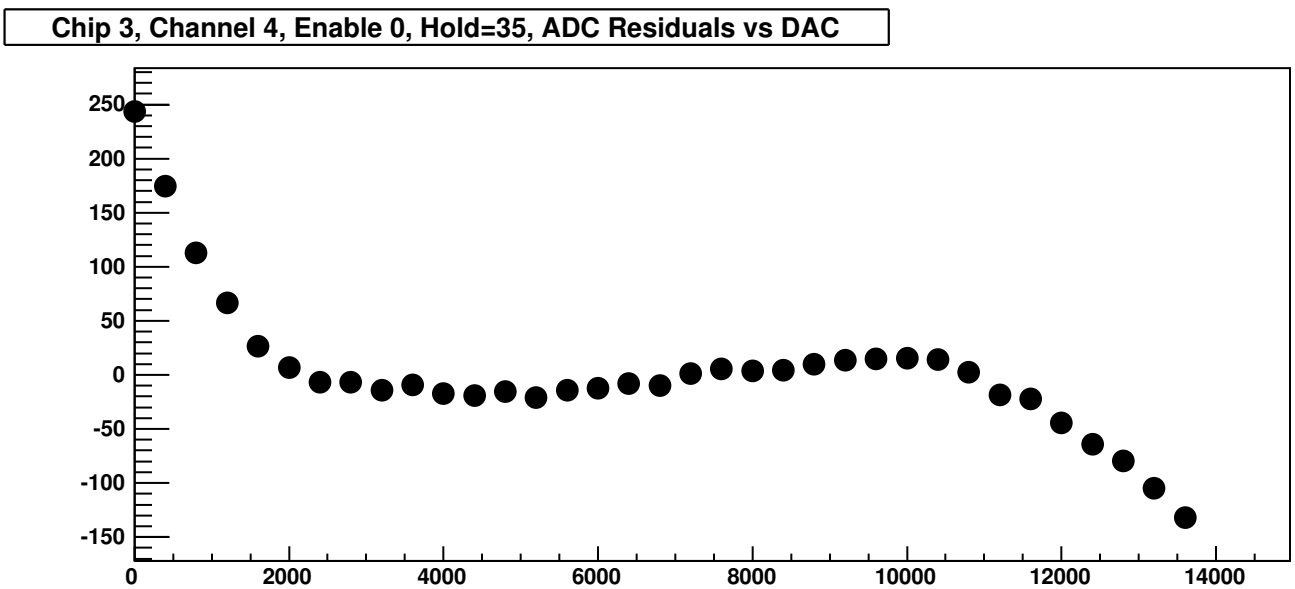

Chip 3, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.575e+05 / 23
p0 -894.4 ± 1.023
p1 -0.0485 ± 0.0001717
-400
-600
-800
-1000
-1200
-1400
0
2000
4000
6000
8000
10000
12000
14000

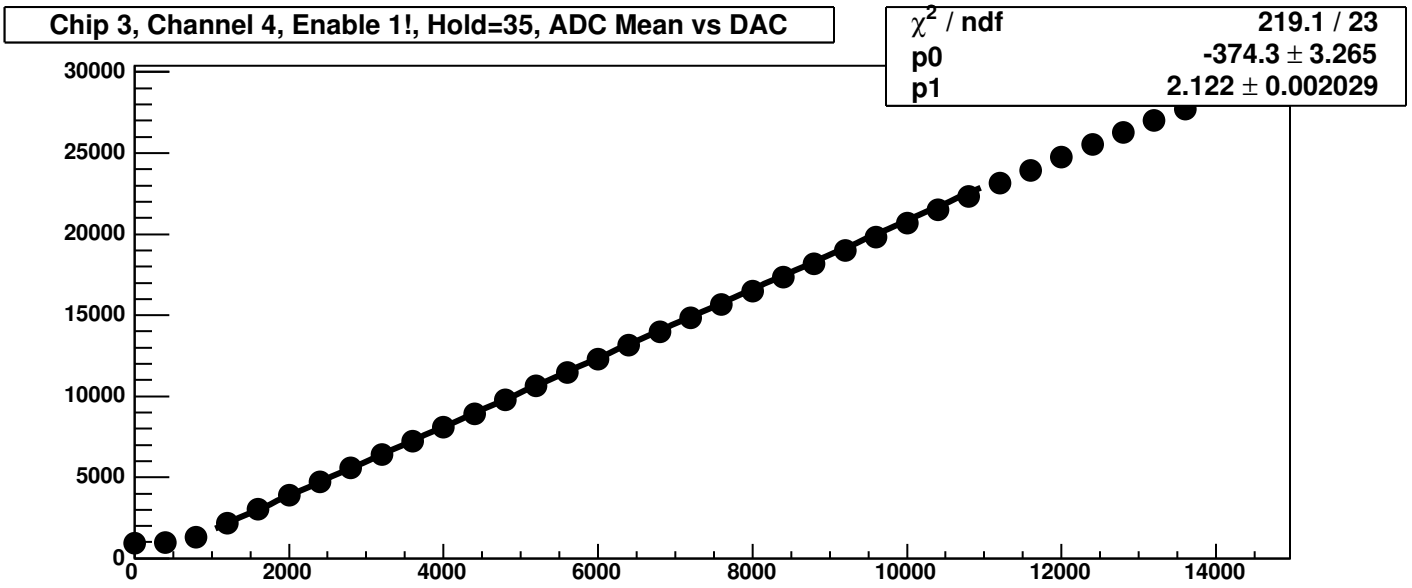

Chip 3, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

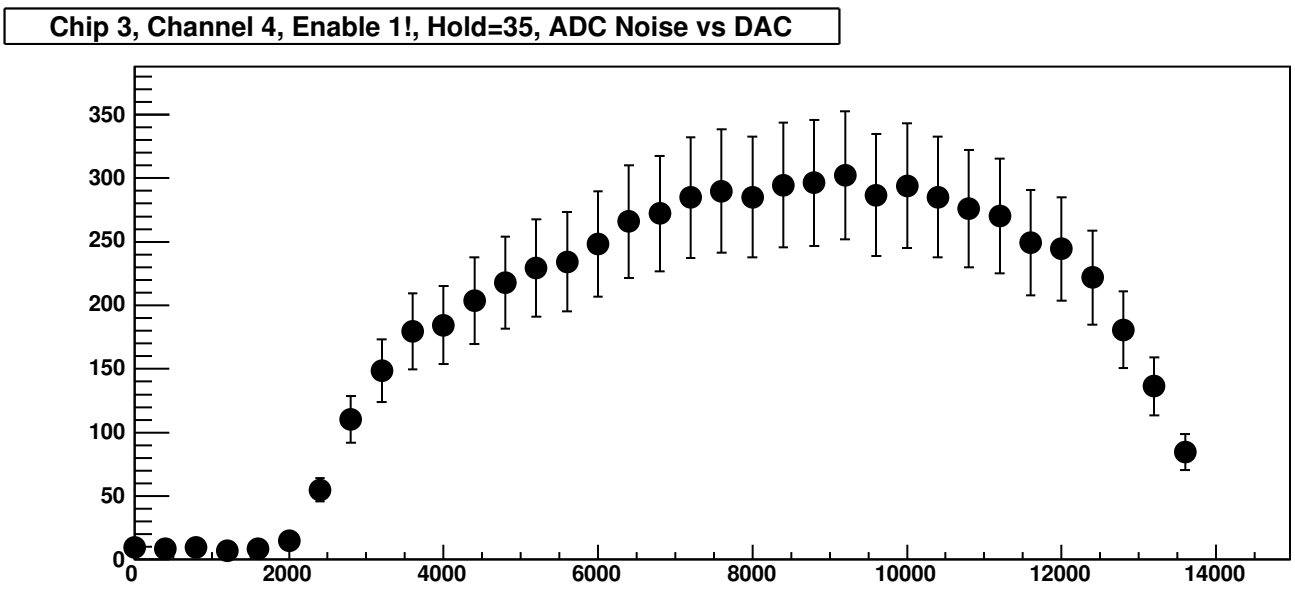

Chip 3, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

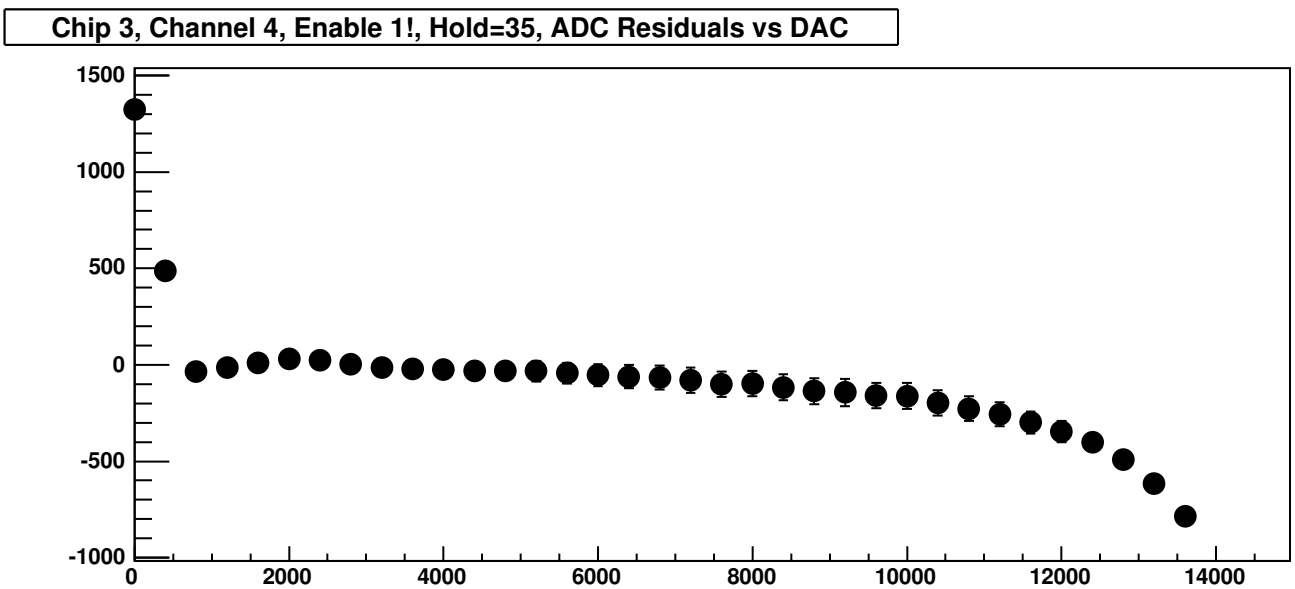

Chip 3, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 785.5 / 23
p0 -676.9 ± 1.238
p1 0.169 ± 0.0002054
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

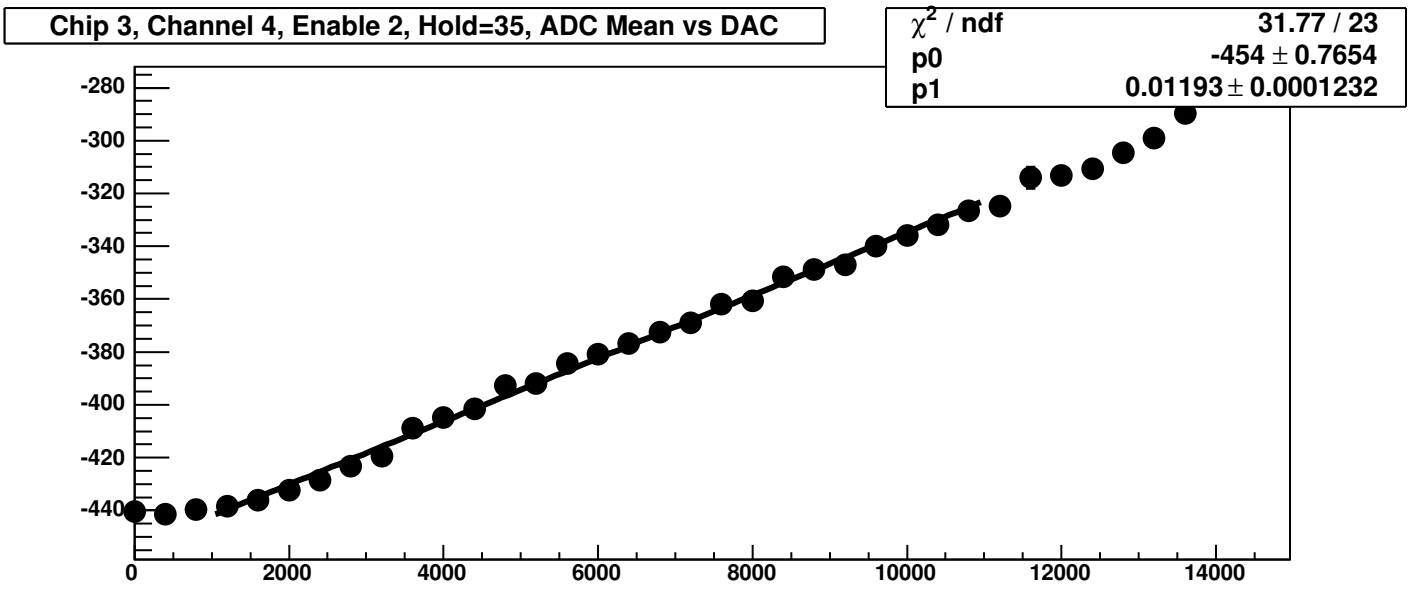

Chip 3, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

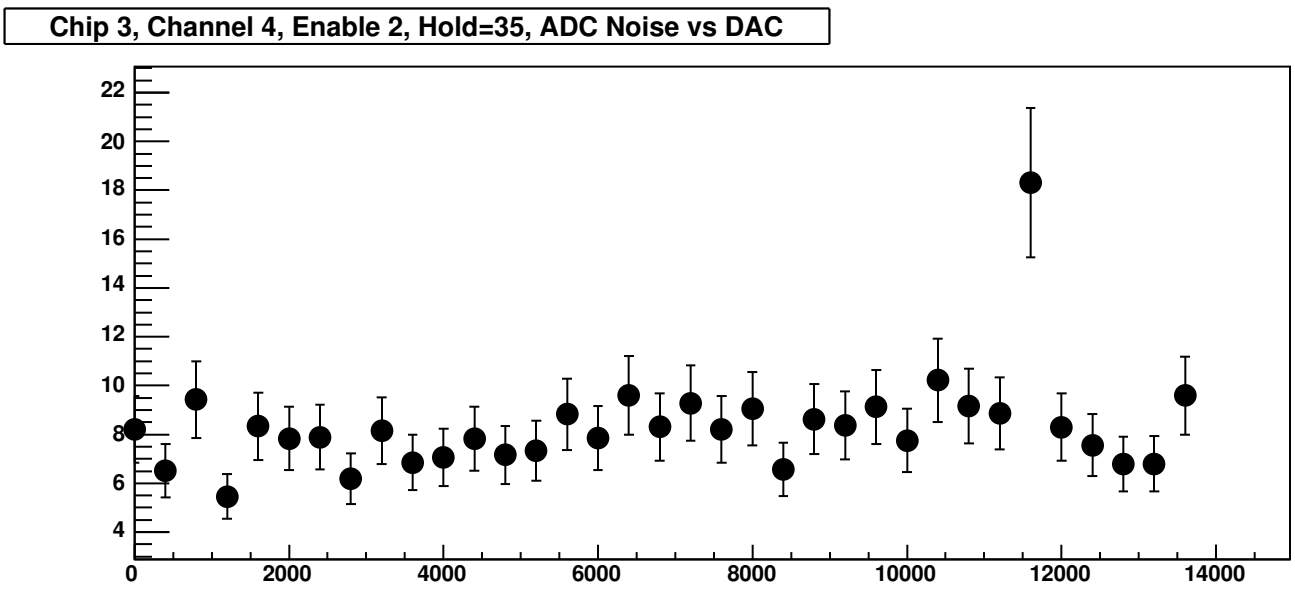

Chip 3, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

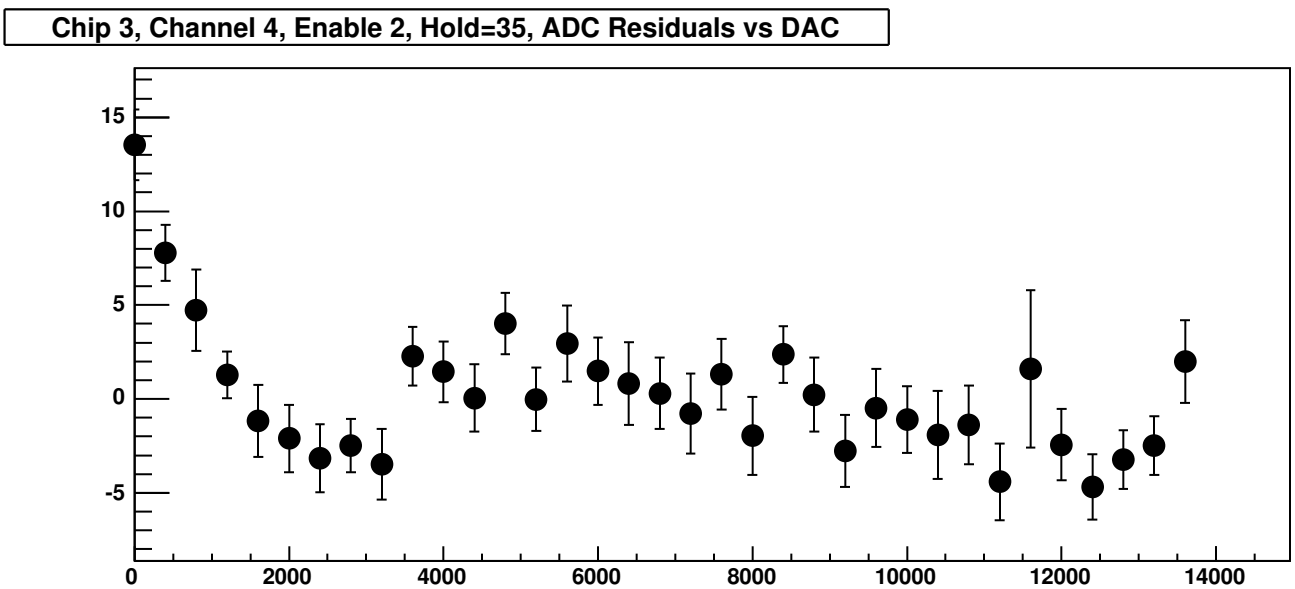

Chip 3, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 219.1 / 23
p0 -374.3 ± 3.265
p1 2.122 ± 0.002029
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

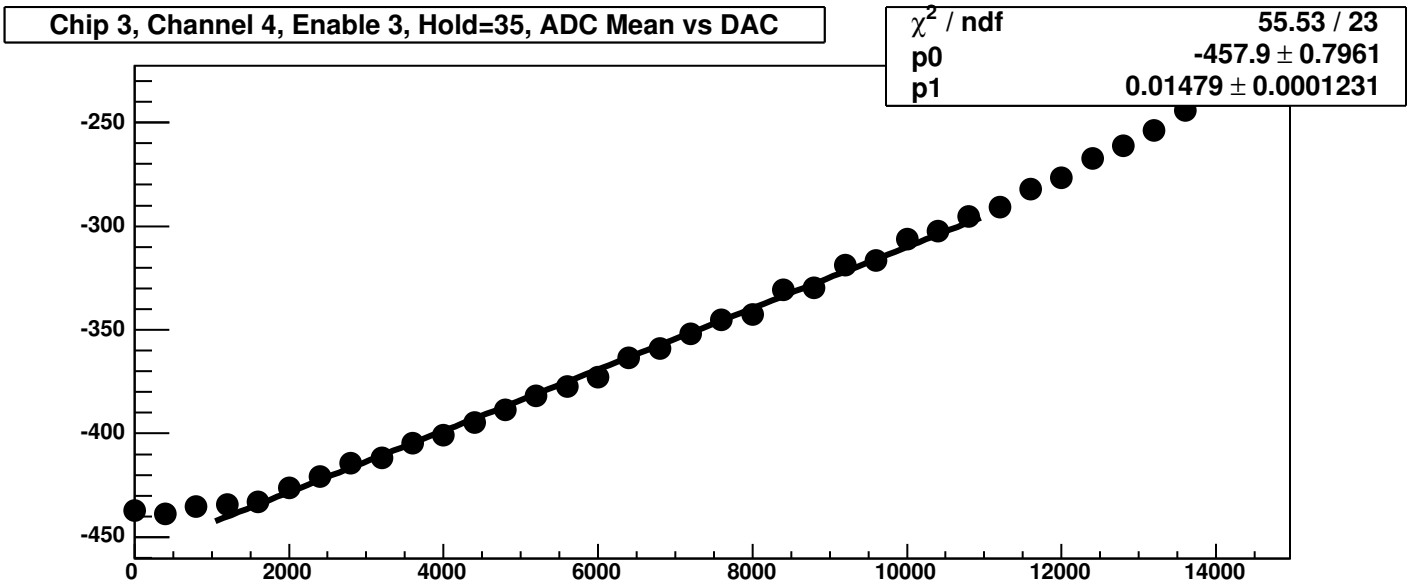

Chip 3, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

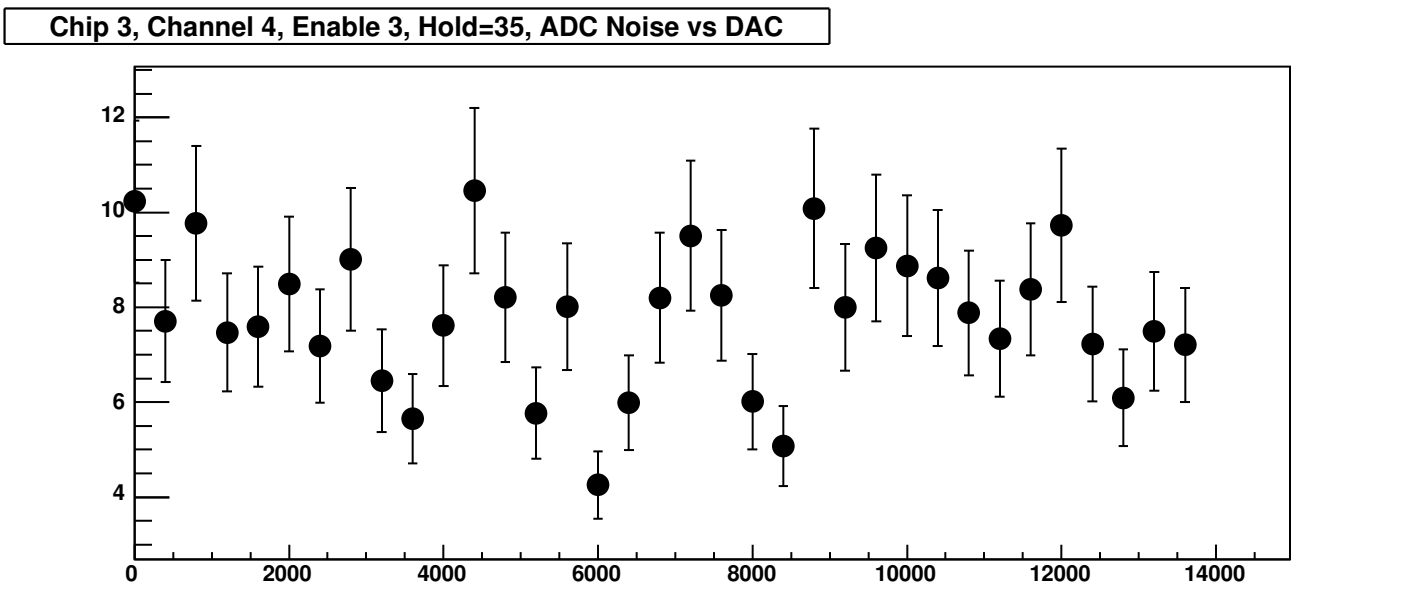

Chip 3, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

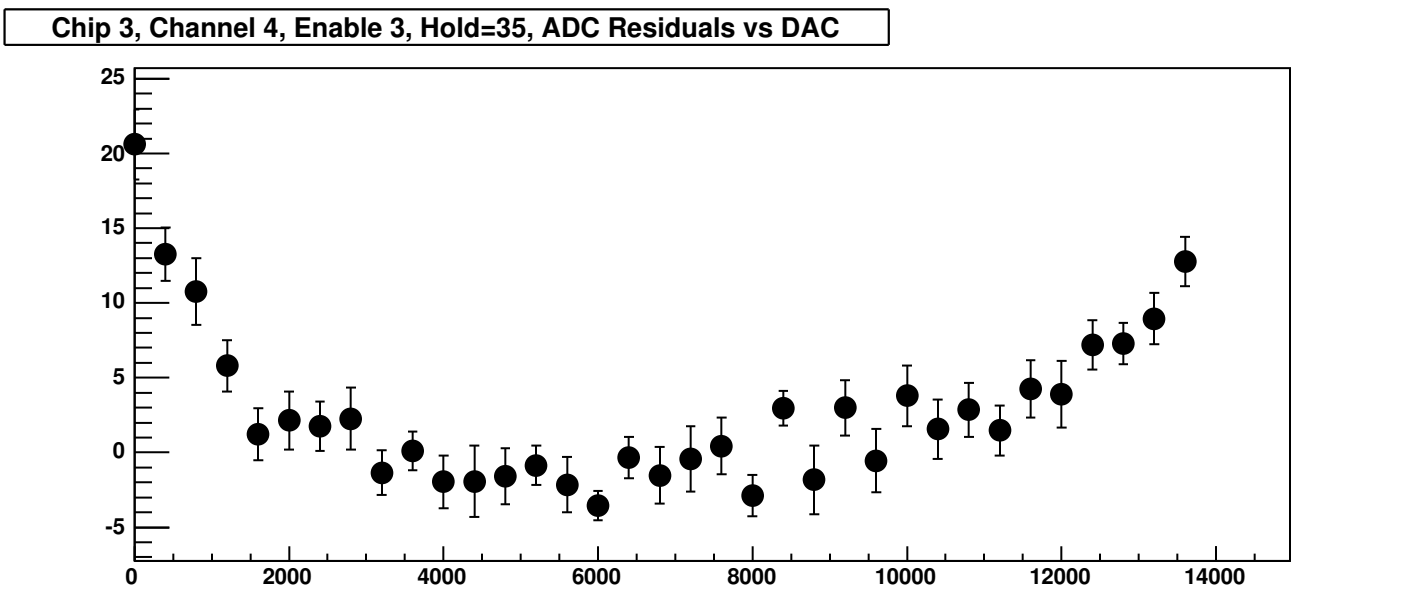

Chip 3, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 31.77 / 23
p0 -454 ± 0.7654
p1 0.01193 ± 0.0001232
-280
-300
-320
-340
-360
-380
-400
-420
-440
0
2000
4000
6000
8000
10000
12000
14000

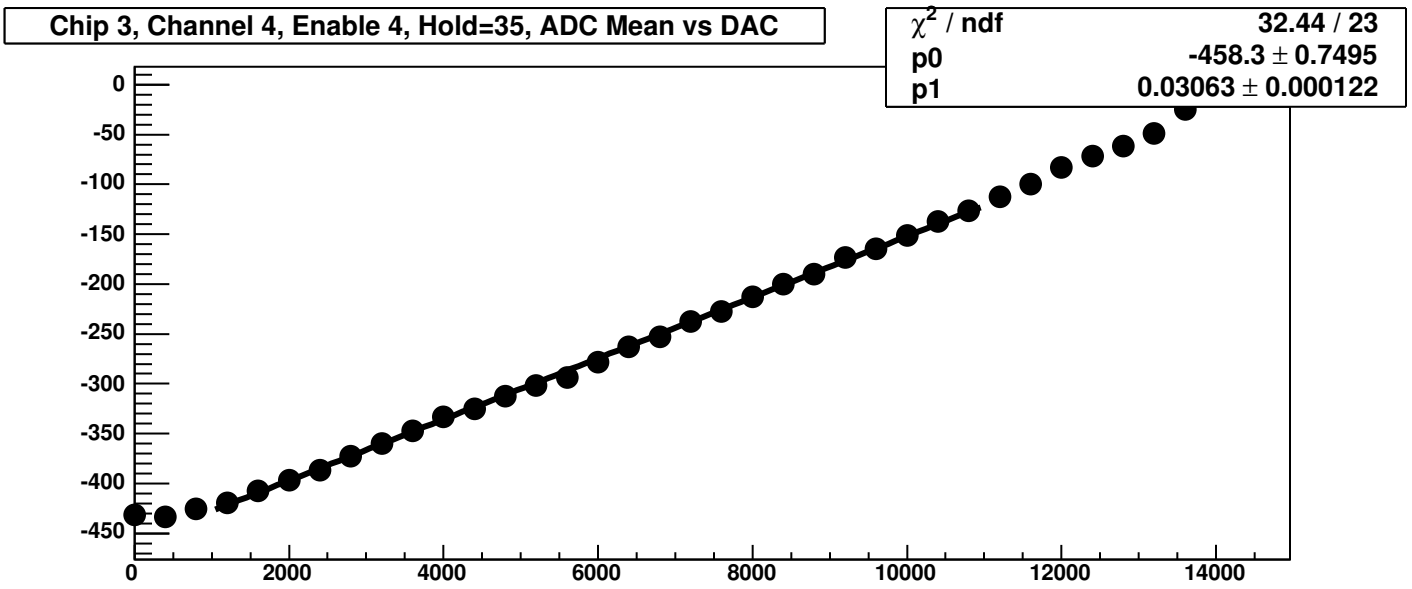

Chip 3, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

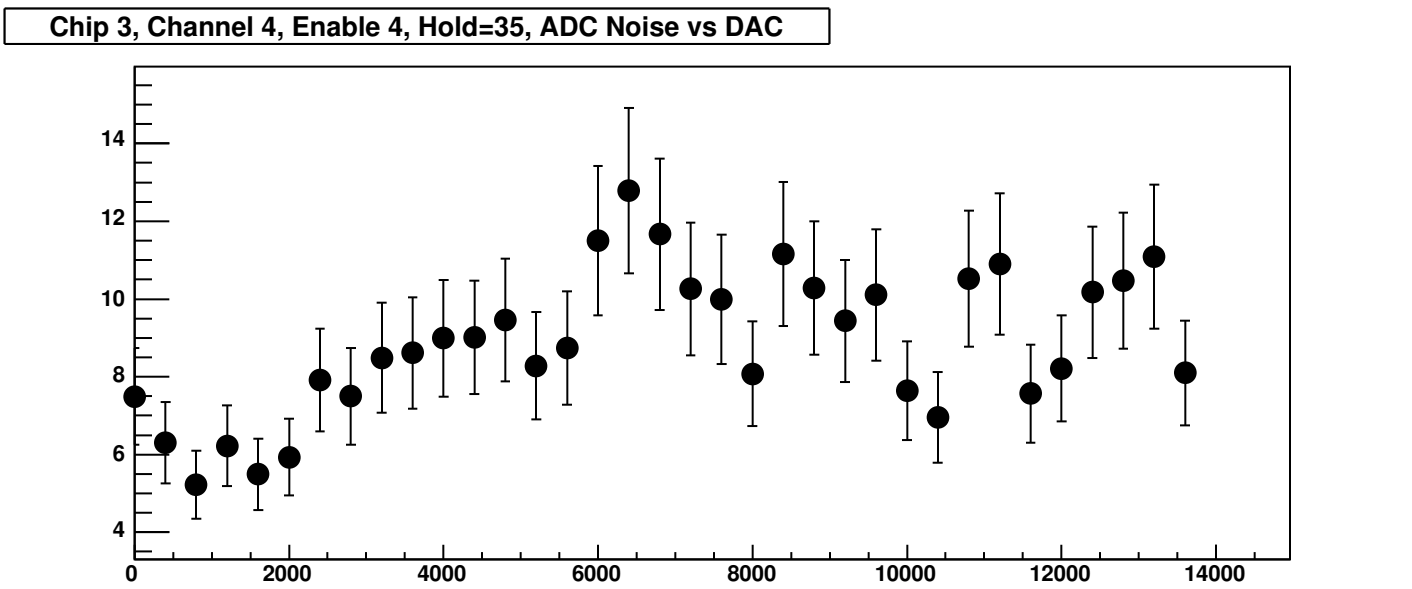

Chip 3, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

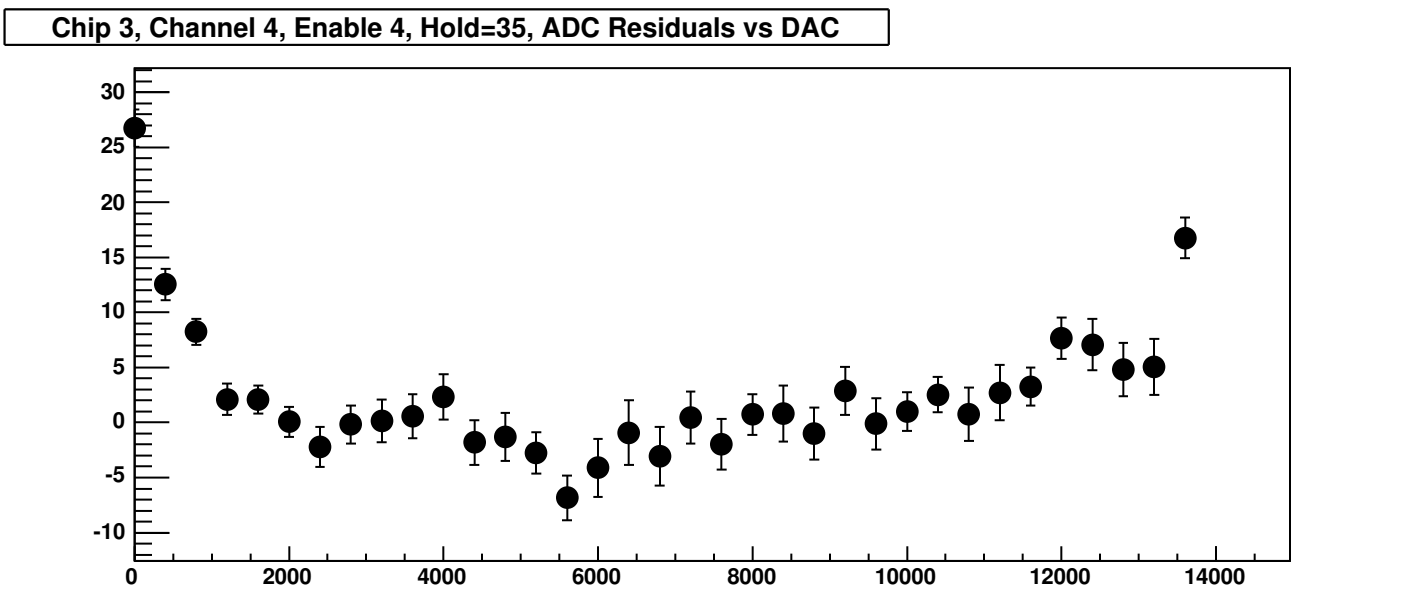

Chip 3, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 55.53 / 23
p0 -457.9 ± 0.7961
p1 0.01479 ± 0.0001231
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

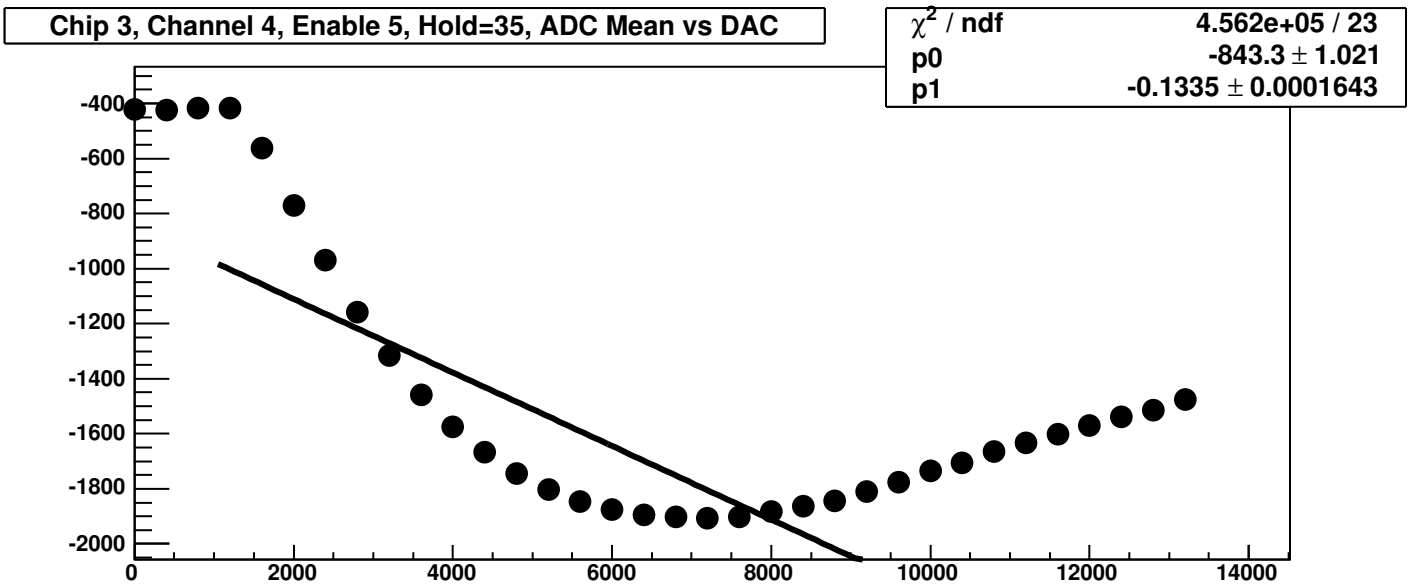

Chip 3, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

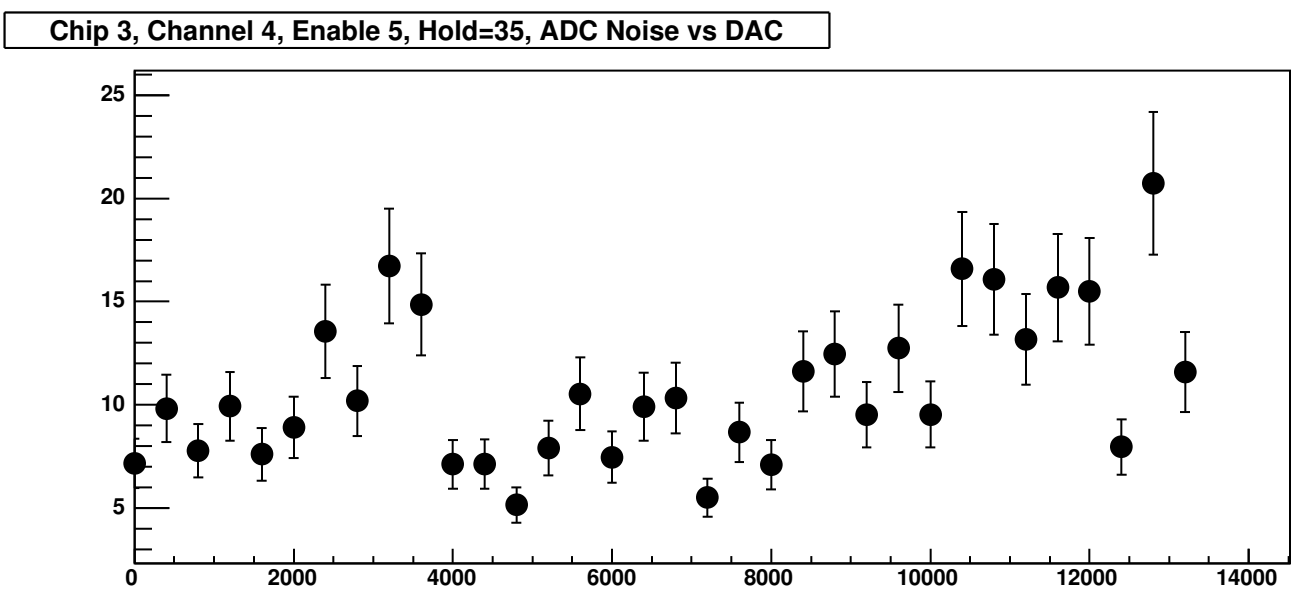

Chip 3, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

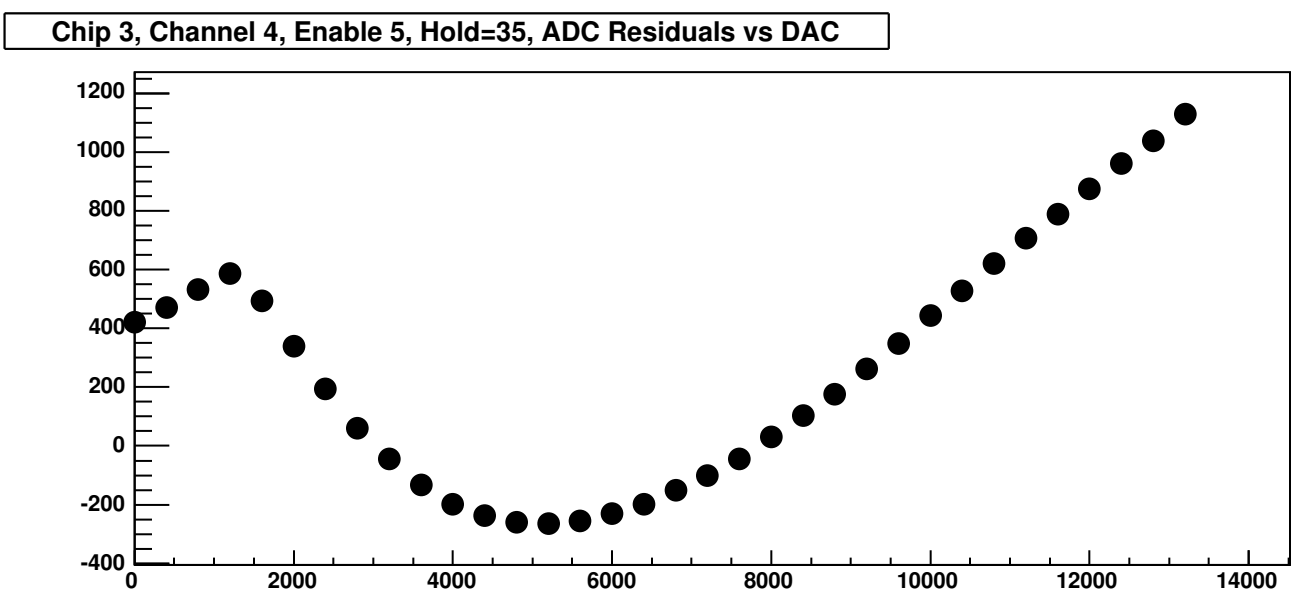

Chip 3, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 32.44 / 23
p0 -458.3 ± 0.7495
p1 0.03063 ± 0.000122
0
-50
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

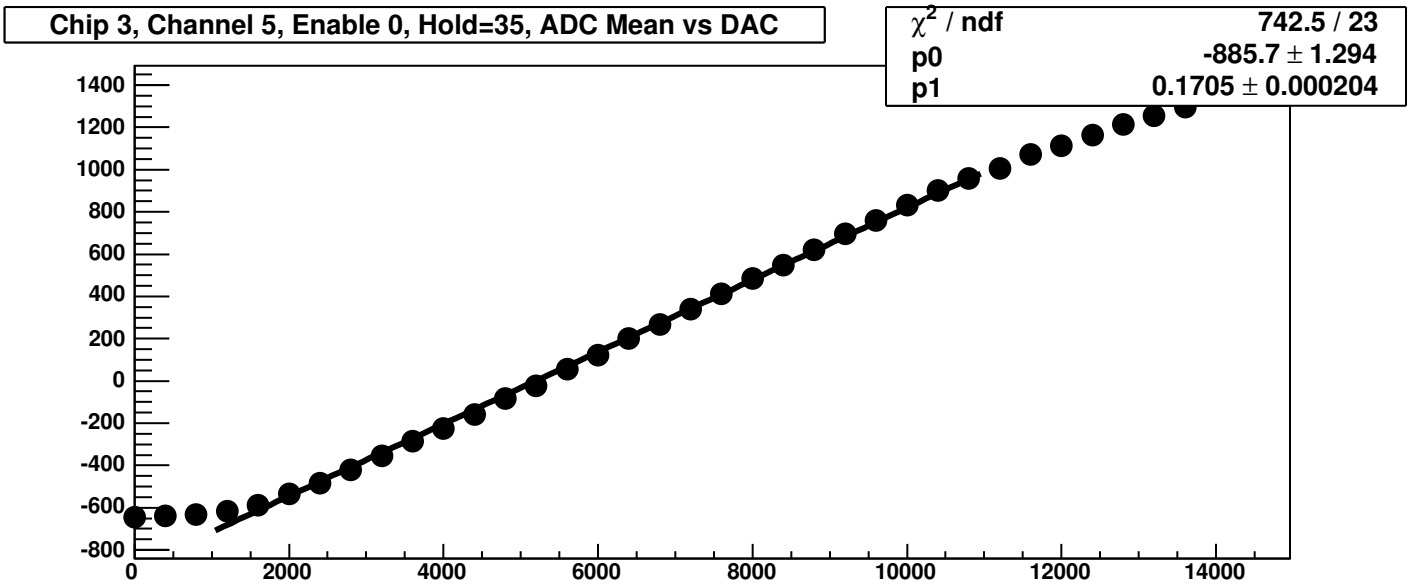

Chip 3, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

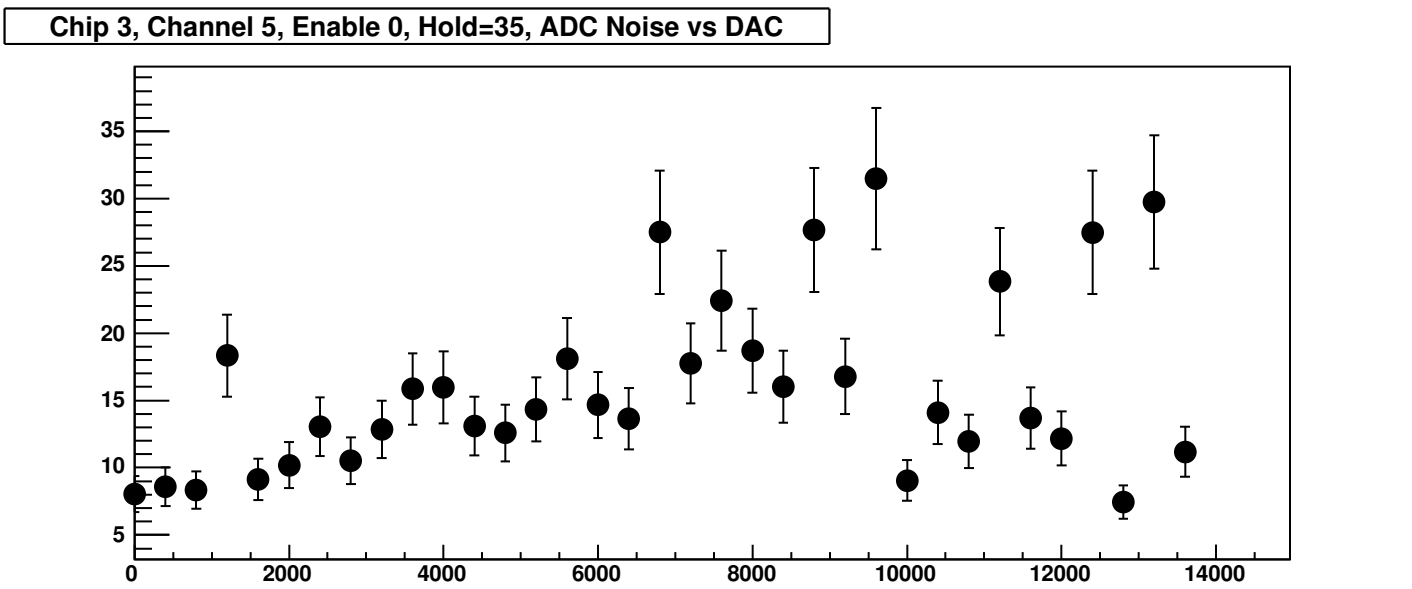

Chip 3, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

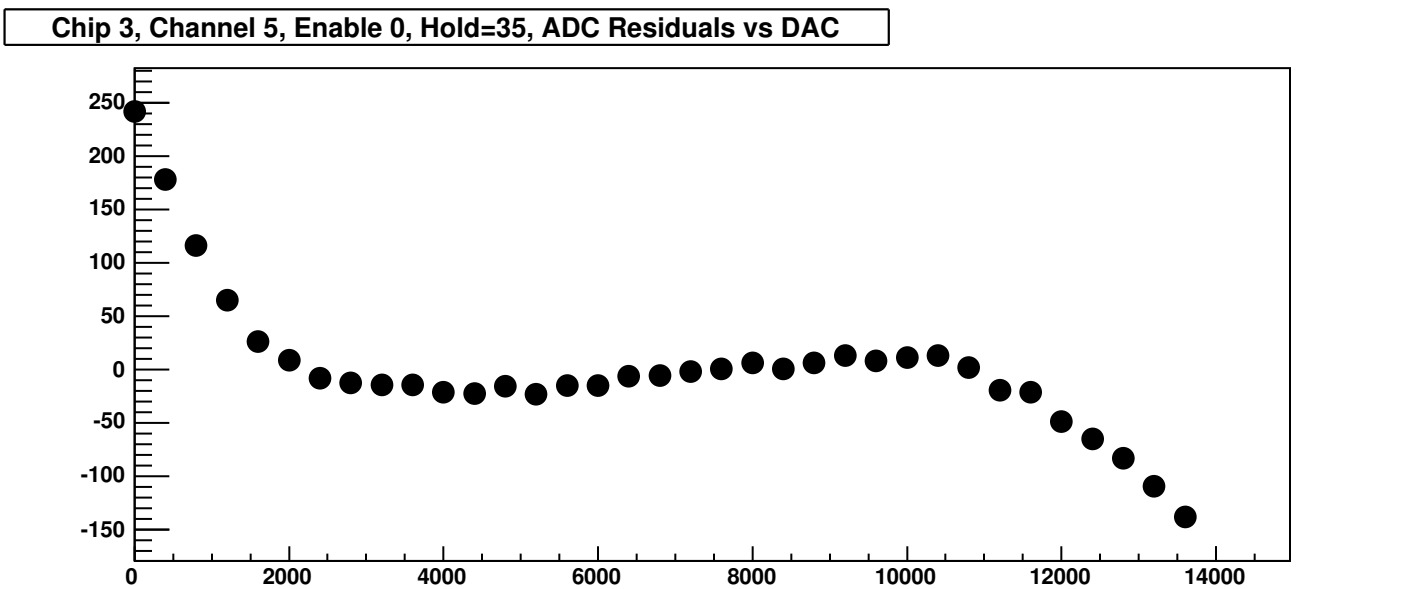

Chip 3, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4.562e+05 / 23
p0 -843.3 ± 1.021
p1 -0.1335 ± 0.0001643
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
-2000
0
2000
4000
6000
8000
10000
12000
14000

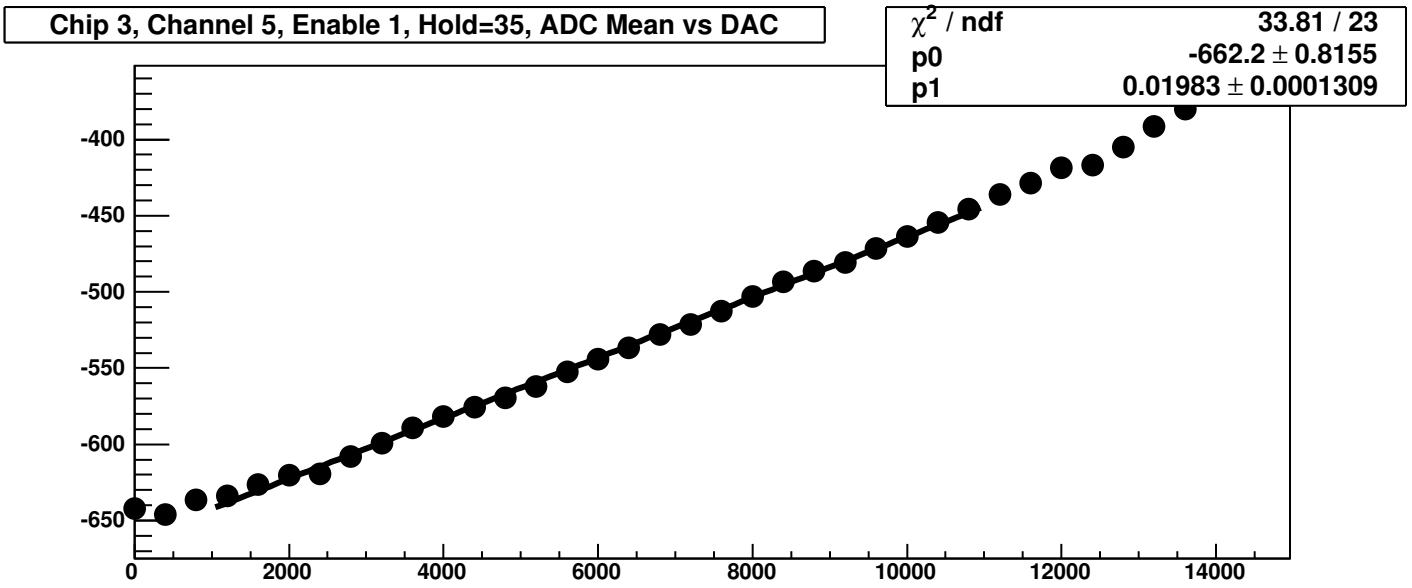

Chip 3, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

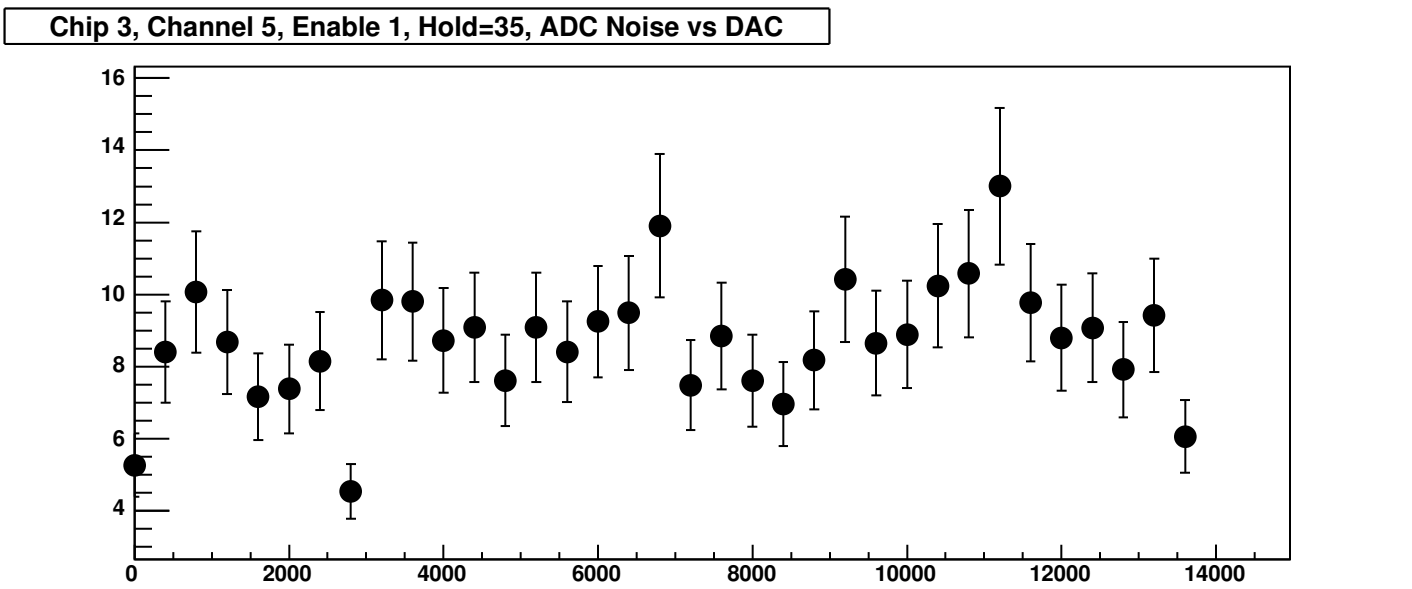

Chip 3, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

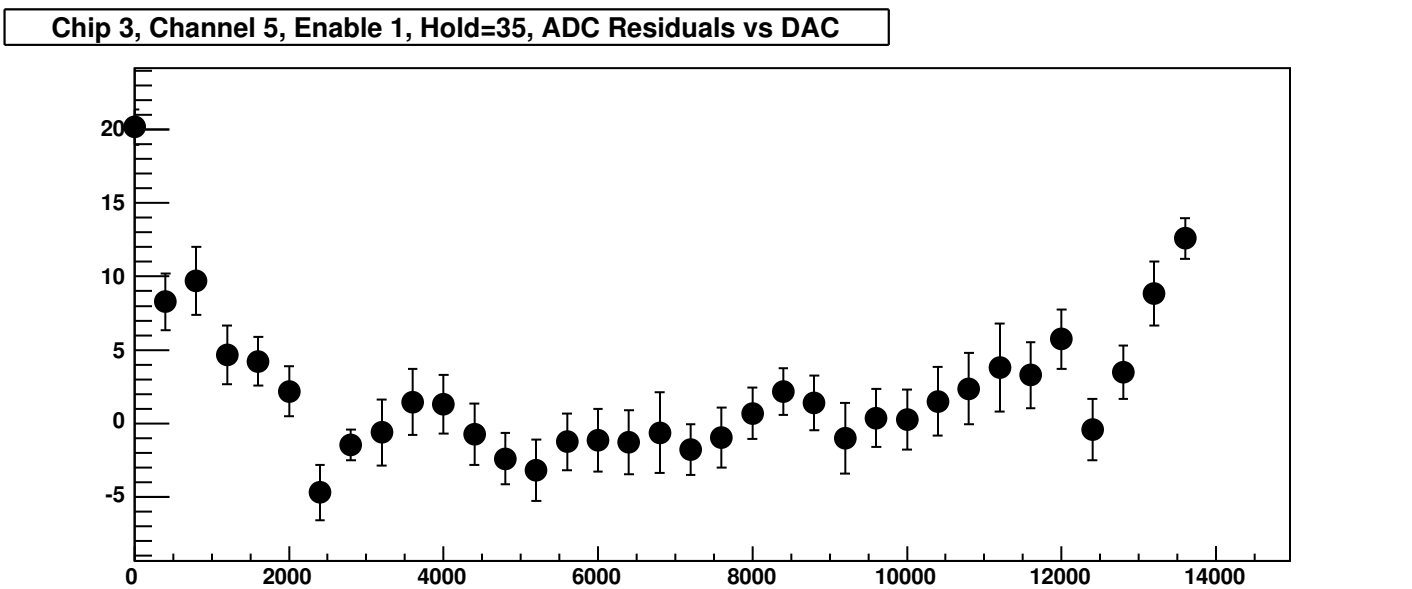

Chip 3, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 742.5 / 23
p0 -885.7 ± 1.294
p1 0.1705 ± 0.000204
1400
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

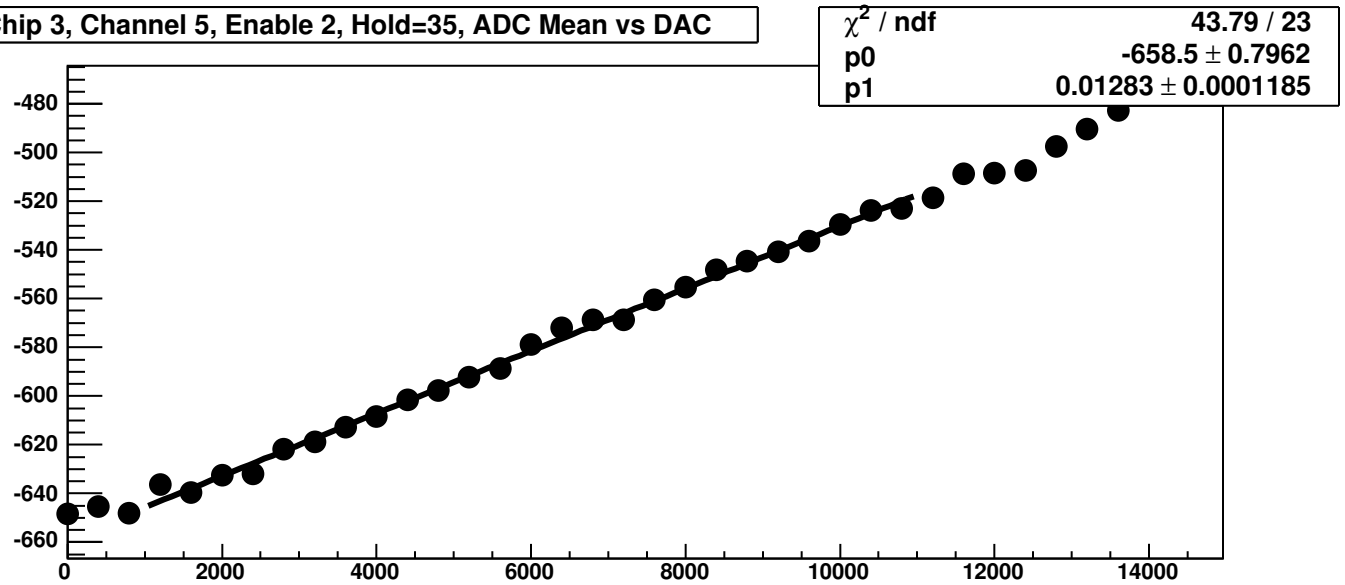

Chip 3, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.81 / 23
p0 -662.2 ± 0.8155
p1 0.01983 ± 0.0001309
-400
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

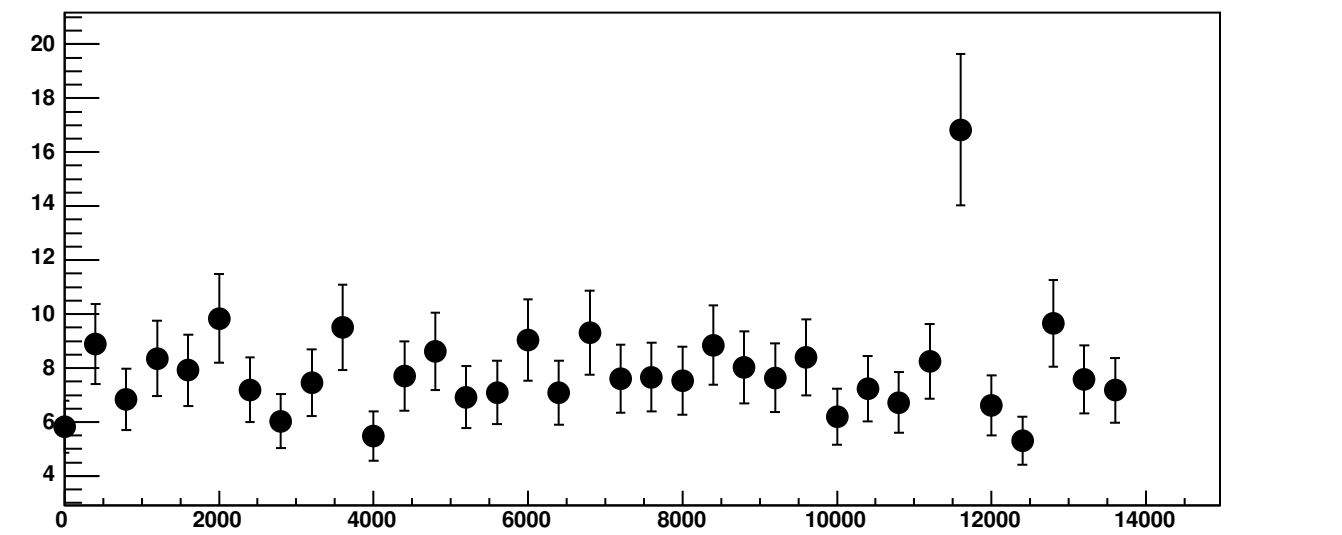

Chip 3, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

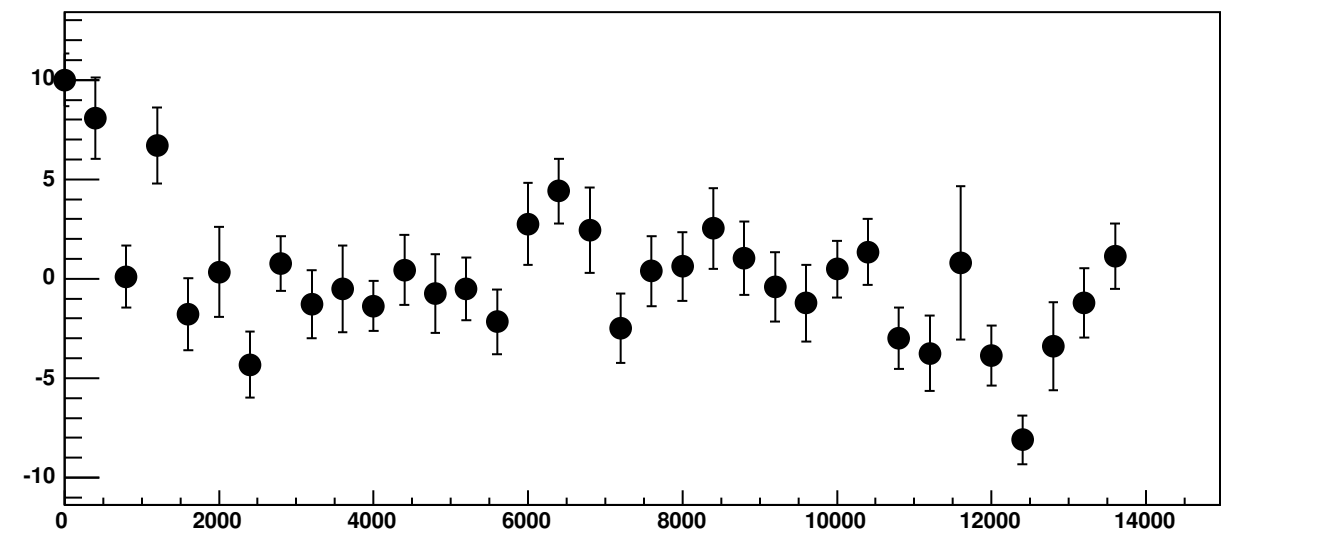
Chip 3, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

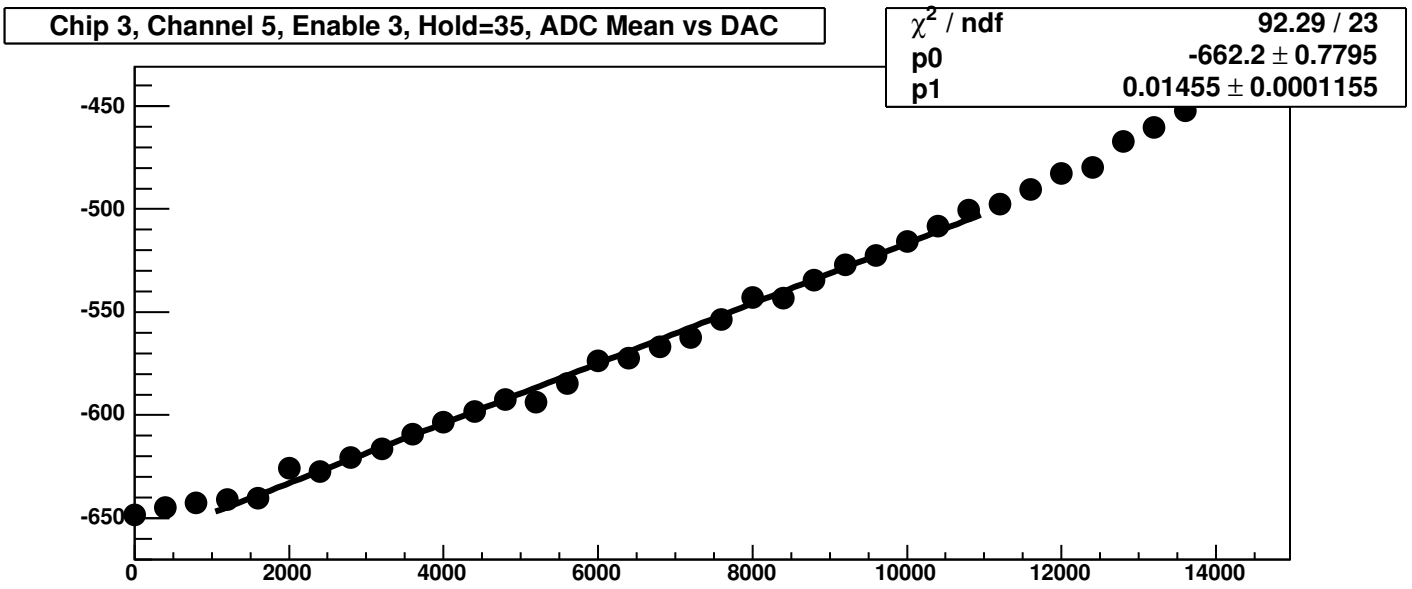


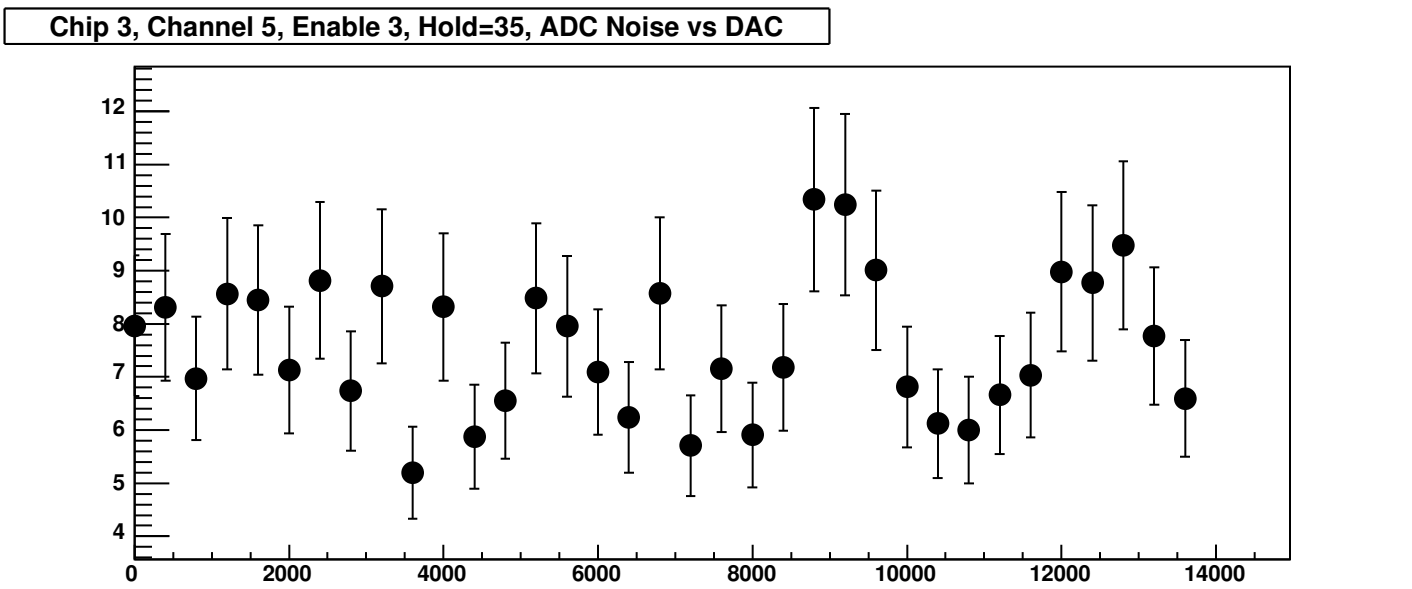
Chip 3, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC

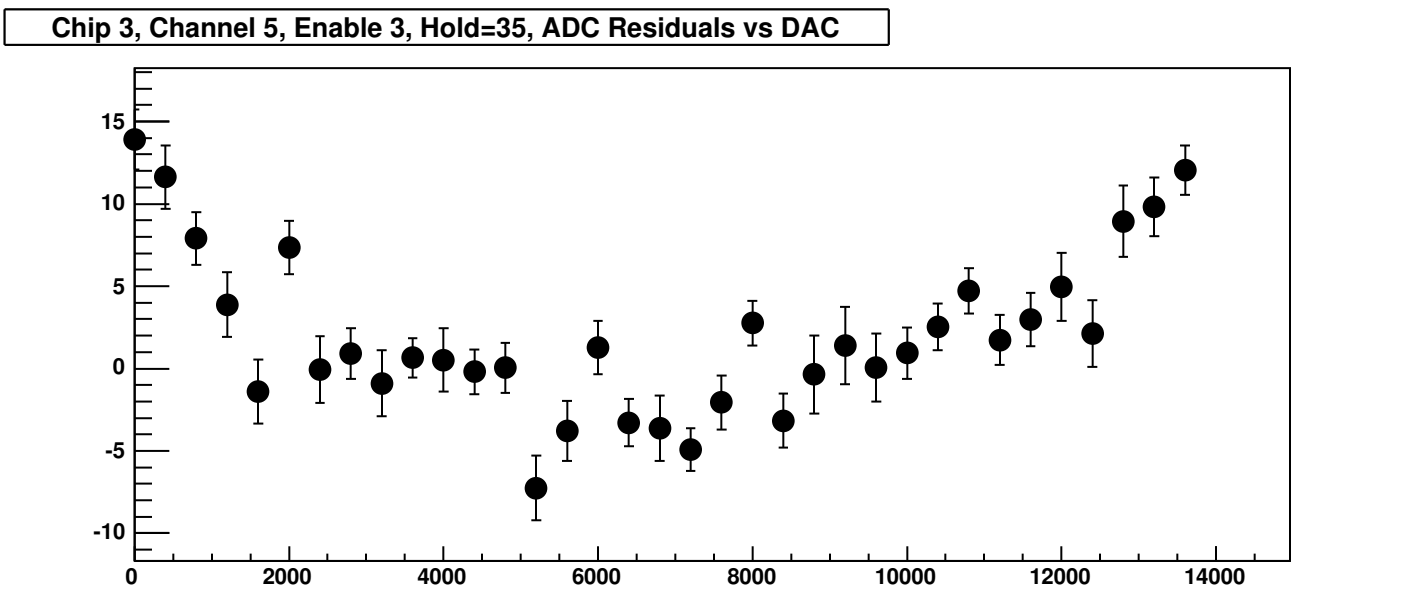


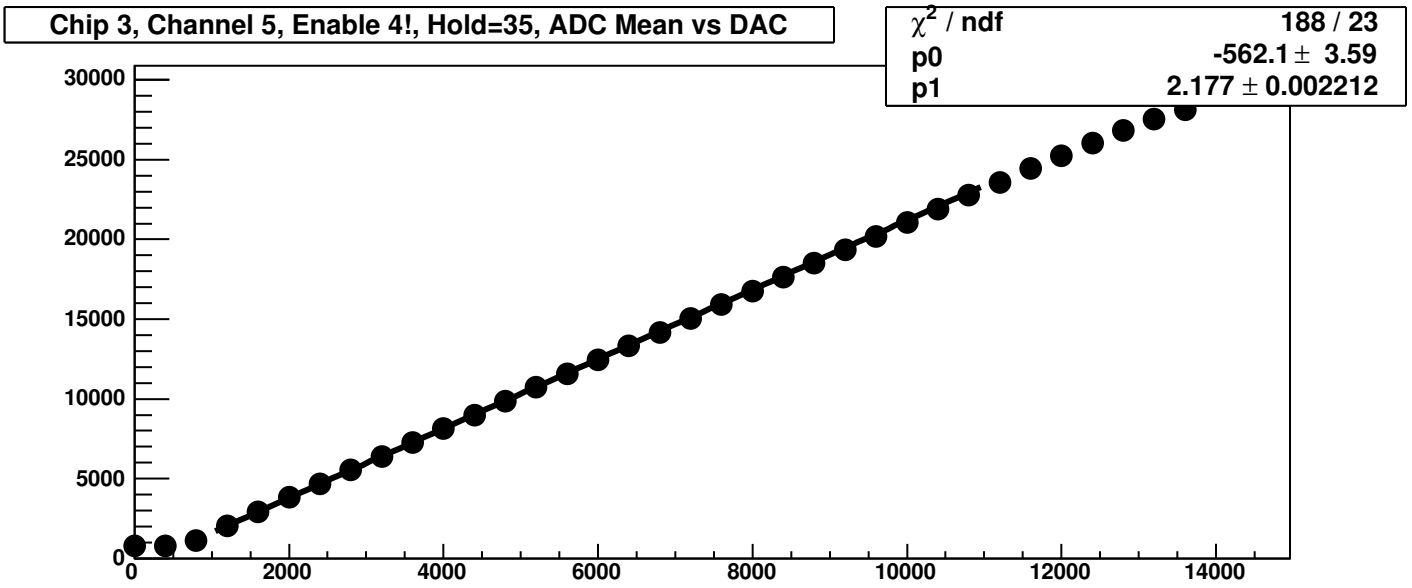
Chip 3, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

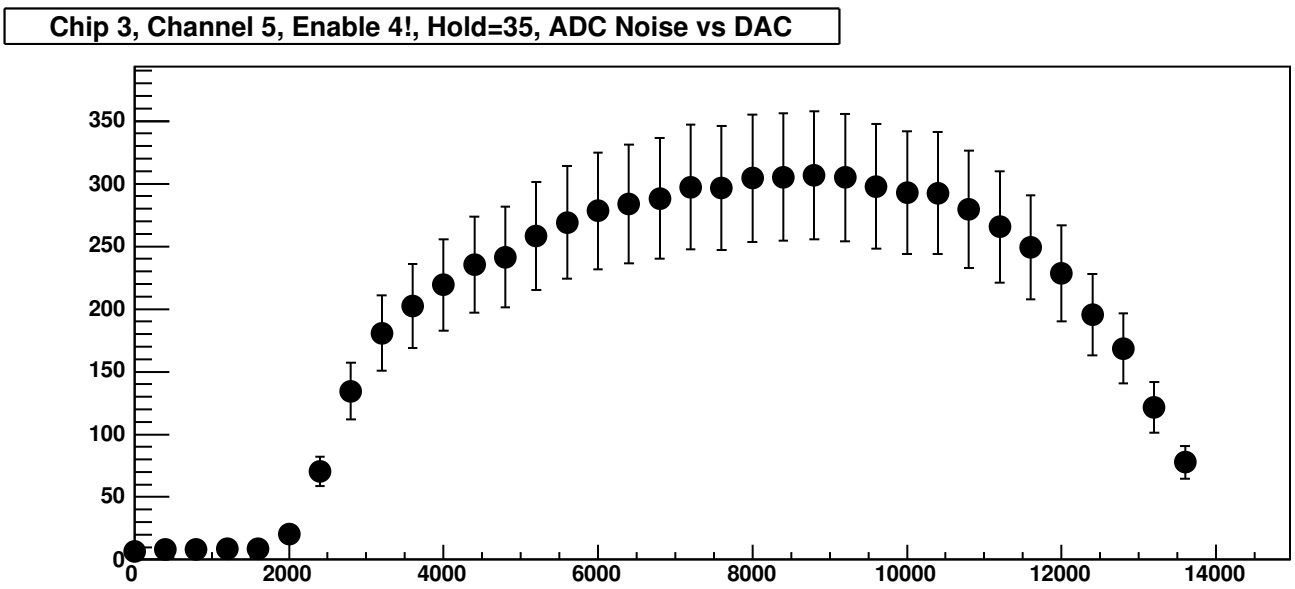


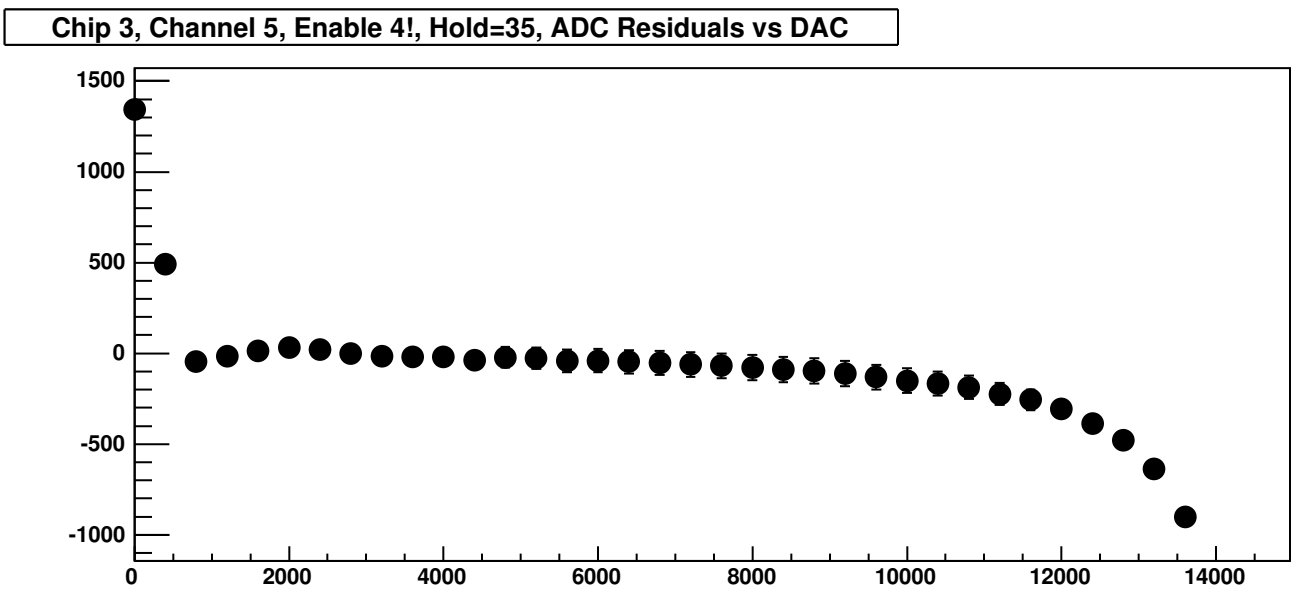

Chip 3, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 92.29 / 23
p0 -662.2 ± 0.7795
p1 0.01455 ± 0.0001155
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

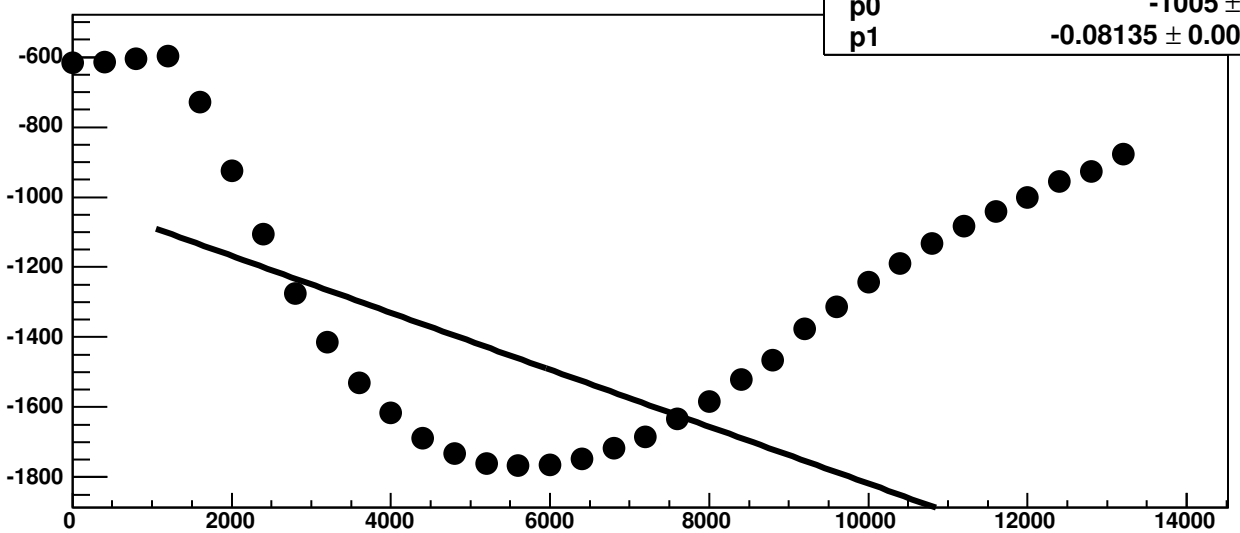

Chip 3, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 188 / 23
p0 -562.1 ± 3.59
p1 2.177 ± 0.002212
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

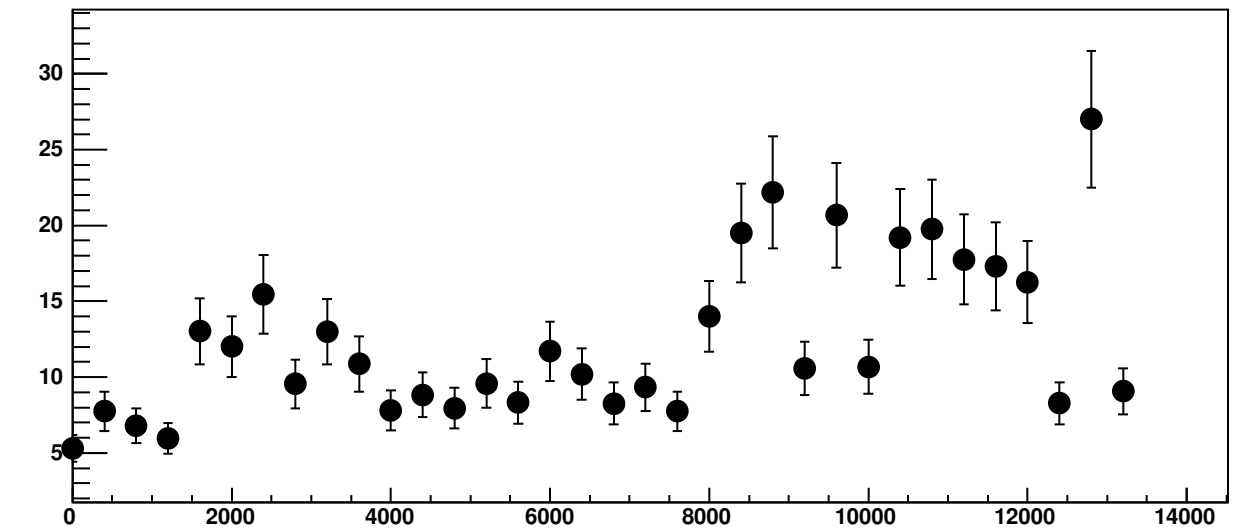

Chip 3, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

Chip 3, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

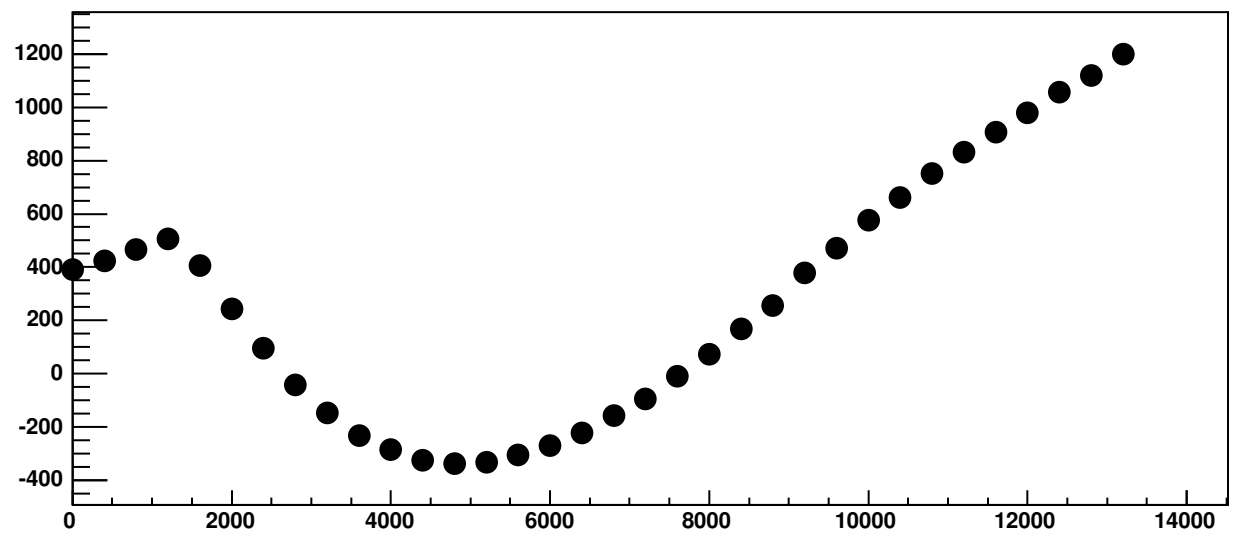
χ^2 / ndf	4.819e+05 / 23
p0	-1005 ± 1.053
p1	-0.08135 ± 0.0001808

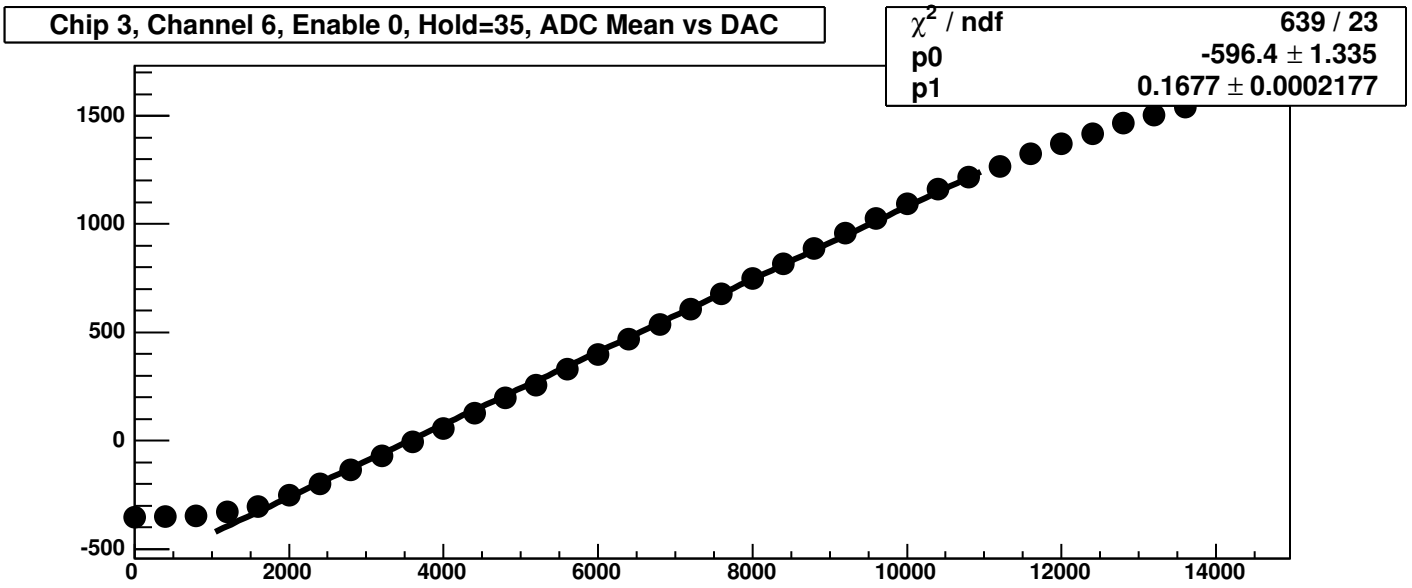


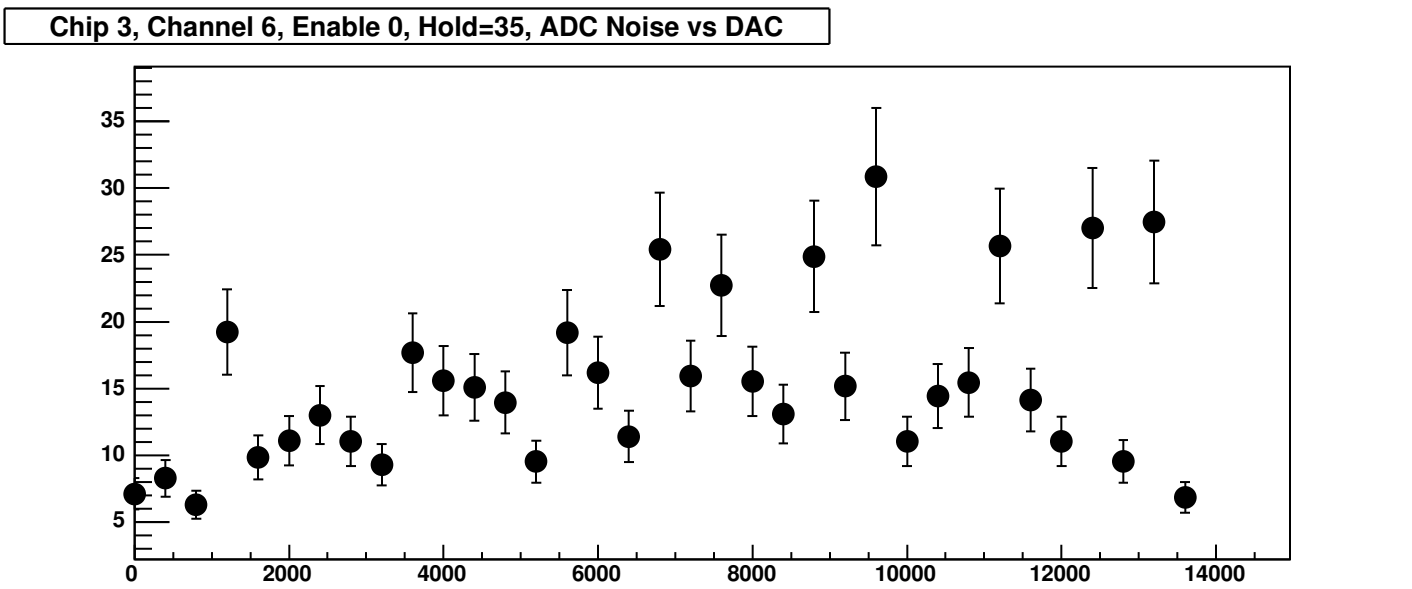
Chip 3, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC

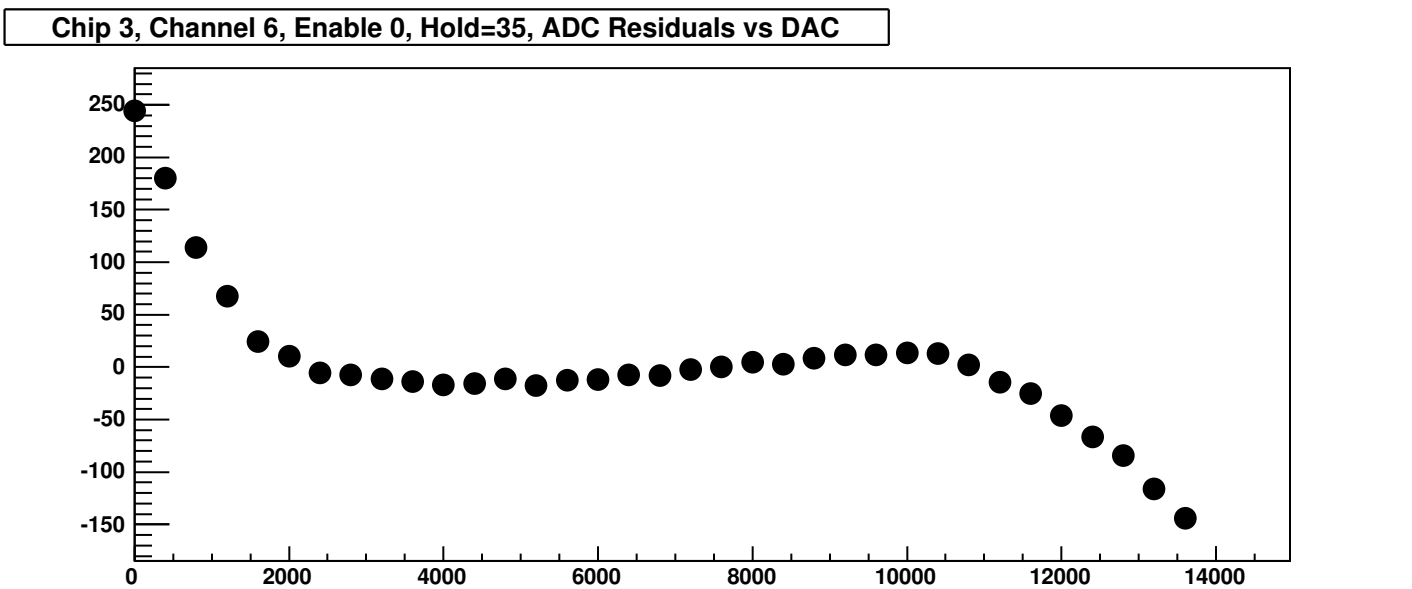


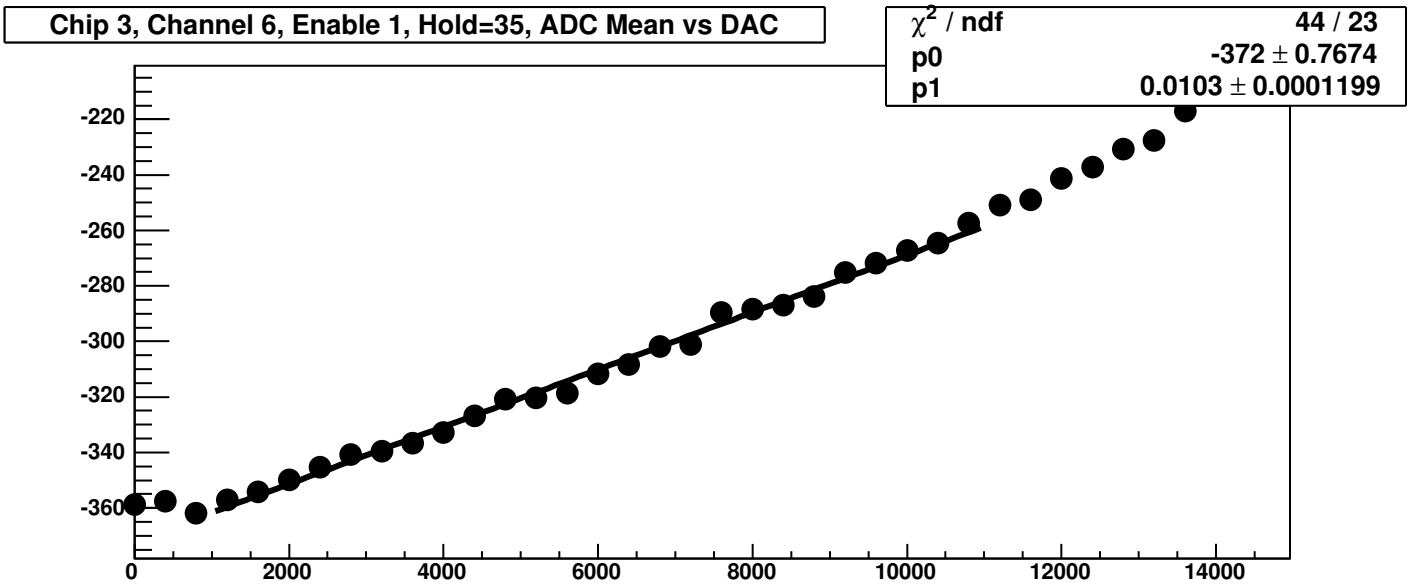
Chip 3, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

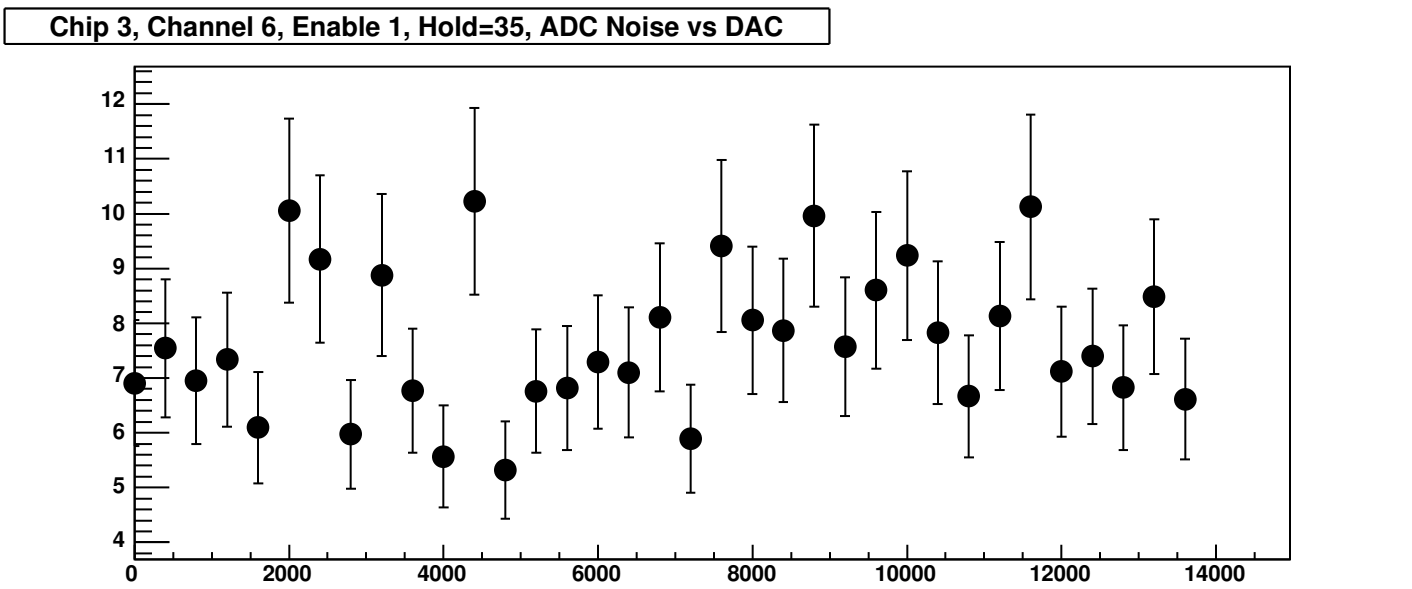


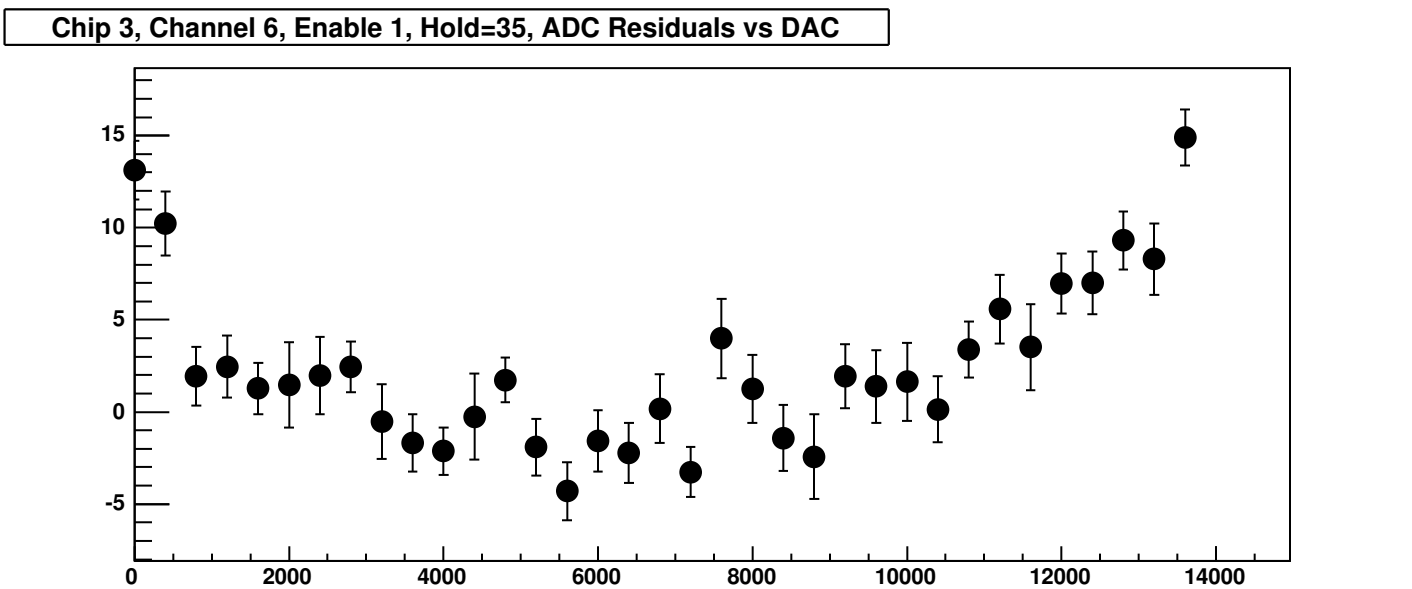

Chip 3, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 639 / 23
p0 -596.4 ± 1.335
p1 0.1677 ± 0.0002177
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

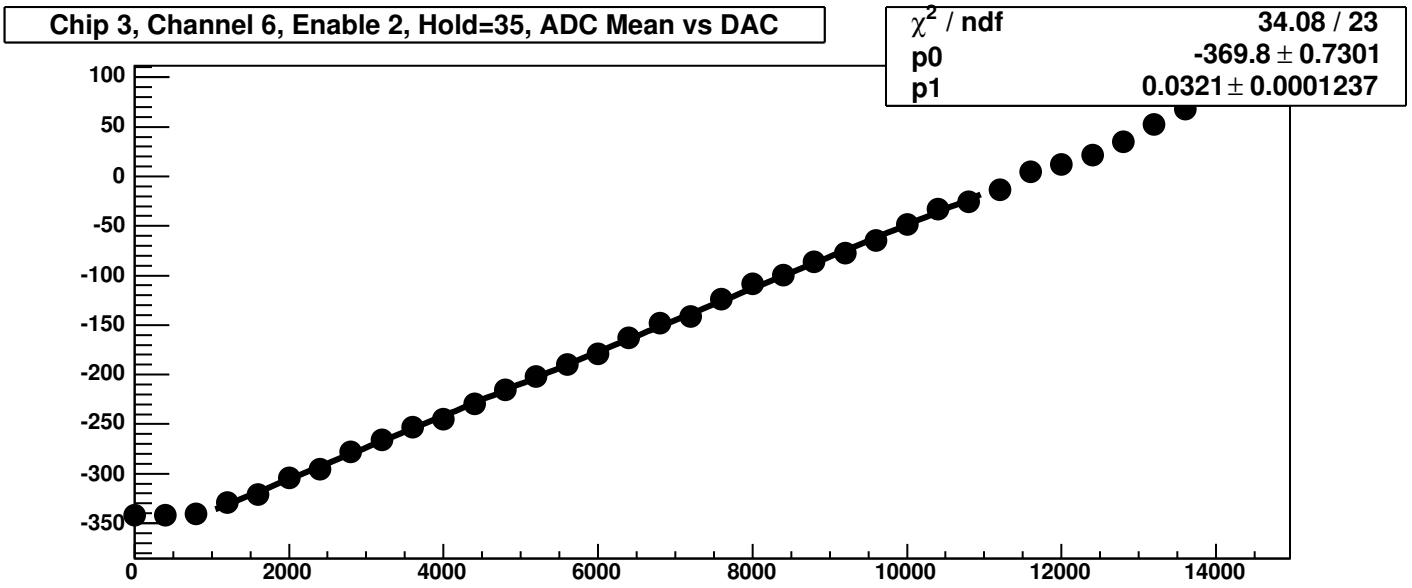

Chip 3, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

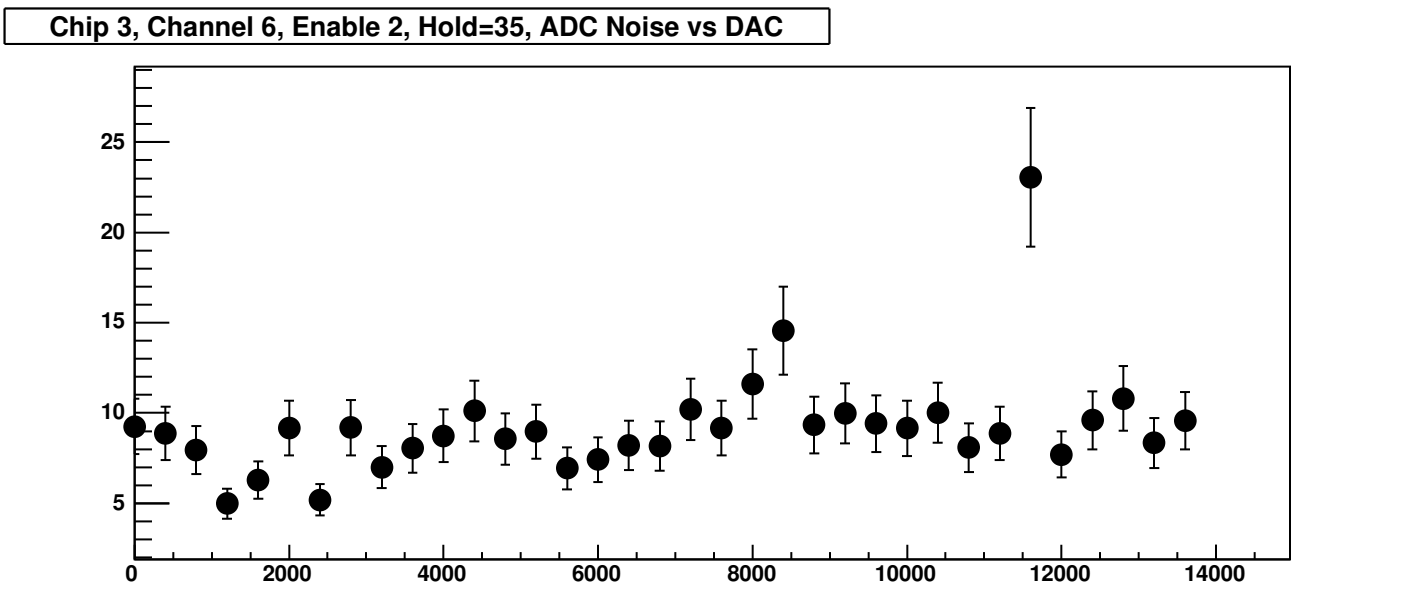

Chip 3, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

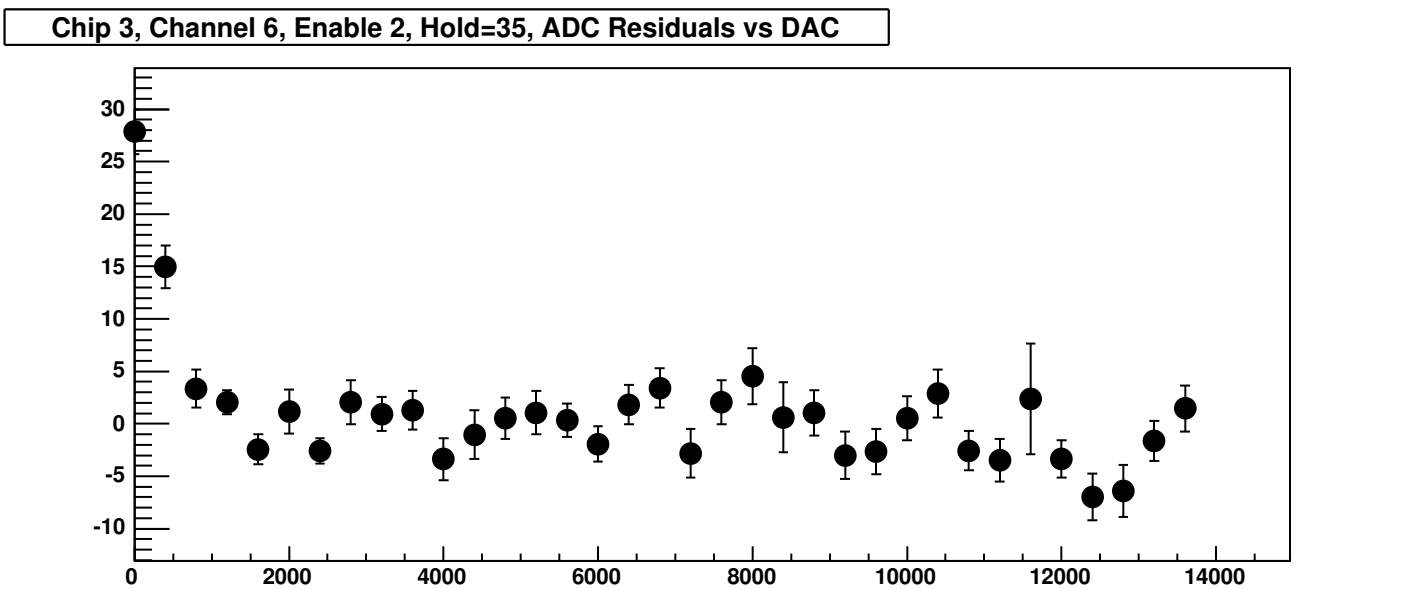

Chip 3, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 44 / 23
p0 -372 ± 0.7674
p1 0.0103 ± 0.0001199
-220
-240
-260
-280
-300
-320
-340
-360
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

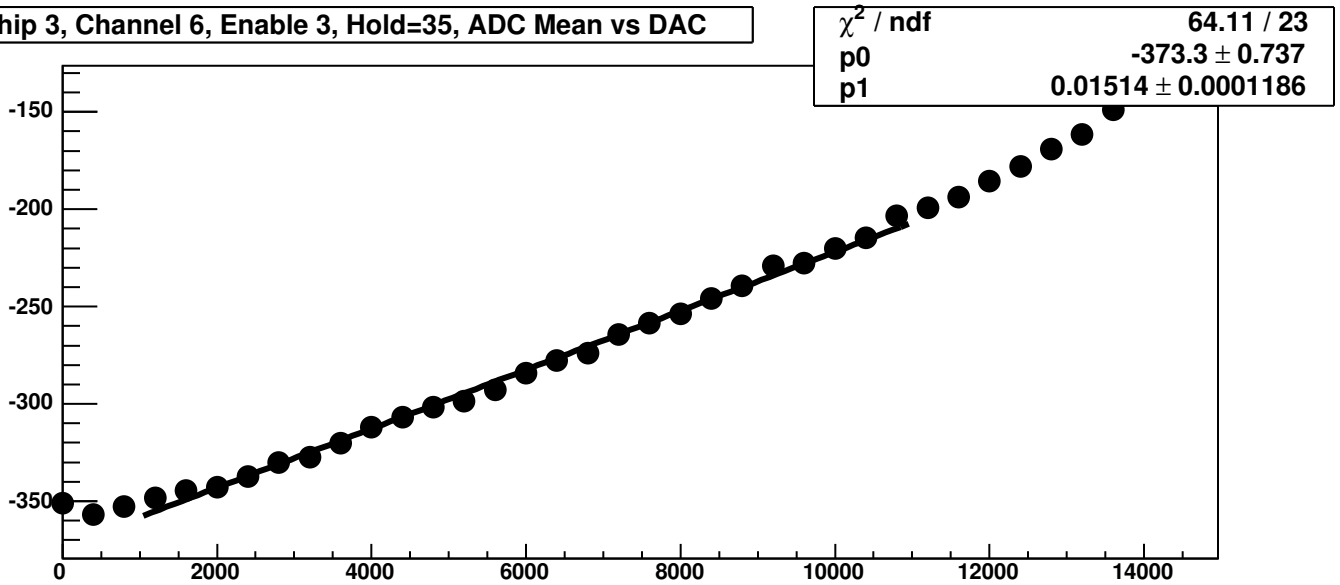

Chip 3, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 34.08 / 23
p0 -369.8 ± 0.7301
p1 0.0321 ± 0.0001237
100
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

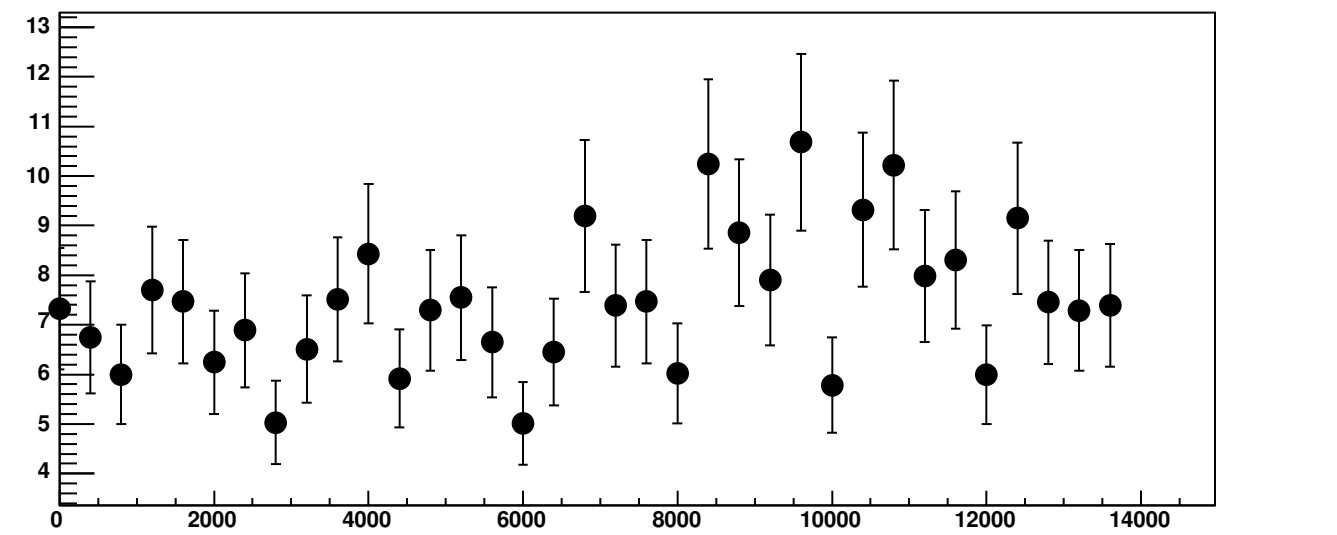

Chip 3, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

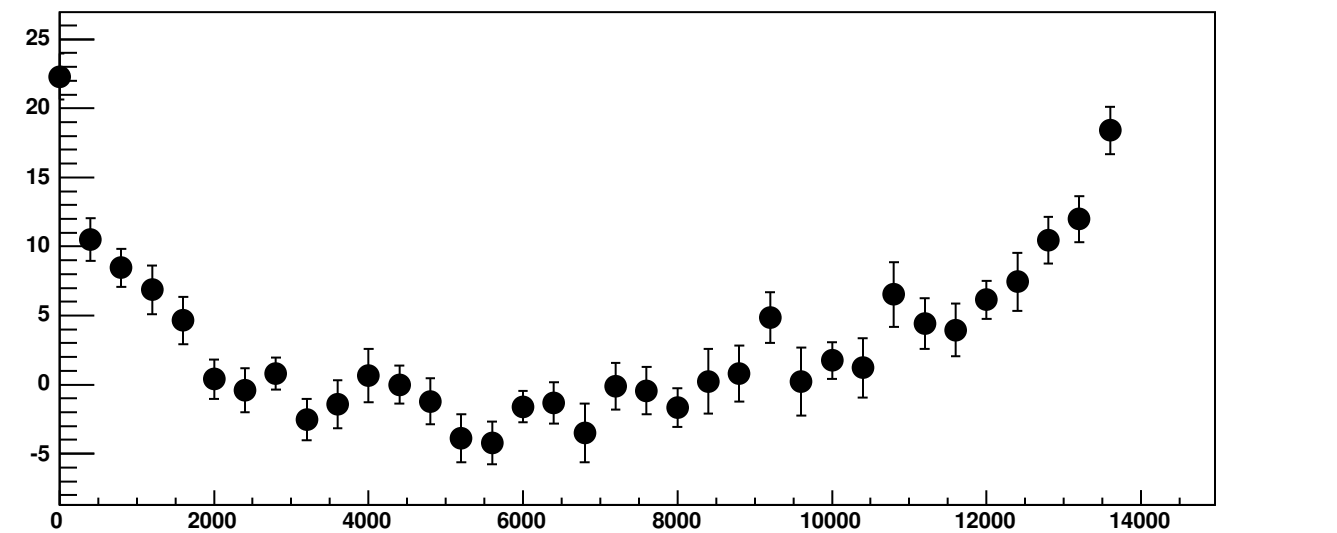
Chip 3, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



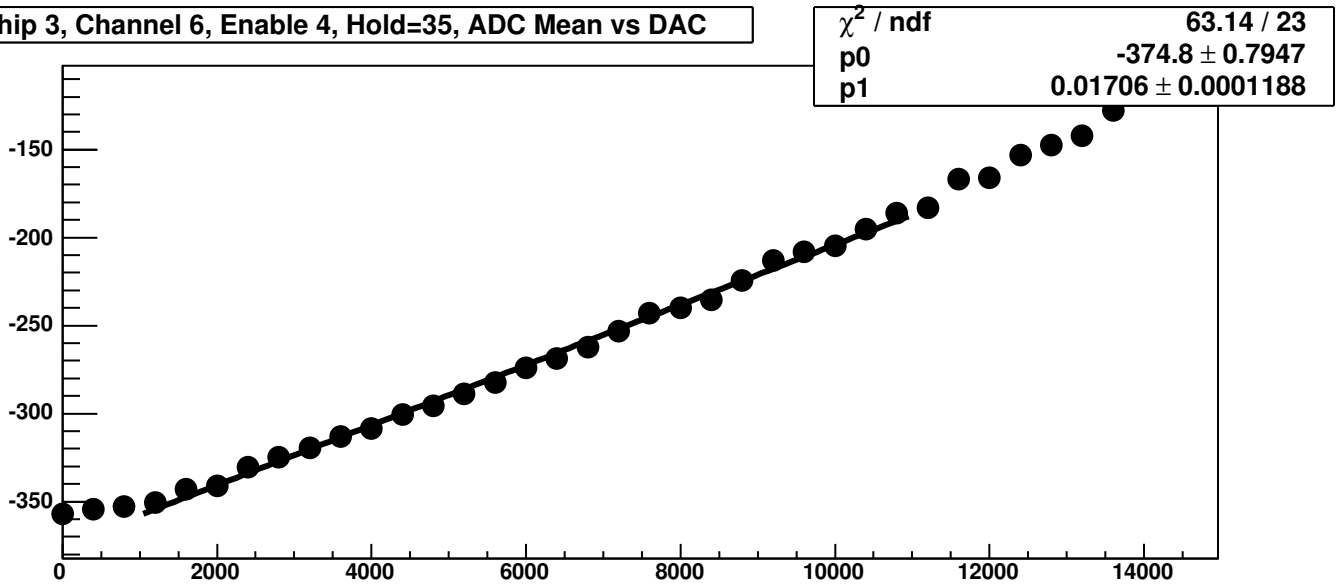
Chip 3, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



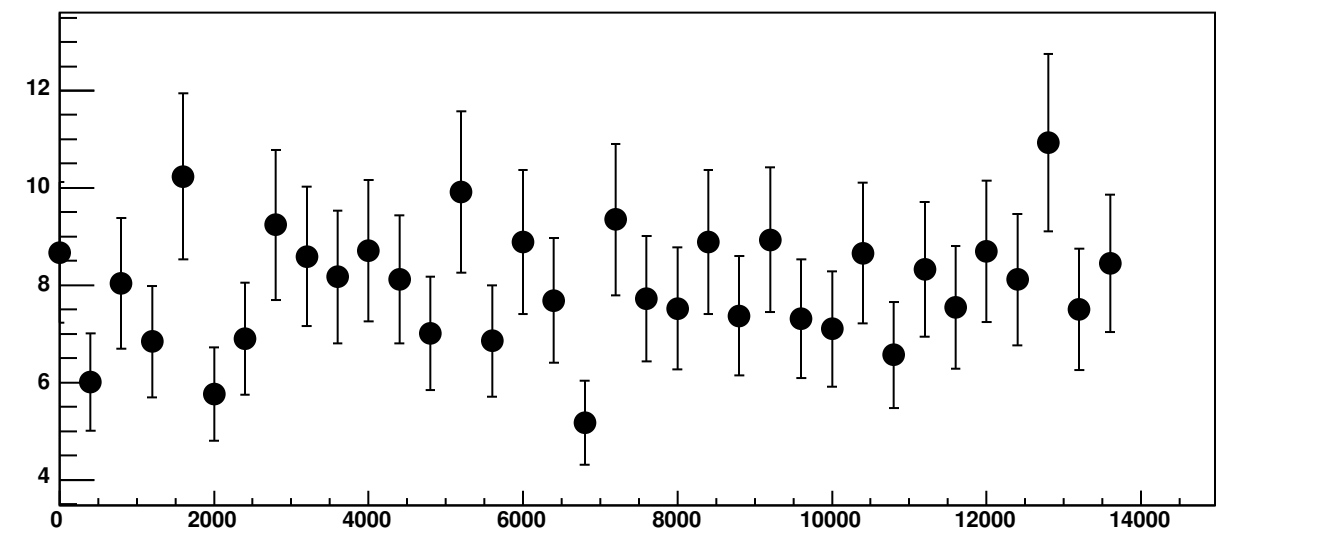
Chip 3, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



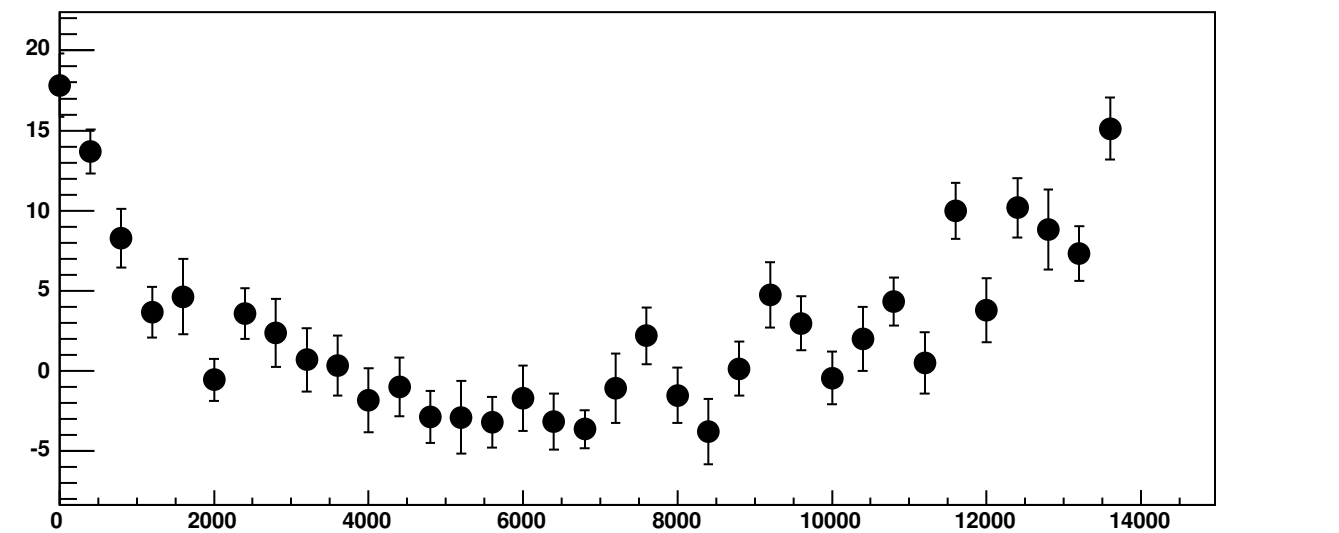
Chip 3, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

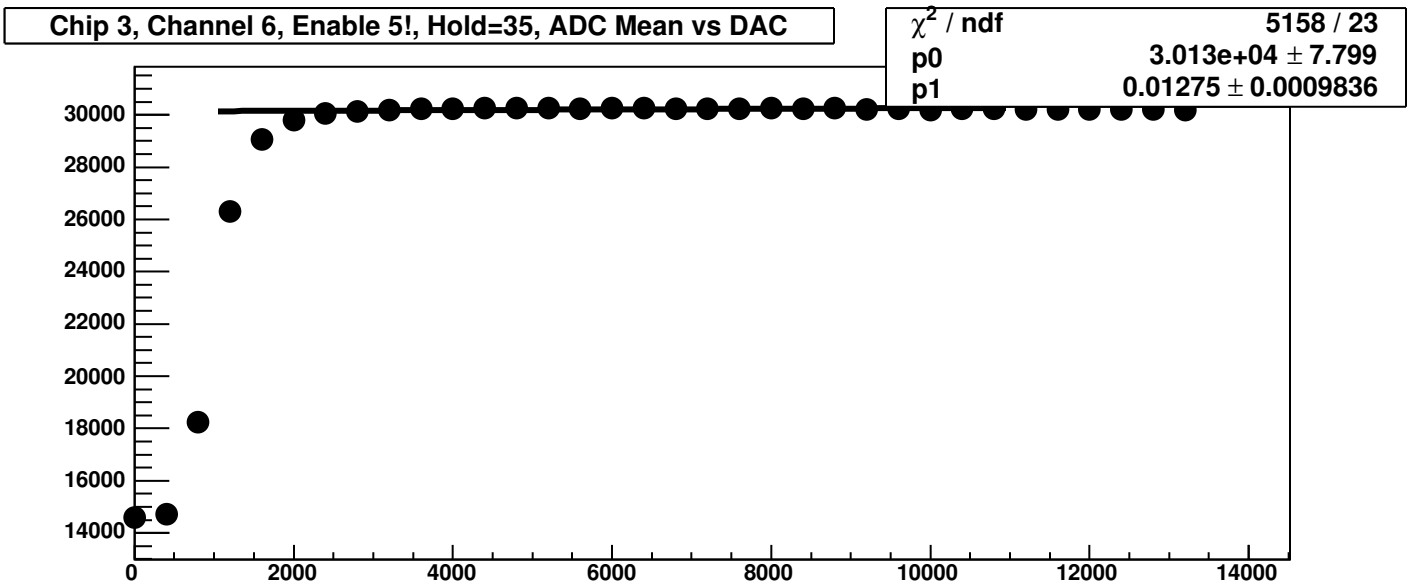


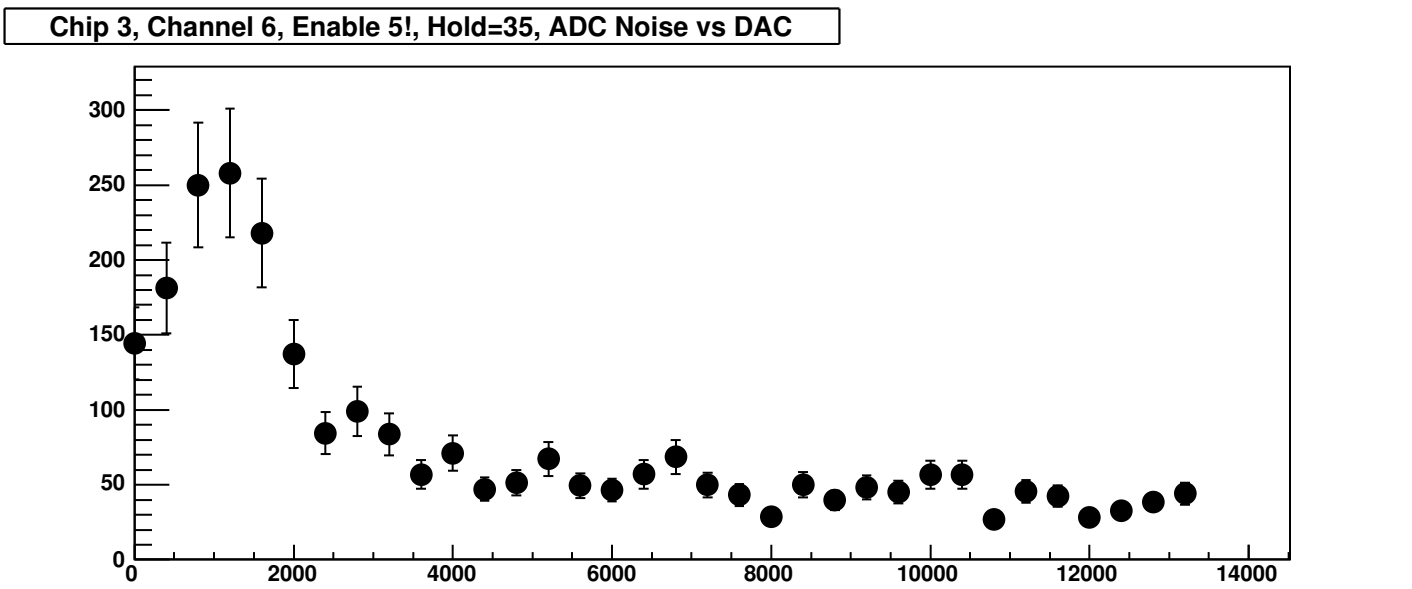
Chip 3, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

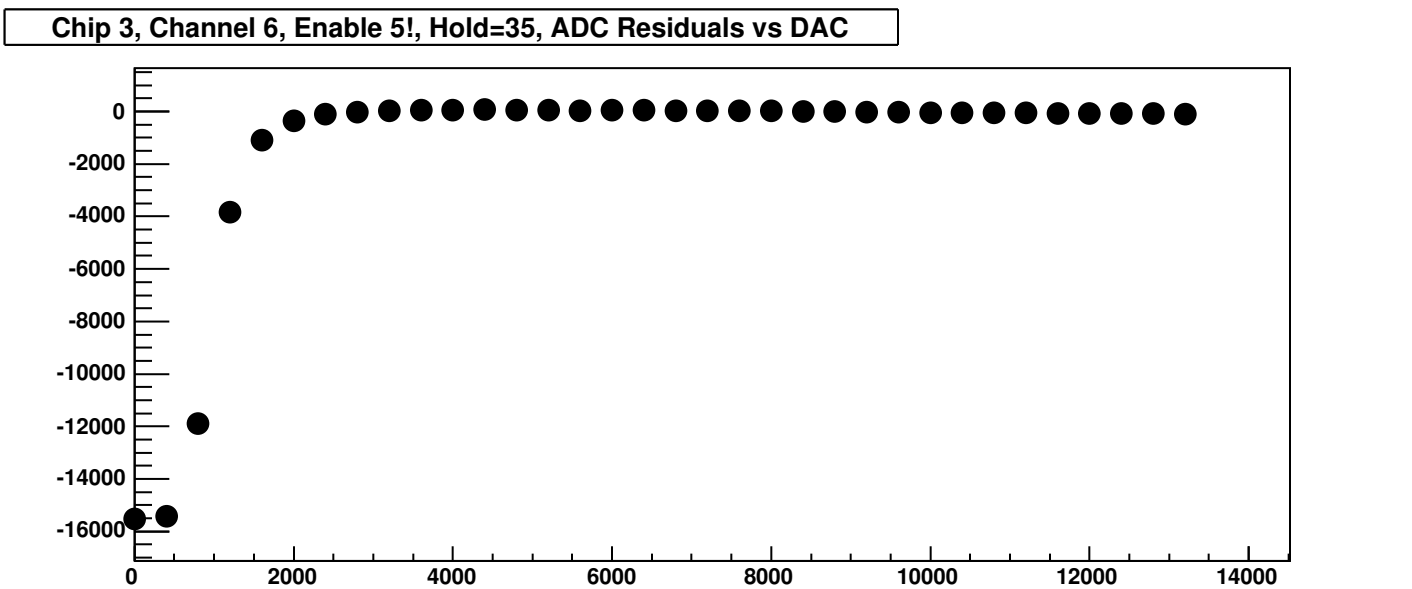


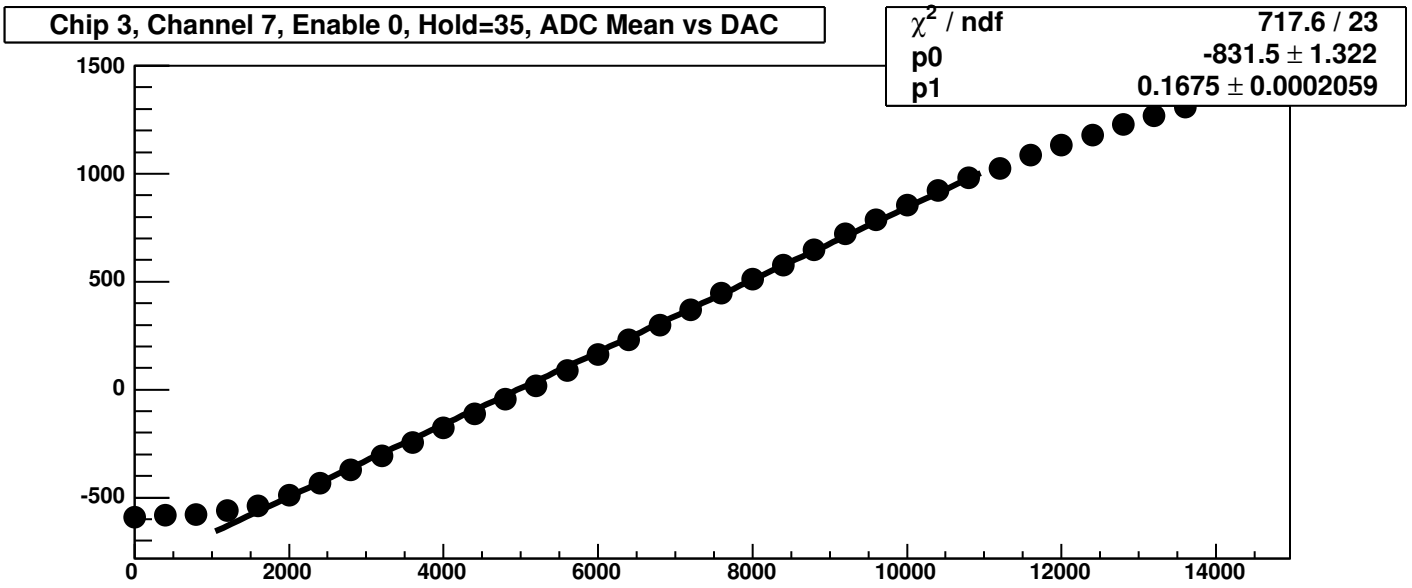
Chip 3, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

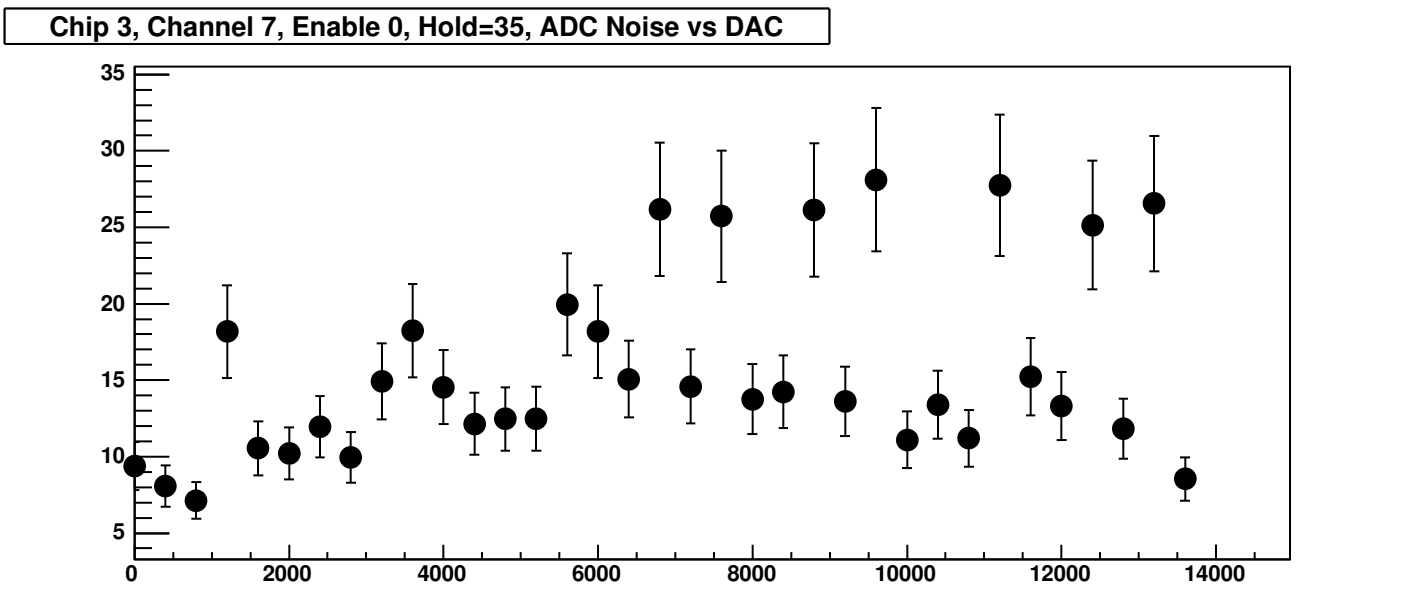


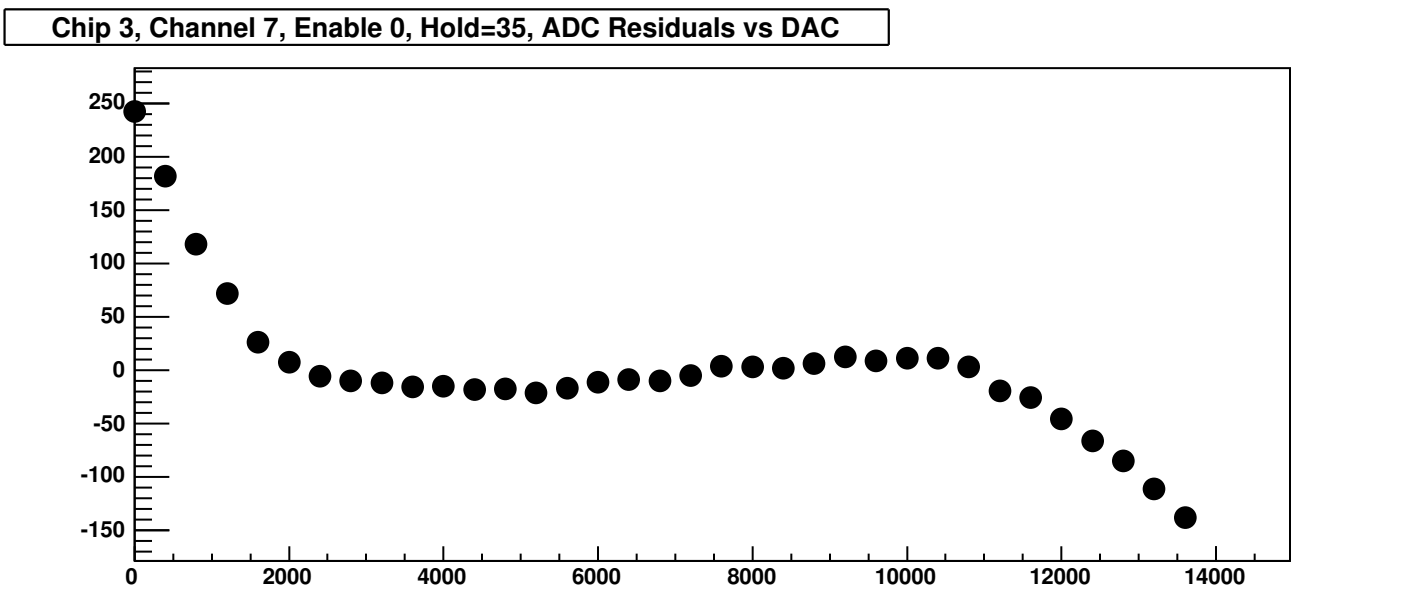

Chip 3, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 5158 / 23
p0 3.013e+04 ± 7.799
p1 0.01275 ± 0.0009836
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

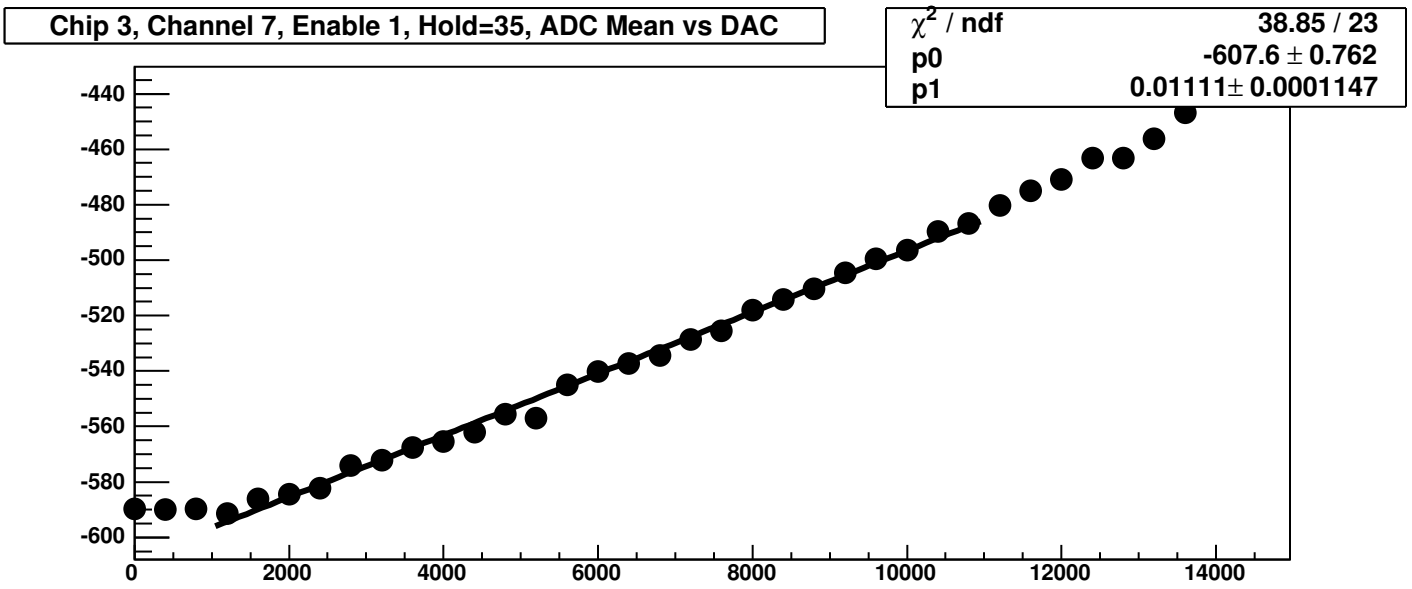

Chip 3, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

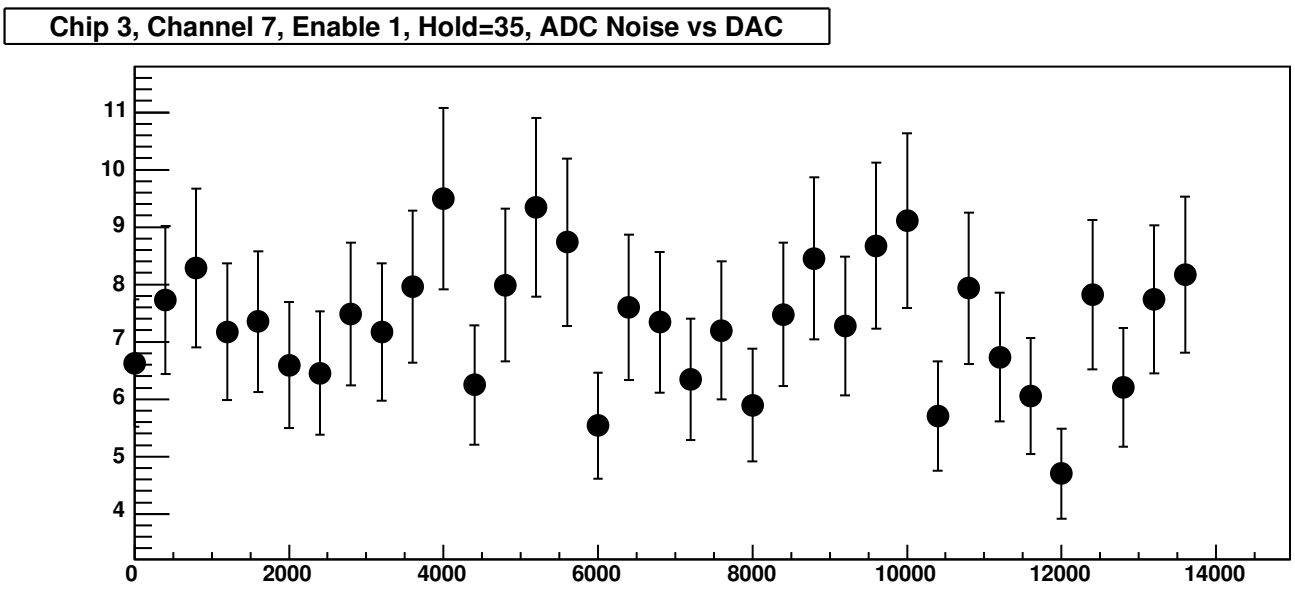

Chip 3, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

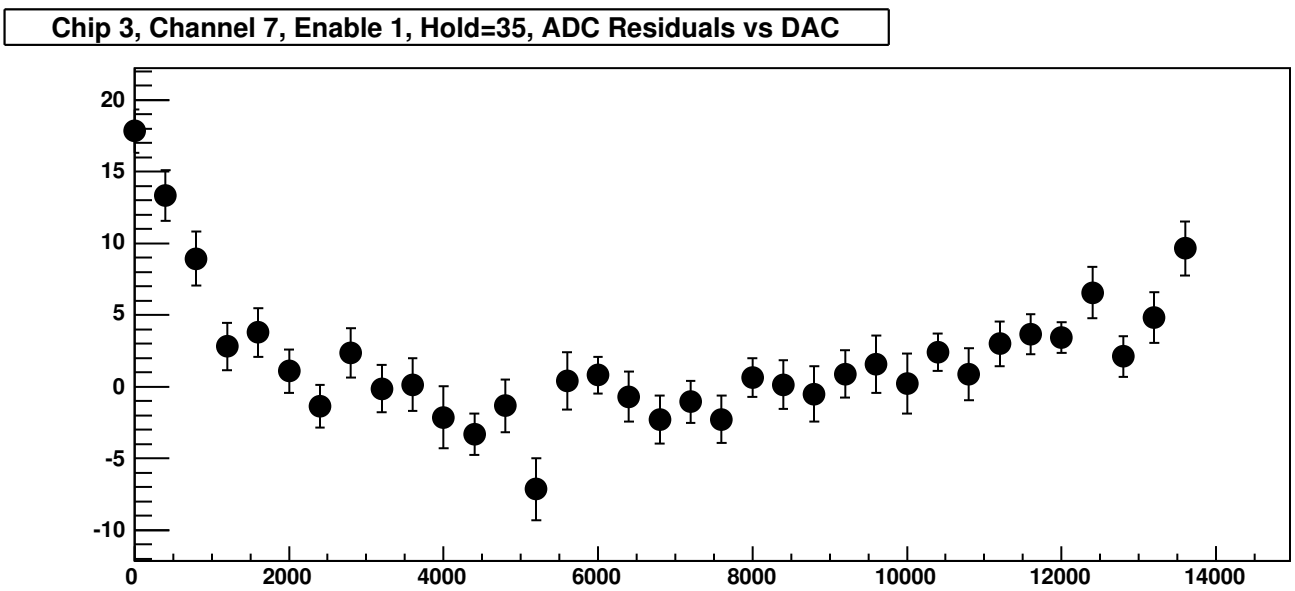

Chip 3, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 717.6 / 23
p0 -831.5 ± 1.322
p1 0.1675 ± 0.0002059
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

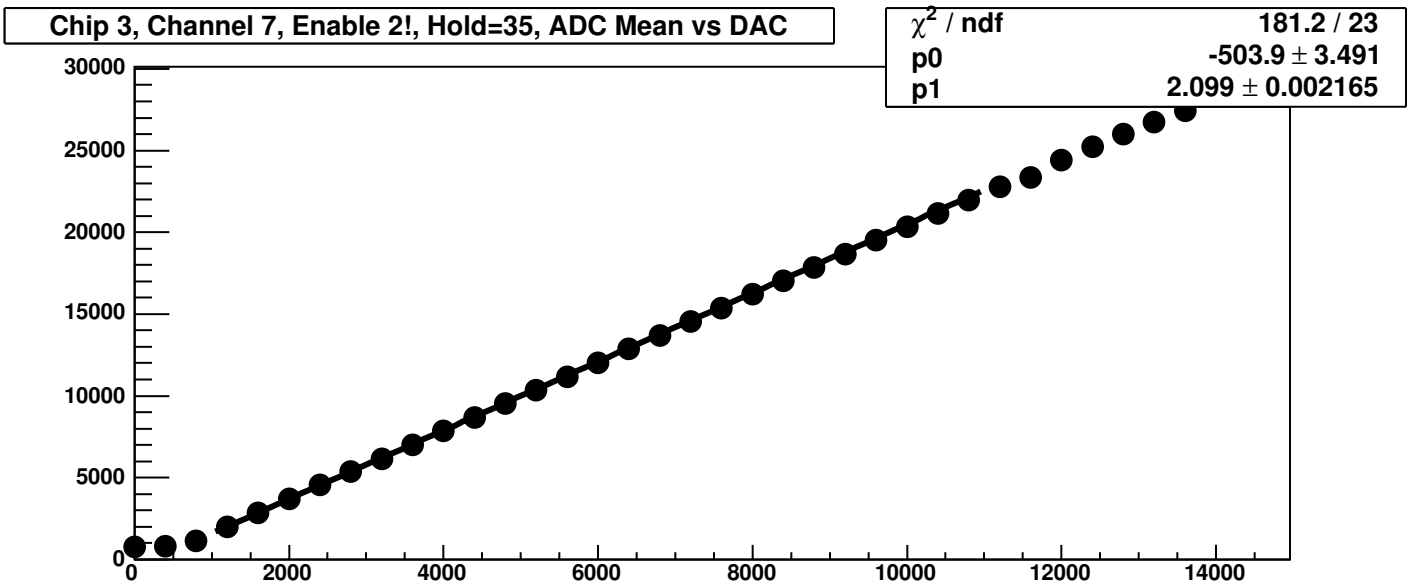

Chip 3, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

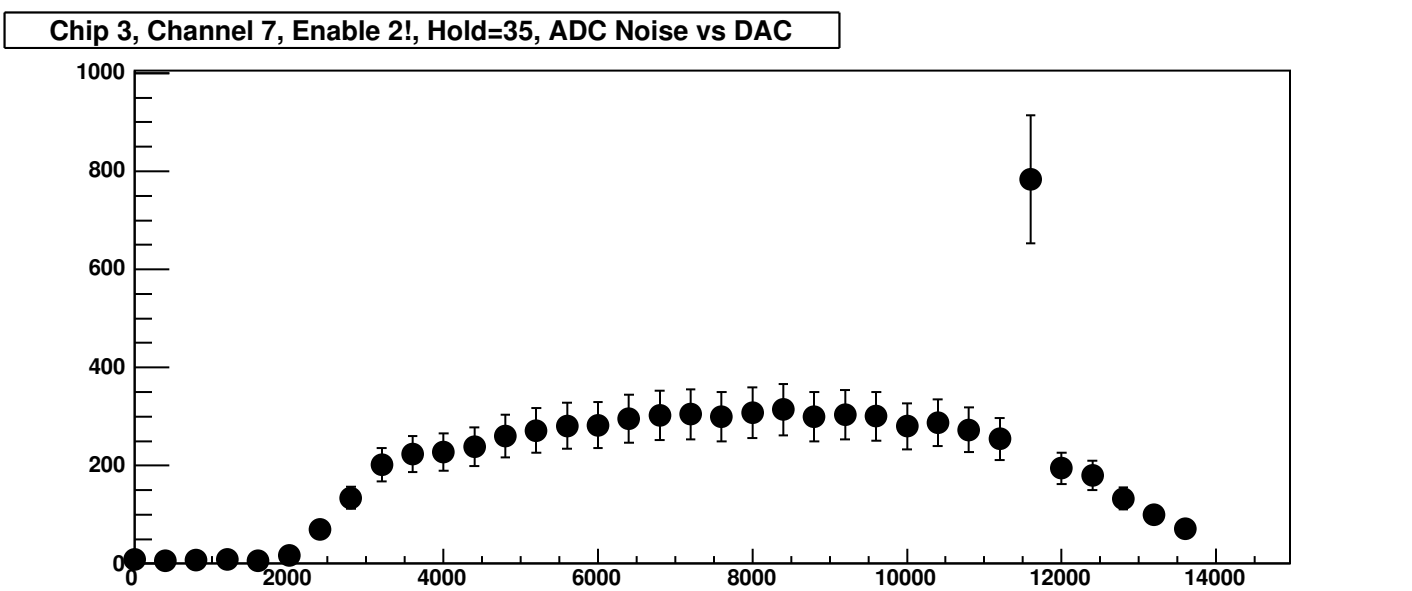

Chip 3, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

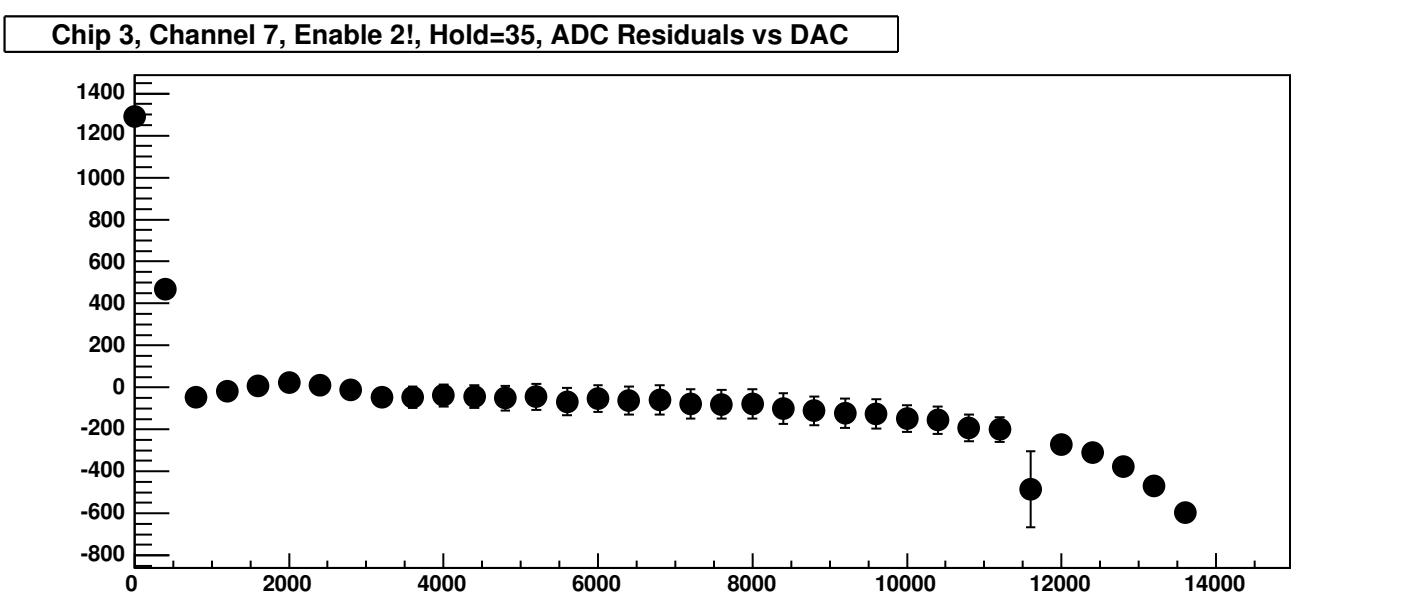

Chip 3, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 38.85 / 23
p0 -607.6 ± 0.762
p1 0.01111± 0.0001147
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
2000
4000
6000
8000
10000
12000
14000

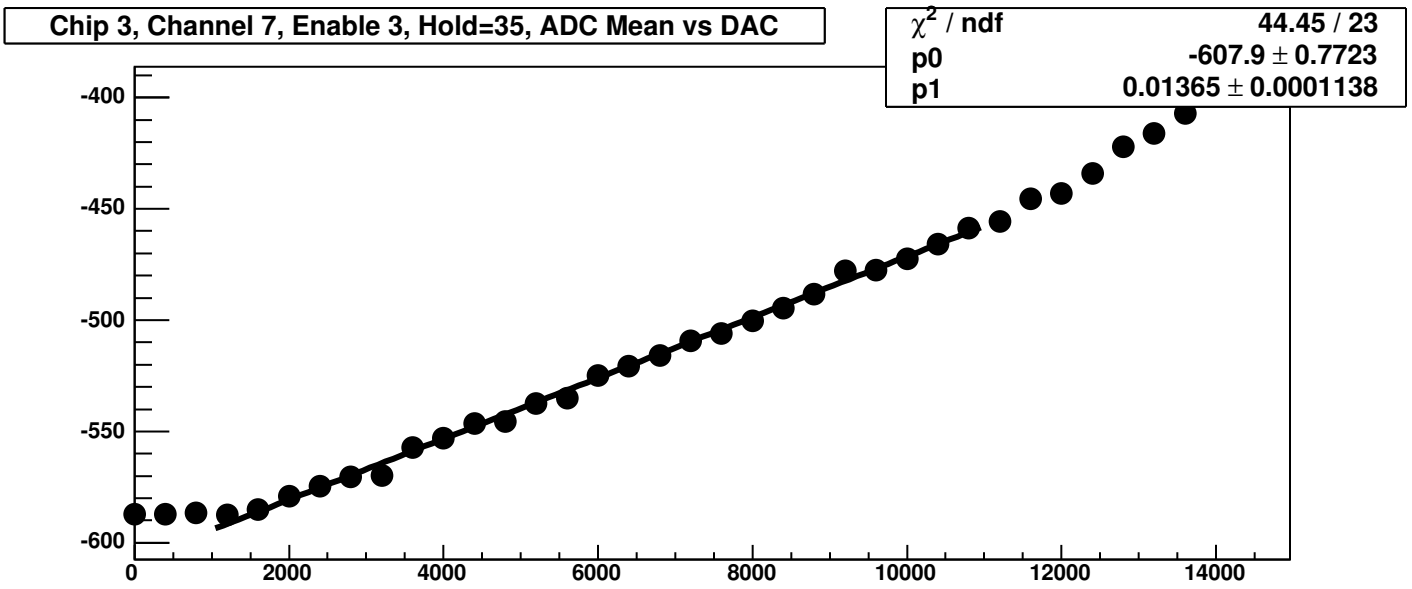

Chip 3, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

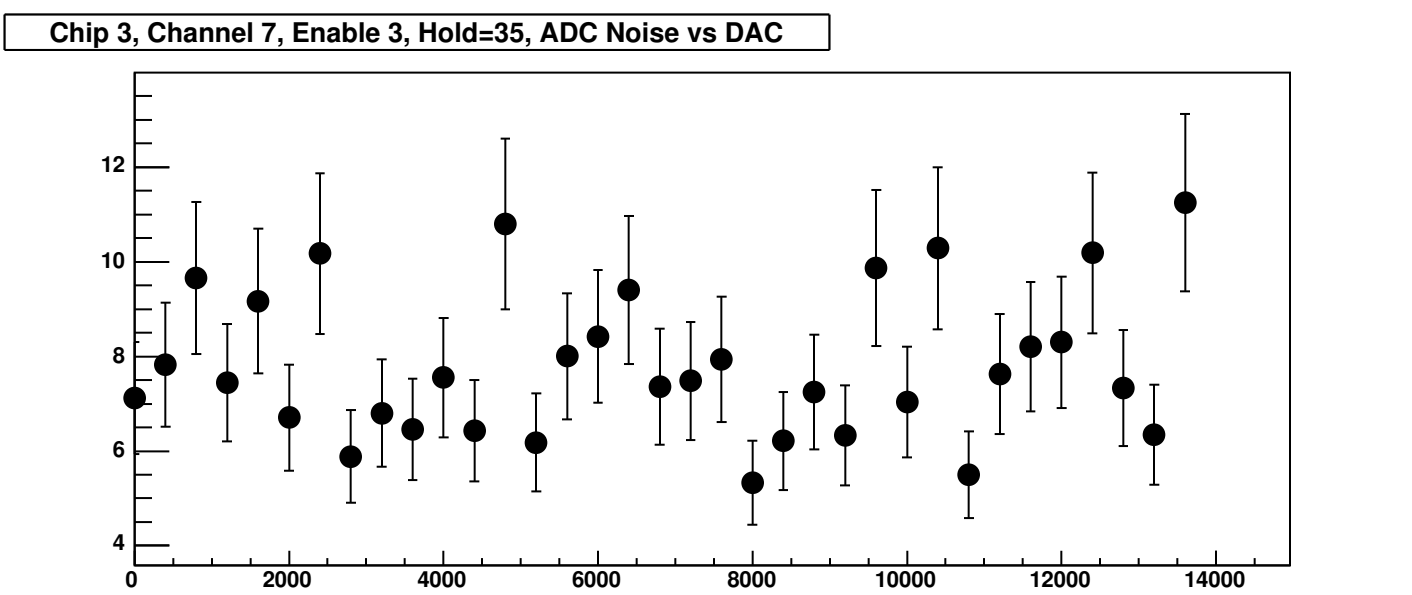

Chip 3, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

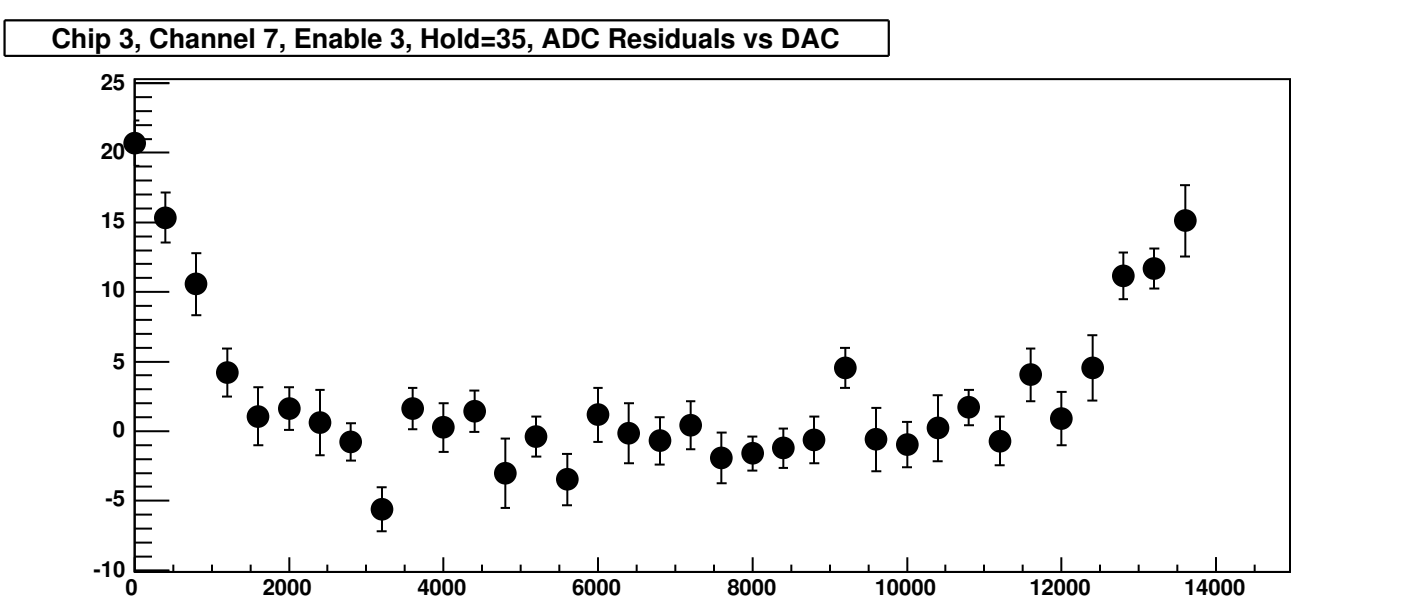

Chip 3, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 181.2 / 23
p0 -503.9 ± 3.491
p1 2.099 ± 0.002165
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

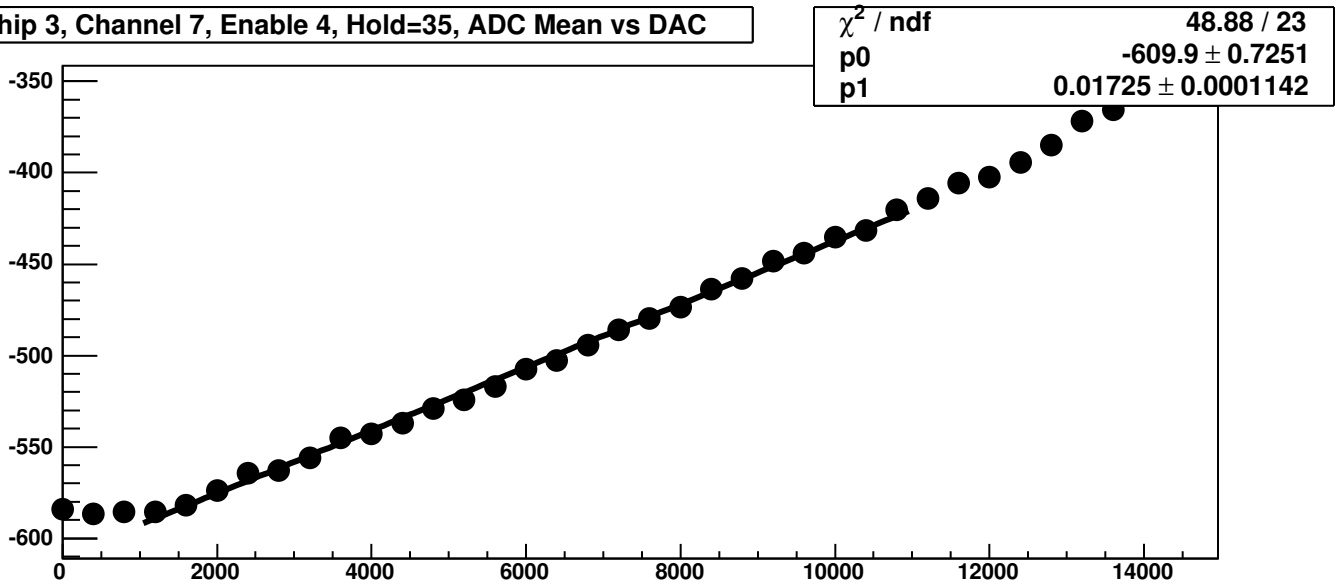

Chip 3, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 44.45 / 23
p0 -607.9 ± 0.7723
p1 0.01365 ± 0.0001138
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

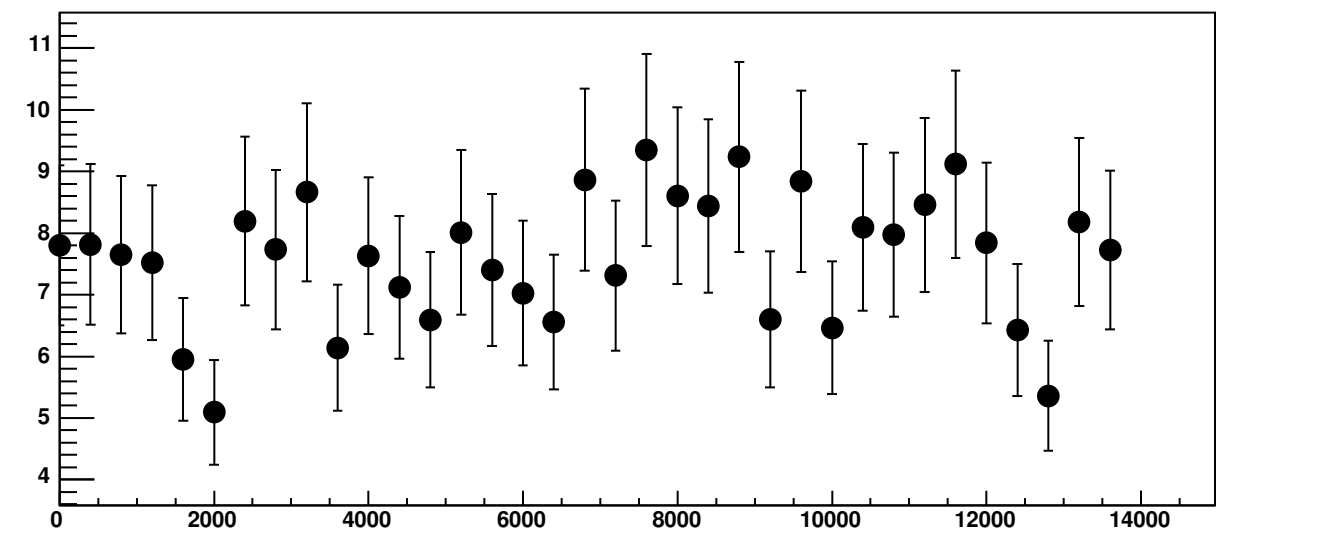

Chip 3, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

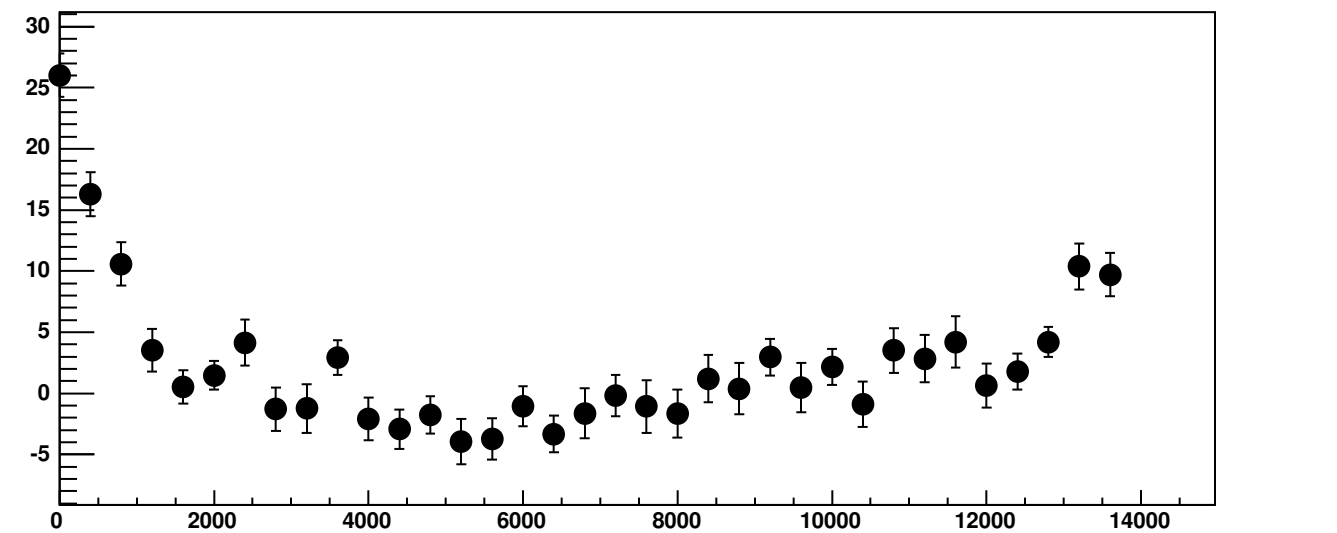
Chip 3, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

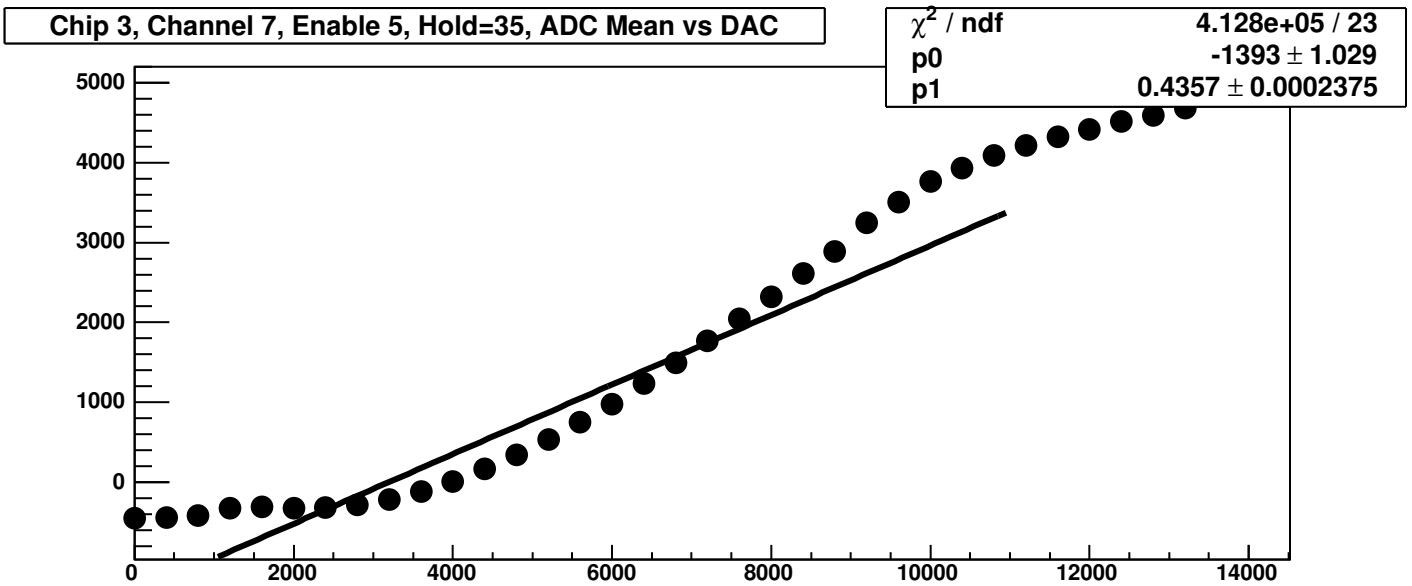


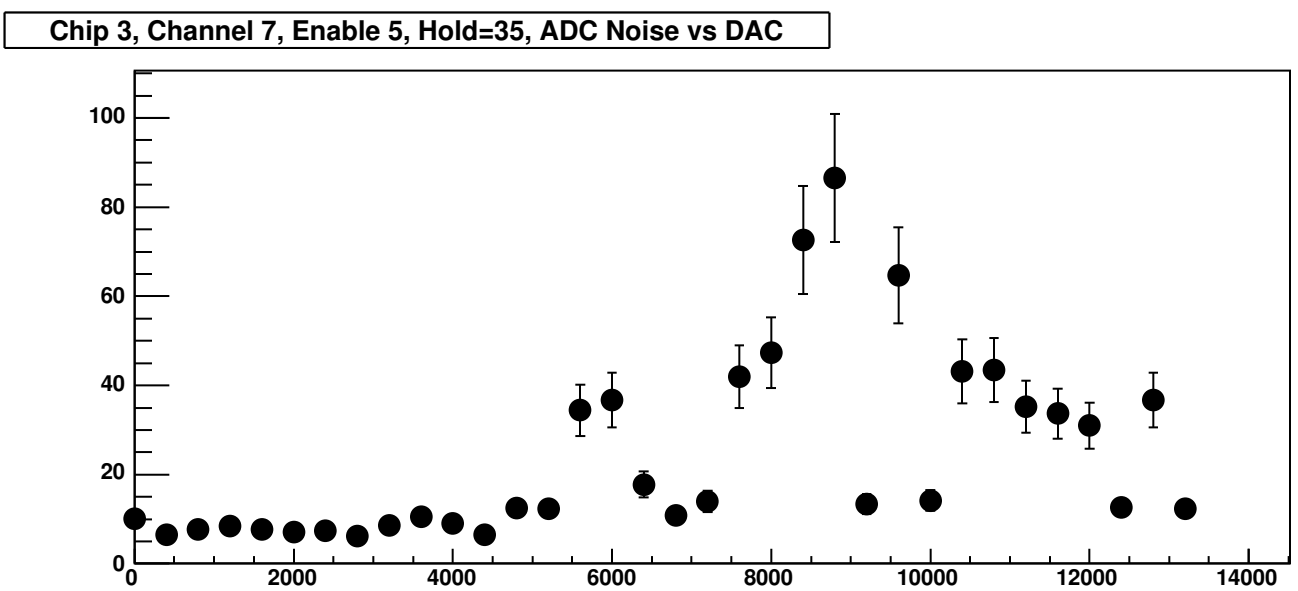
Chip 3, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

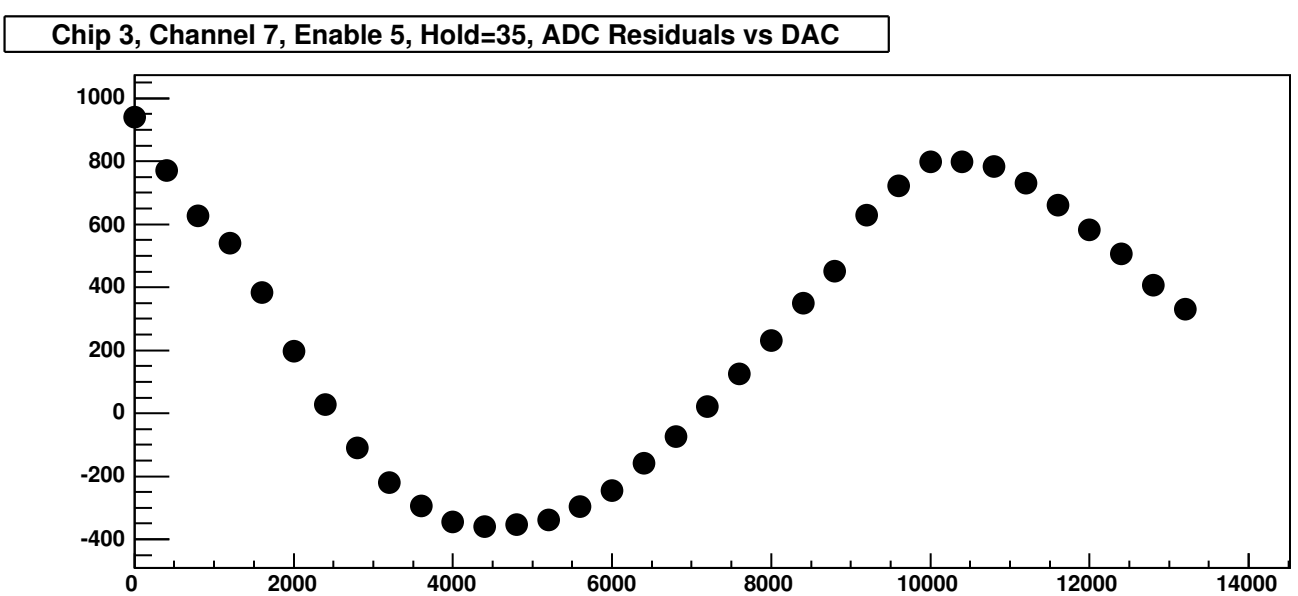


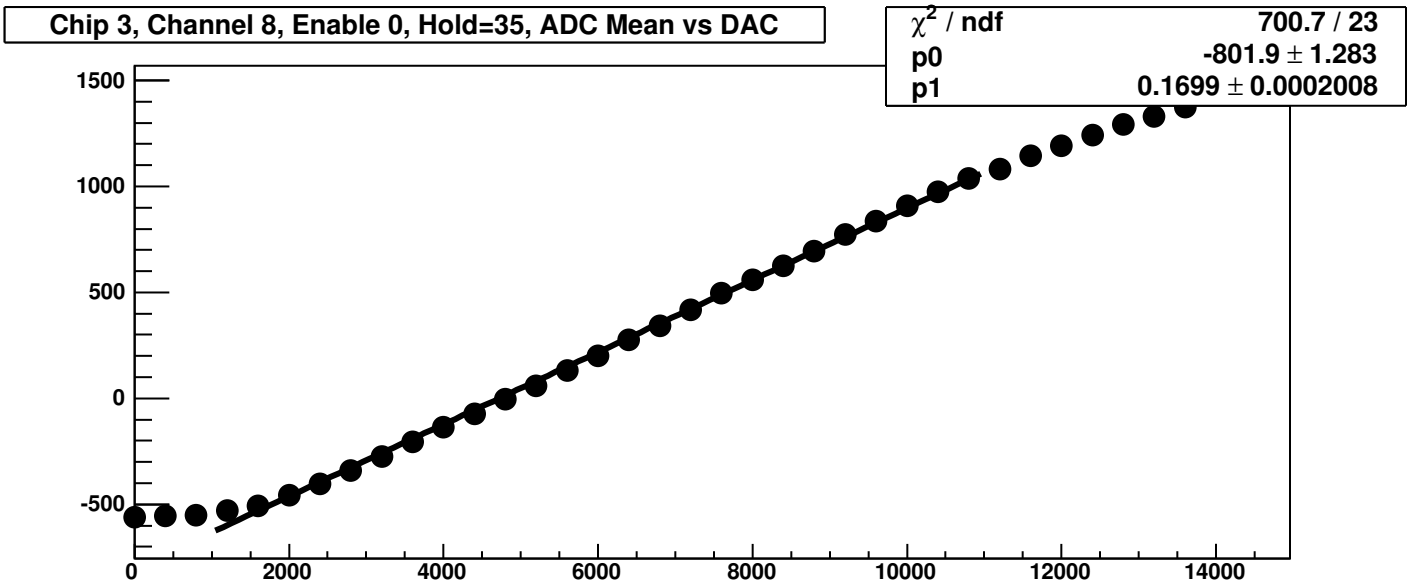
Chip 3, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

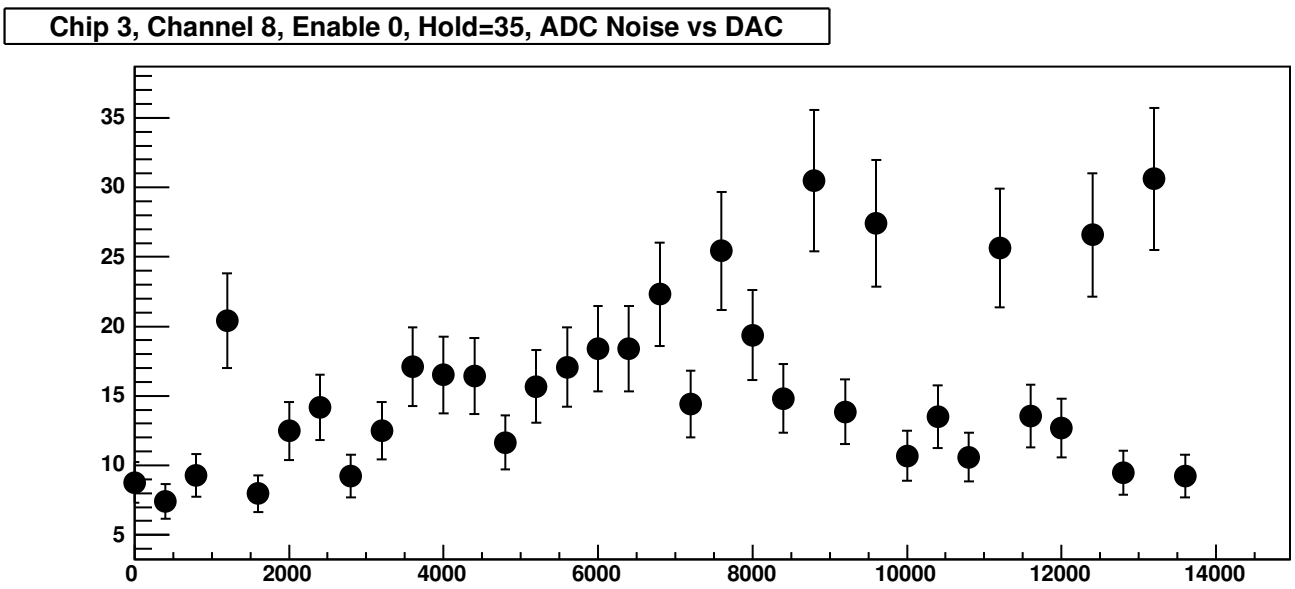


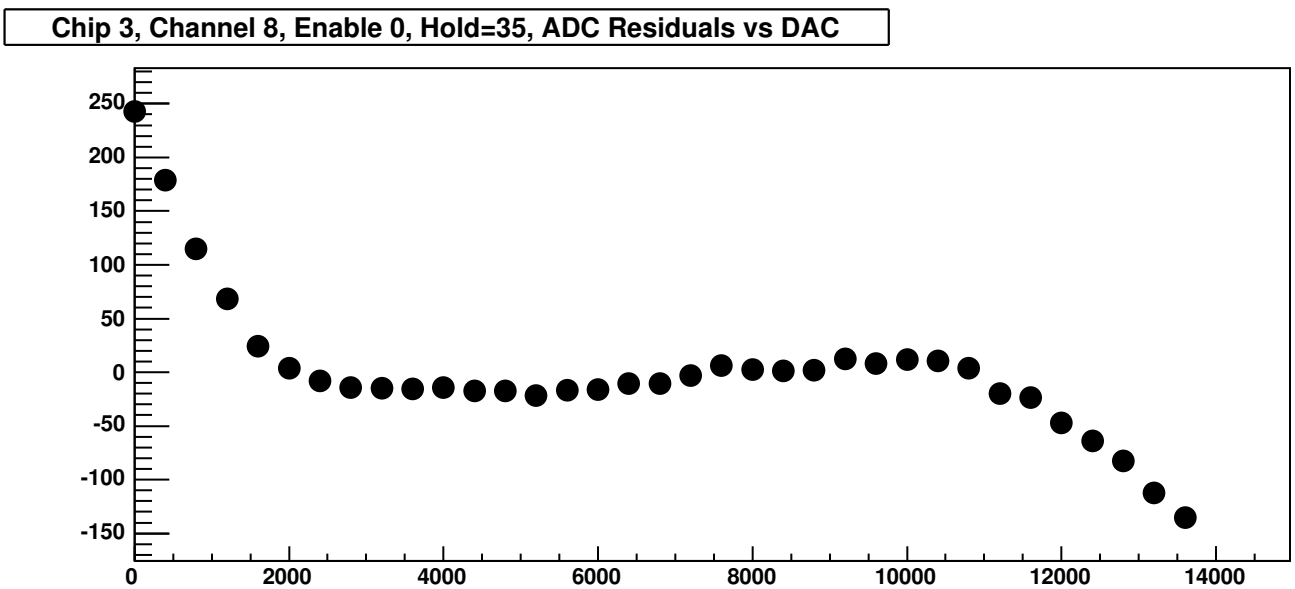

Chip 3, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4.128e+05 / 23
p0 -1393 ± 1.029
p1 0.4357 ± 0.0002375
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

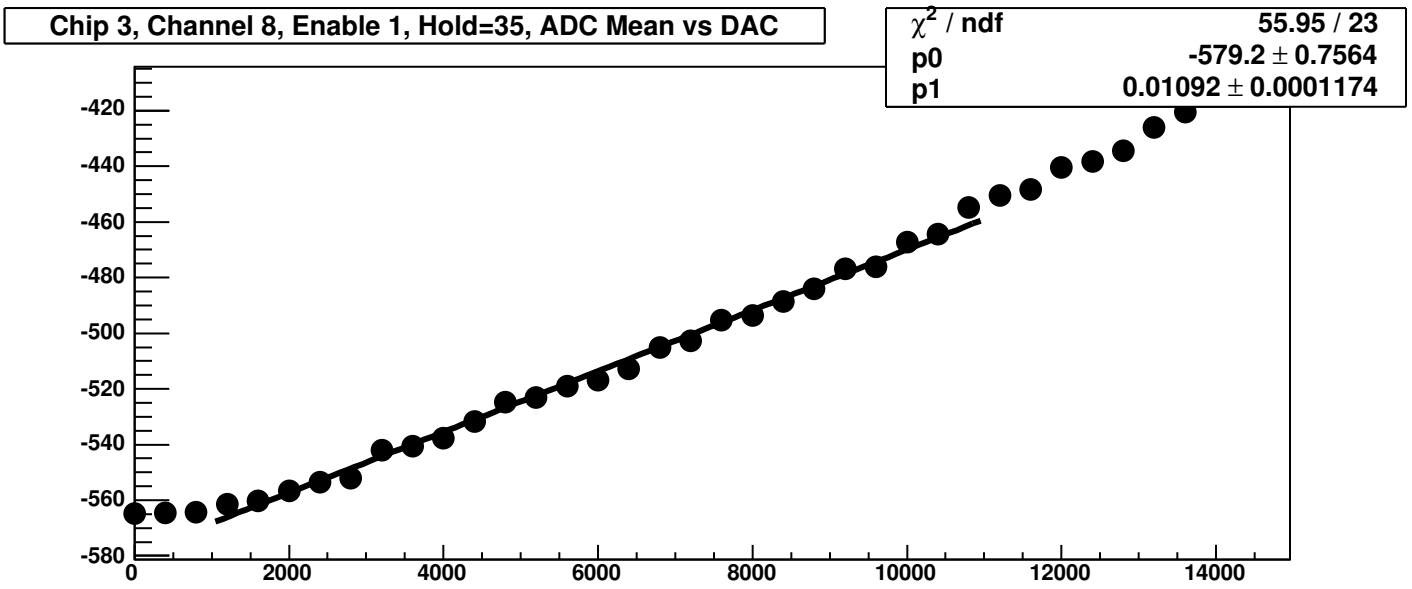

Chip 3, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

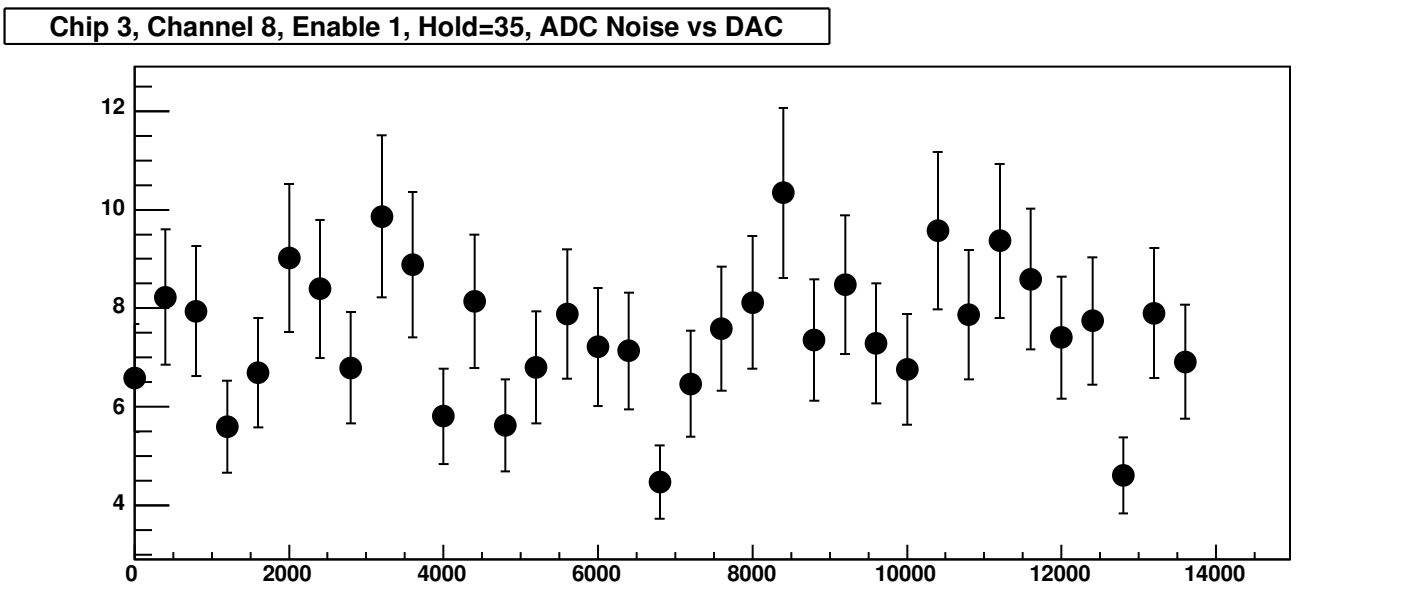

Chip 3, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

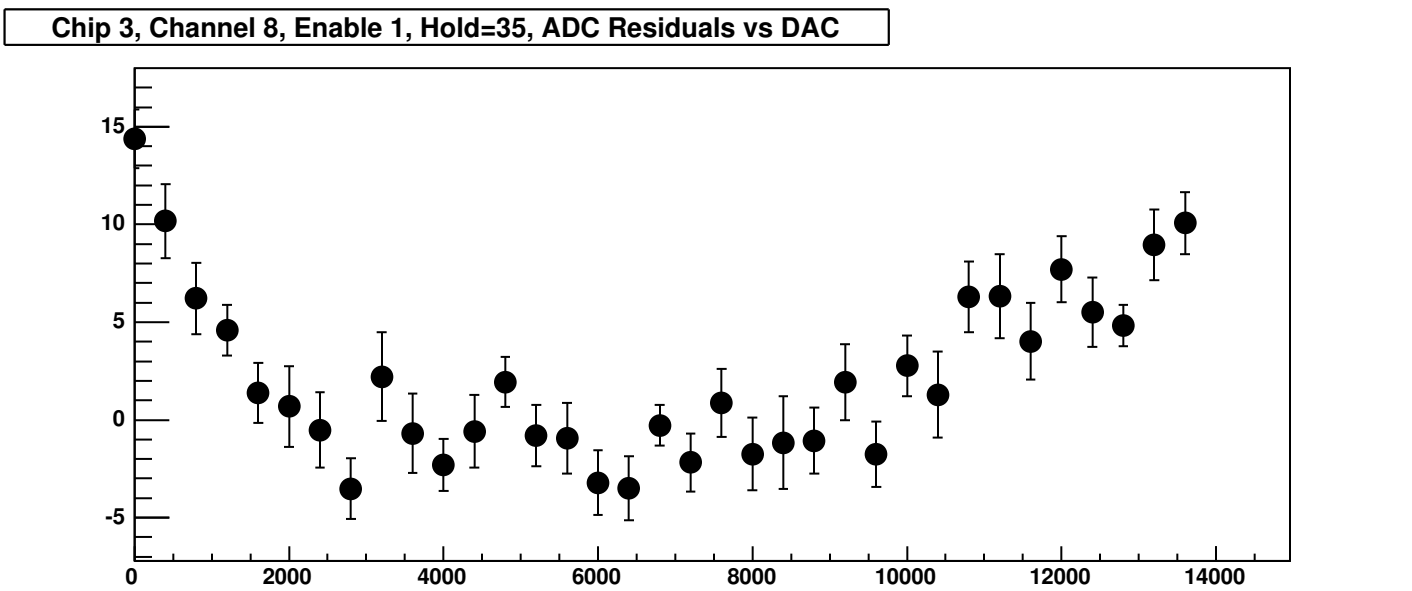

Chip 3, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 700.7 / 23
p0 -801.9 ± 1.283
p1 0.1699 ± 0.0002008
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

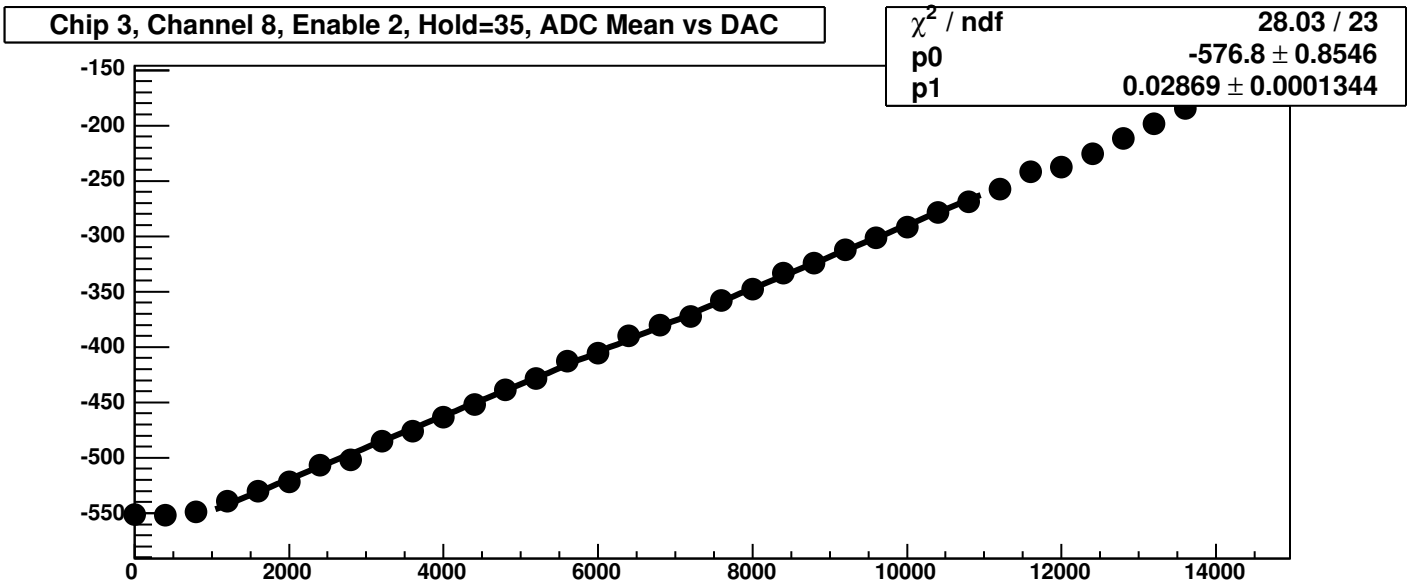

Chip 3, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

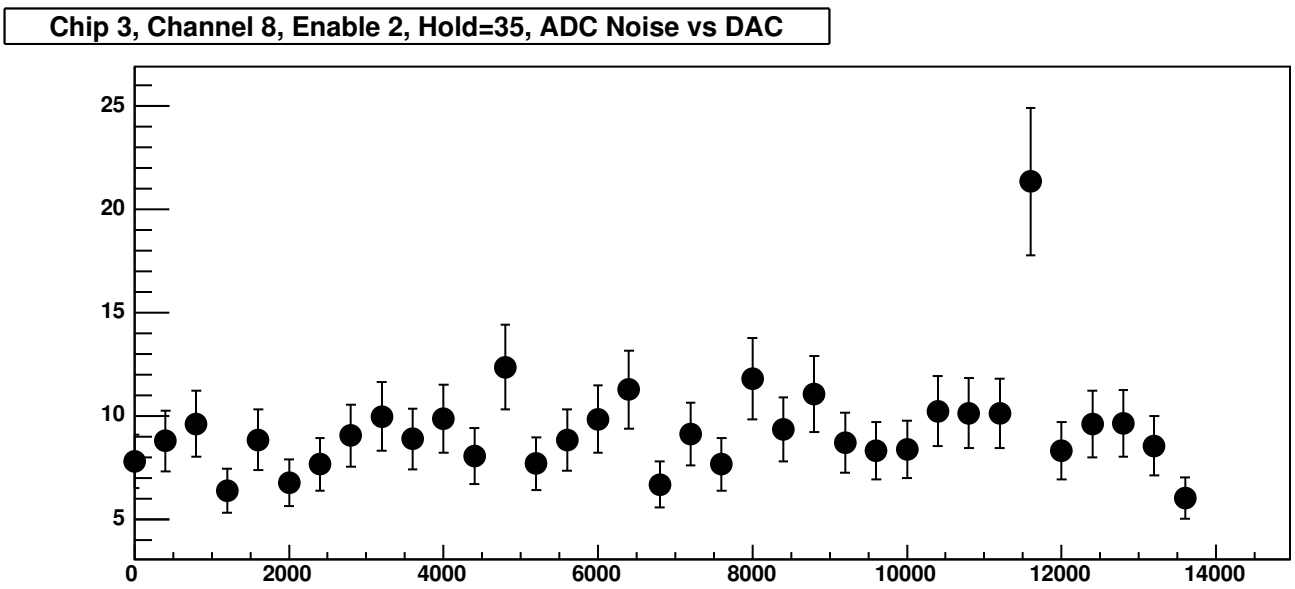

Chip 3, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

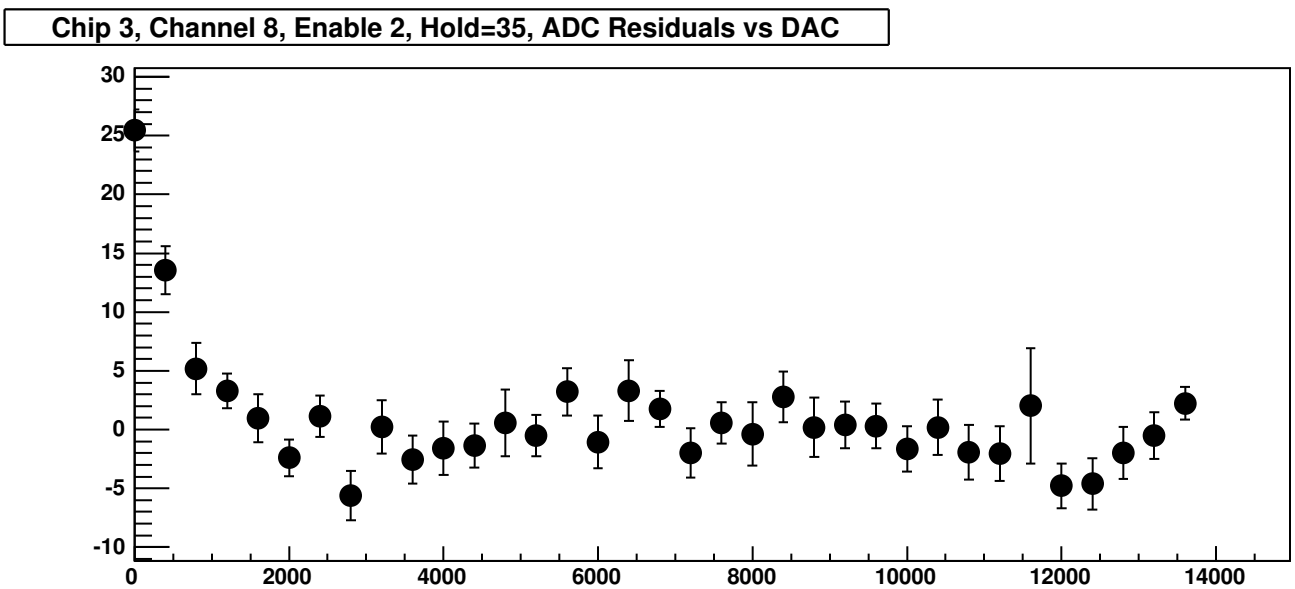

Chip 3, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 55.95 / 23
p0 -579.2 ± 0.7564
p1 0.01092 ± 0.0001174
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000

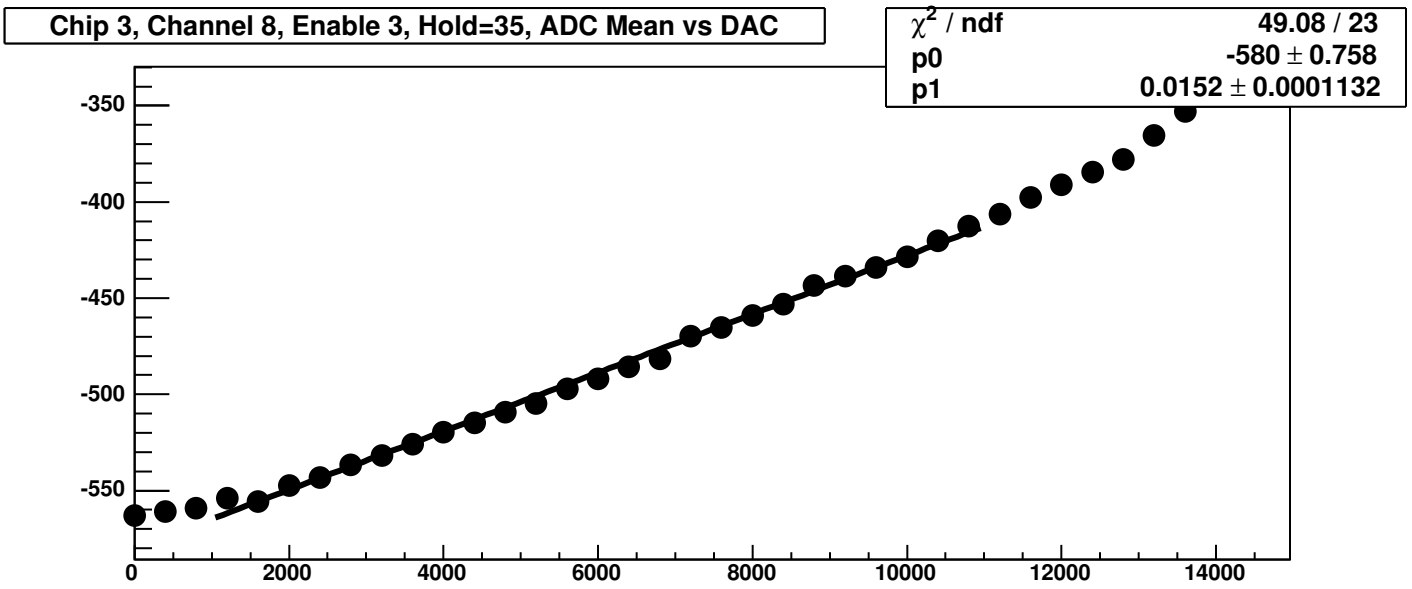

Chip 3, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

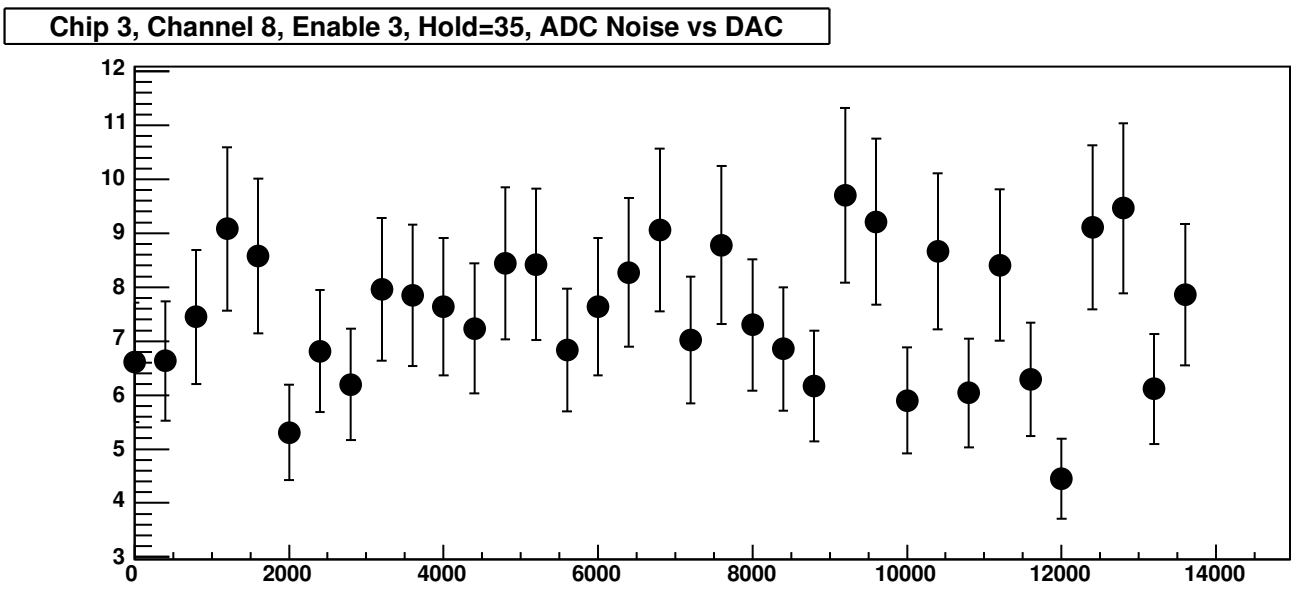

Chip 3, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

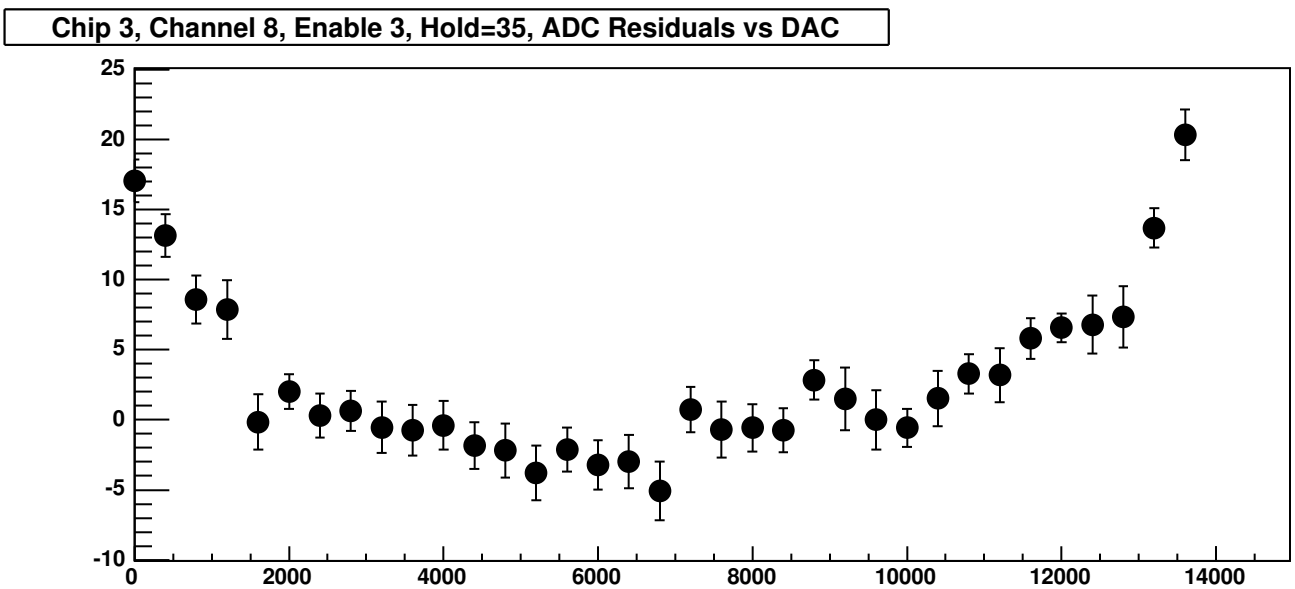

Chip 3, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 28.03 / 23
p0 -576.8 ± 0.8546
p1 0.02869 ± 0.0001344
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

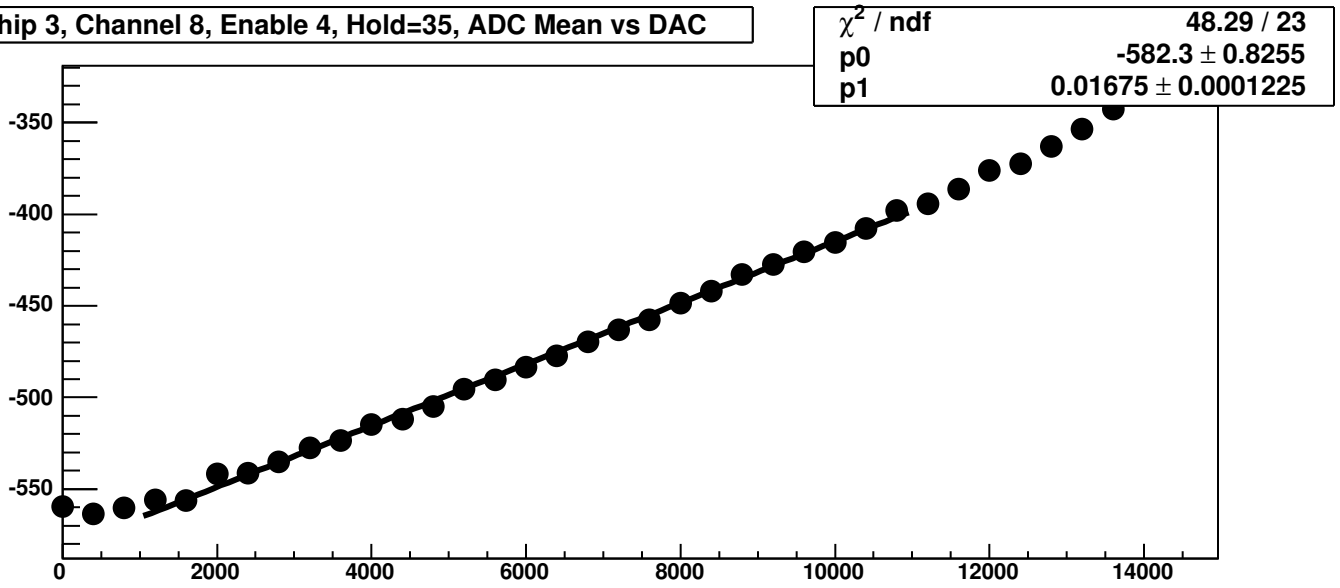

Chip 3, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 49.08 / 23
p0 -580 ± 0.758
p1 0.0152 ± 0.0001132
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

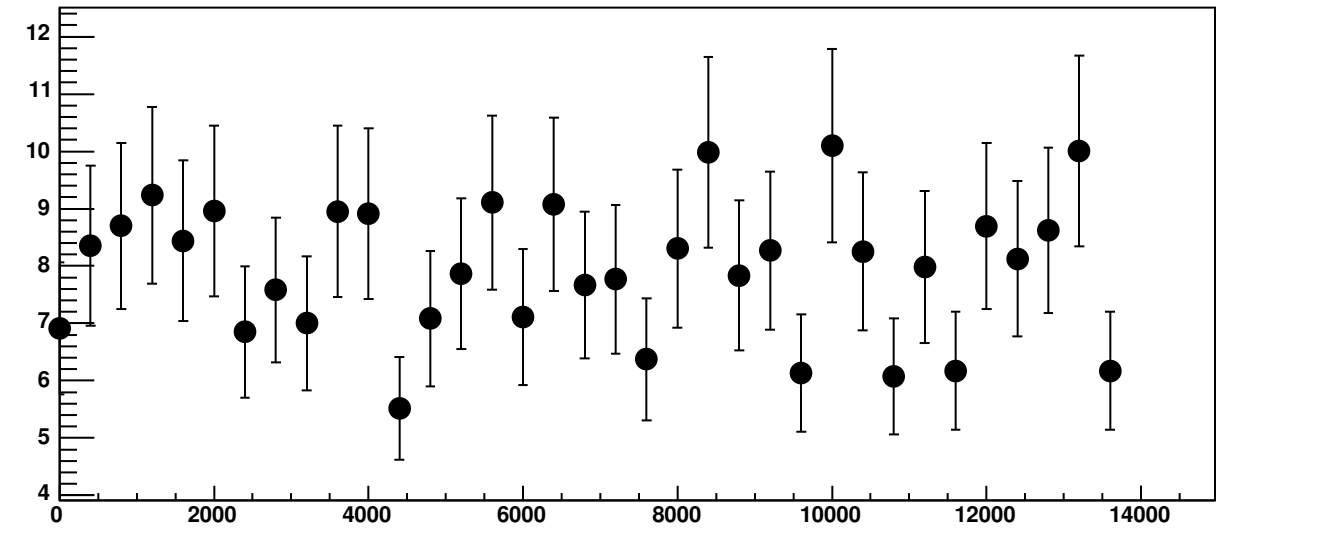

Chip 3, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

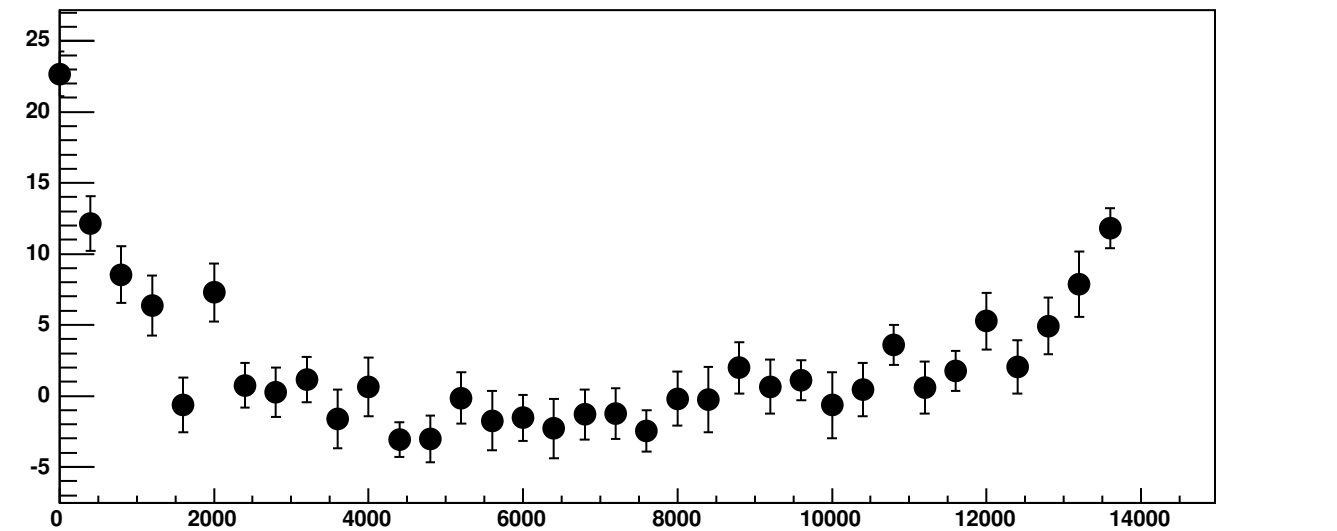
Chip 3, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

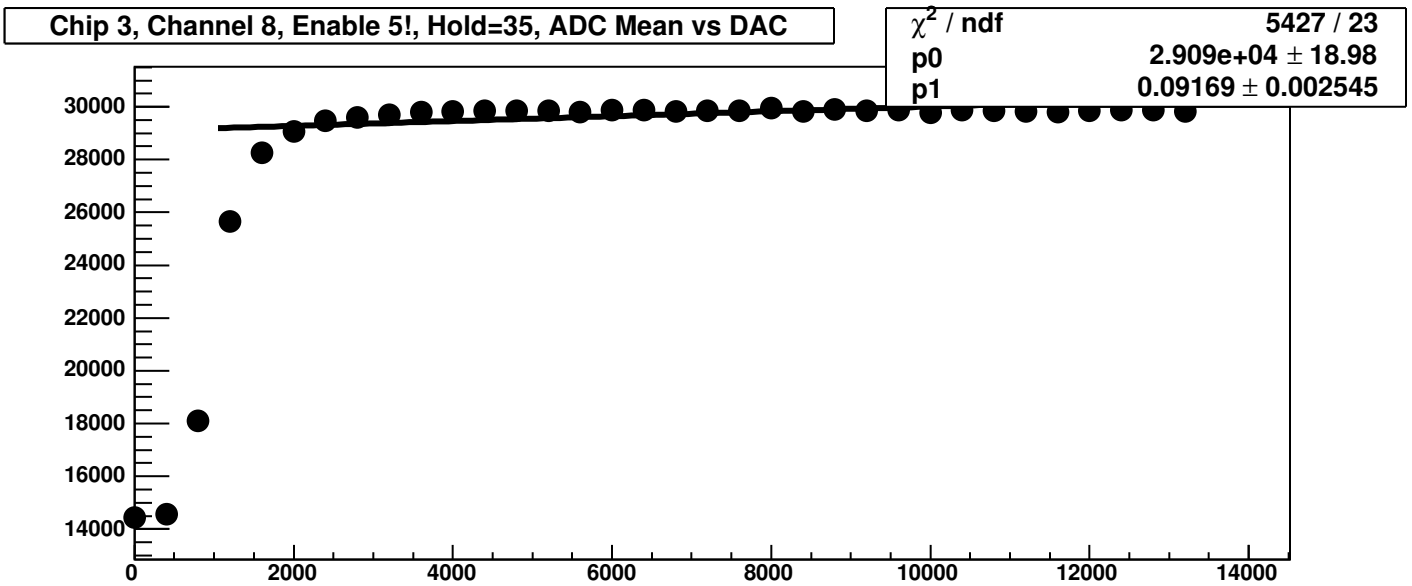


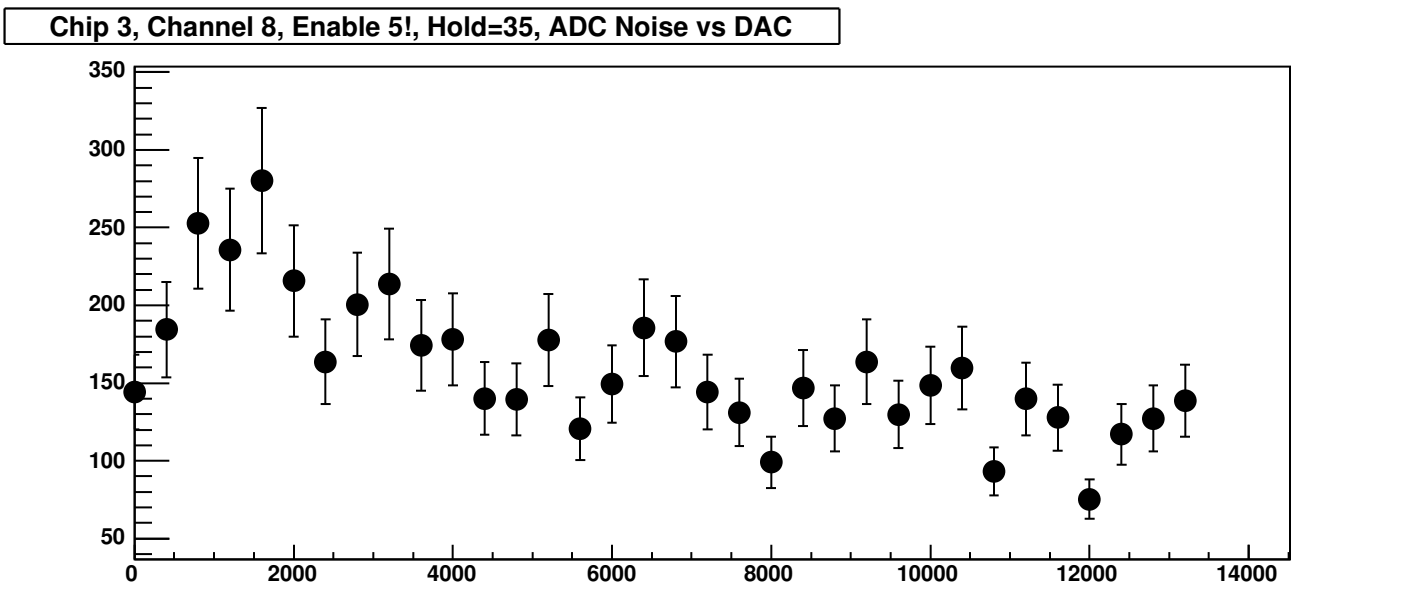
Chip 3, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC

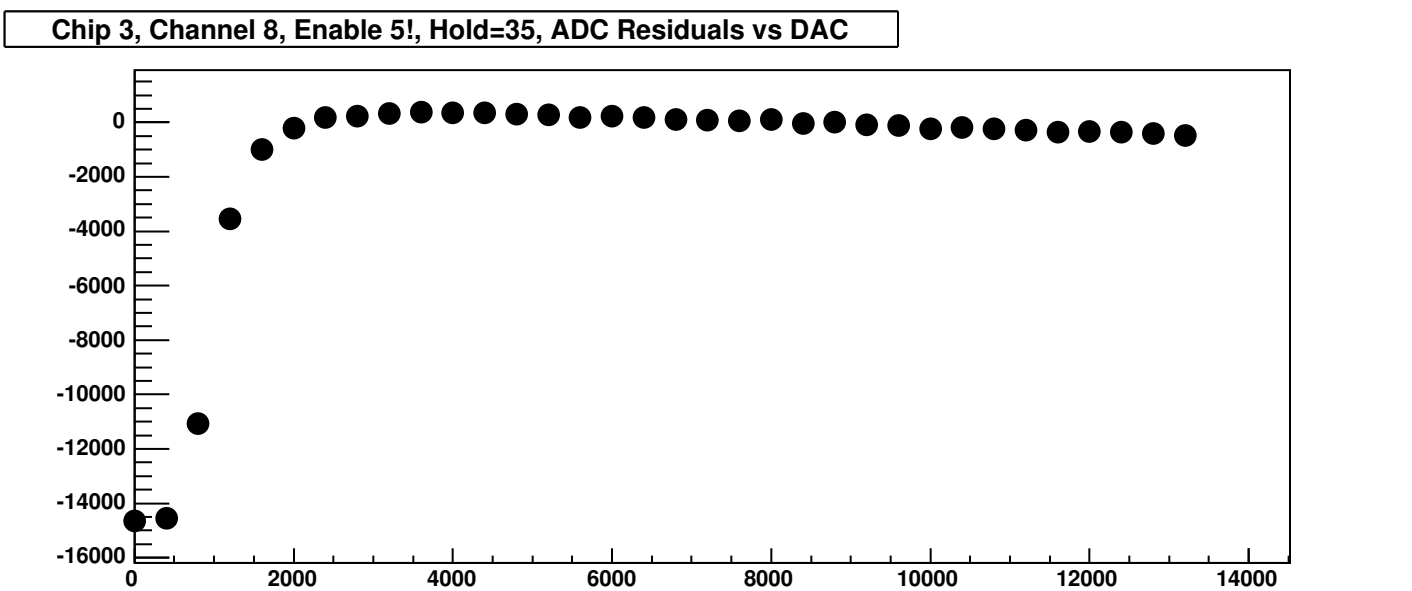


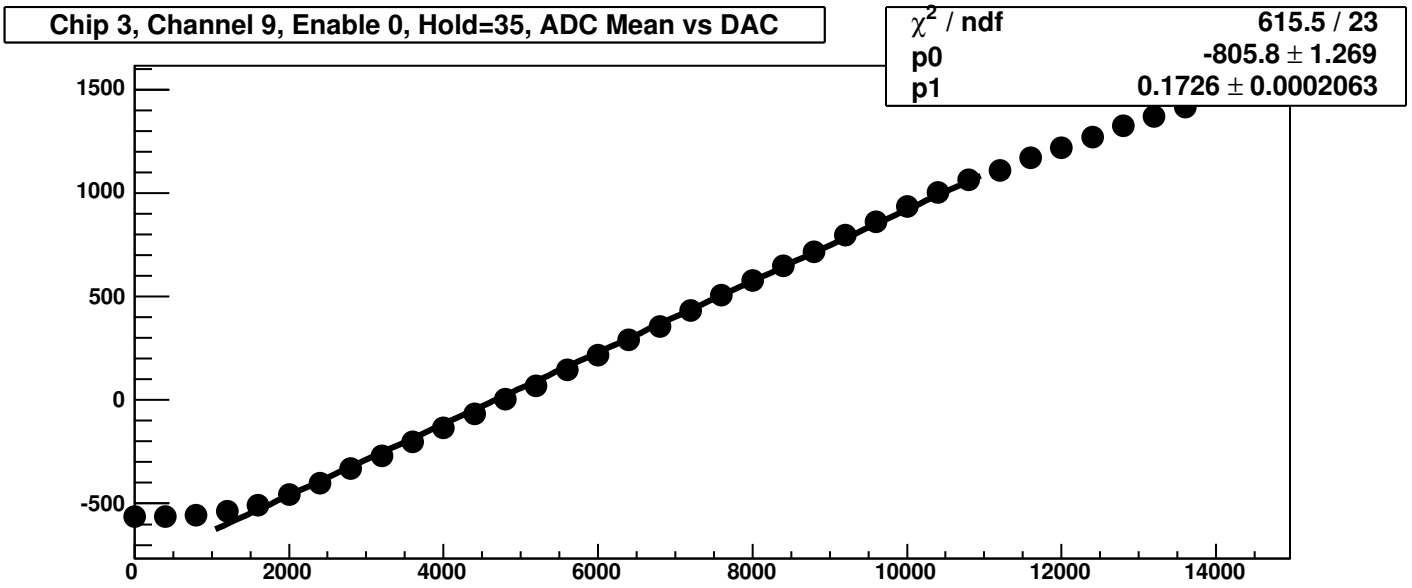
Chip 3, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

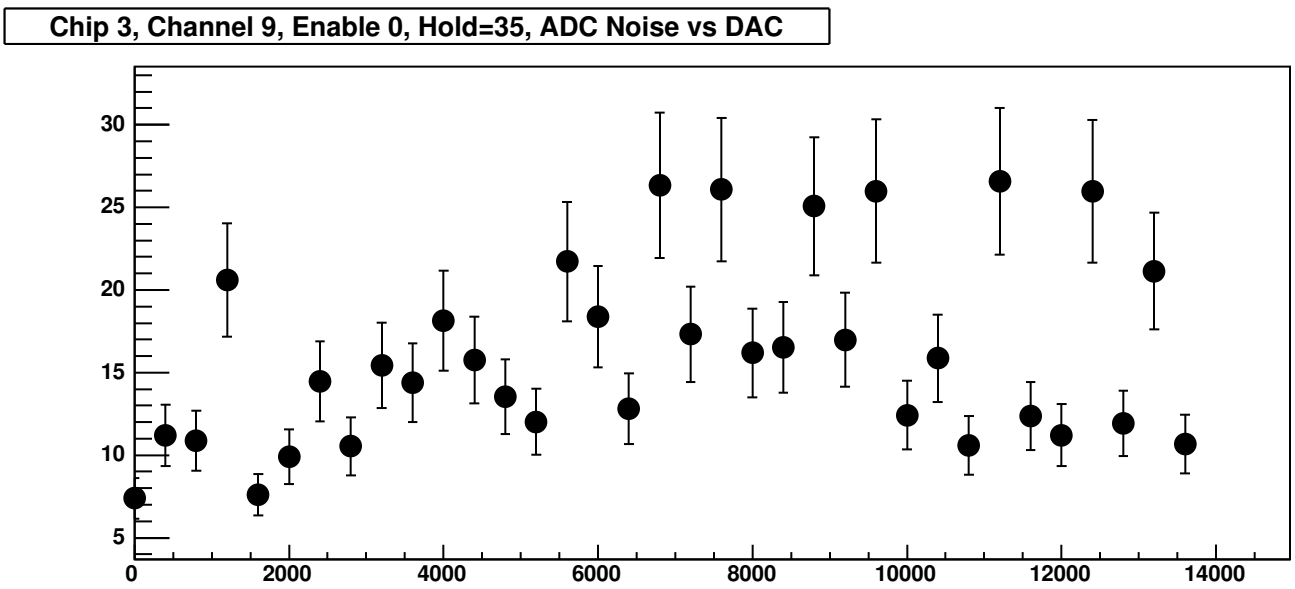


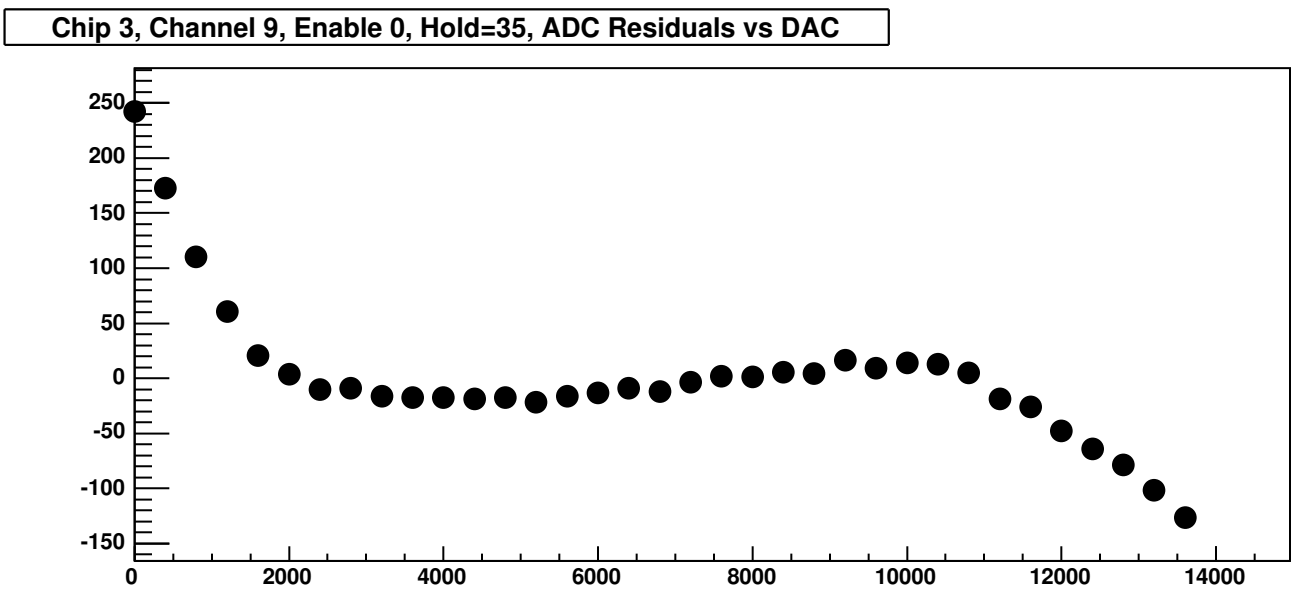

Chip 3, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 5427 / 23
p0 2.909e+04 ± 18.98
p1 0.09169 ± 0.002545
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

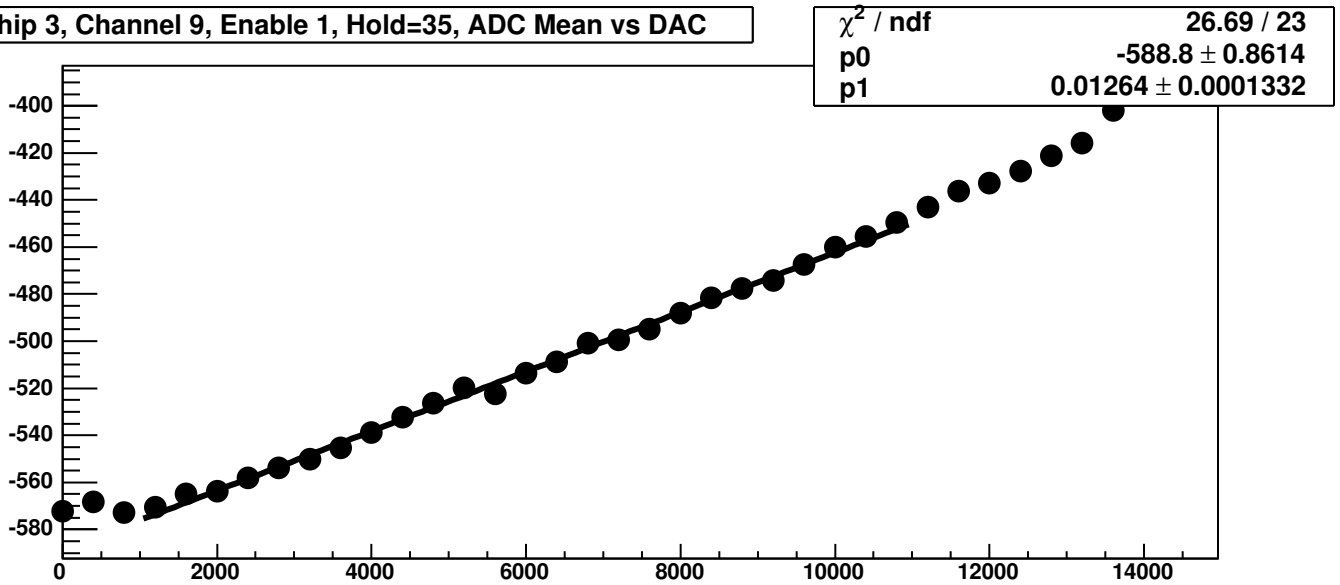

Chip 3, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 615.5 / 23
p0 -805.8 ± 1.269
p1 0.1726 ± 0.0002063
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

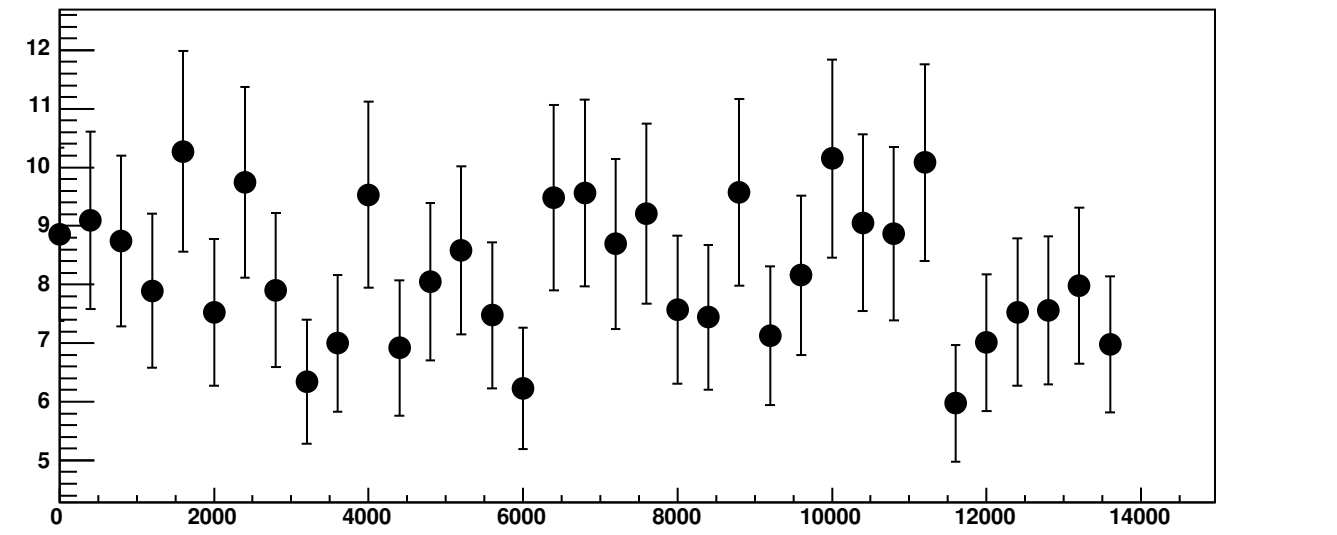

Chip 3, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

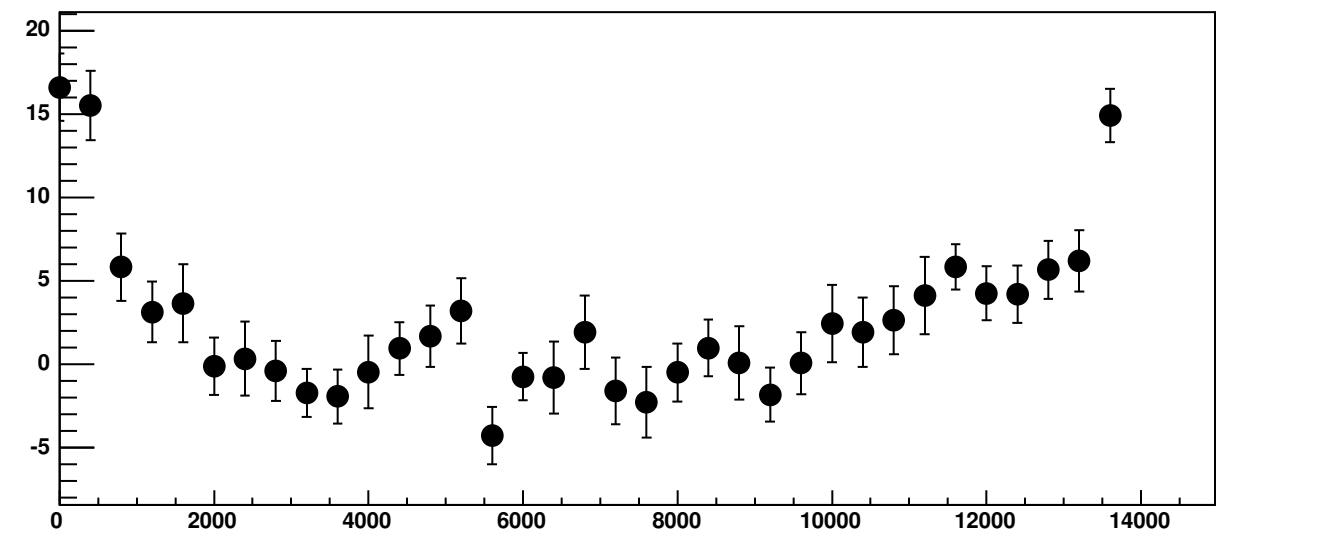
Chip 3, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

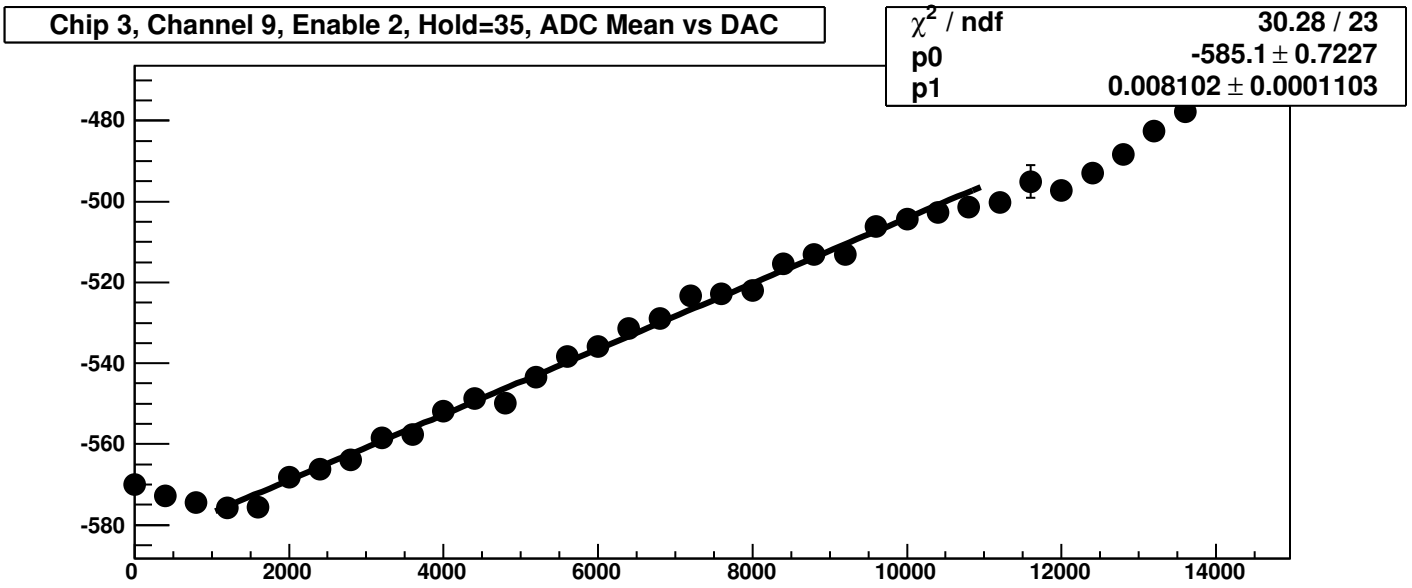


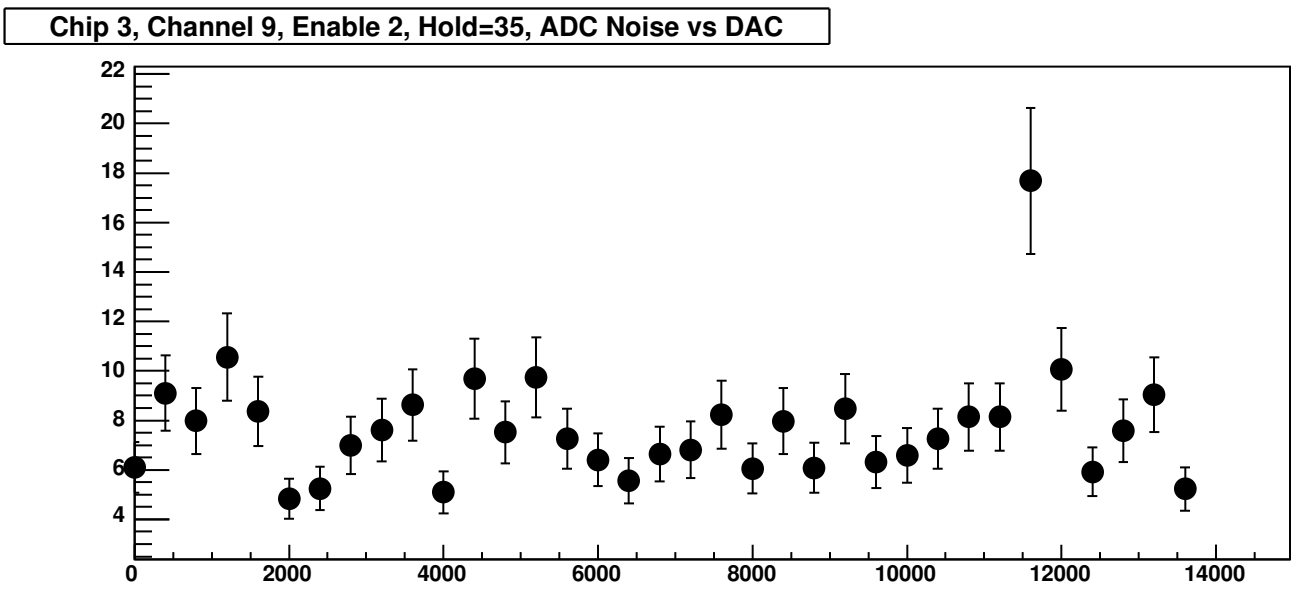
Chip 3, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC

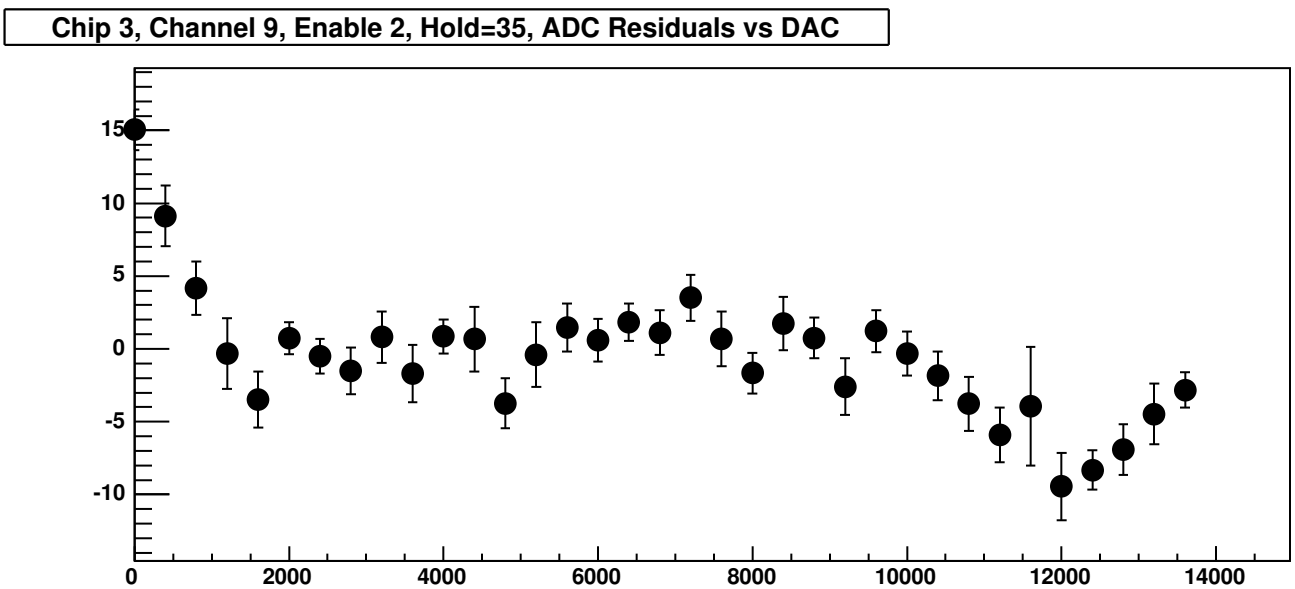


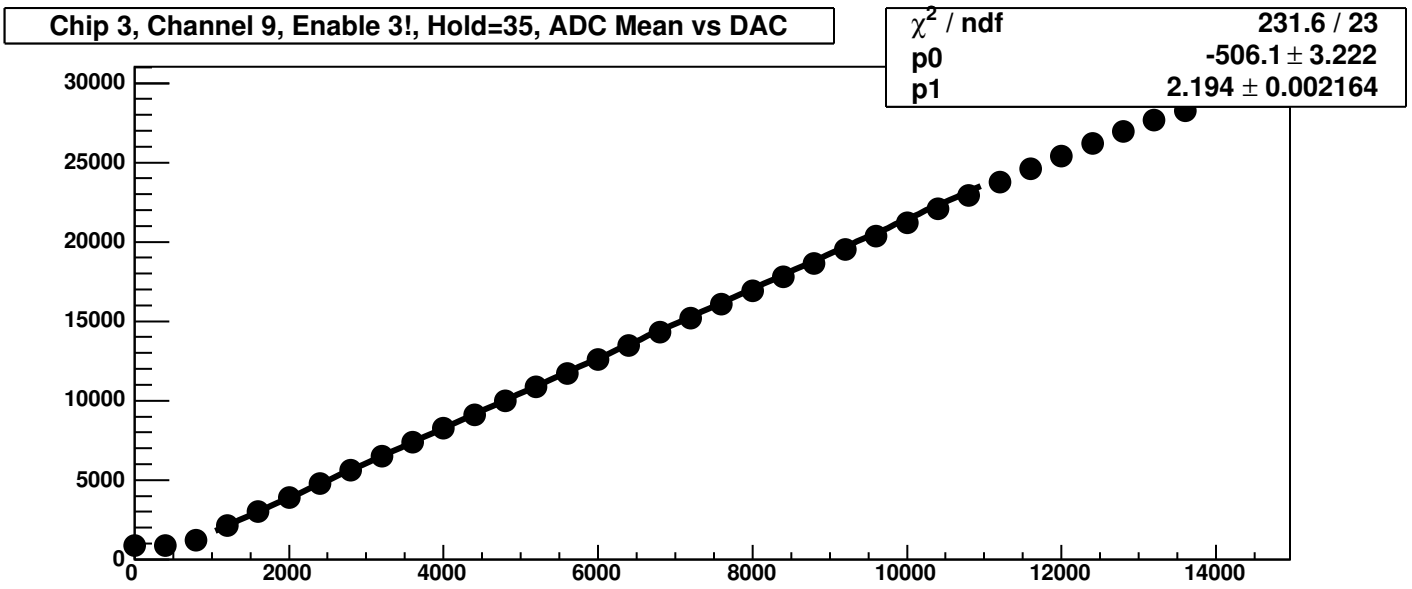
Chip 3, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

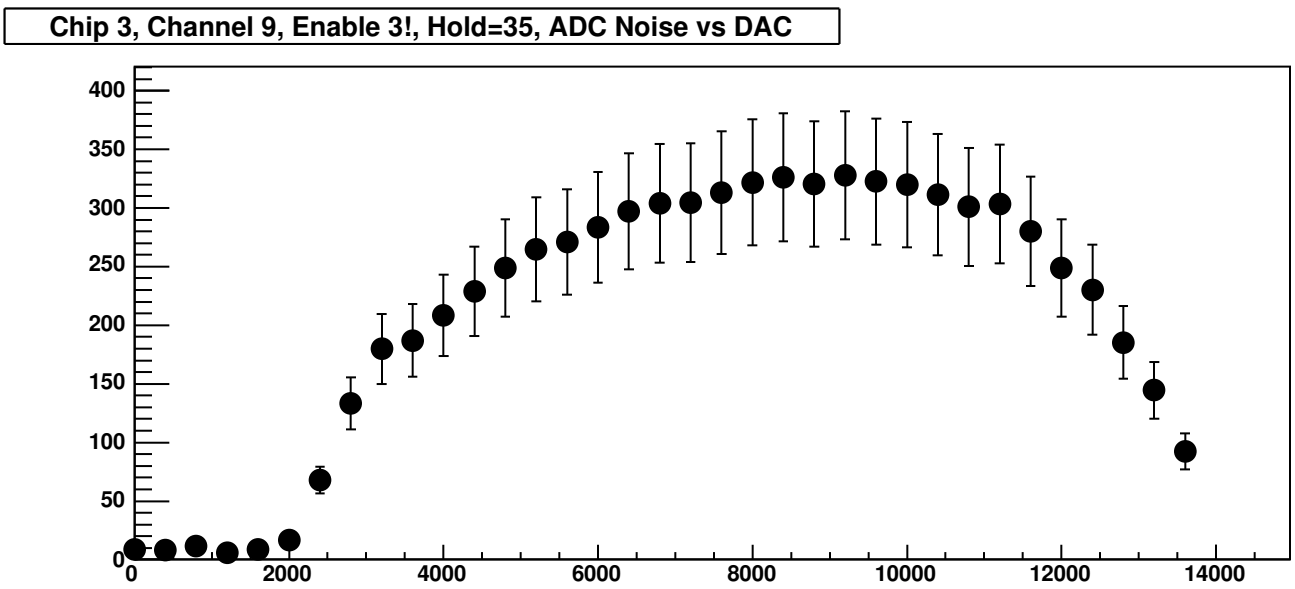


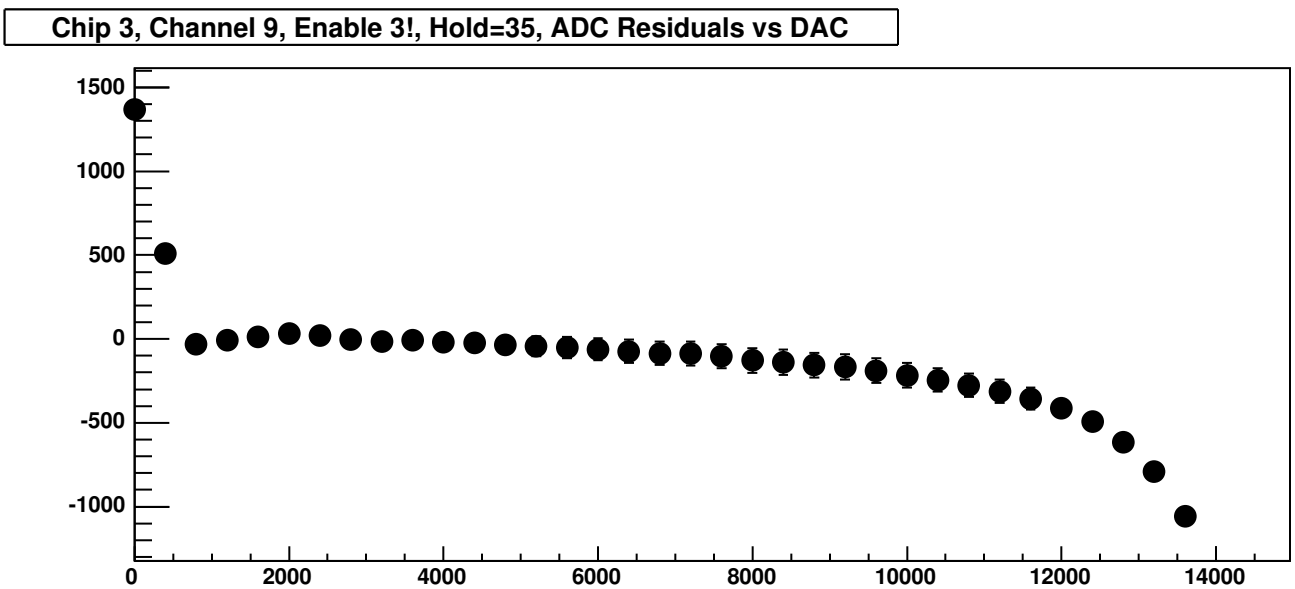

Chip 3, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 30.28 / 23
p0 -585.1 ± 0.7227
p1 0.008102 ± 0.0001103
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

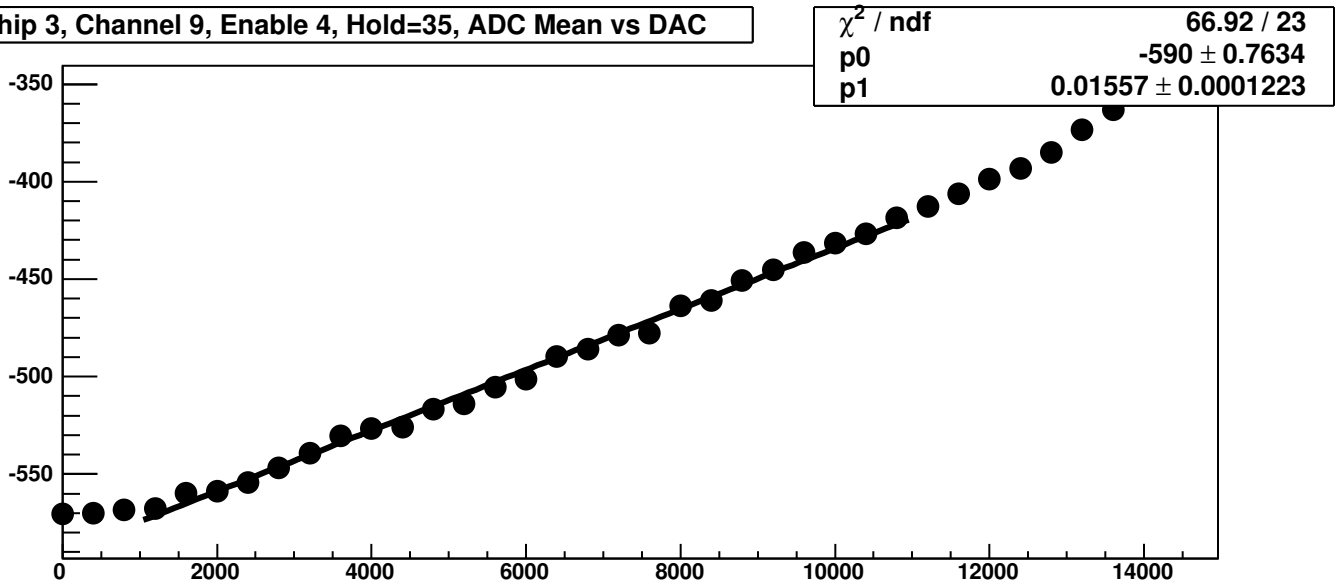

Chip 3, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 231.6 / 23
p0 -506.1 ± 3.222
p1 2.194 ± 0.002164
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

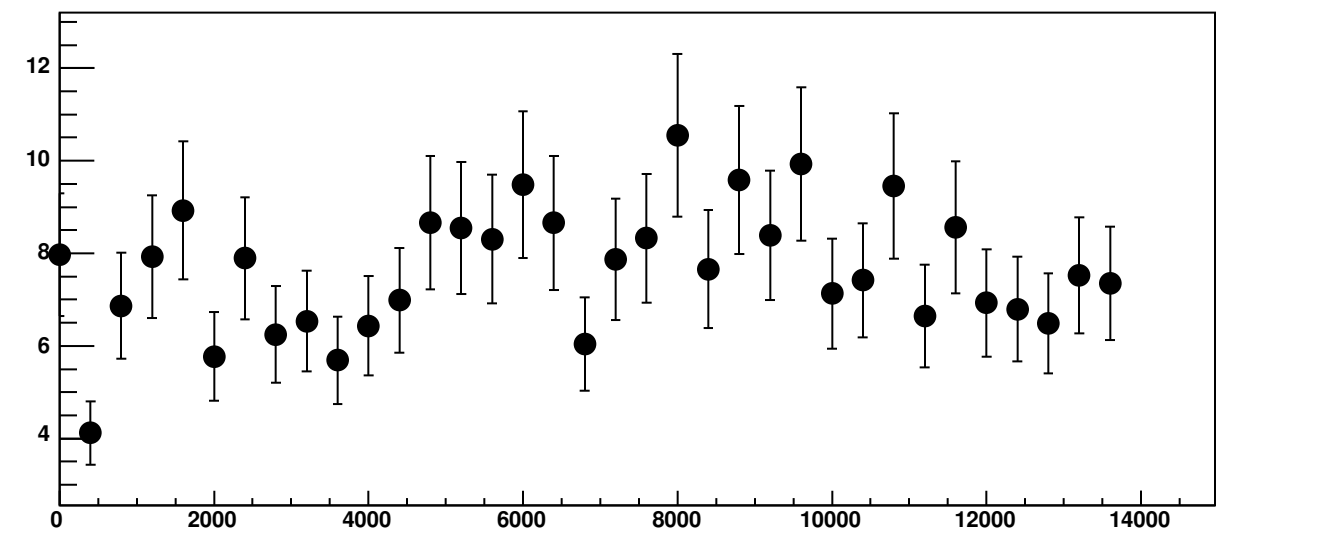

Chip 3, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

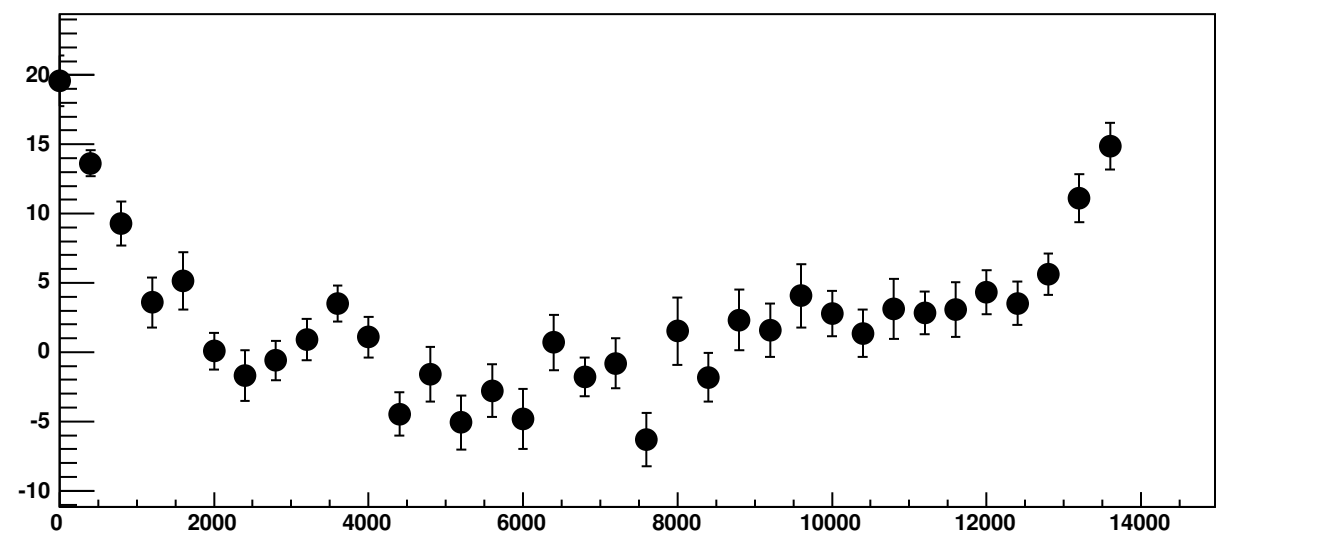
Chip 3, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



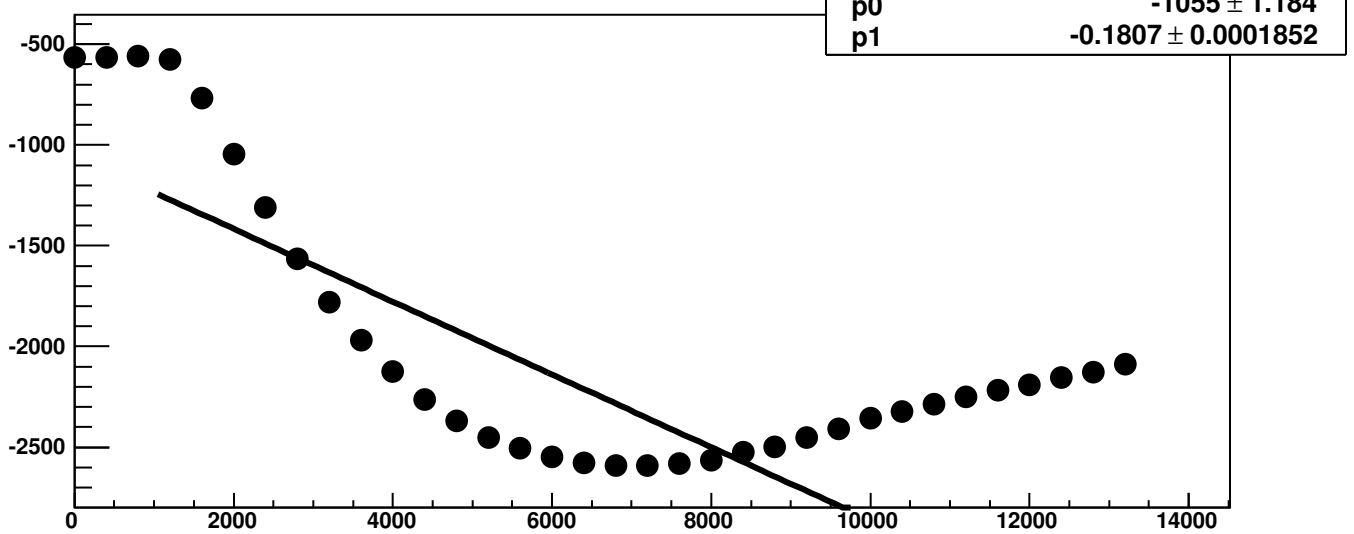
Chip 3, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



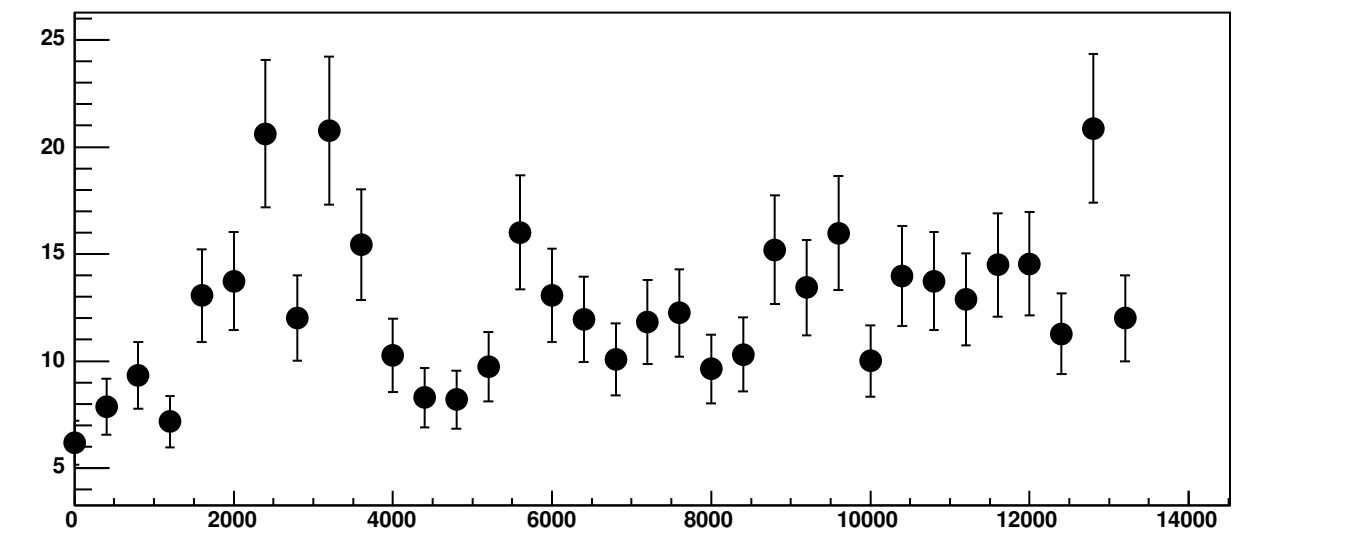
Chip 3, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



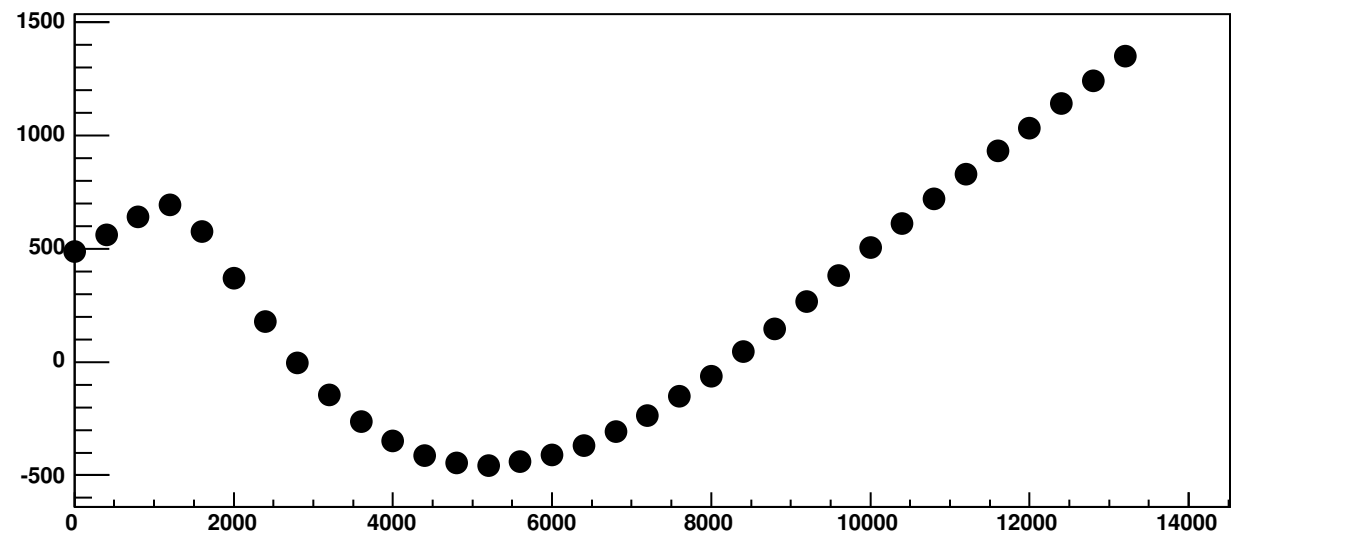
Chip 3, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

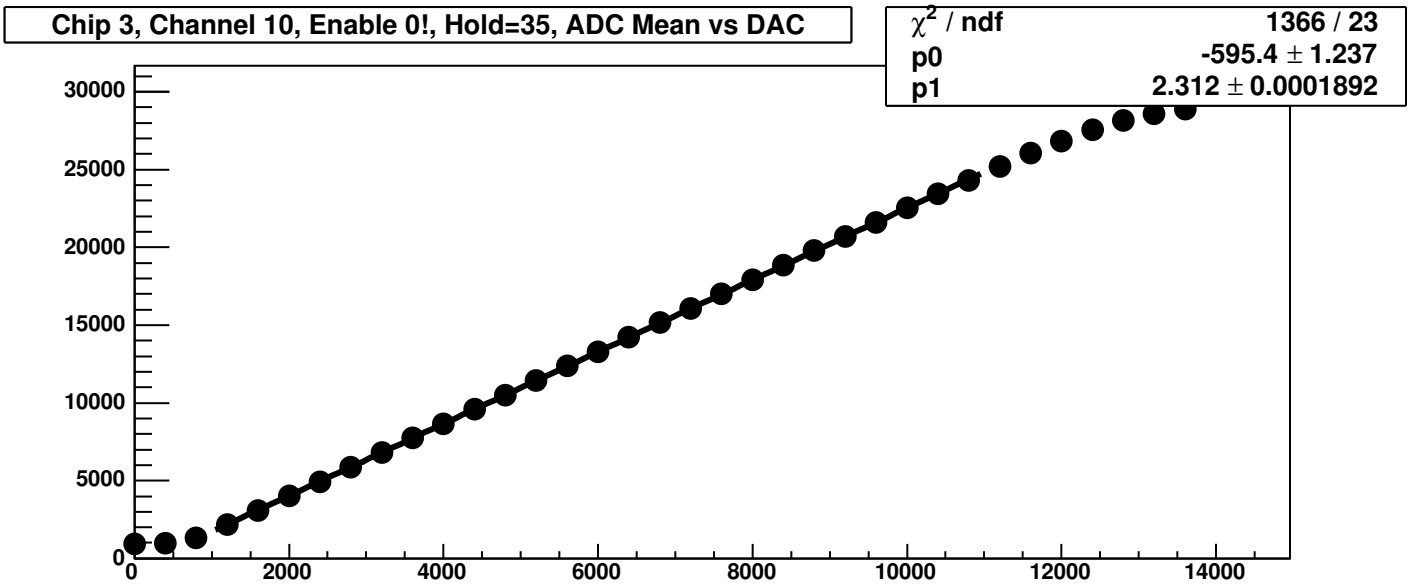


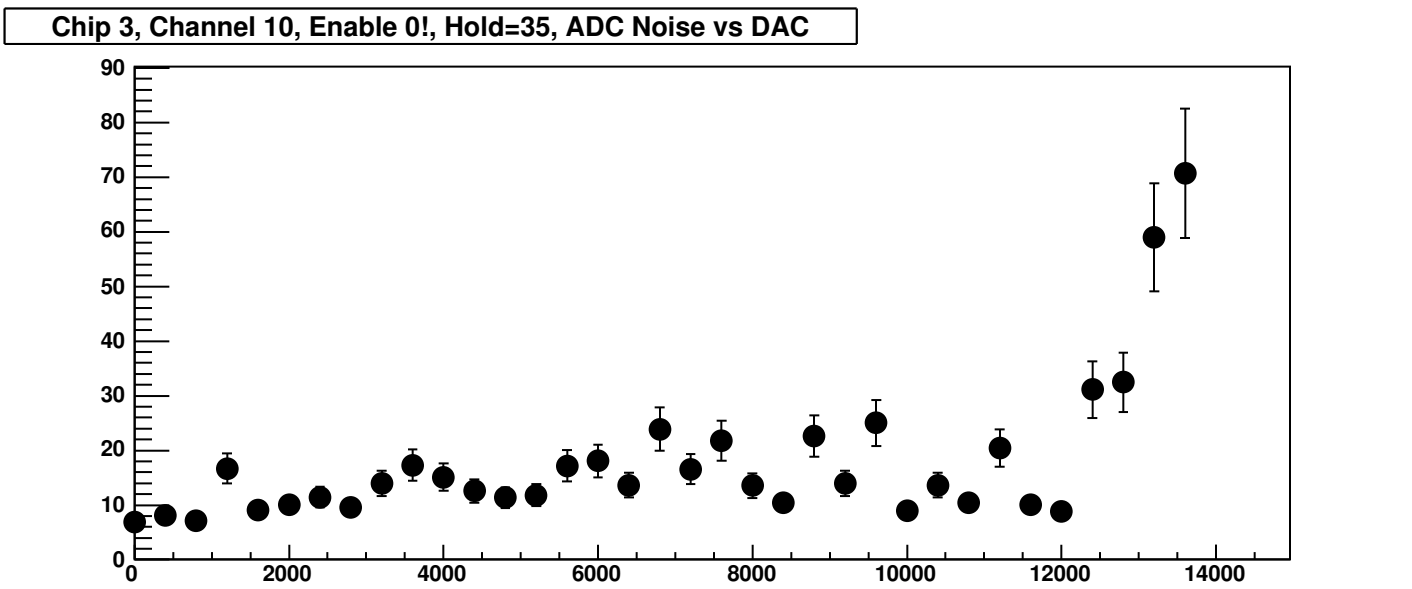
Chip 3, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

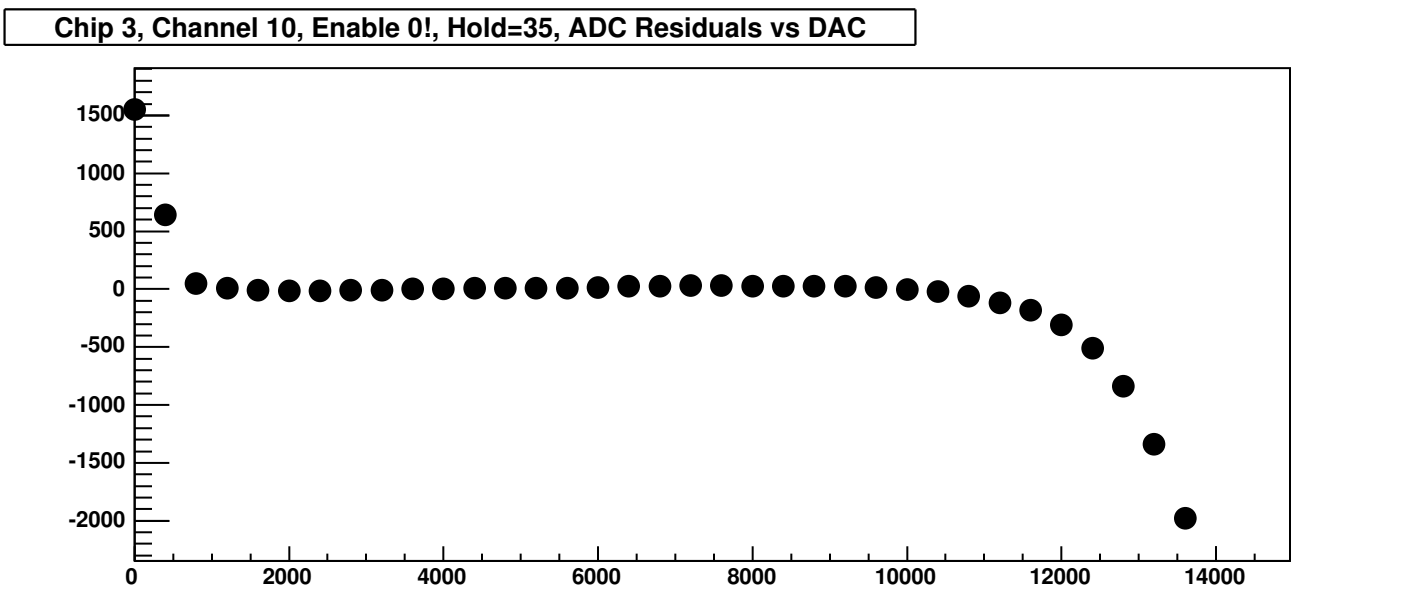


Chip 3, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

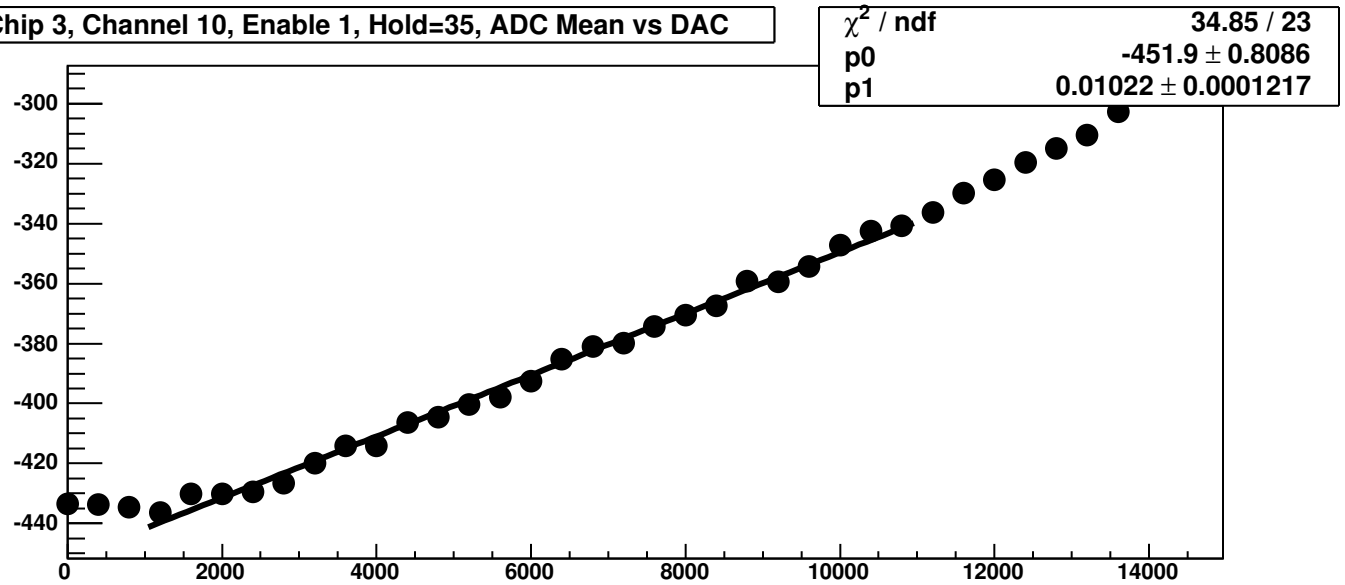



Chip 3, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1366 / 23
p0 -595.4 ± 1.237
p1 2.312 ± 0.0001892
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

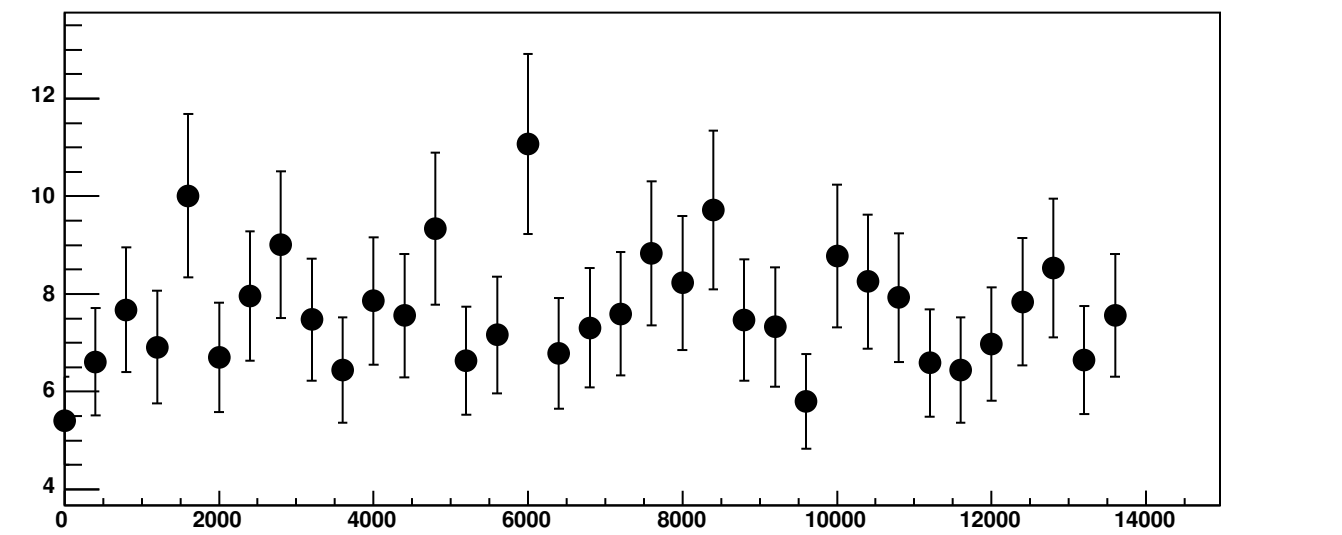

Chip 3, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

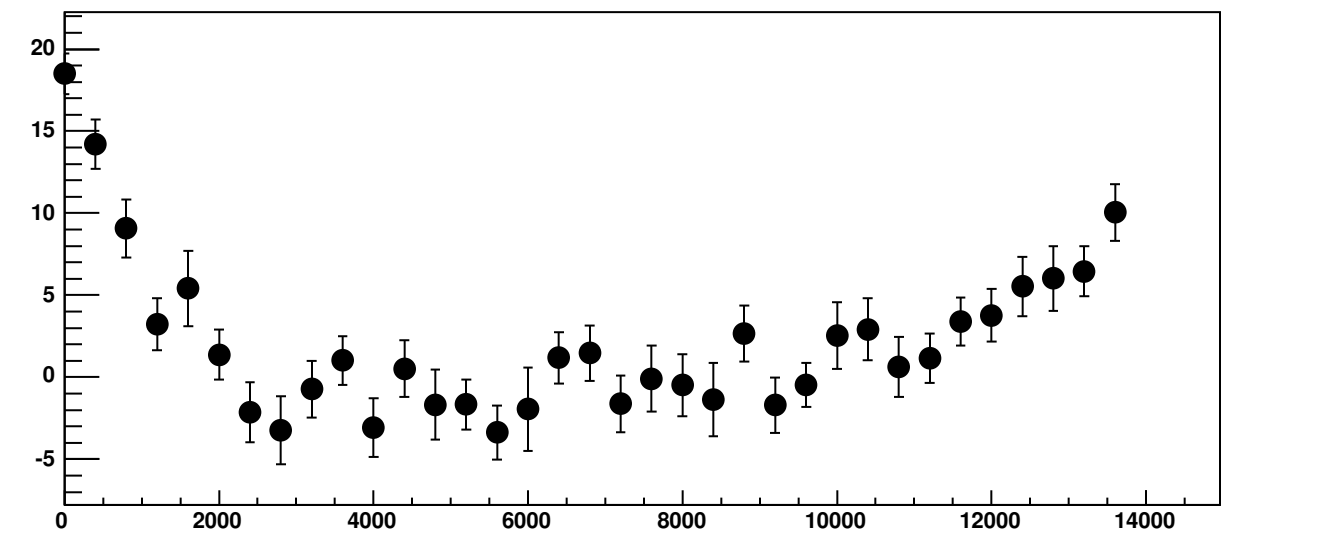
Chip 3, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

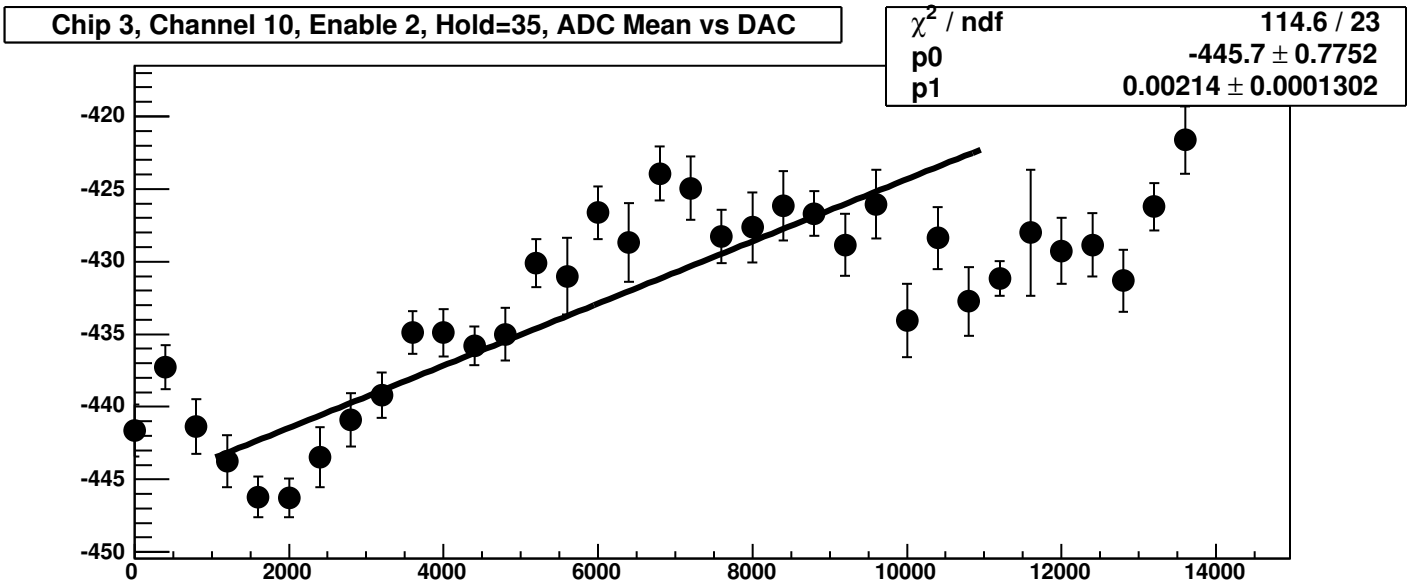


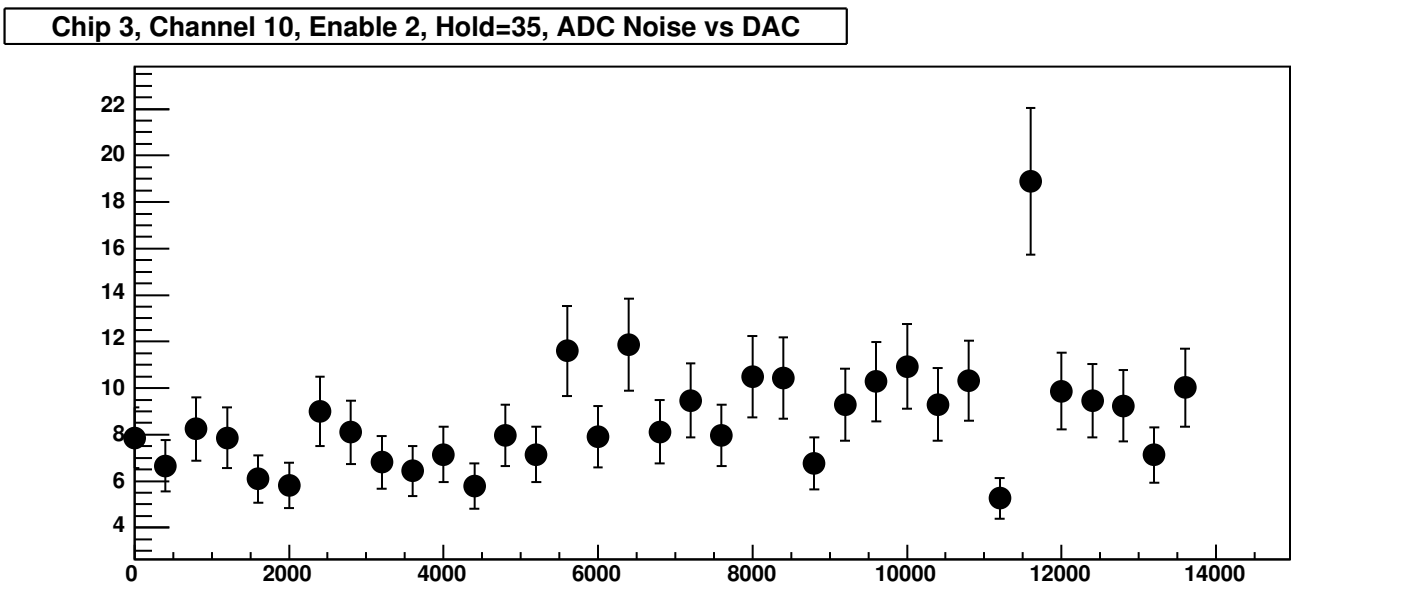
Chip 3, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC

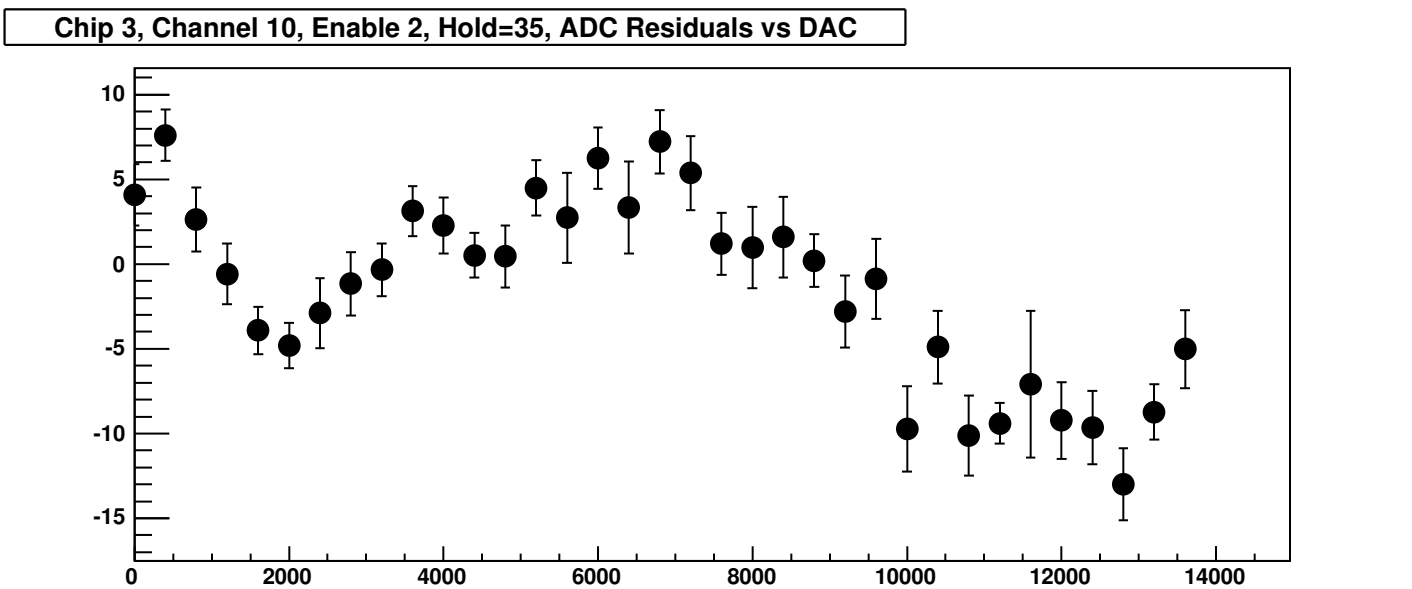


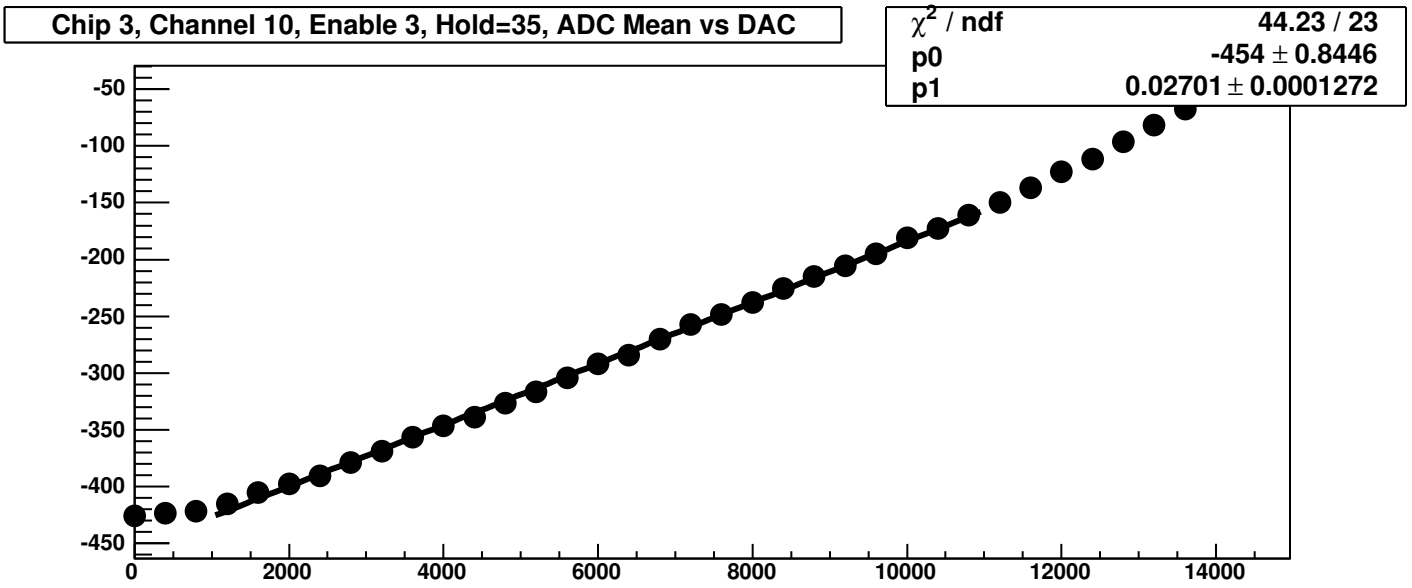
Chip 3, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

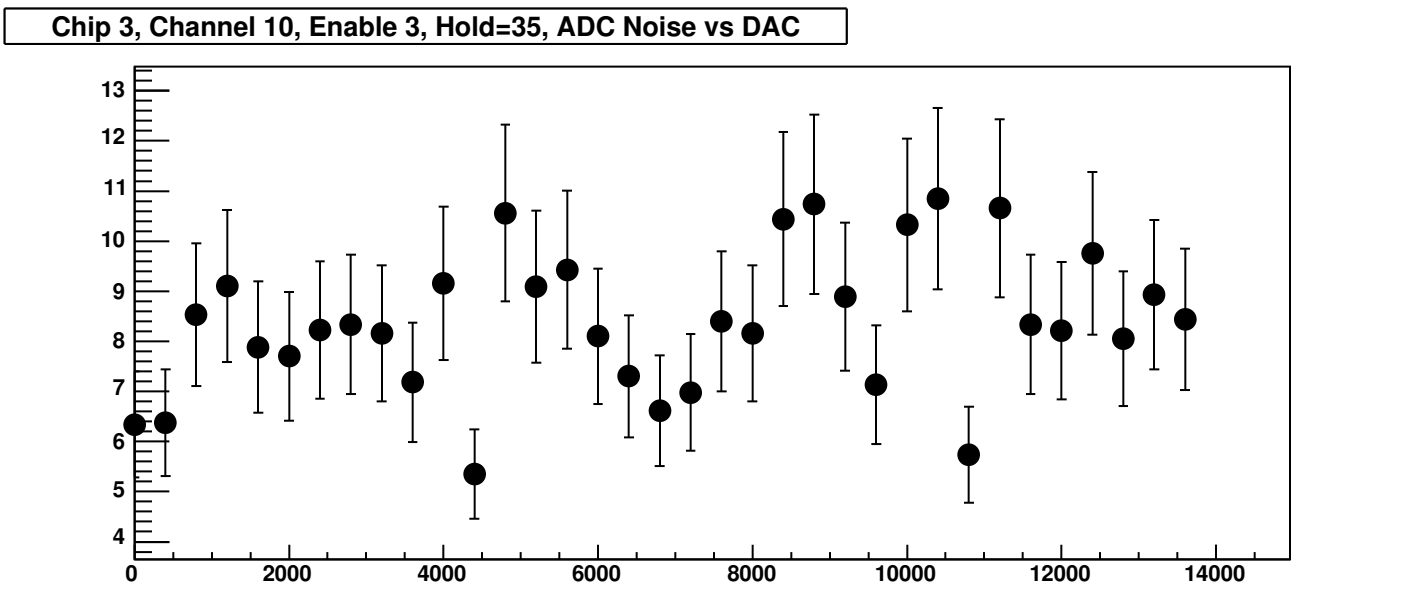


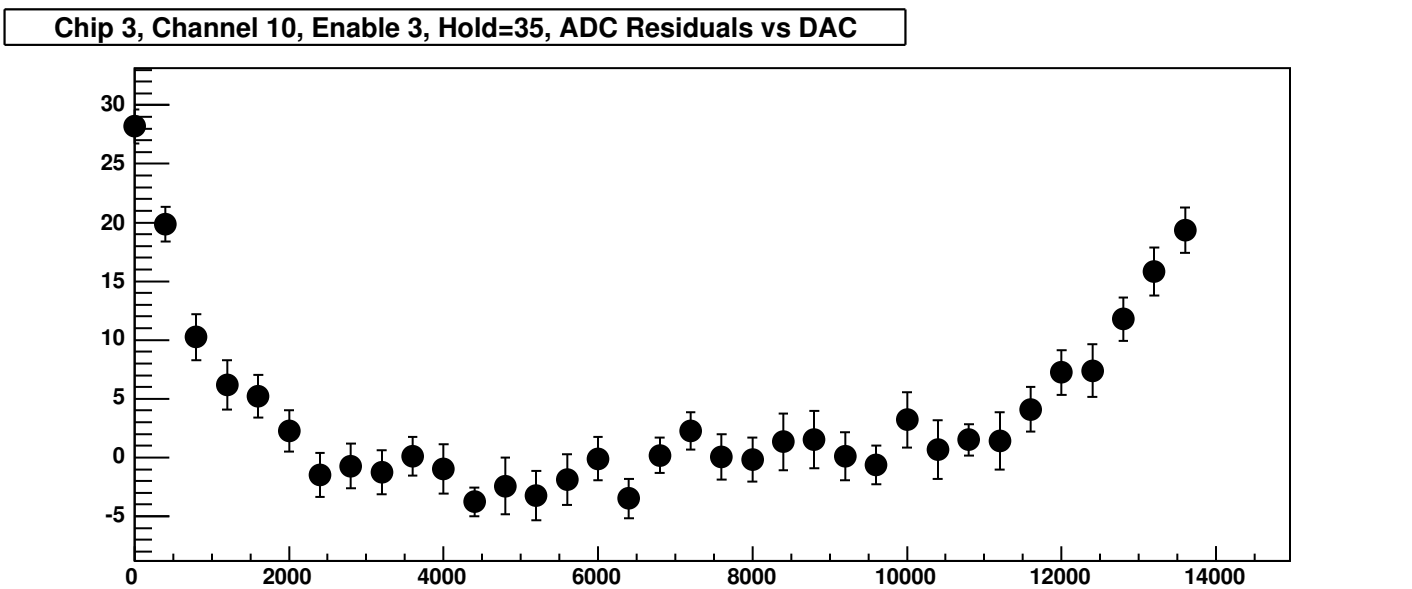

Chip 3, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 114.6 / 23
p0 -445.7 ± 0.7752
p1 0.00214 ± 0.0001302
-420
-425
-430
-435
-440
-445
-450
0
2000
4000
6000
8000
10000
12000
14000

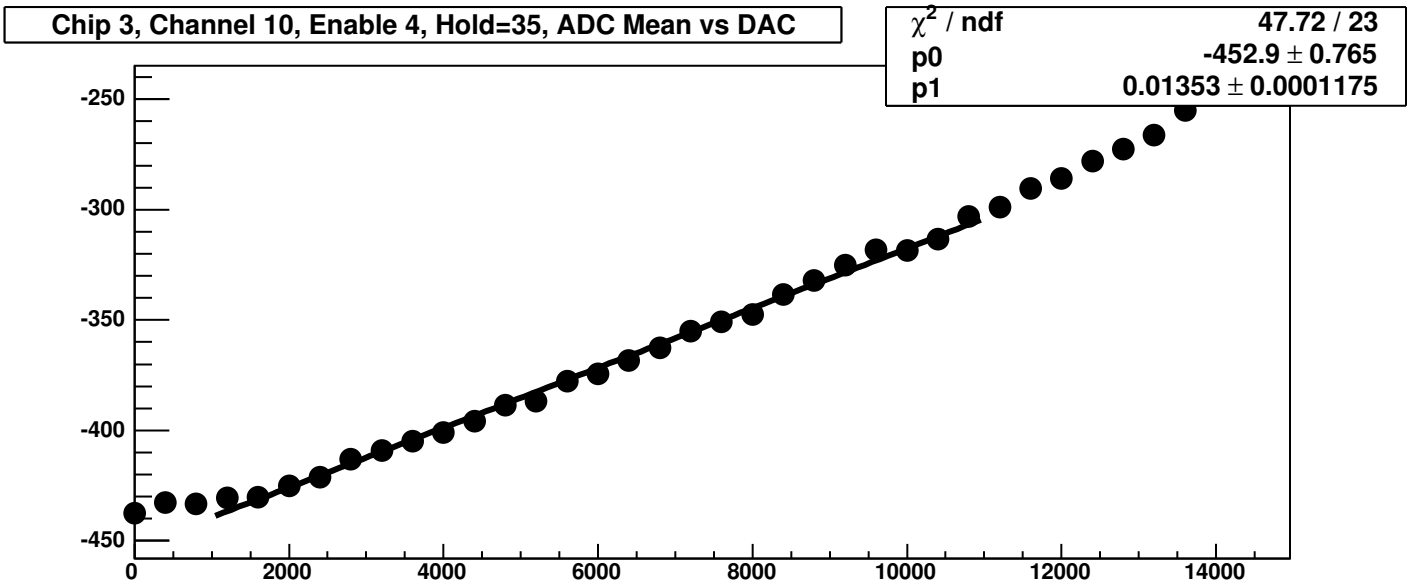

Chip 3, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

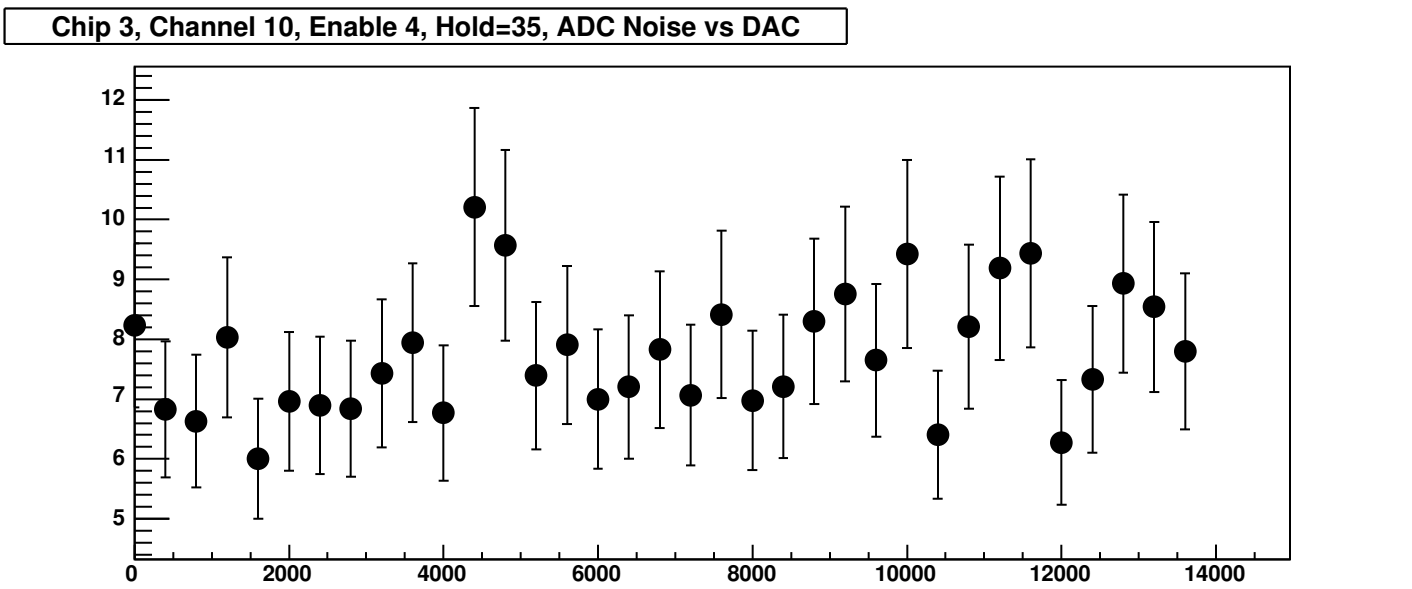

Chip 3, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

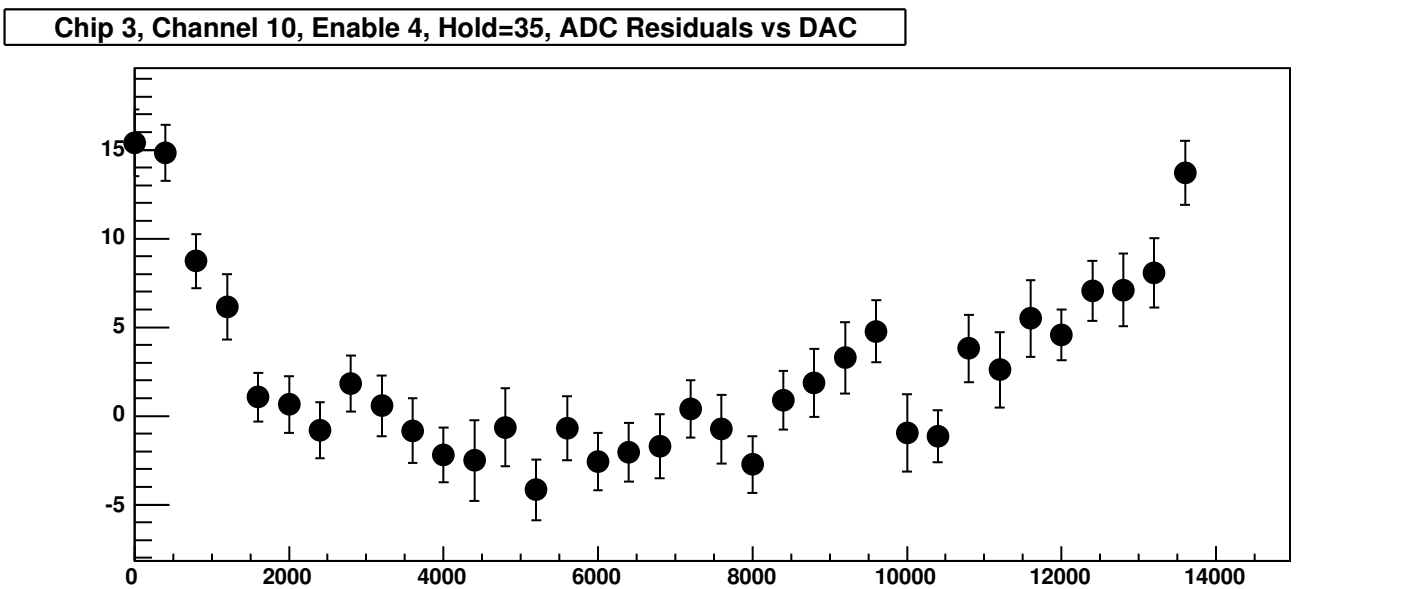

Chip 3, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 44.23 / 23
p0 -454 ± 0.8446
p1 0.02701 ± 0.0001272
-50
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

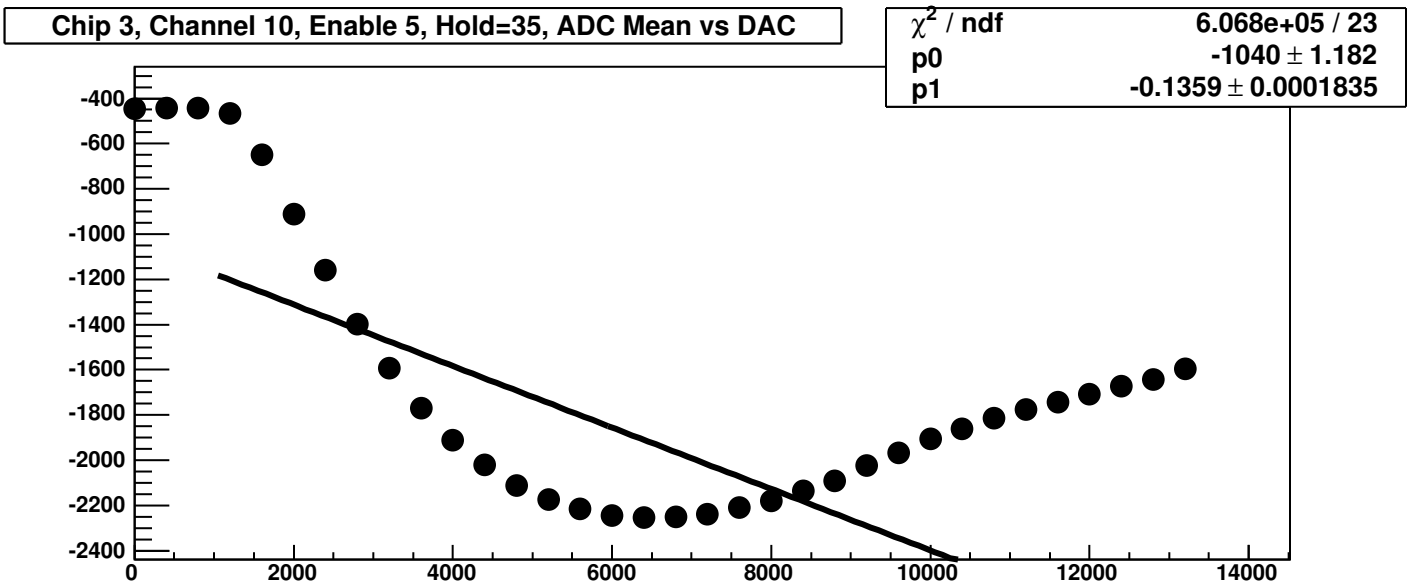

Chip 3, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

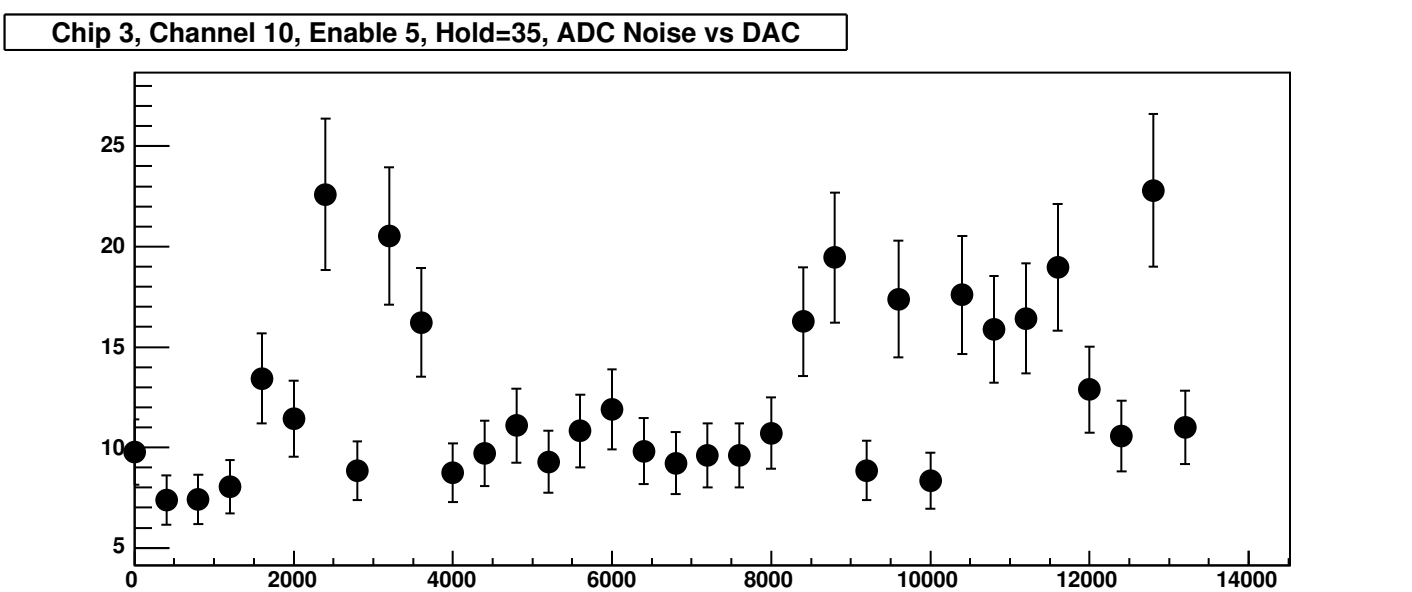

Chip 3, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

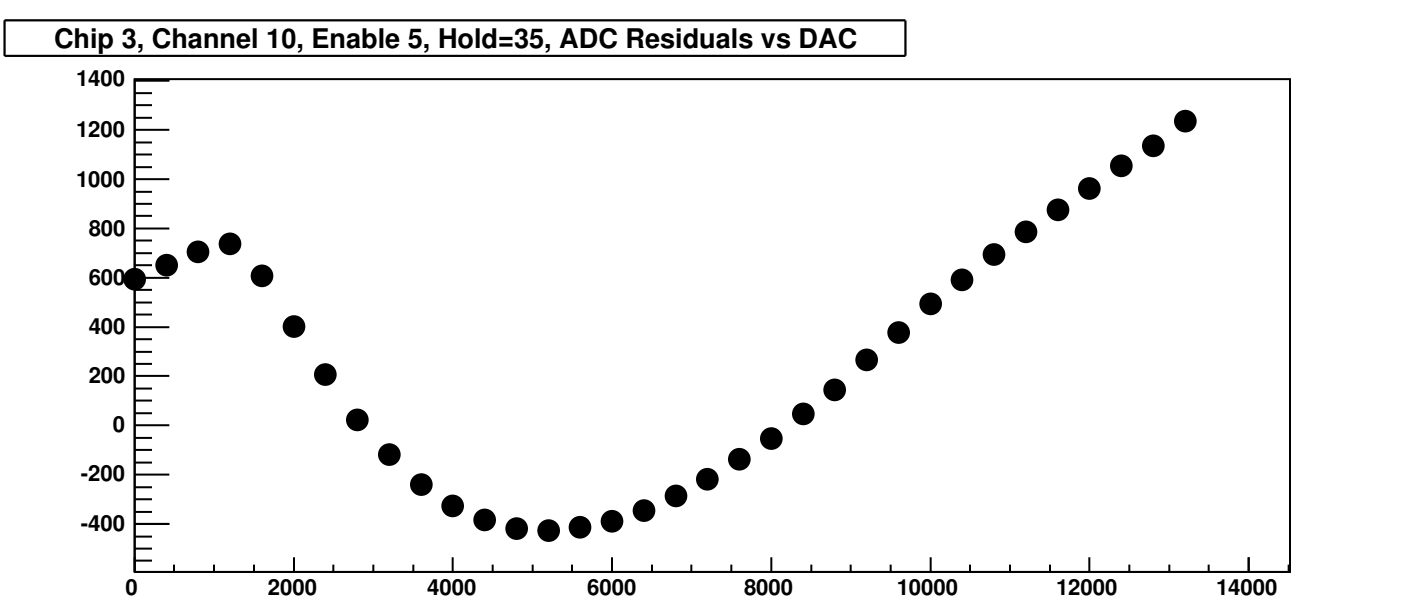

Chip 3, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 47.72 / 23
p0 -452.9 ± 0.765
p1 0.01353 ± 0.0001175
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

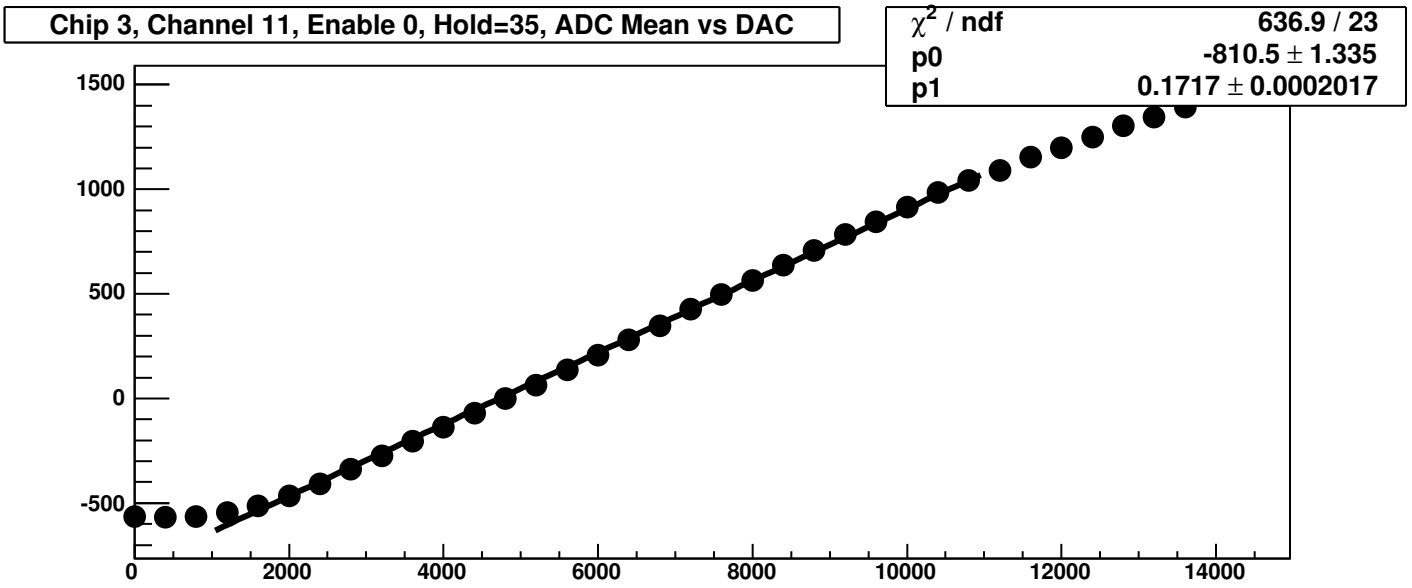

Chip 3, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

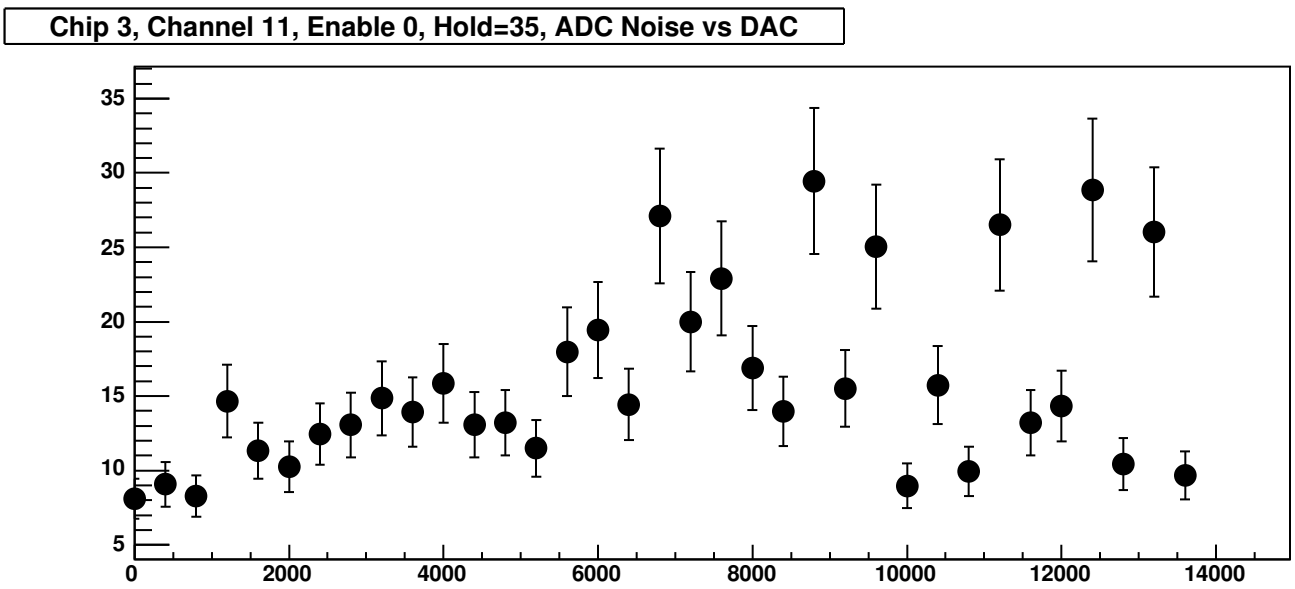

Chip 3, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

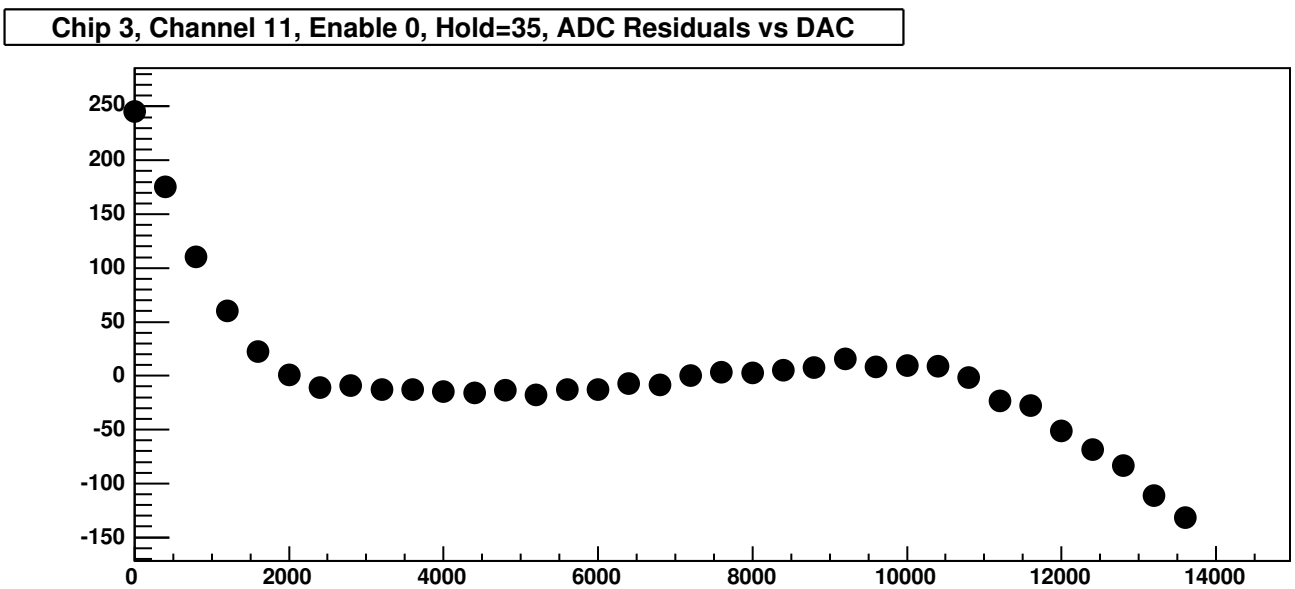

Chip 3, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 6.068e+05 / 23
p0 -1040 ± 1.182
p1 -0.1359 ± 0.0001835
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
-2000
-2200
-2400
0
2000
4000
6000
8000
10000
12000
14000

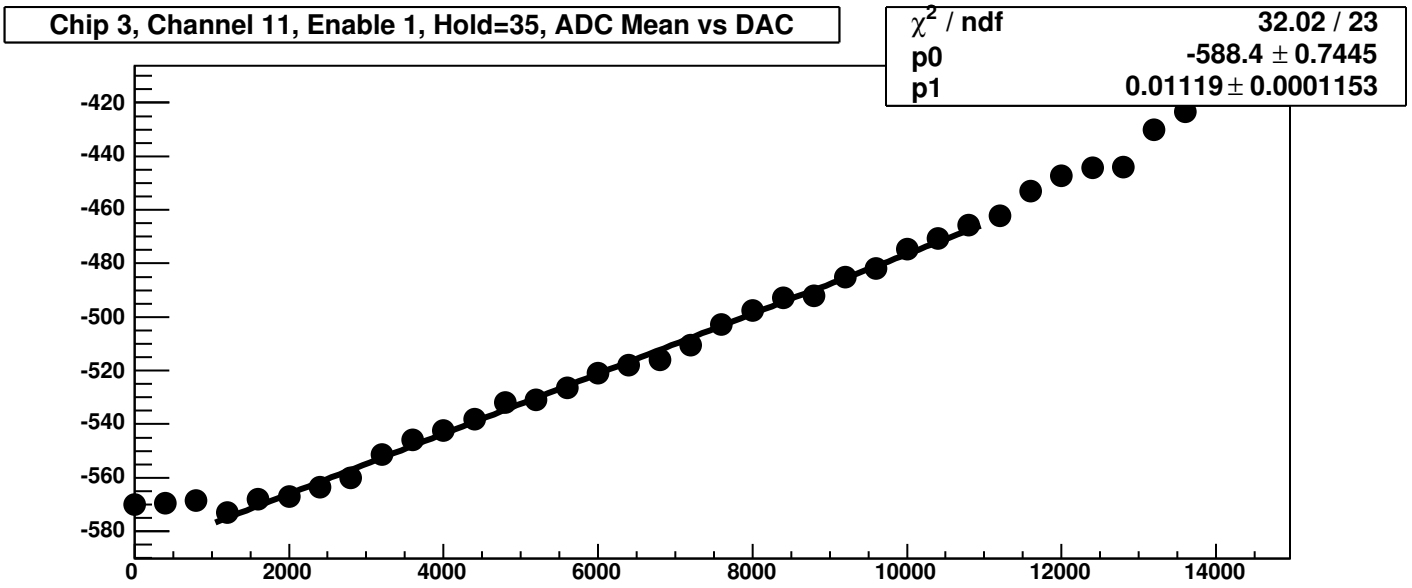

Chip 3, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

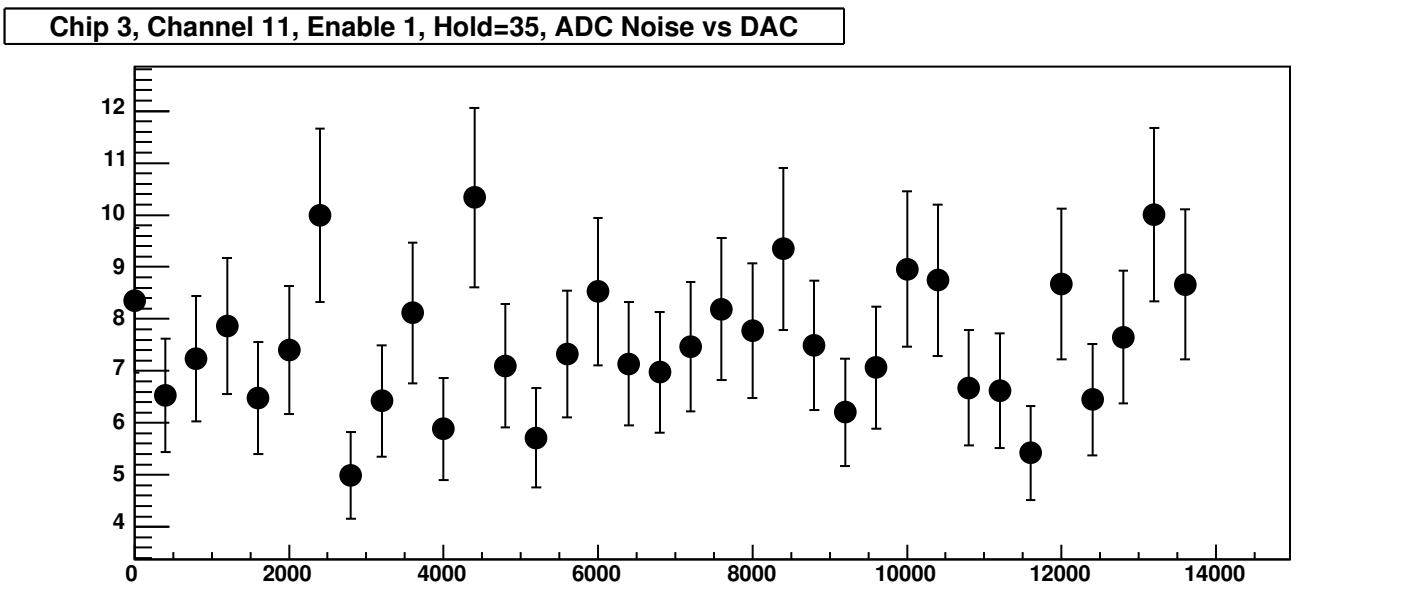

Chip 3, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

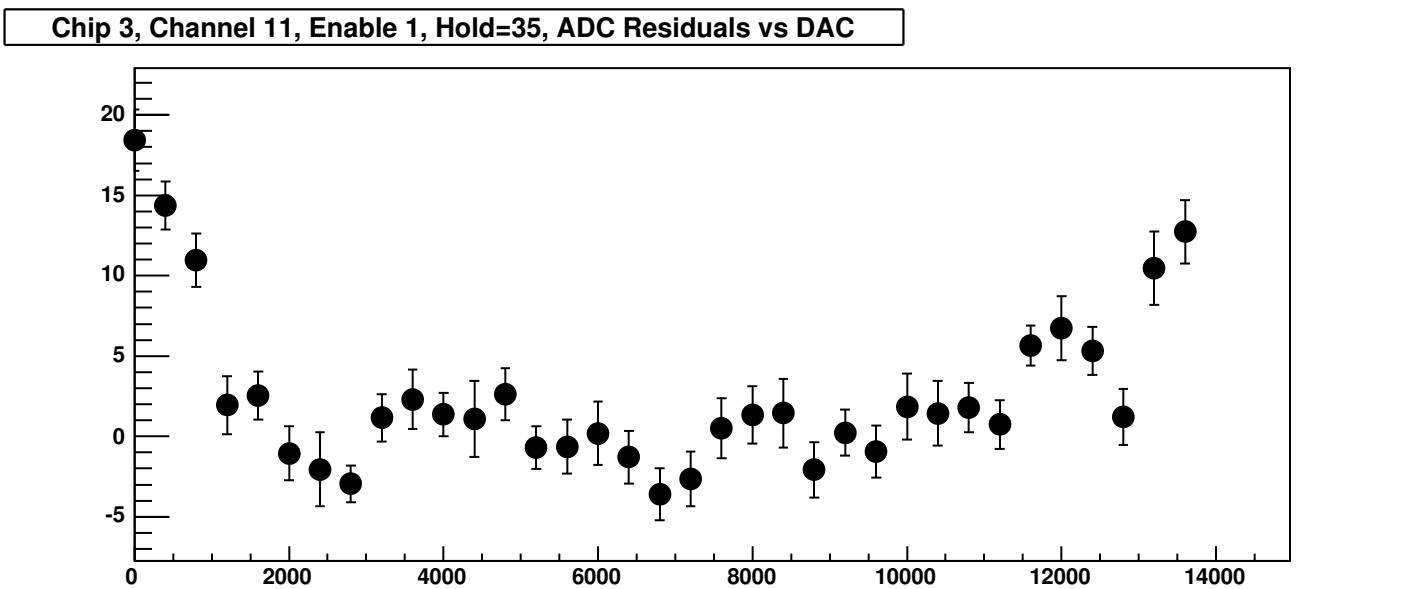

Chip 3, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 636.9 / 23
p0 -810.5 ± 1.335
p1 0.1717 ± 0.0002017
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

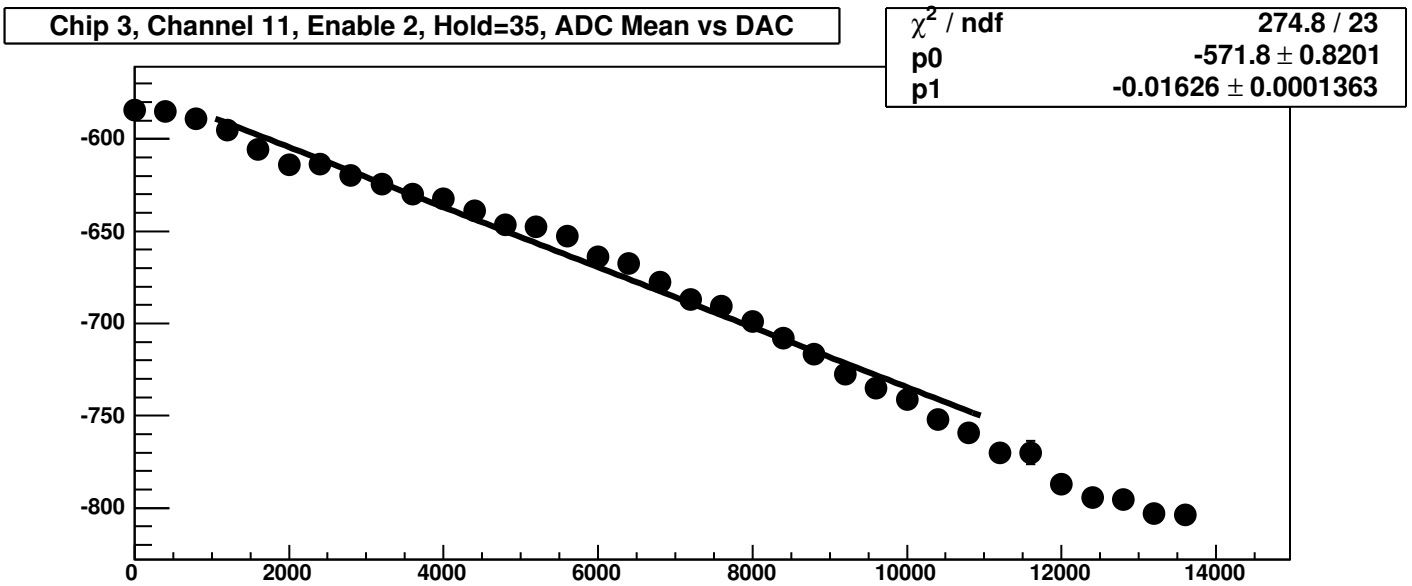

Chip 3, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

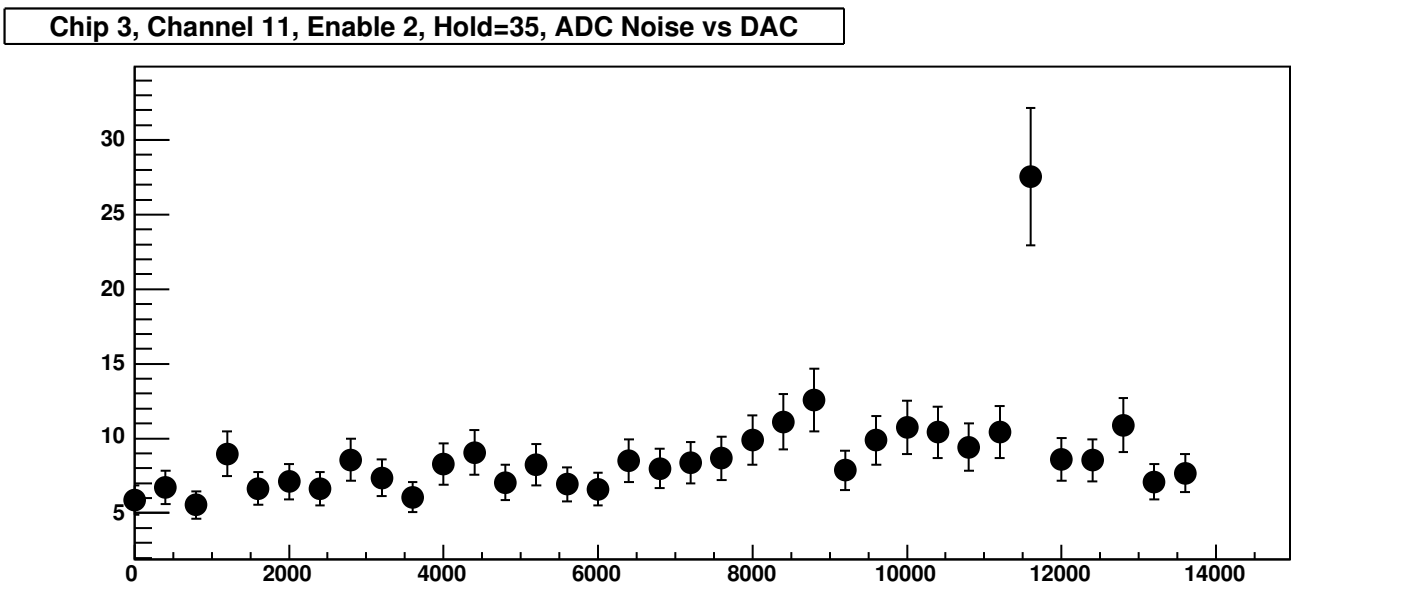

Chip 3, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

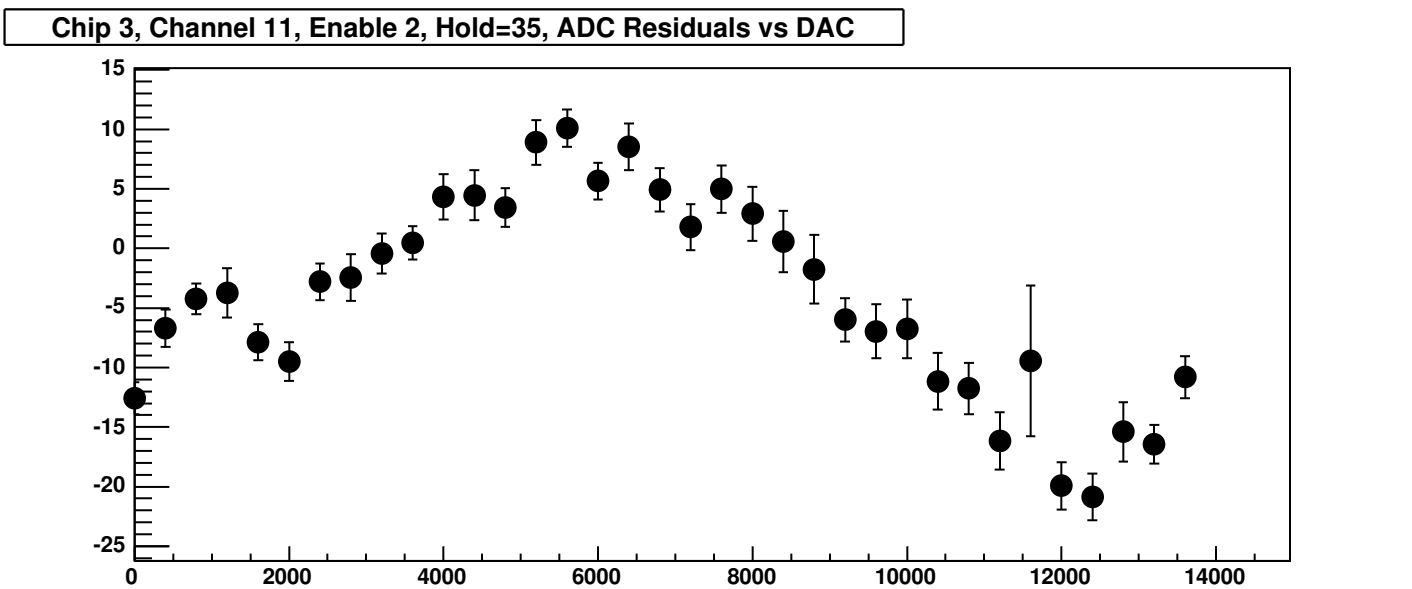

Chip 3, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.02 / 23
p0 -588.4 ± 0.7445
p1 0.01119 ± 0.0001153
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000

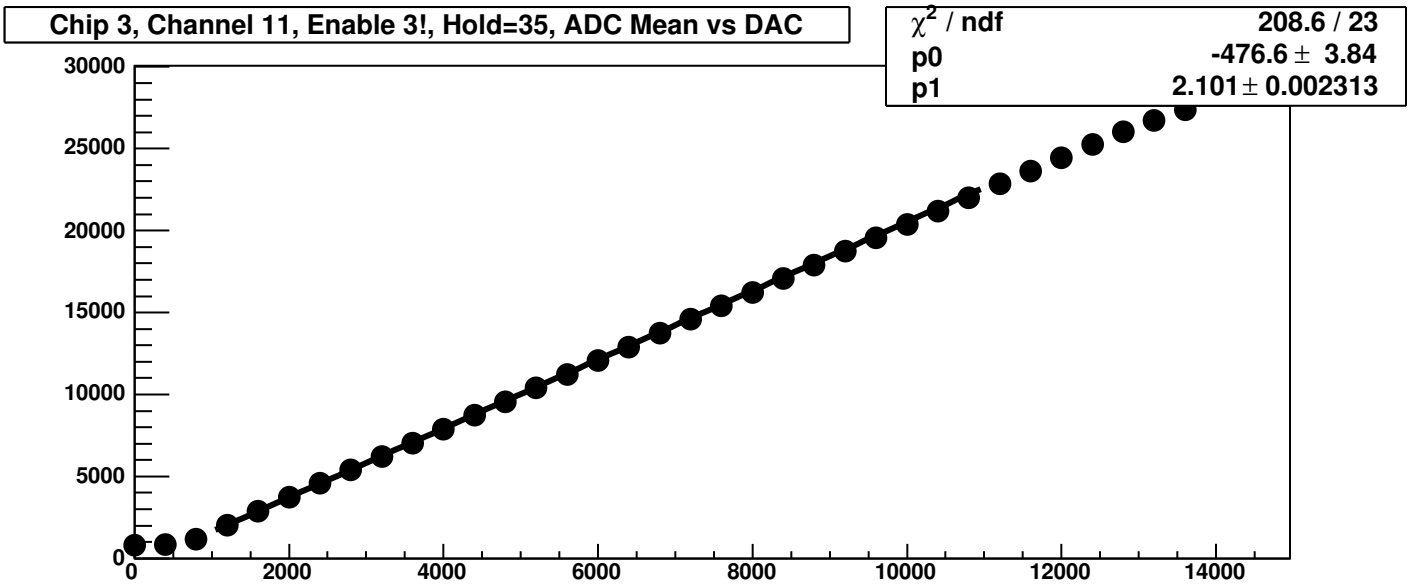

Chip 3, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

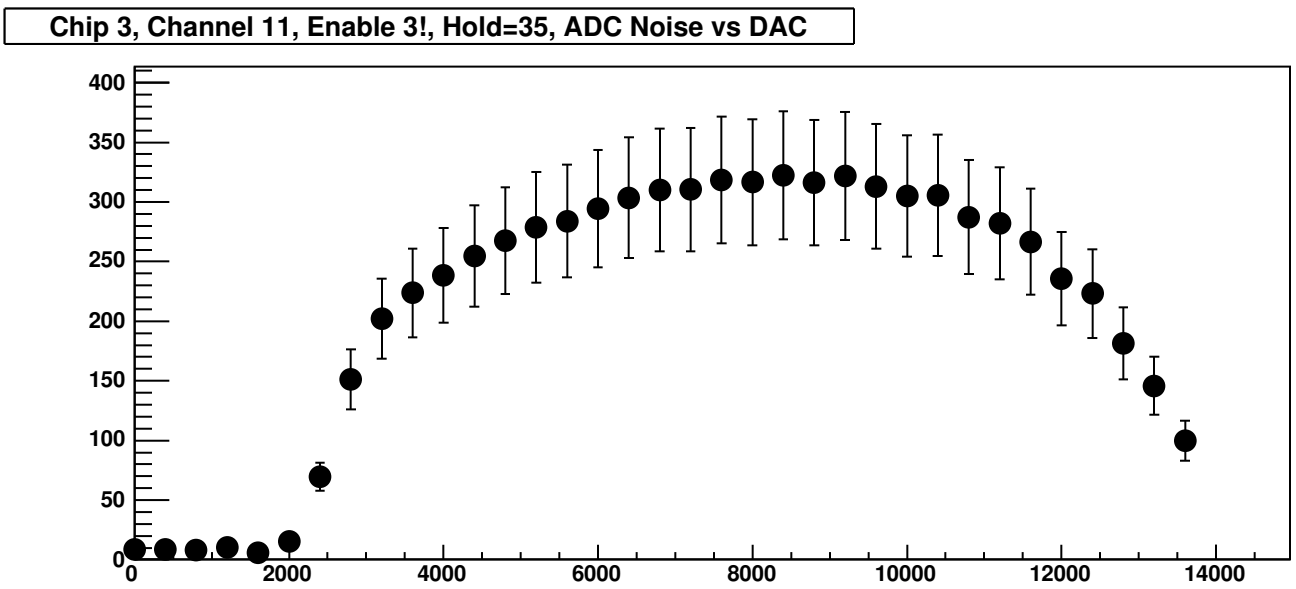

Chip 3, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

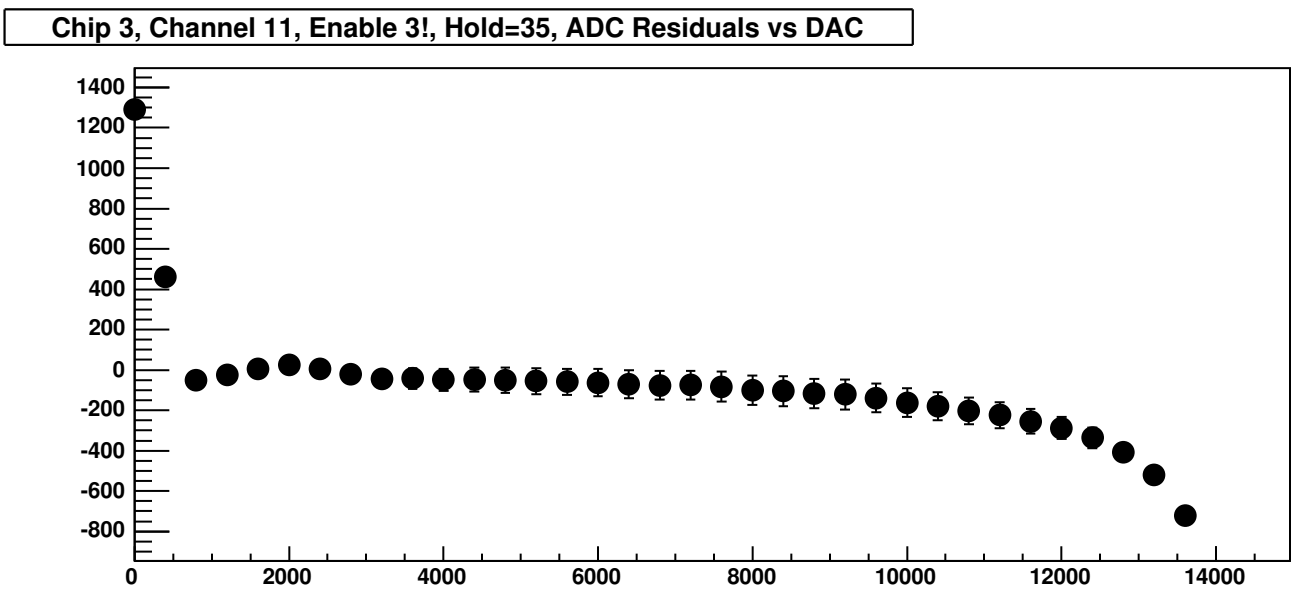

Chip 3, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 274.8 / 23
p0 -571.8 ± 0.8201
p1 -0.01626 ± 0.0001363
-600
-650
-700
-750
-800
0
2000
4000
6000
8000
10000
12000
14000

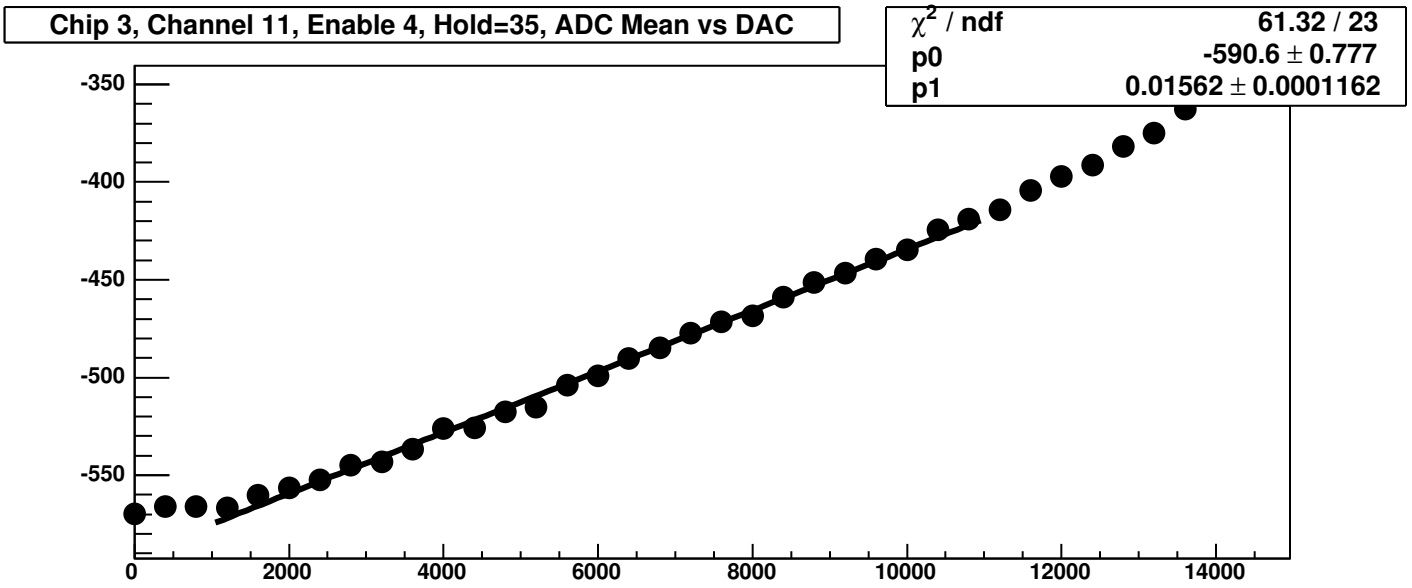

Chip 3, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

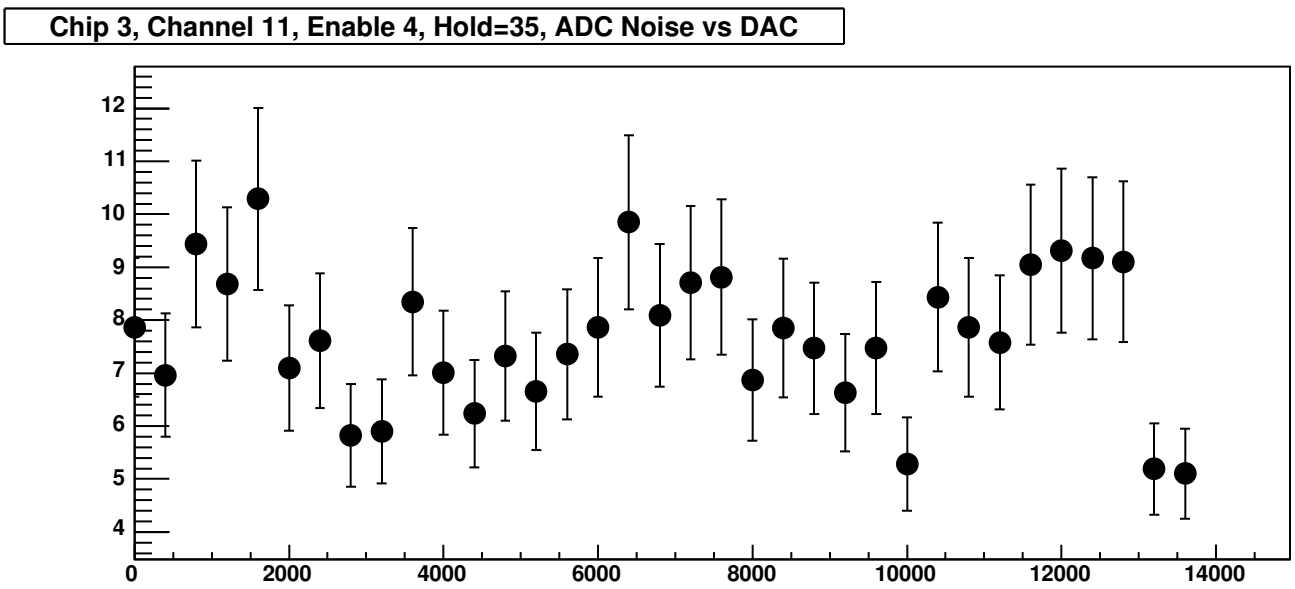

Chip 3, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

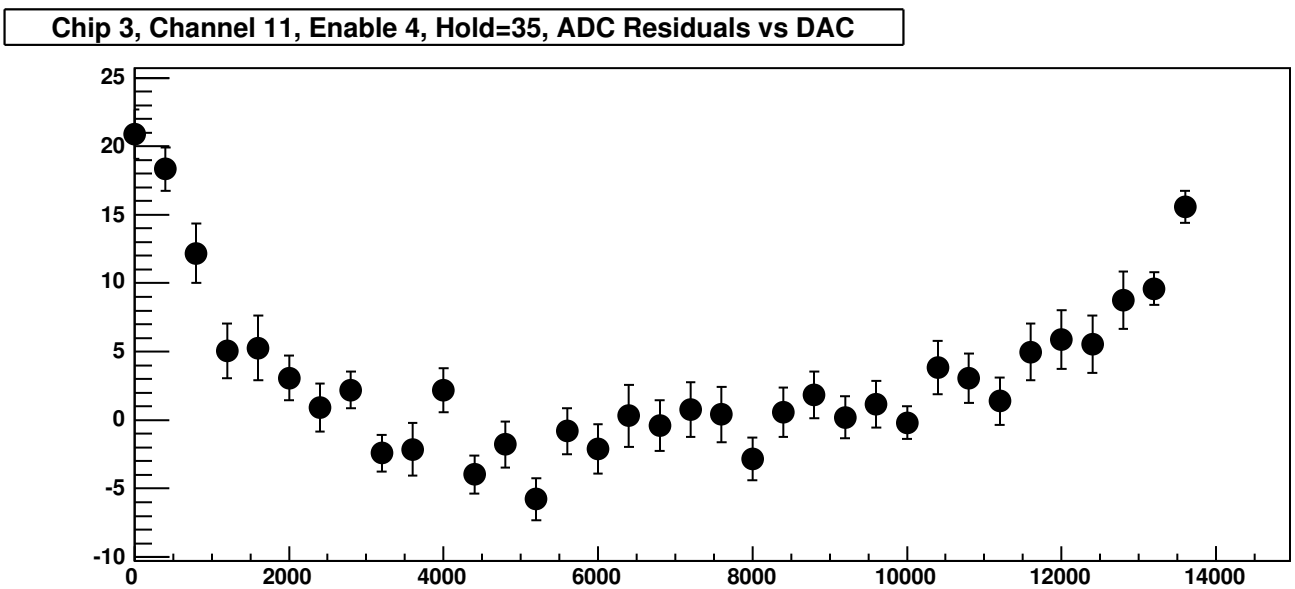

Chip 3, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 208.6 / 23
p0 -476.6 ± 3.84
p1 2.101 ± 0.002313
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

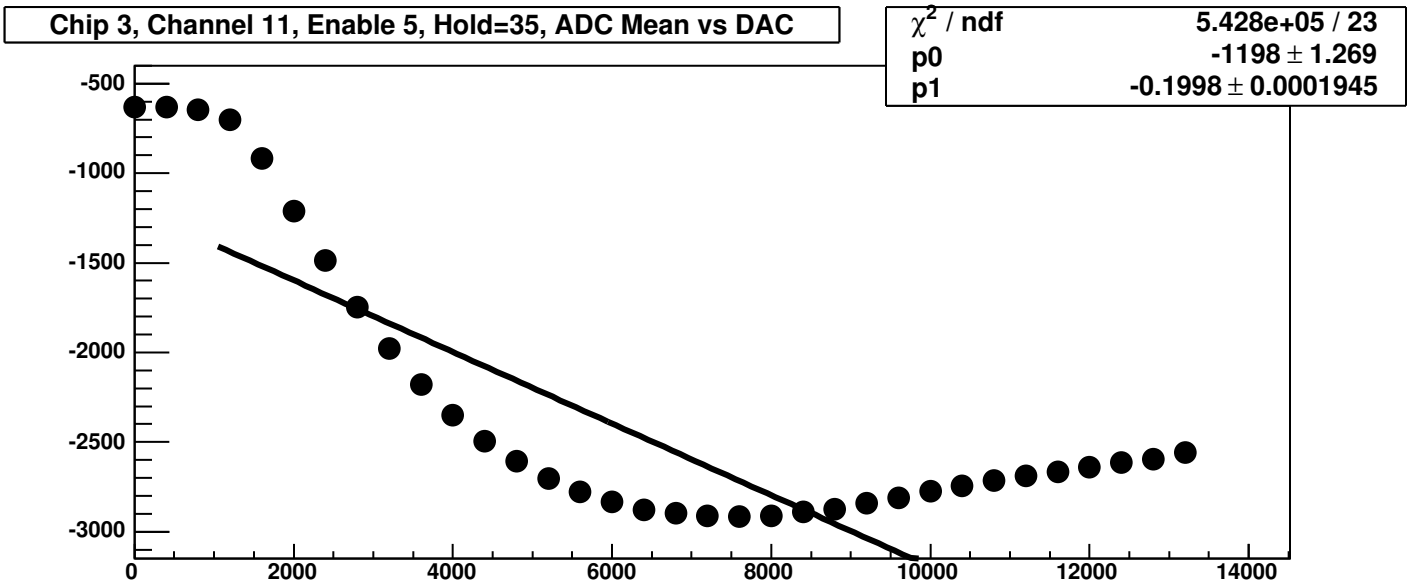

Chip 3, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

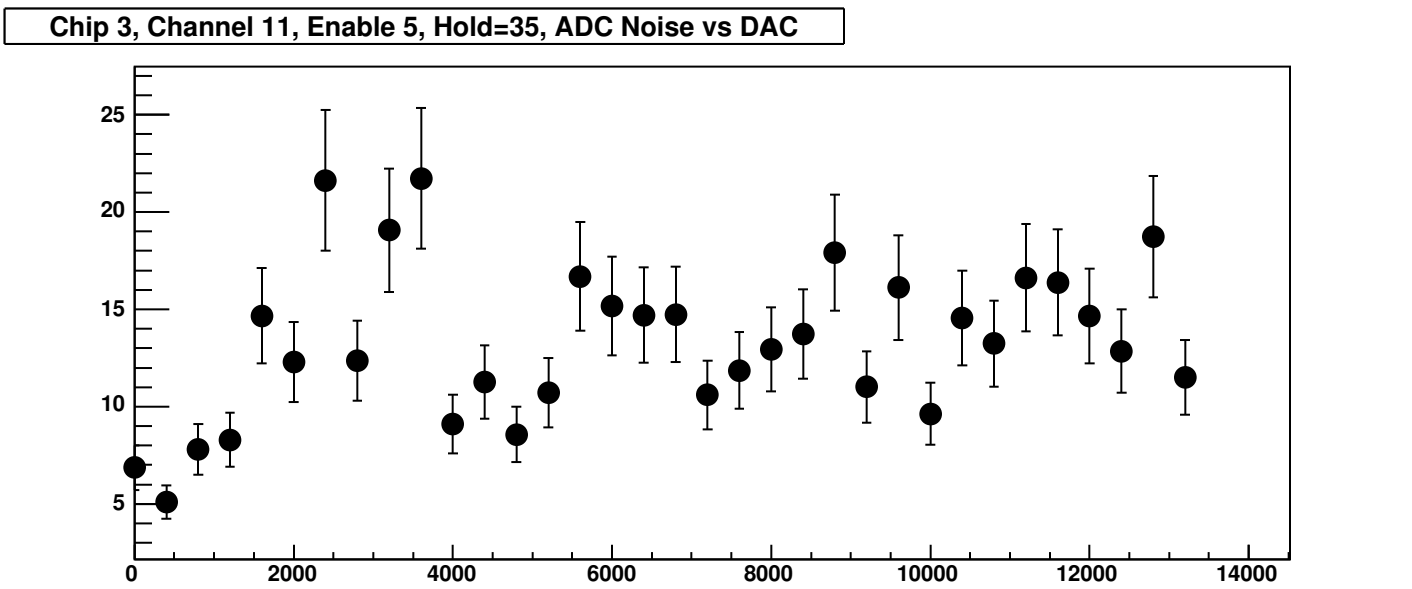

Chip 3, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

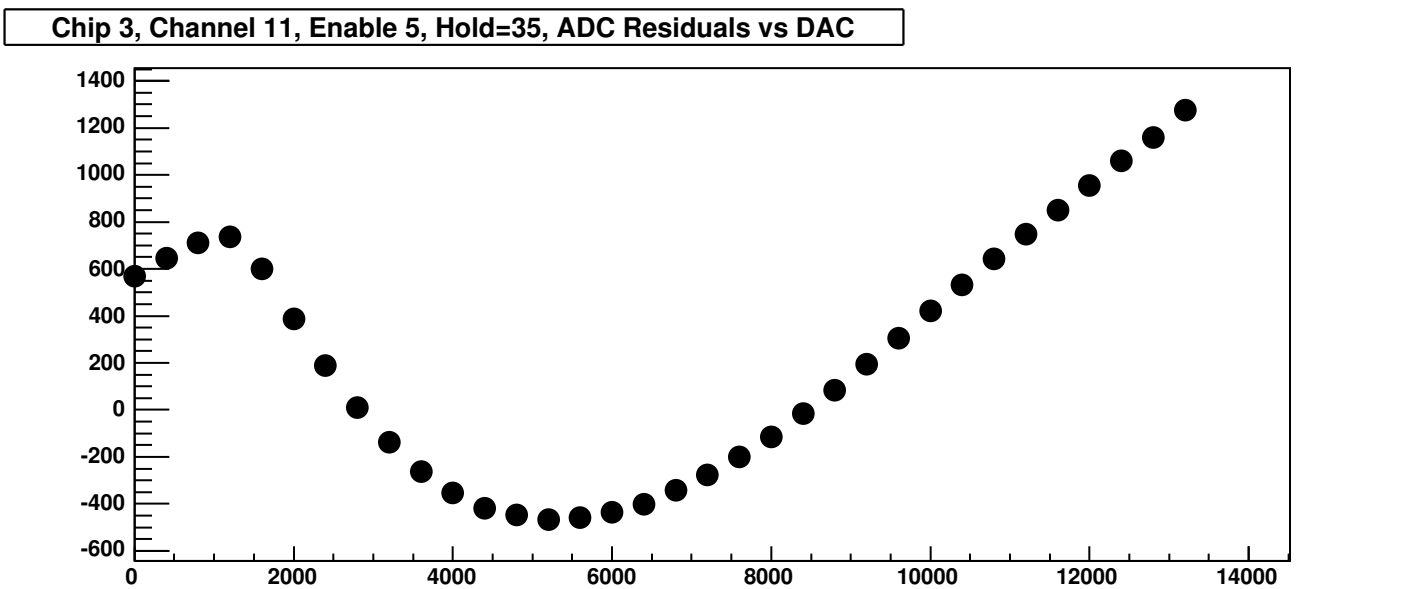

Chip 3, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 61.32 / 23
p0 -590.6 ± 0.777
p1 0.01562 ± 0.0001162
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

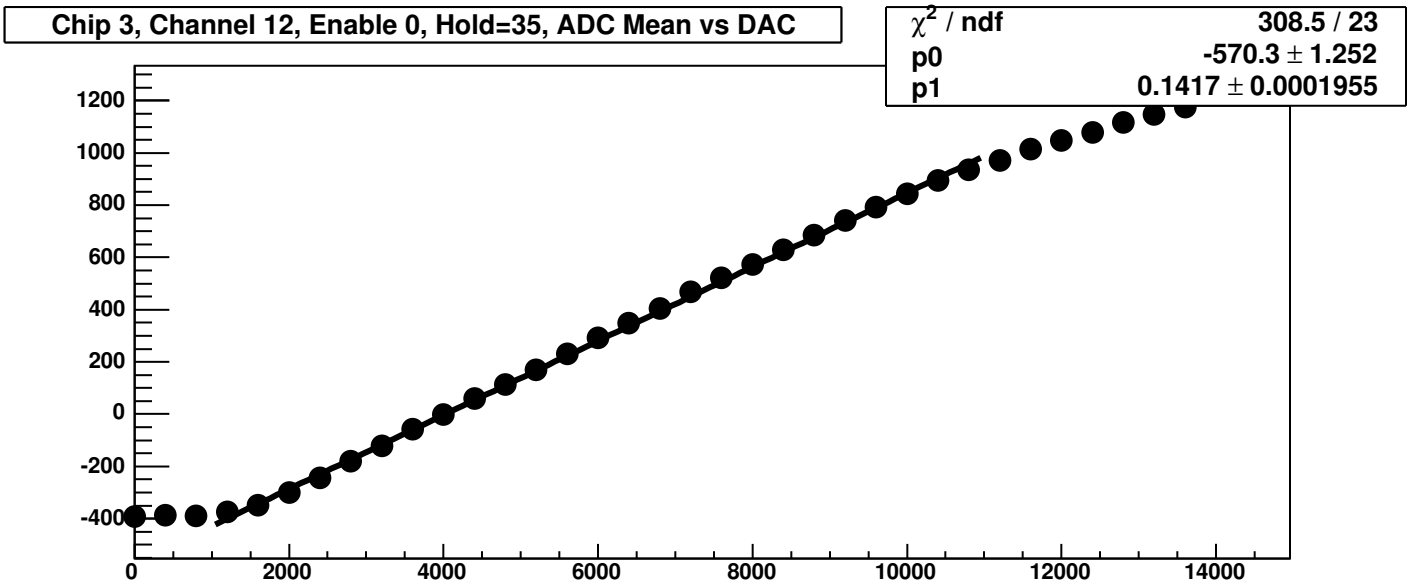

Chip 3, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

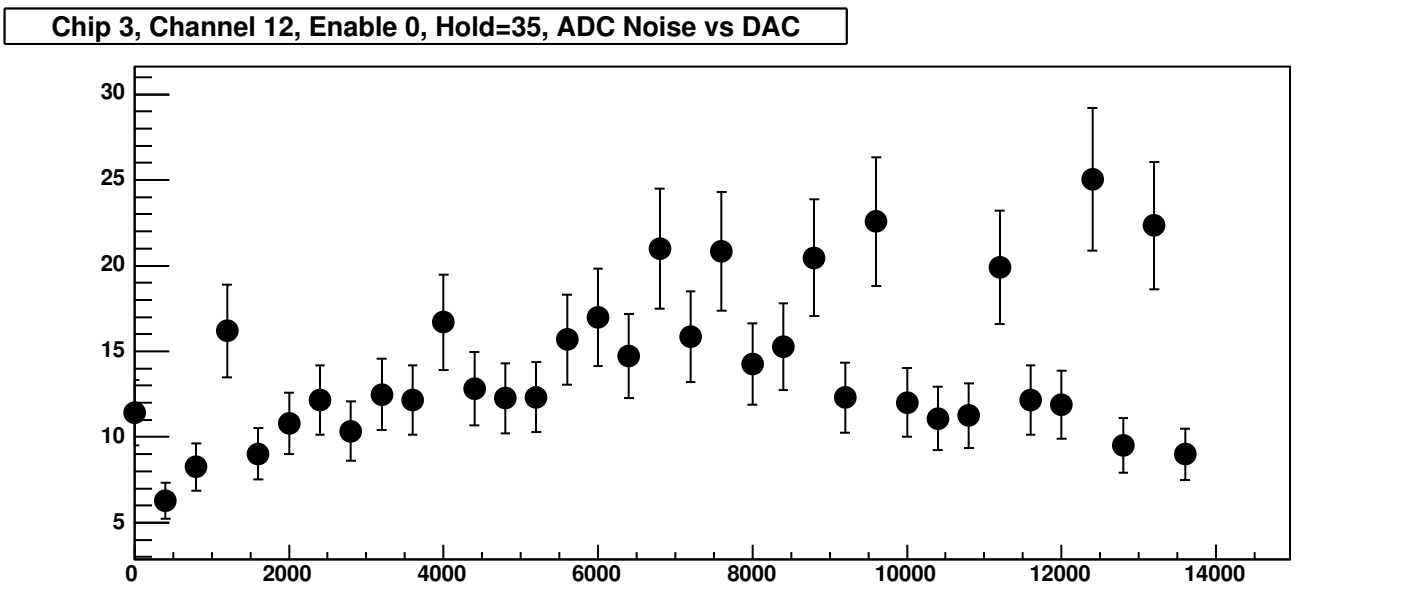

Chip 3, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

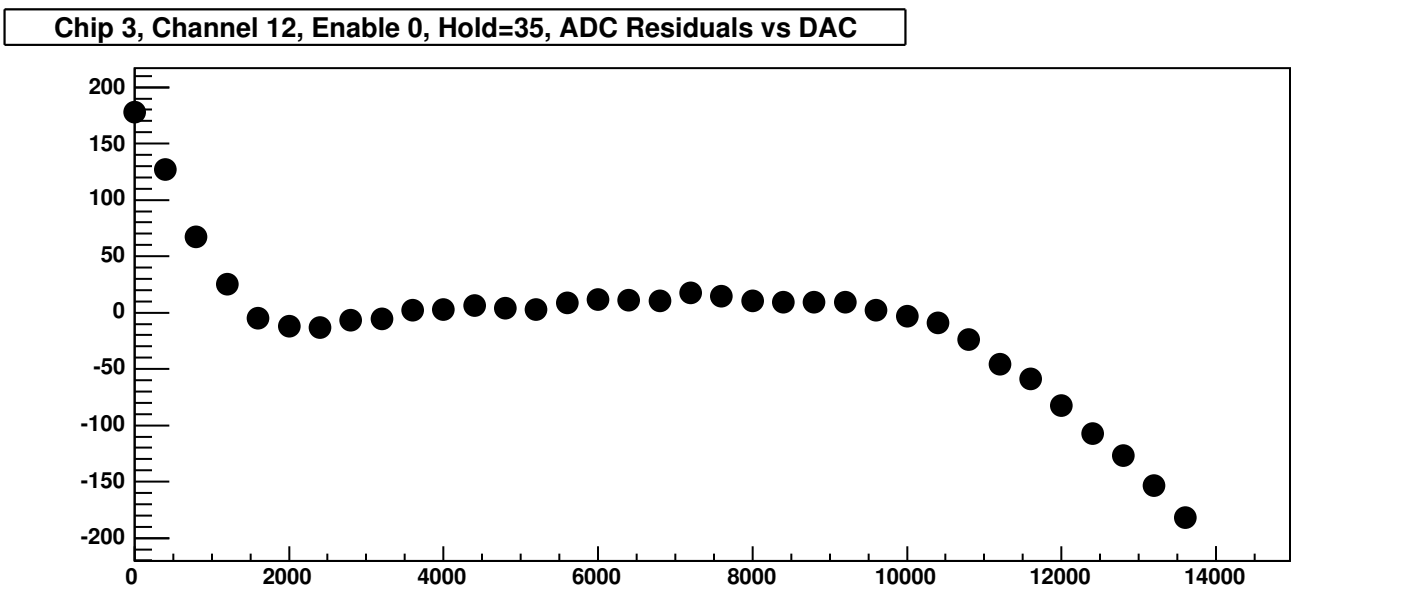

Chip 3, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5.428e+05 / 23
p0 -1198 ± 1.269
p1 -0.1998 ± 0.0001945
-500
-1000
-1500
-2000
-2500
-3000
0
2000
4000
6000
8000
10000
12000
14000

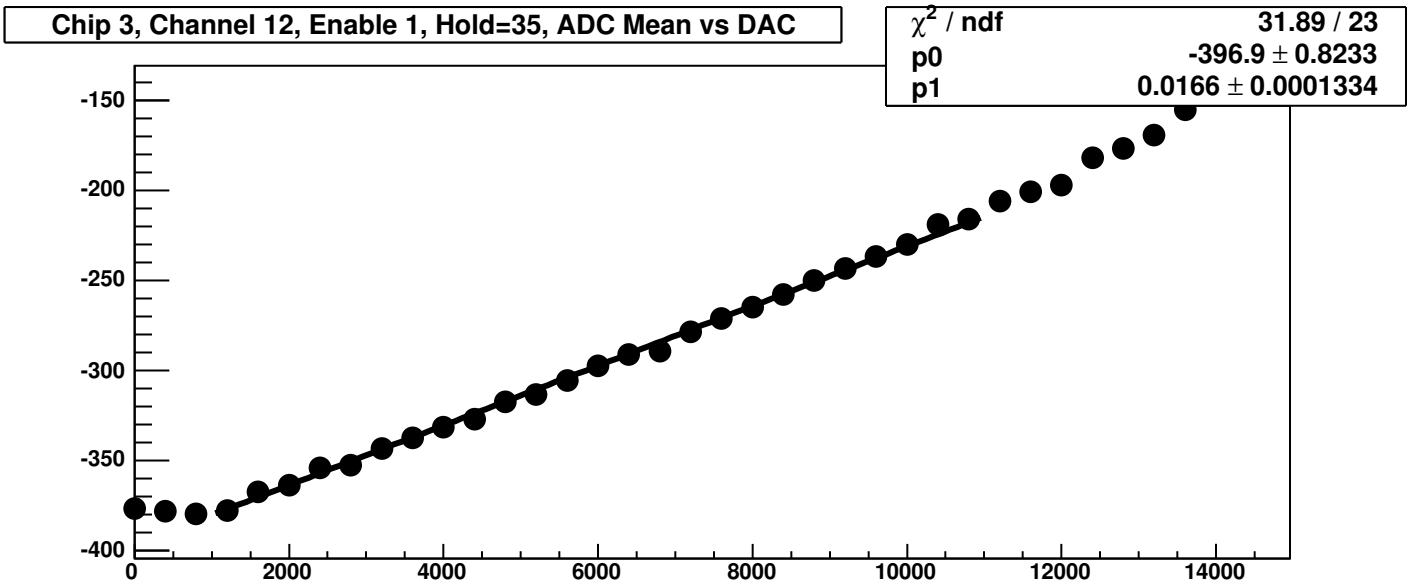

Chip 3, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

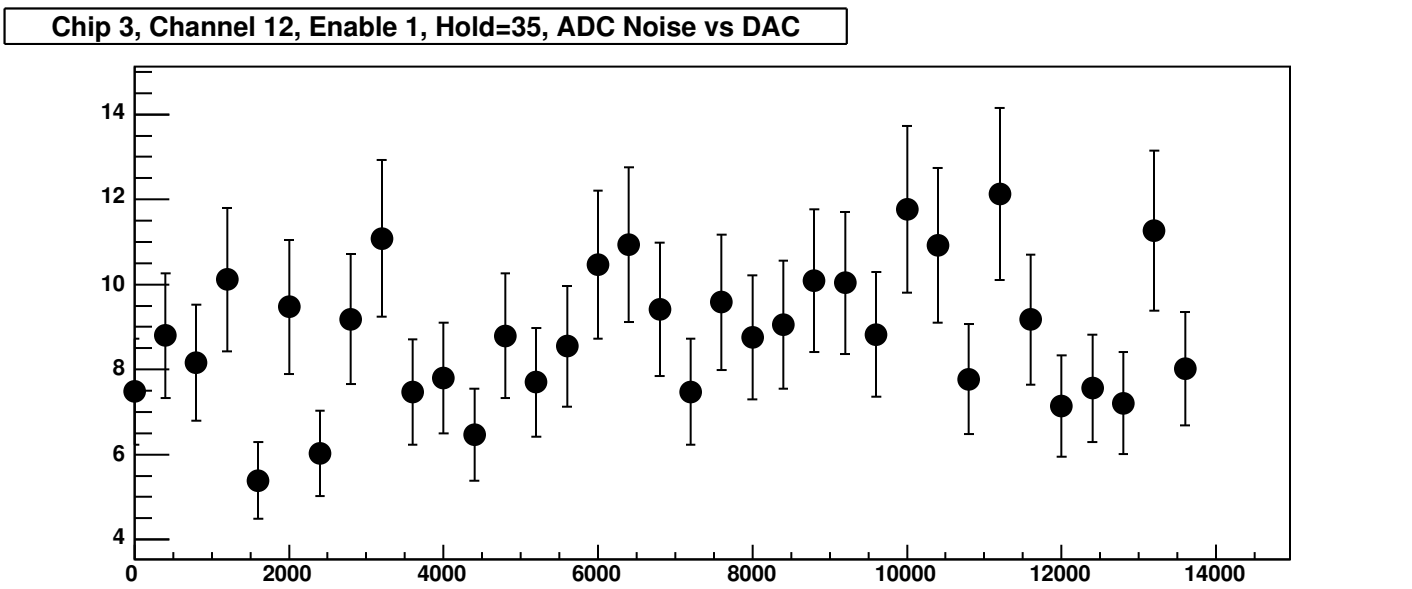

Chip 3, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

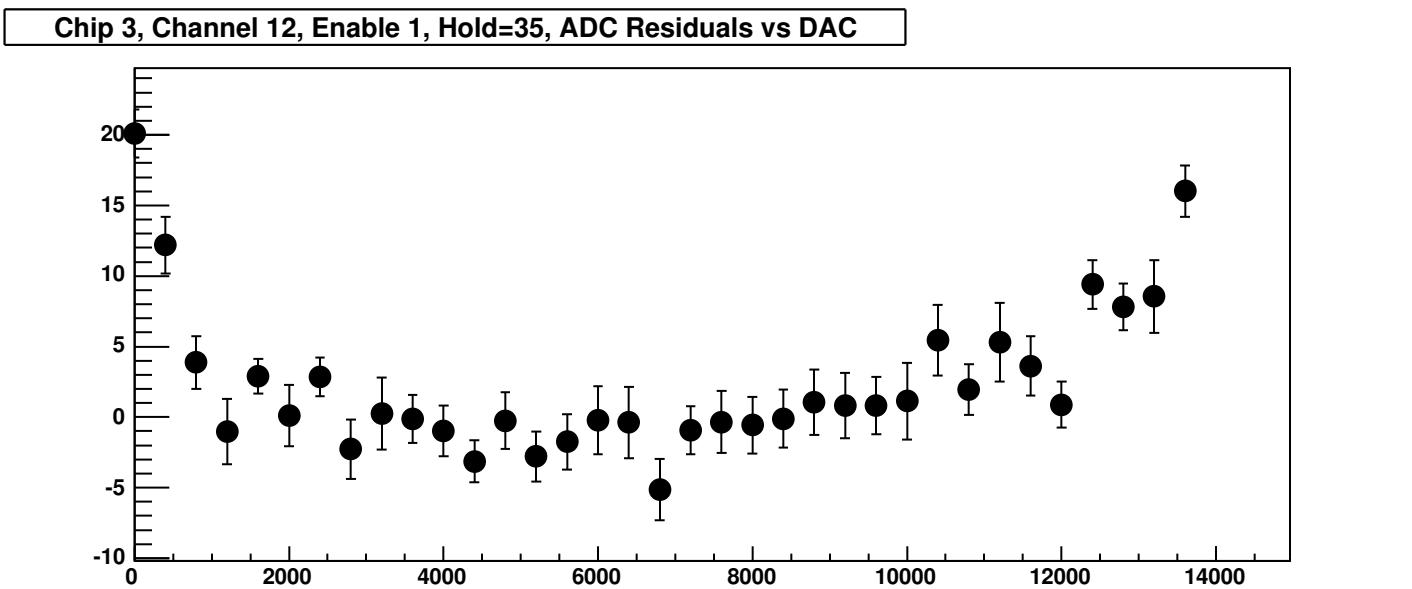

Chip 3, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 308.5 / 23
p0 -570.3 ± 1.252
p1 0.1417 ± 0.0001955
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

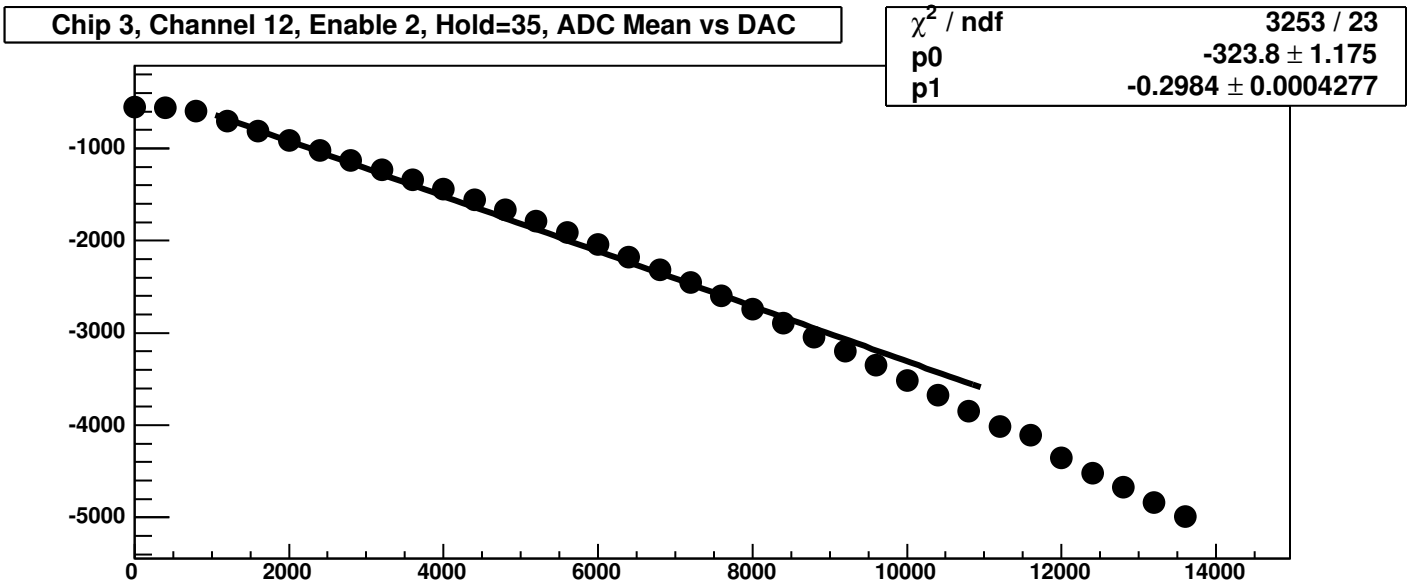

Chip 3, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

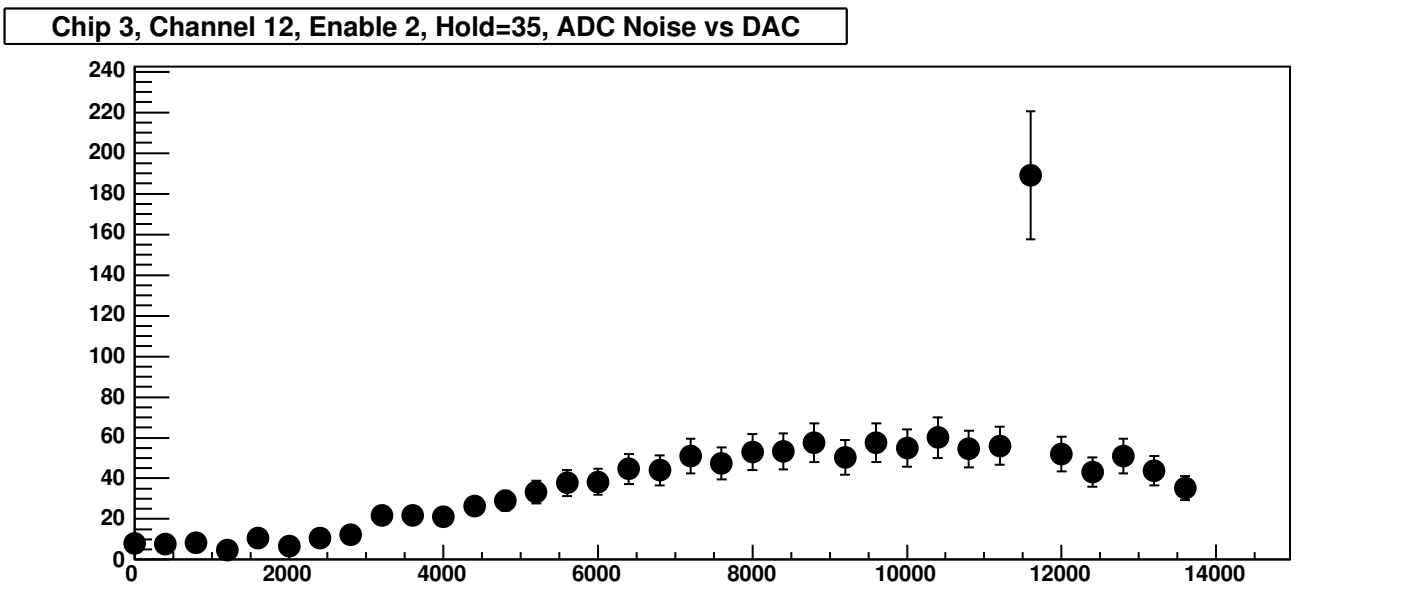

Chip 3, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

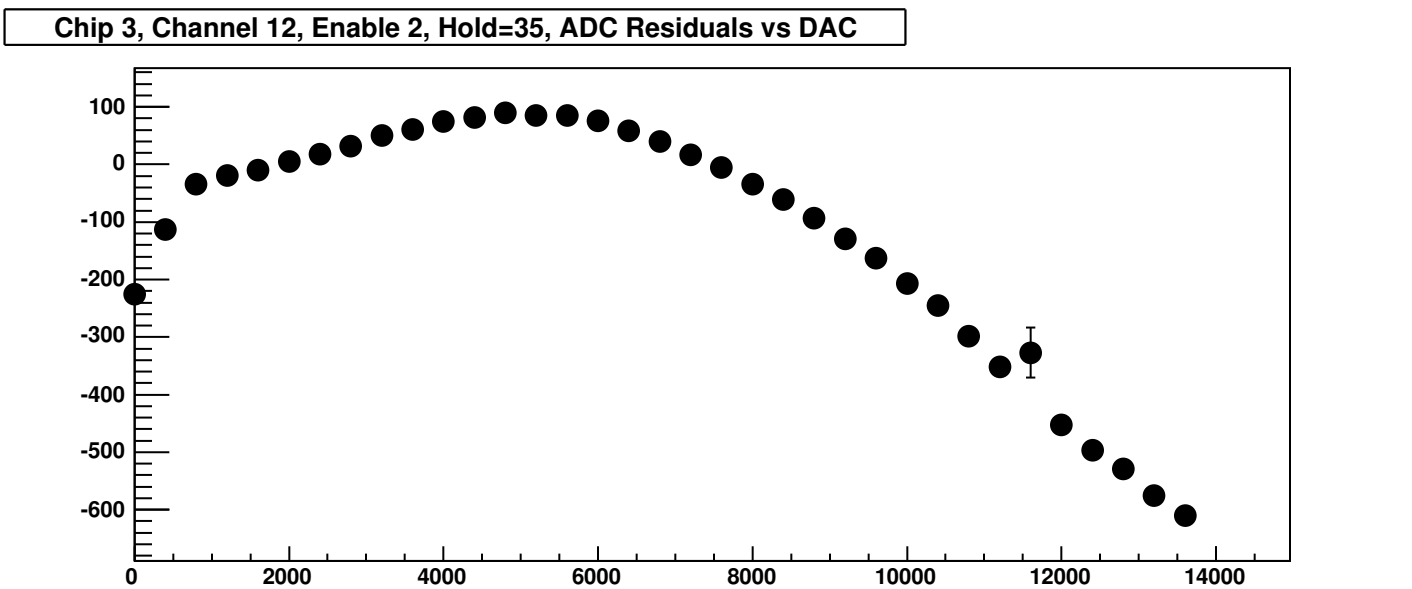

Chip 3, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.89 / 23
p0 -396.9 ± 0.8233
p1 0.0166 ± 0.0001334
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

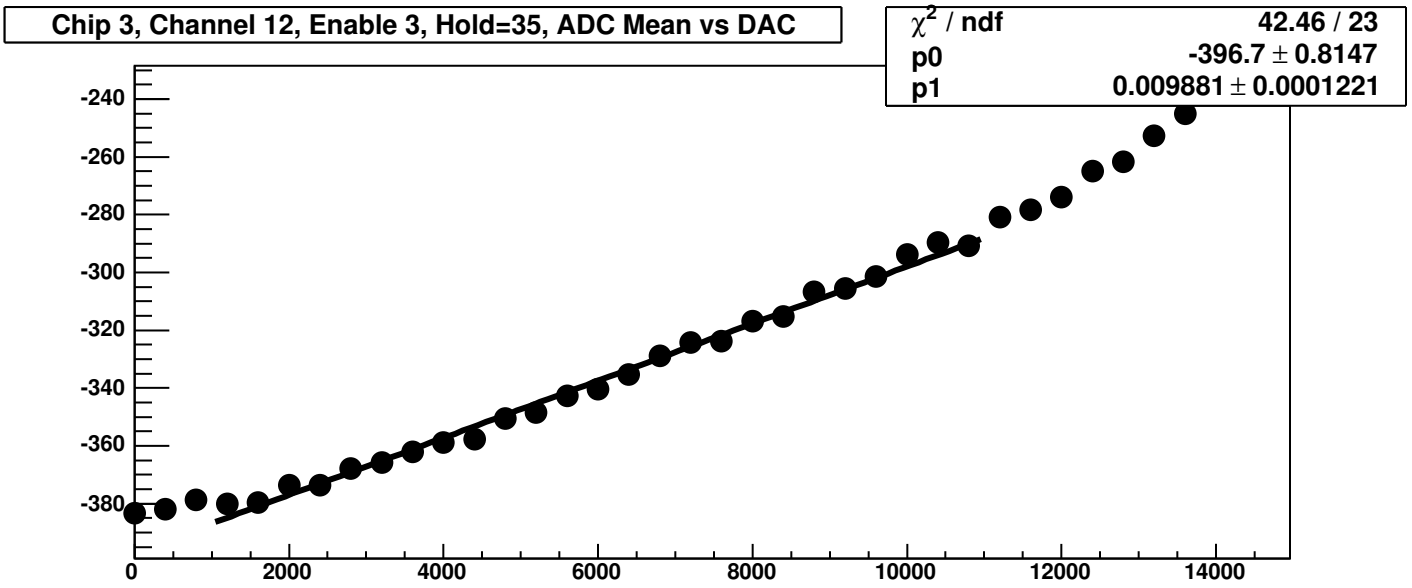

Chip 3, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

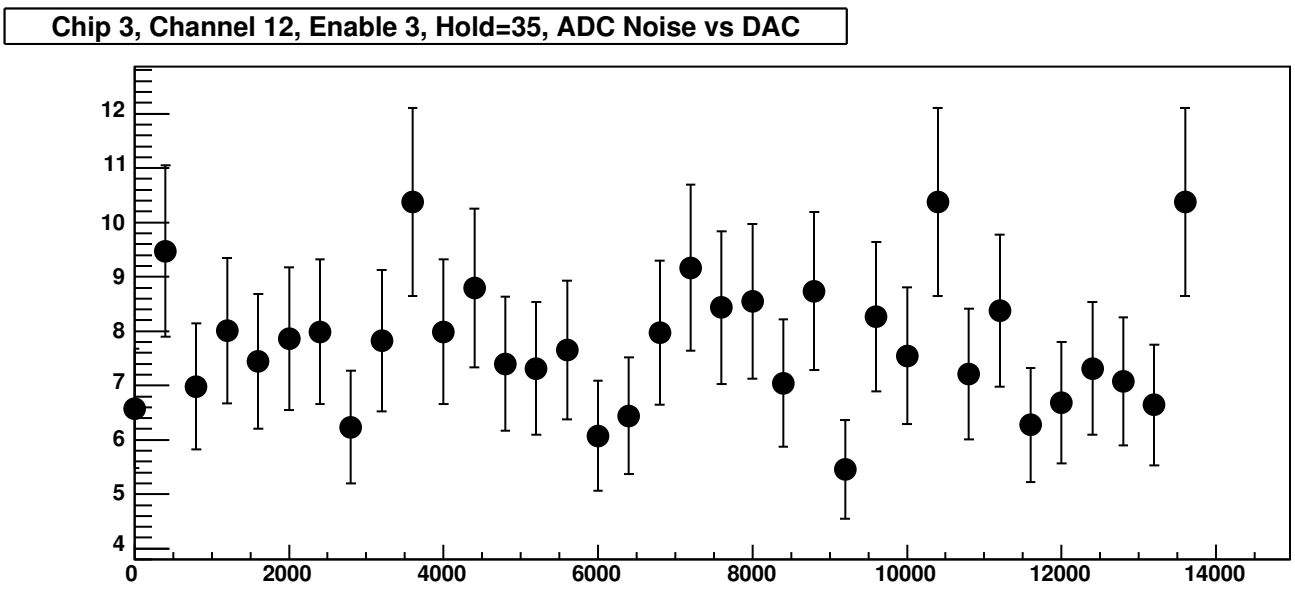

Chip 3, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

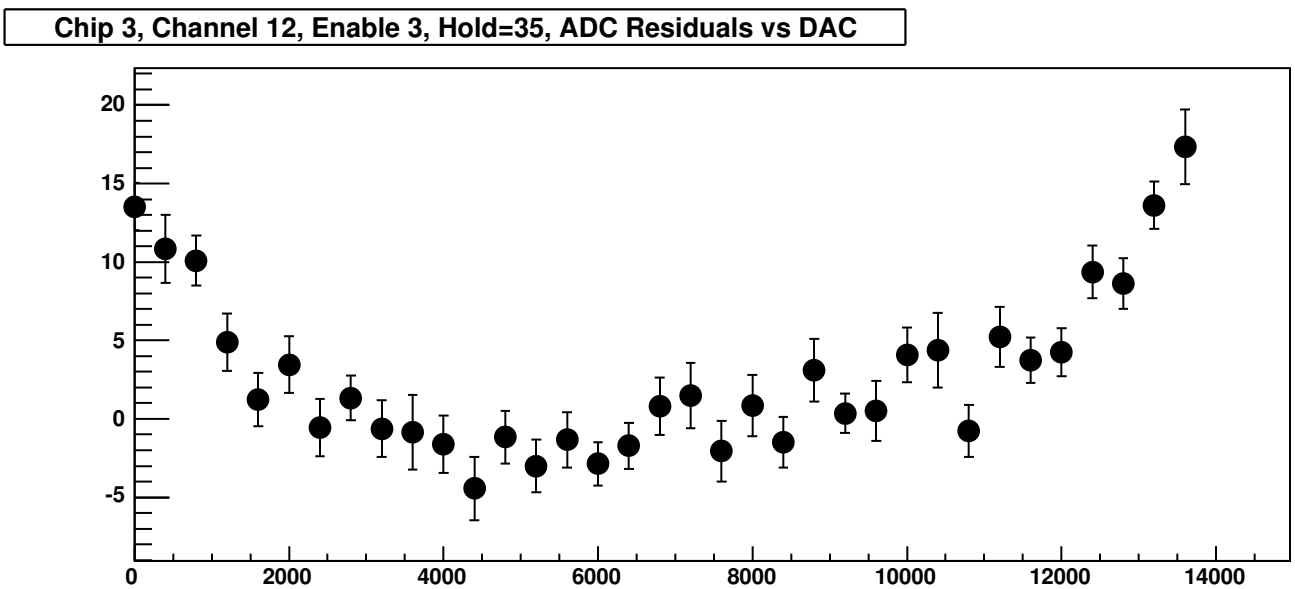

Chip 3, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 3253 / 23
p0 -323.8 ± 1.175
p1 -0.2984 ± 0.0004277
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000

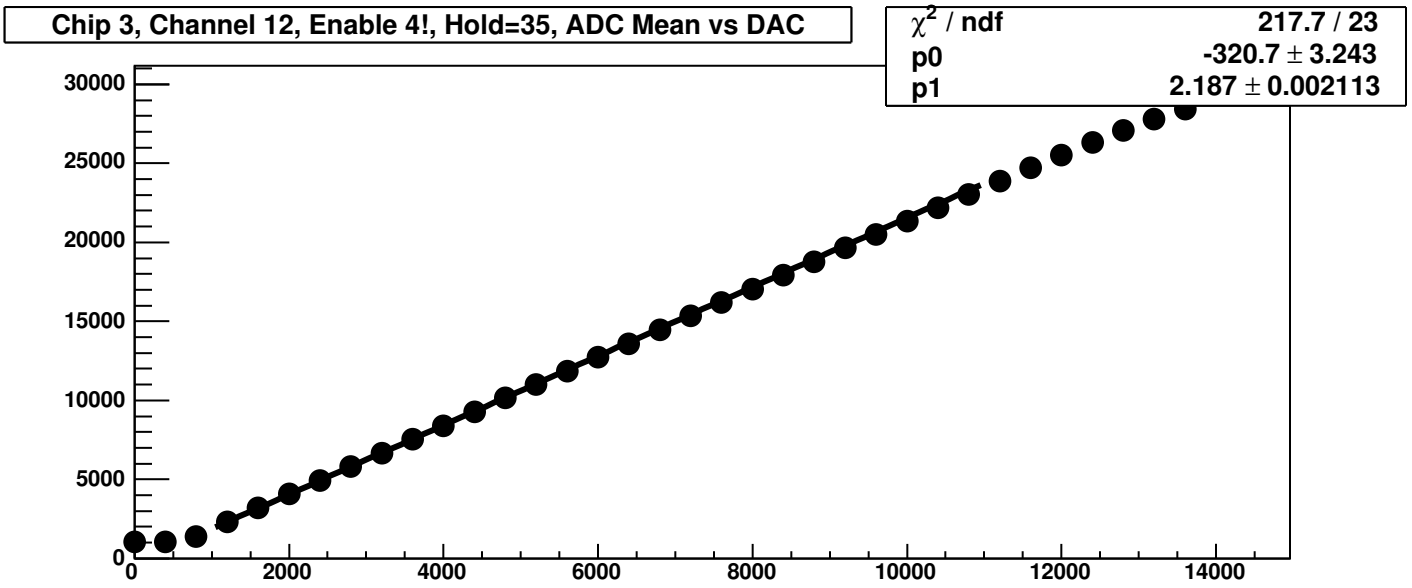

Chip 3, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
240
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

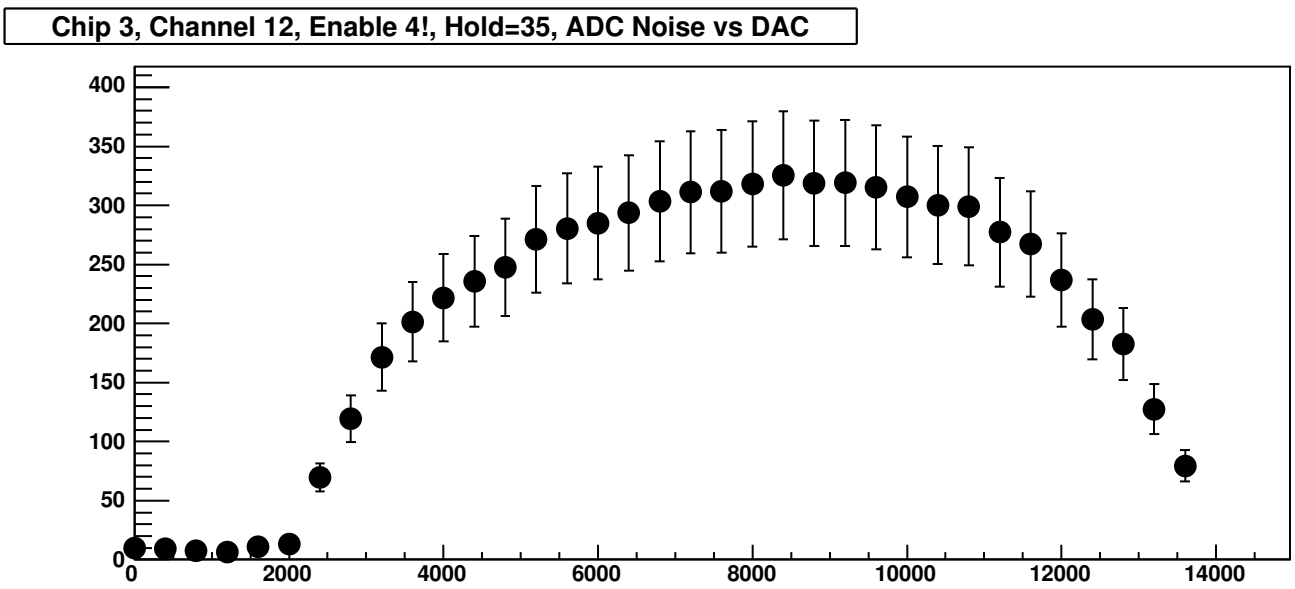

Chip 3, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
100
0
-100
-200
-300
-400
-500
-600
0
2000
4000
6000
8000
10000
12000
14000

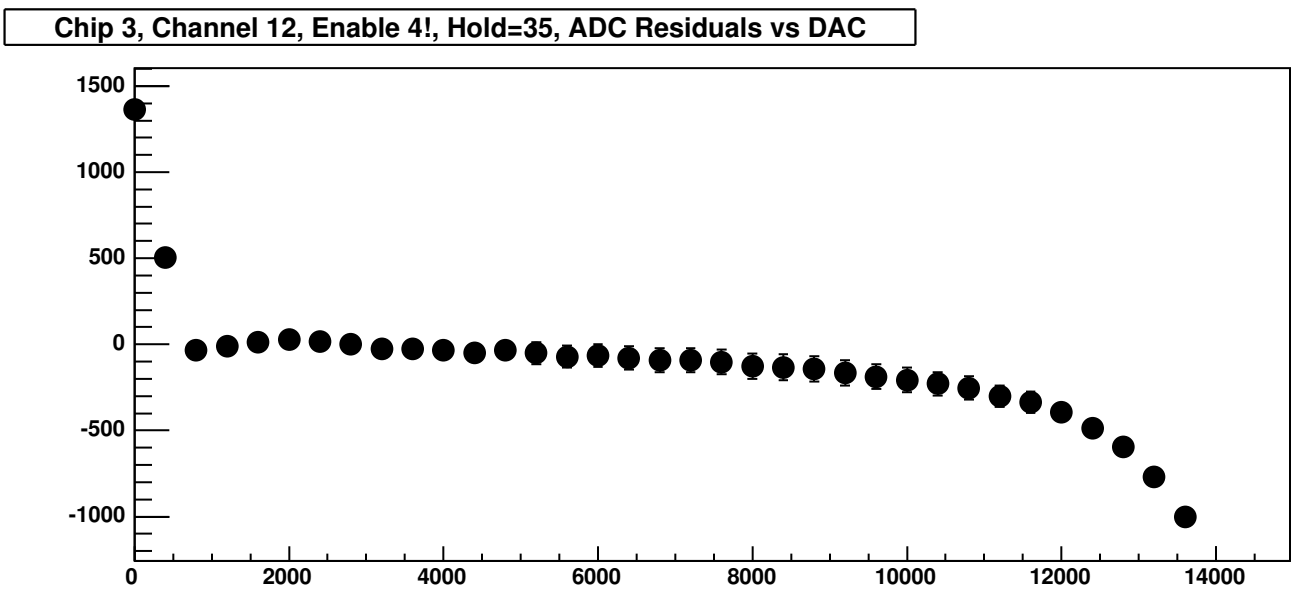

Chip 3, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 42.46 / 23
p0 -396.7 ± 0.8147
p1 0.009881 ± 0.0001221
-240
-260
-280
-300
-320
-340
-360
-380
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

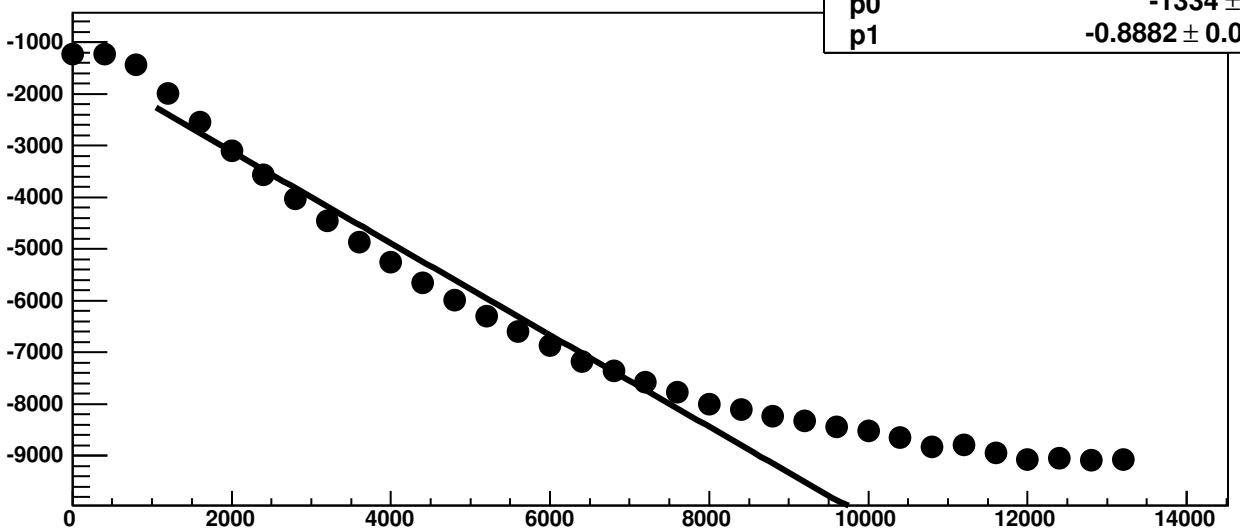

Chip 3, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 217.7 / 23
p0 -320.7 ± 3.243
p1 2.187 ± 0.002113
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

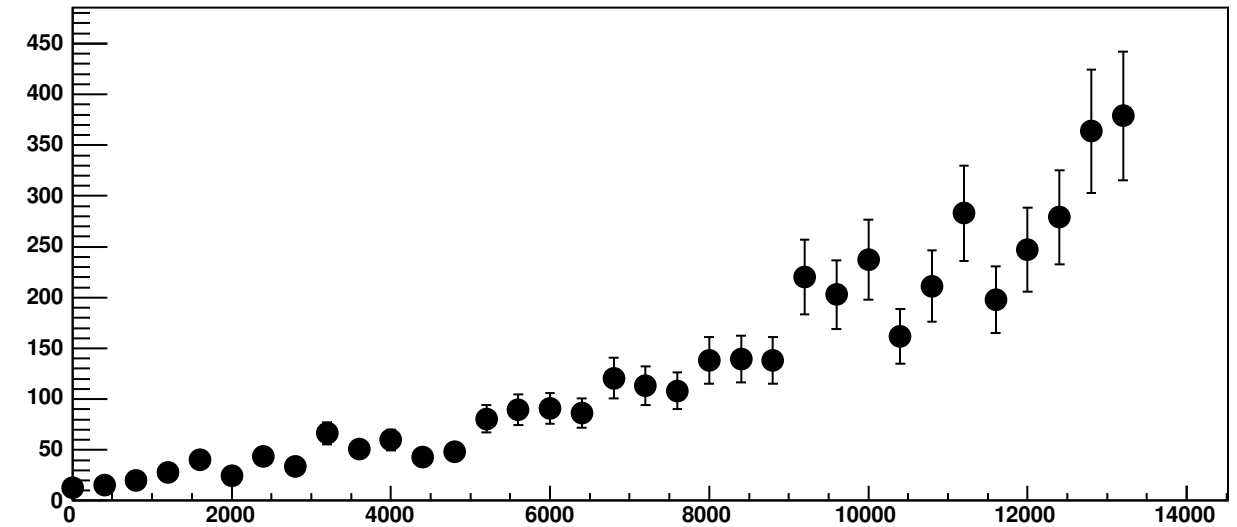

Chip 3, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

Chip 3, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

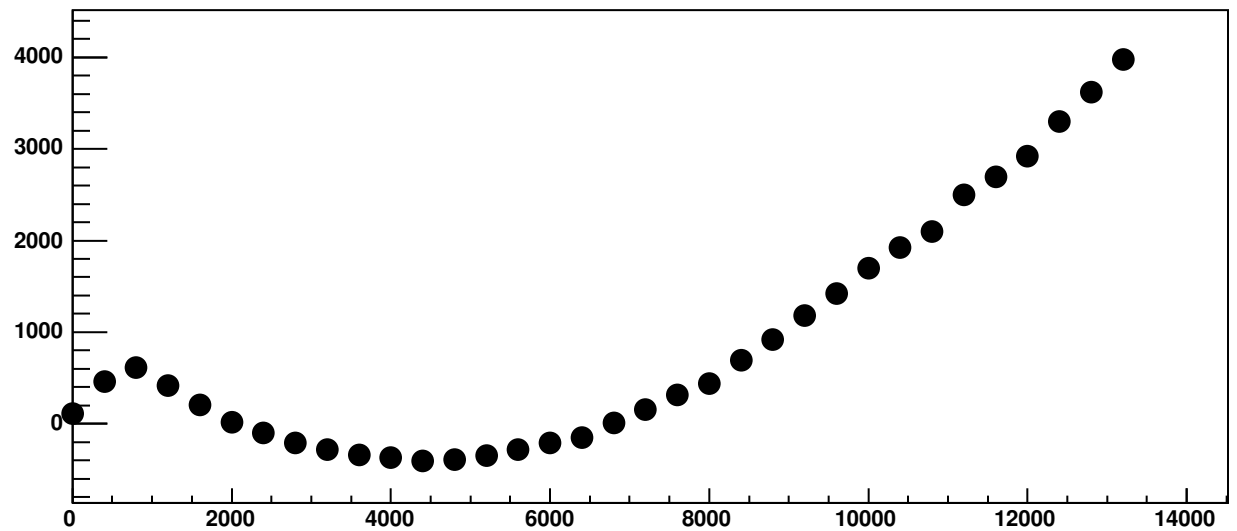
χ^2 / ndf	1.982e+04 / 23
p0	-1334 ± 4.908
p1	-0.8882 ± 0.001341

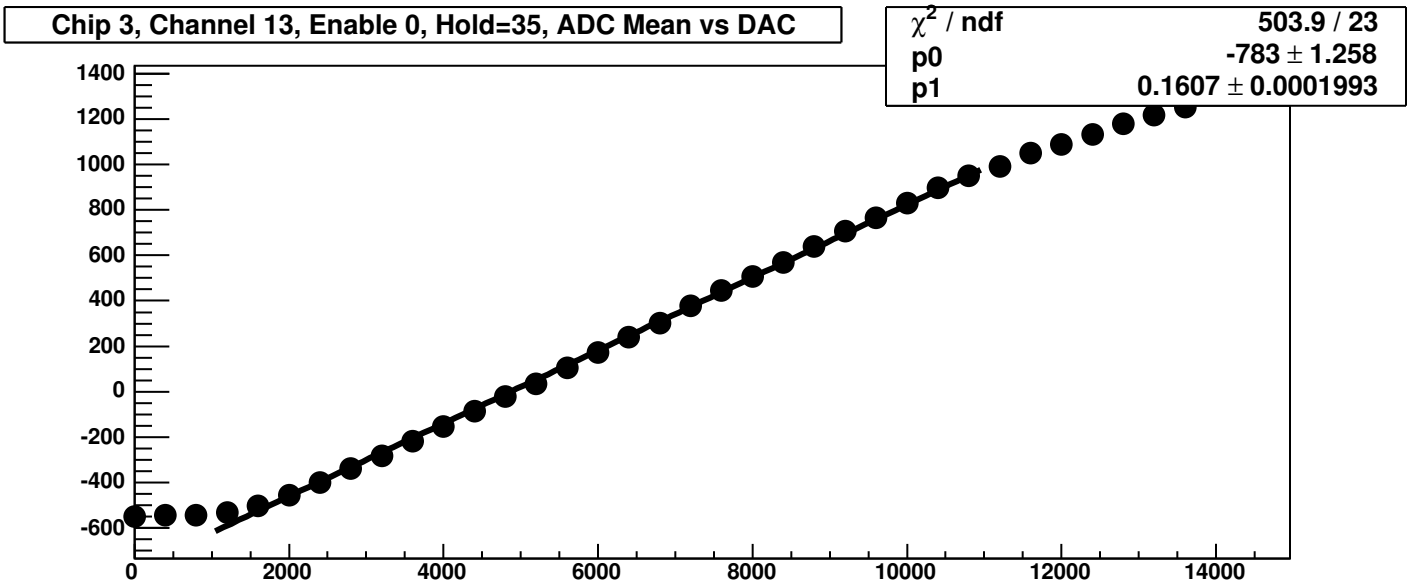


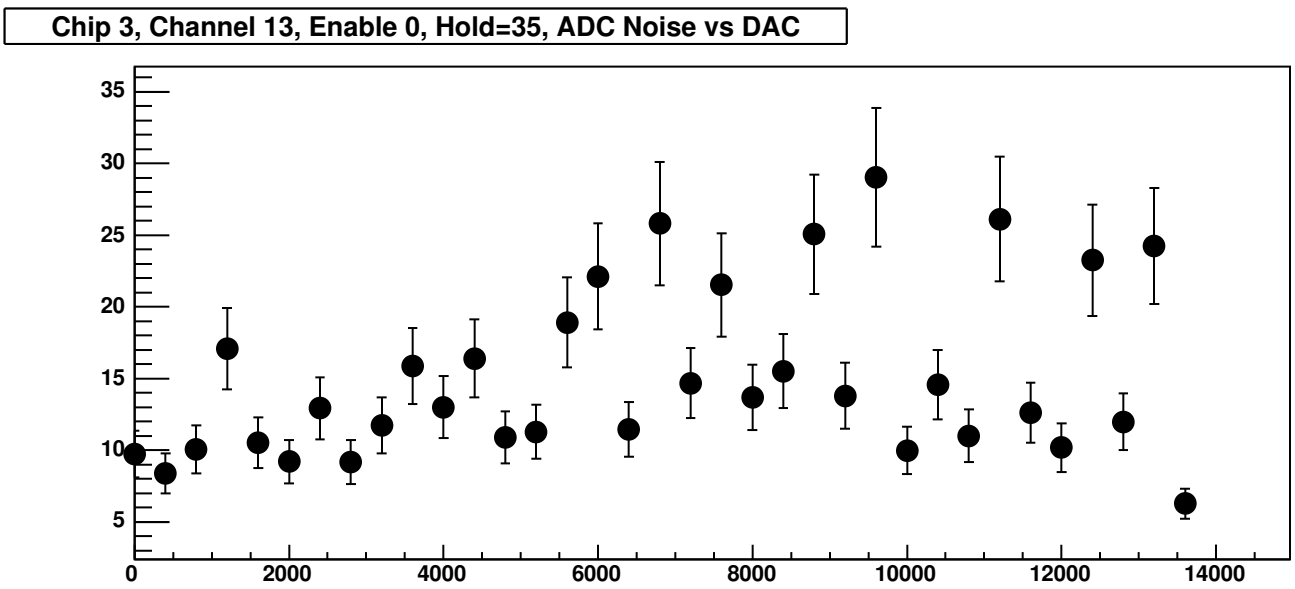
Chip 3, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

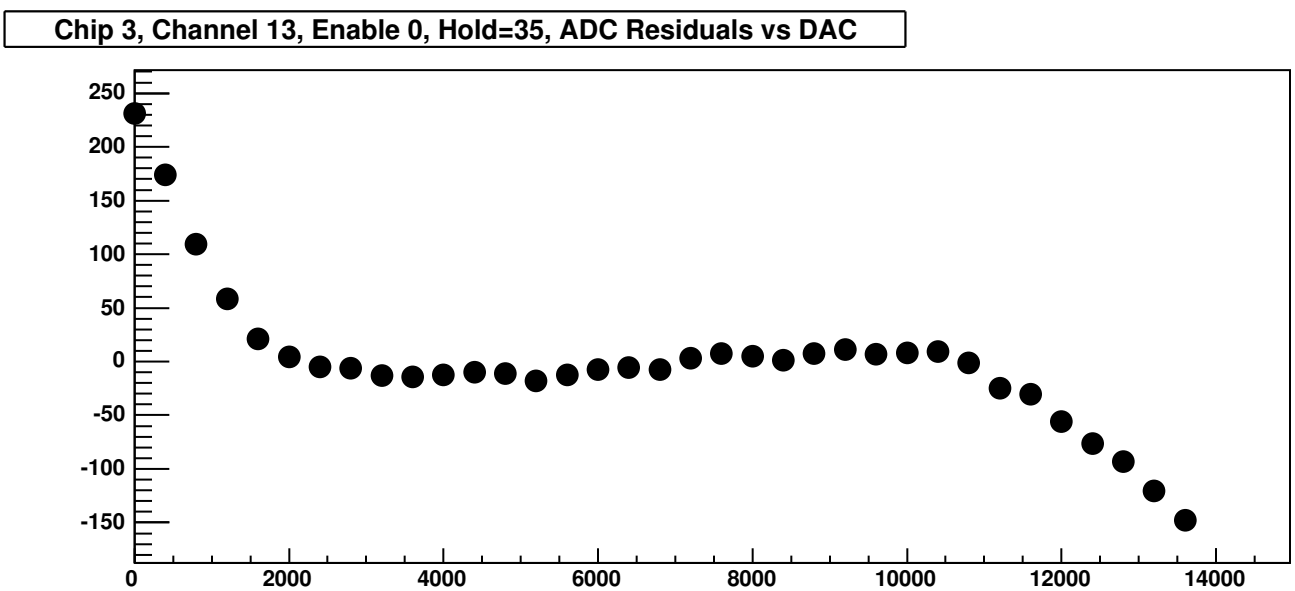


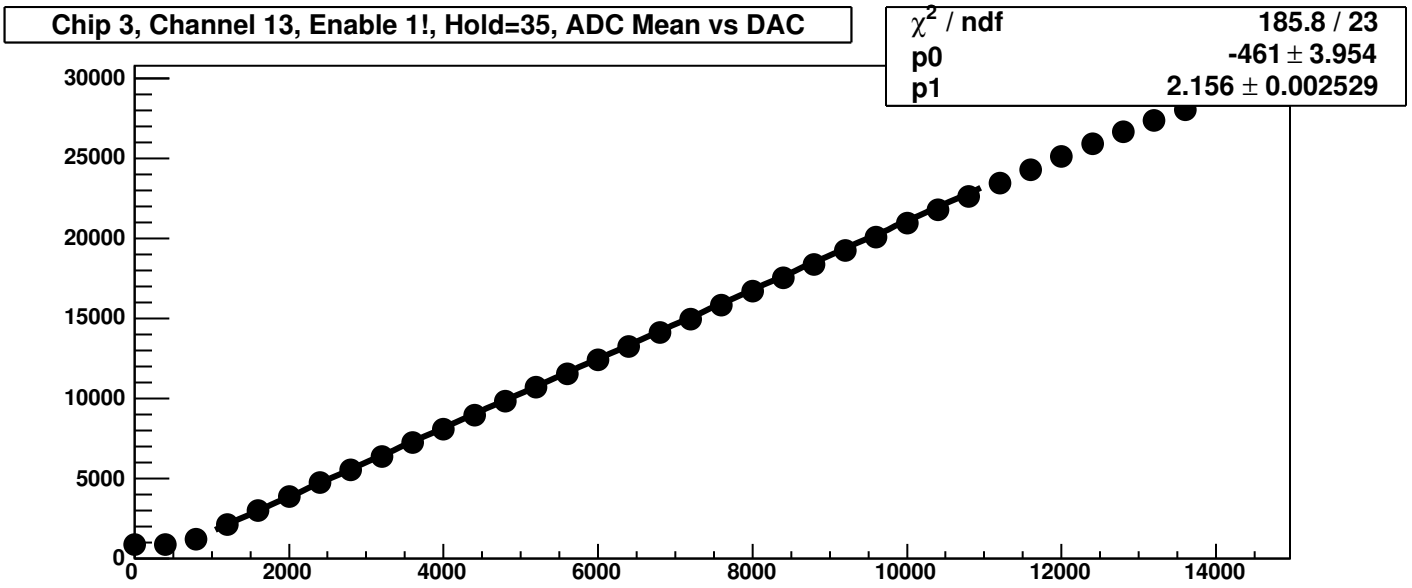
Chip 3, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

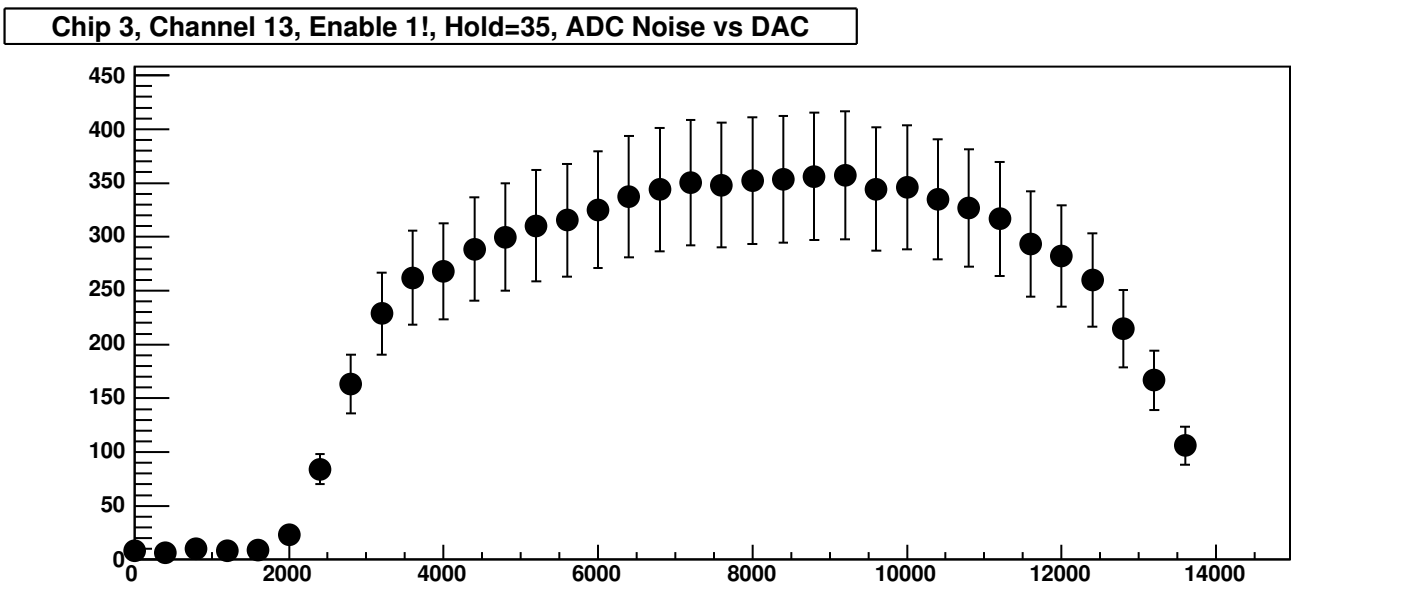


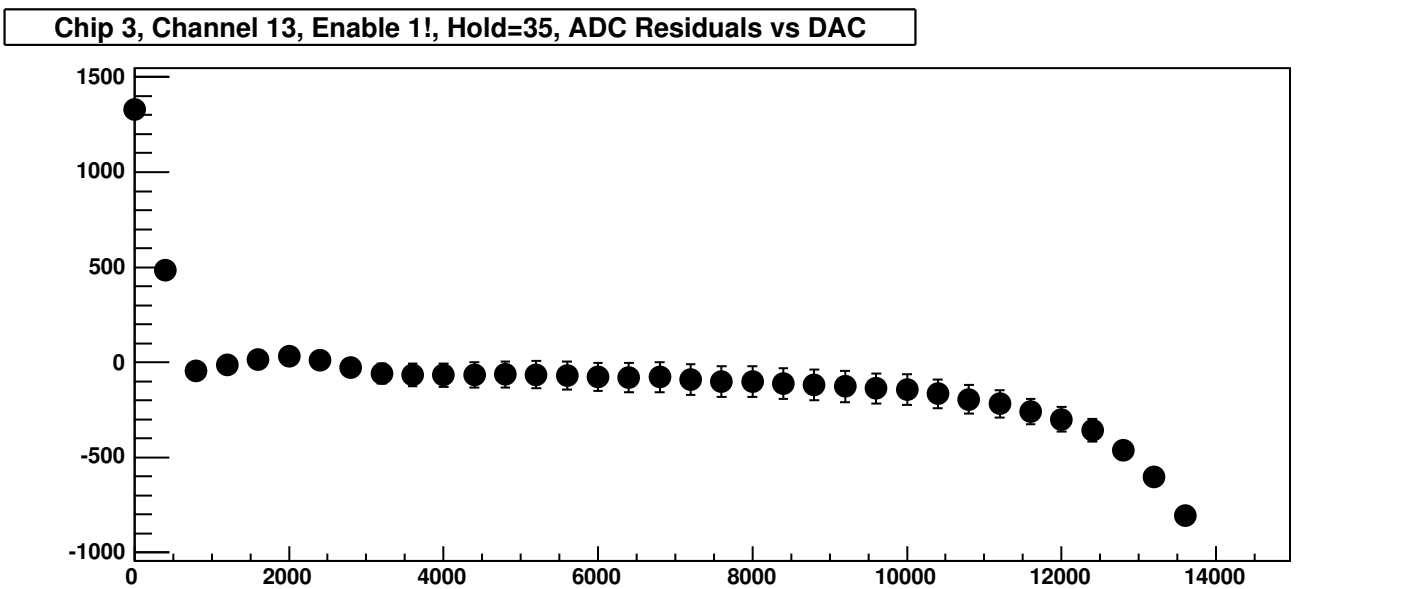

Chip 3, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 503.9 / 23
p0 -783 ± 1.258
p1 0.1607 ± 0.0001993
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

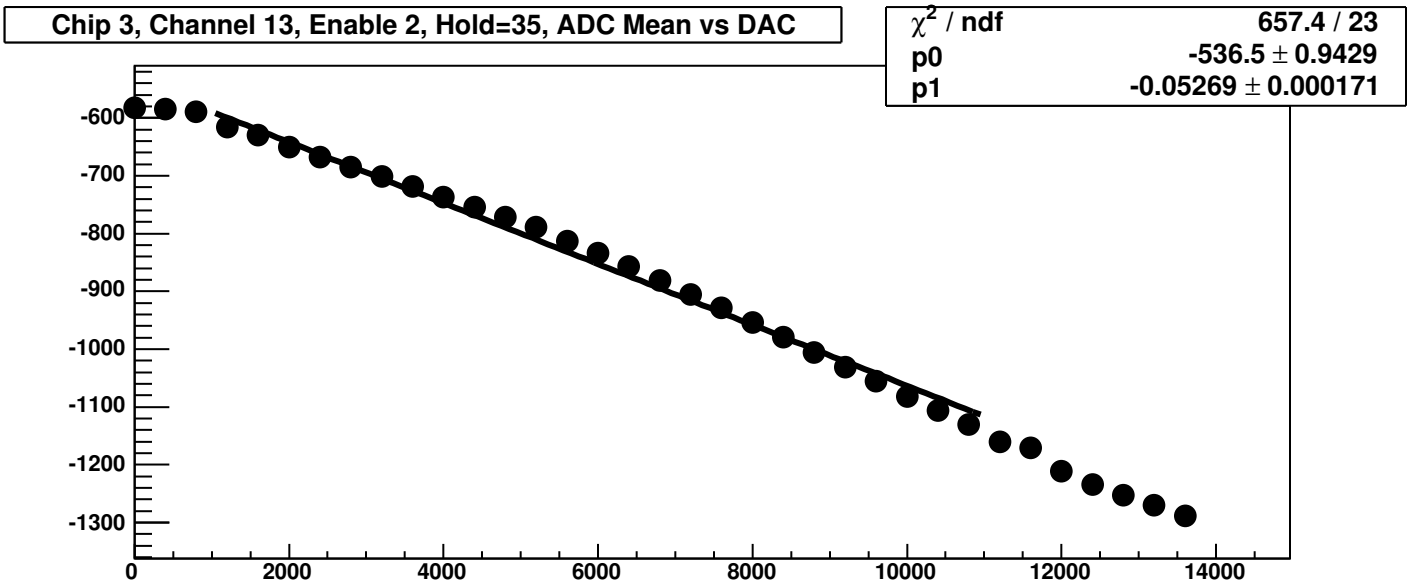

Chip 3, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

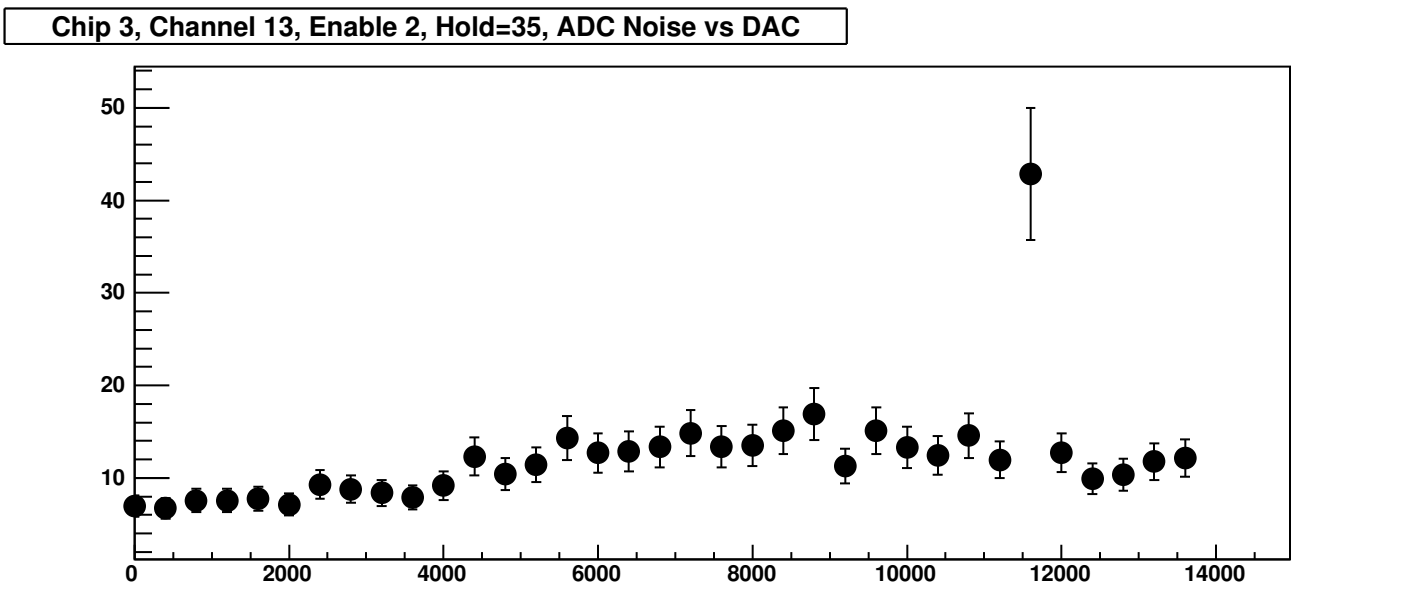

Chip 3, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

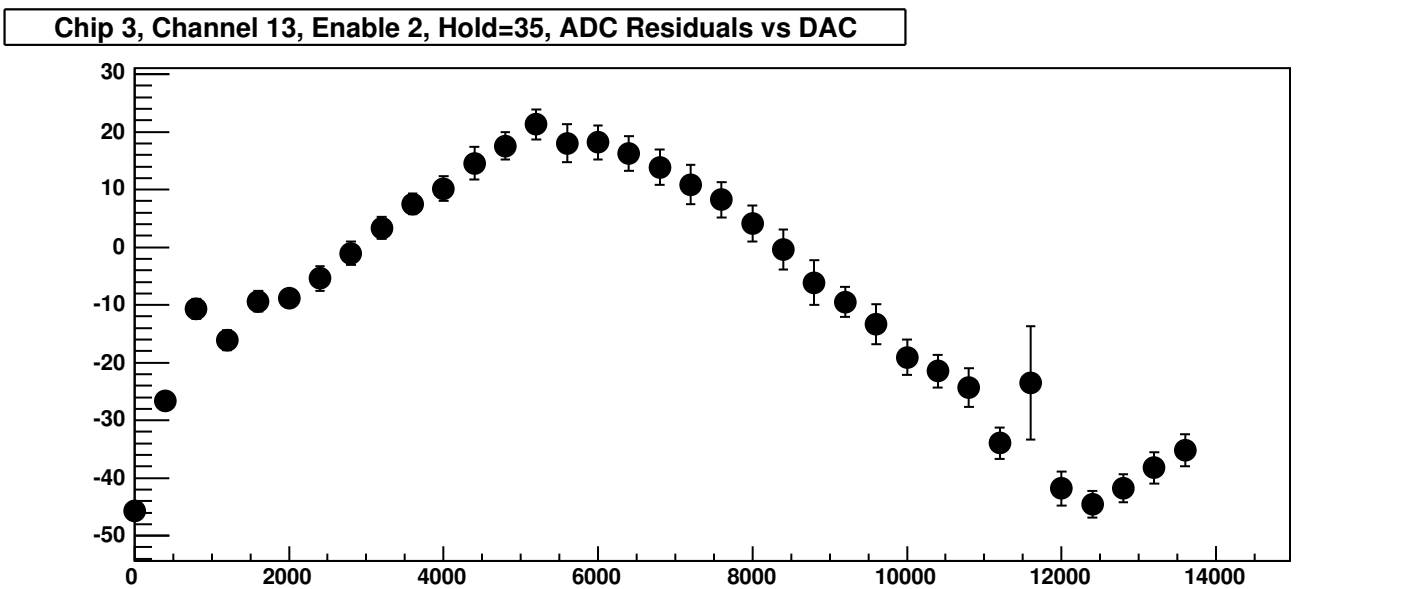

Chip 3, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 185.8 / 23
p0 -461 ± 3.954
p1 2.156 ± 0.002529
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

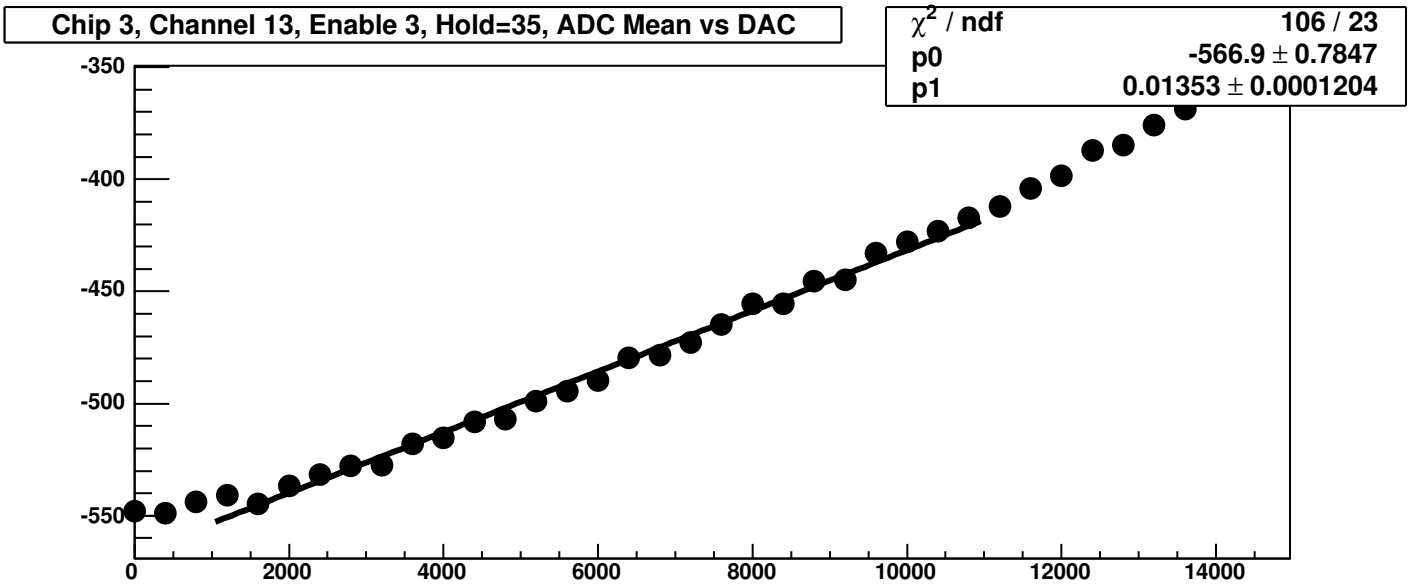

Chip 3, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

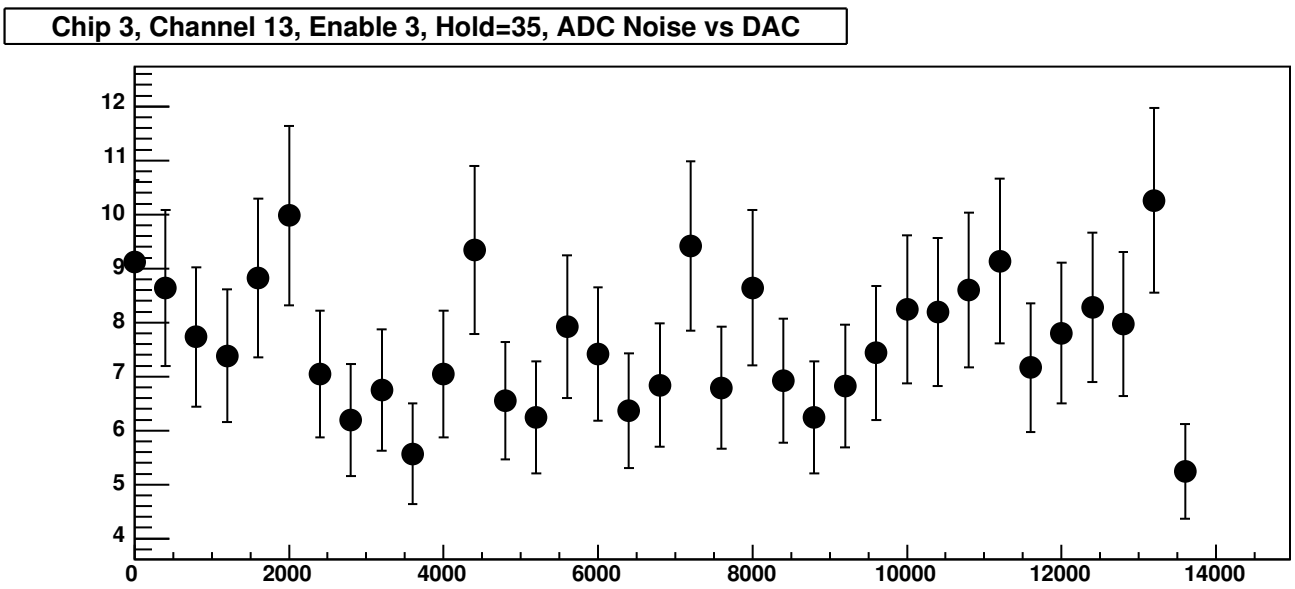

Chip 3, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

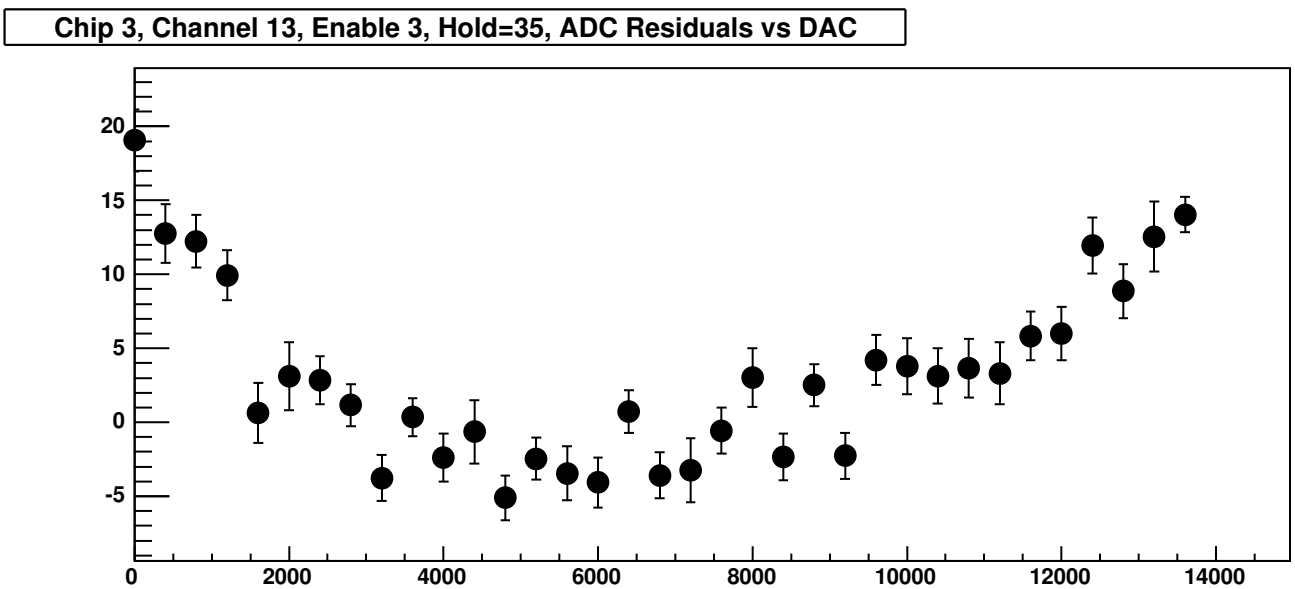

Chip 3, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 657.4 / 23
p0 -536.5 ± 0.9429
p1 -0.05269 ± 0.000171
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
2000
4000
6000
8000
10000
12000
14000

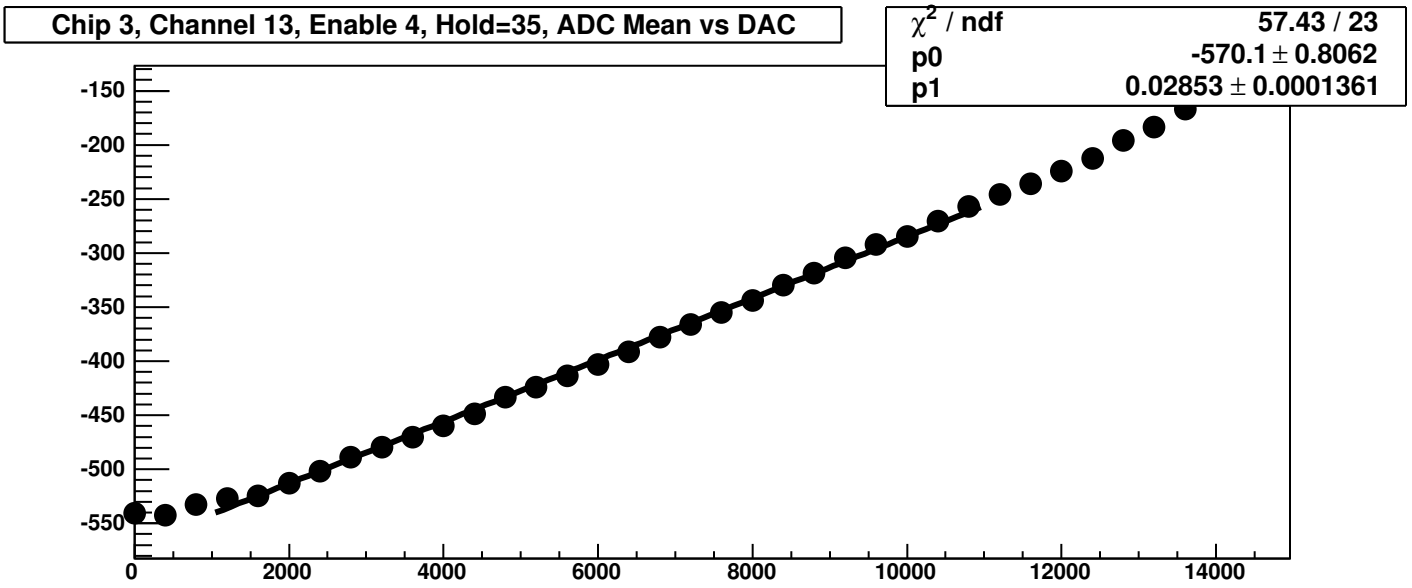

Chip 3, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

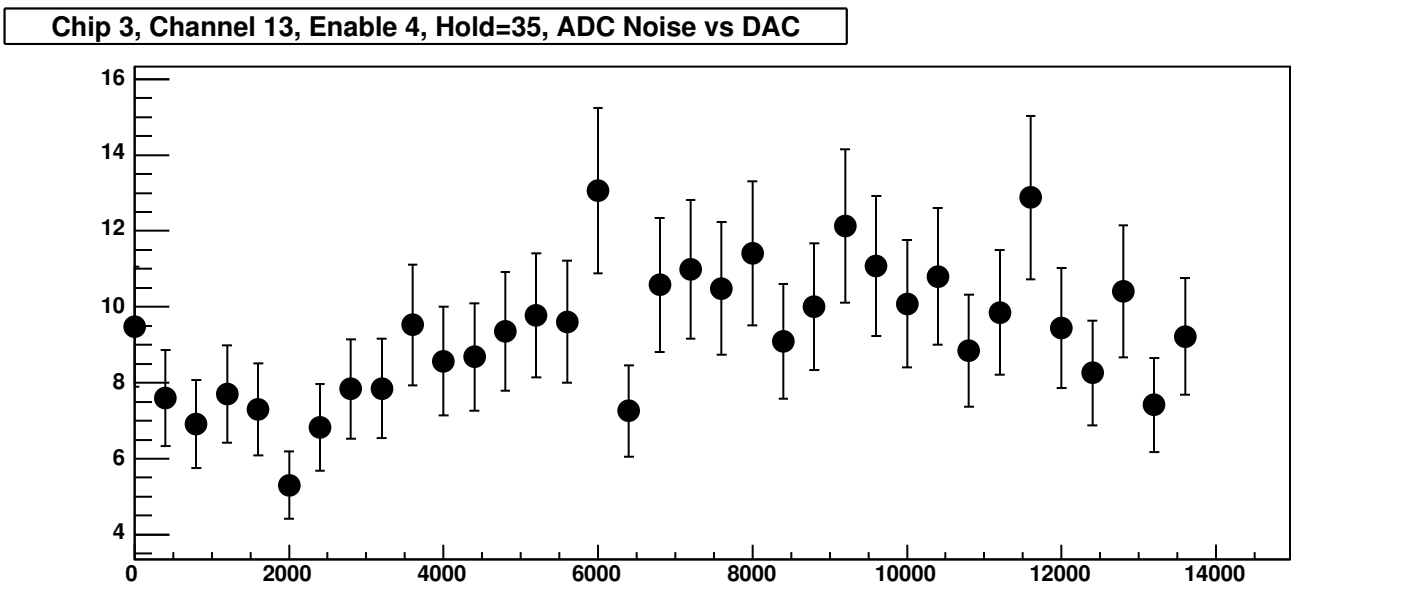

Chip 3, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

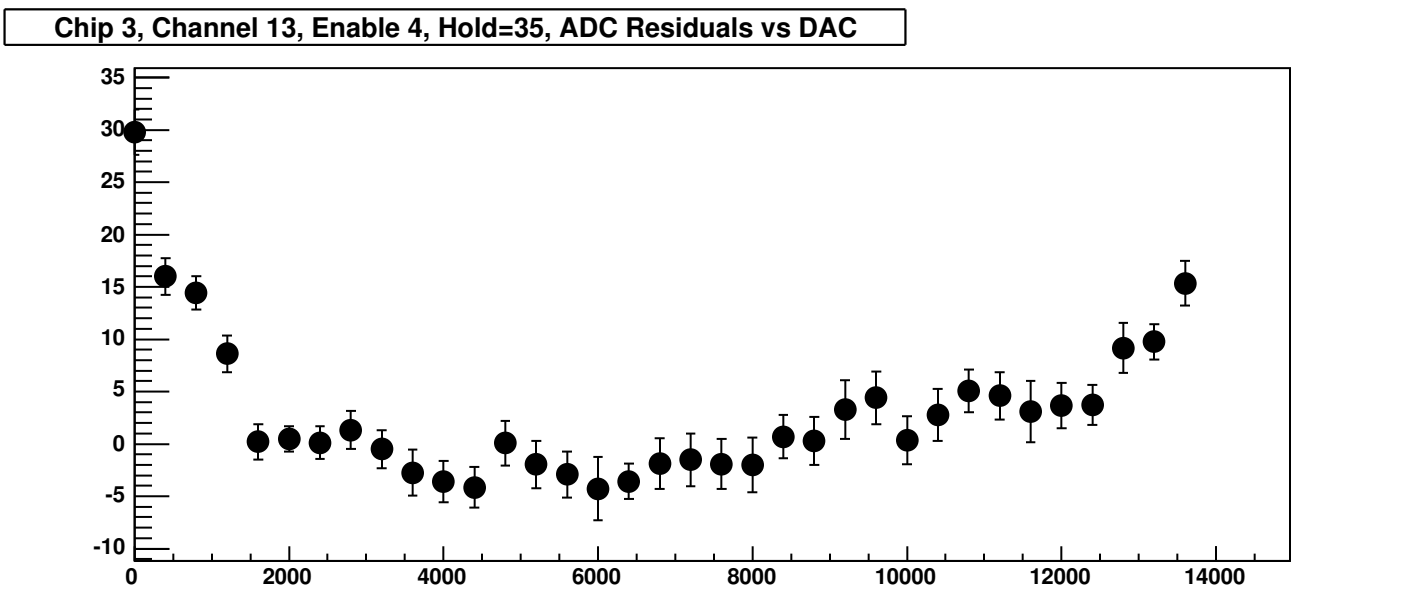

Chip 3, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 106 / 23
p0 -566.9 ± 0.7847
p1 0.01353 ± 0.0001204
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

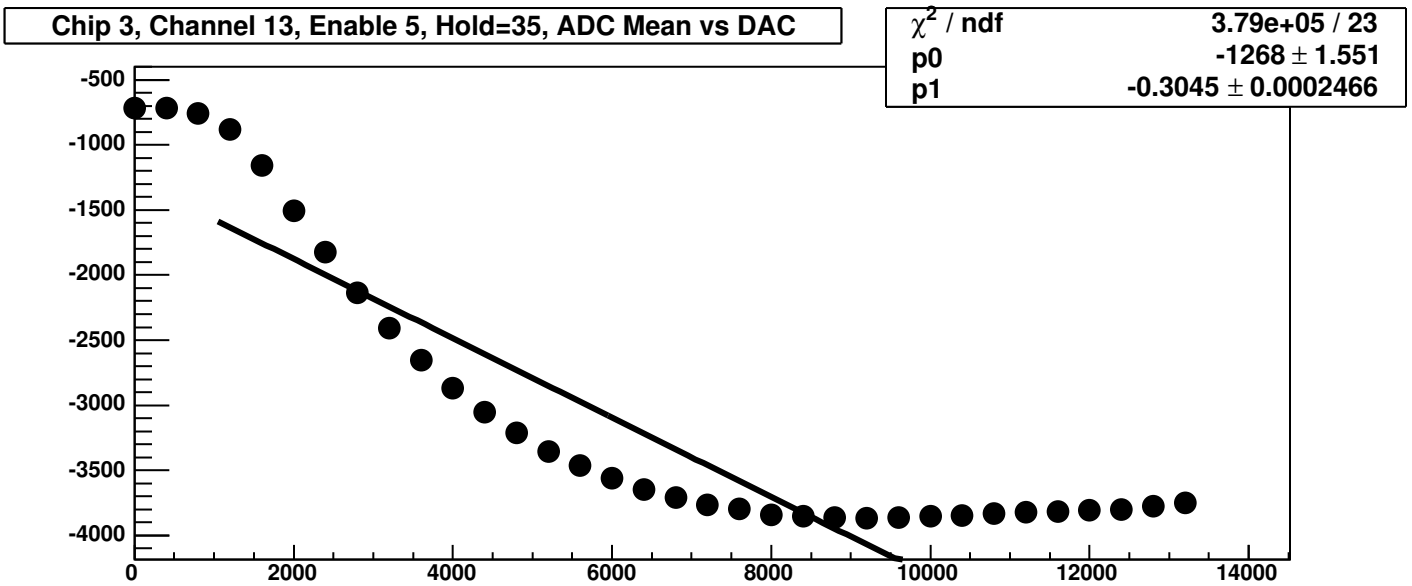

Chip 3, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

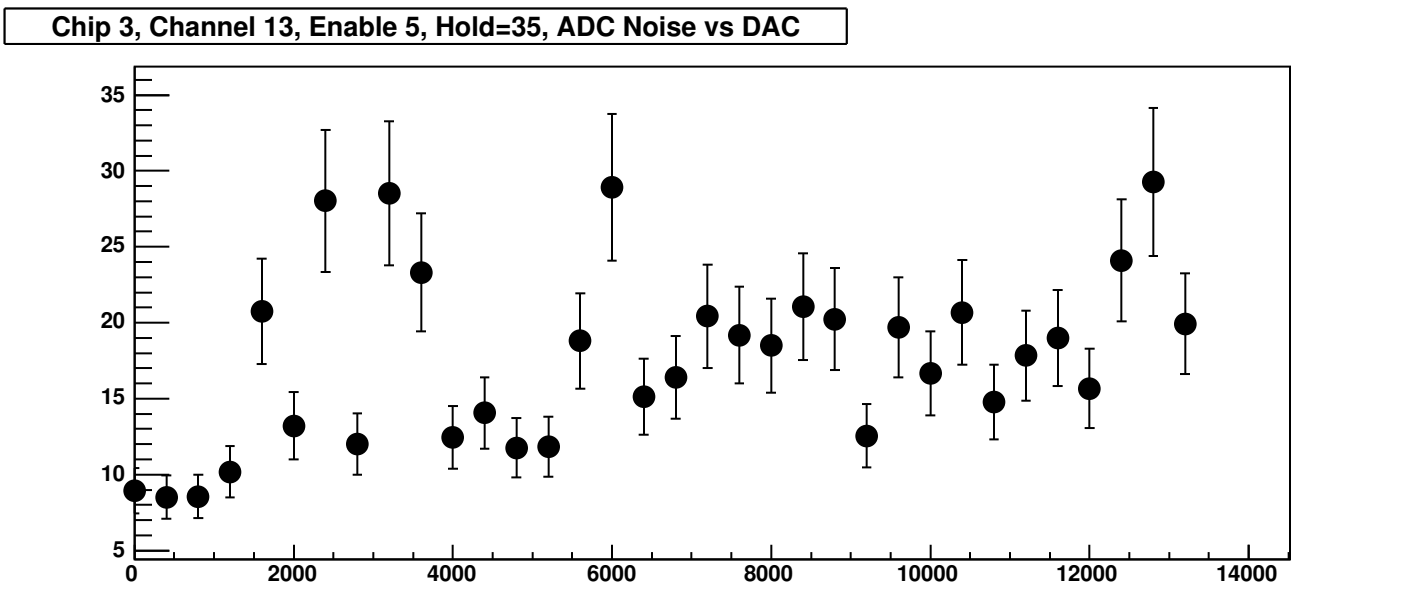

Chip 3, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

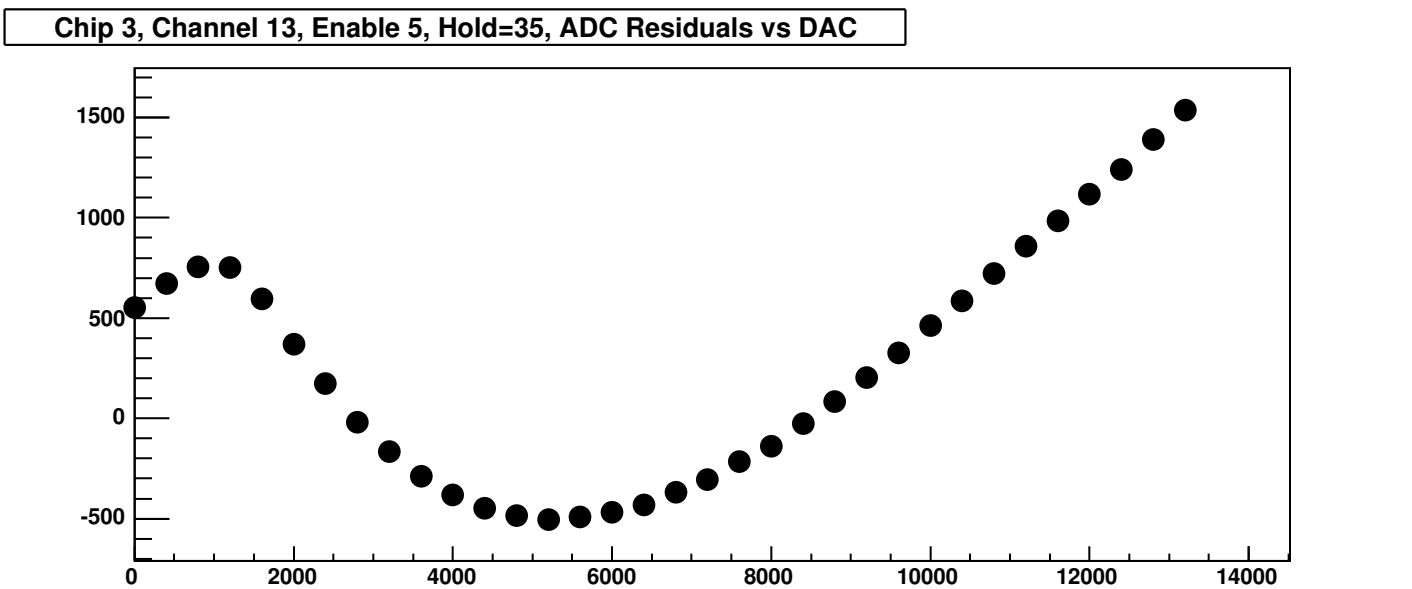

Chip 3, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 57.43 / 23
p0 -570.1 ± 0.8062
p1 0.02853 ± 0.0001361
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

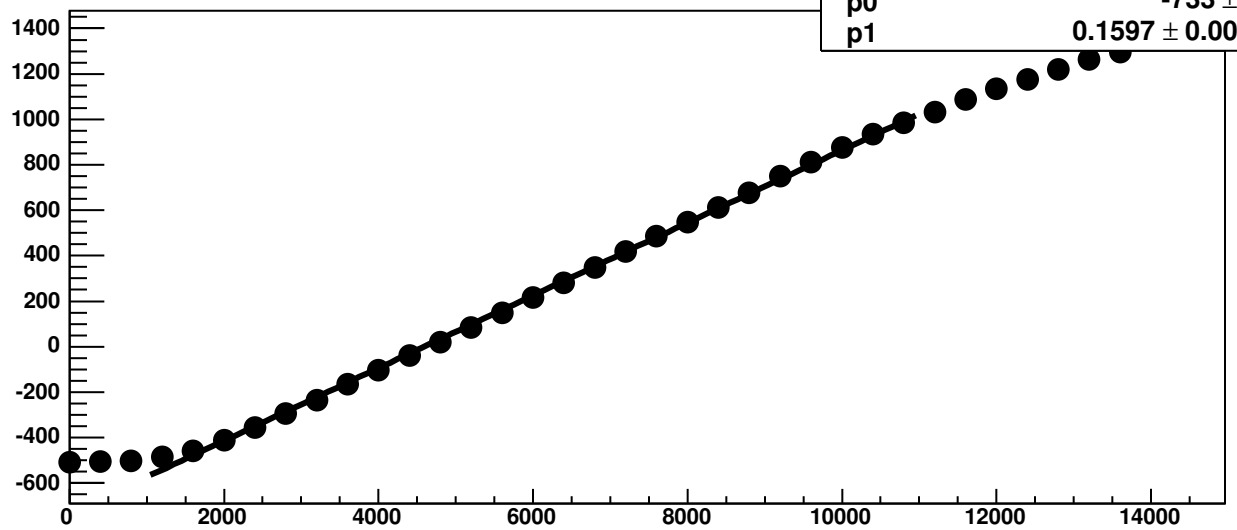

Chip 3, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.79e+05 / 23
p0 -1268 ± 1.551
p1 -0.3045 ± 0.0002466
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

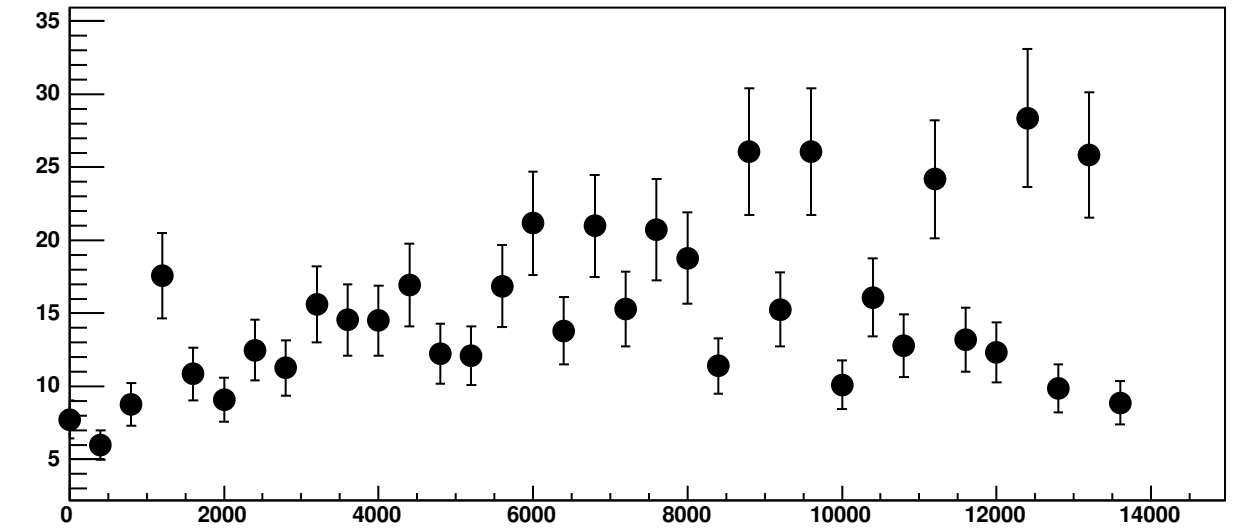

Chip 3, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

Chip 3, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

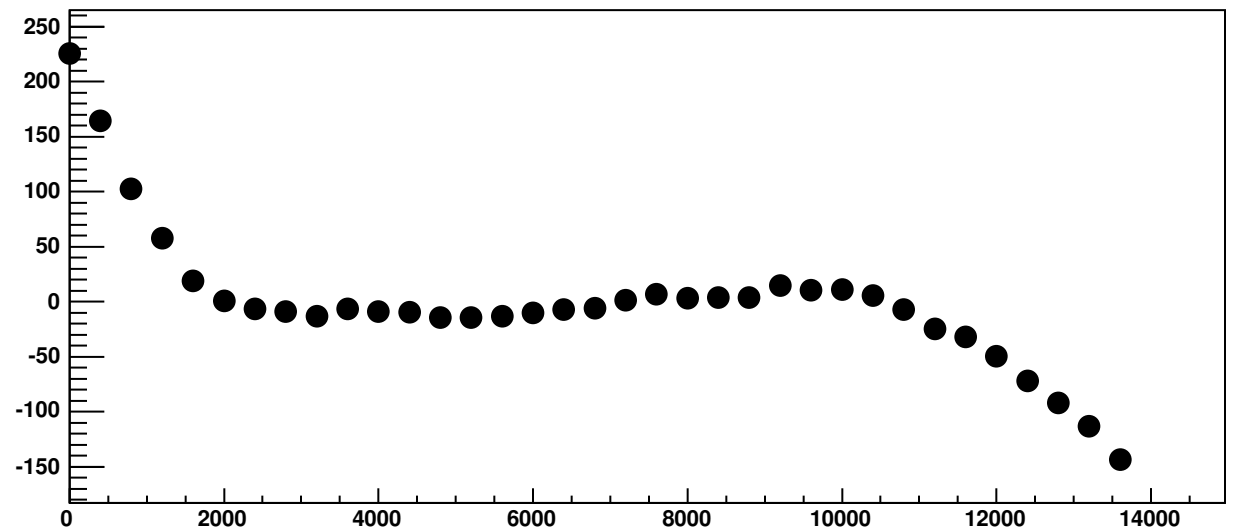
χ^2 / ndf	444.9 / 23
p0	-733 ± 1.334
p1	0.1597 ± 0.0002103

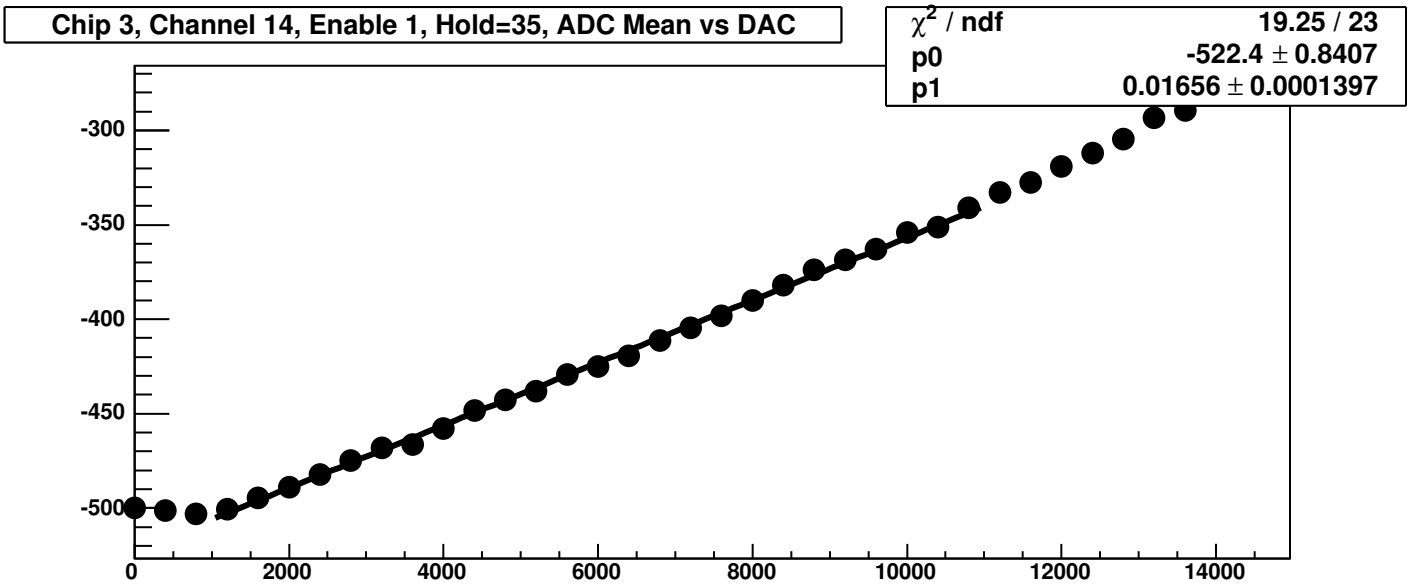


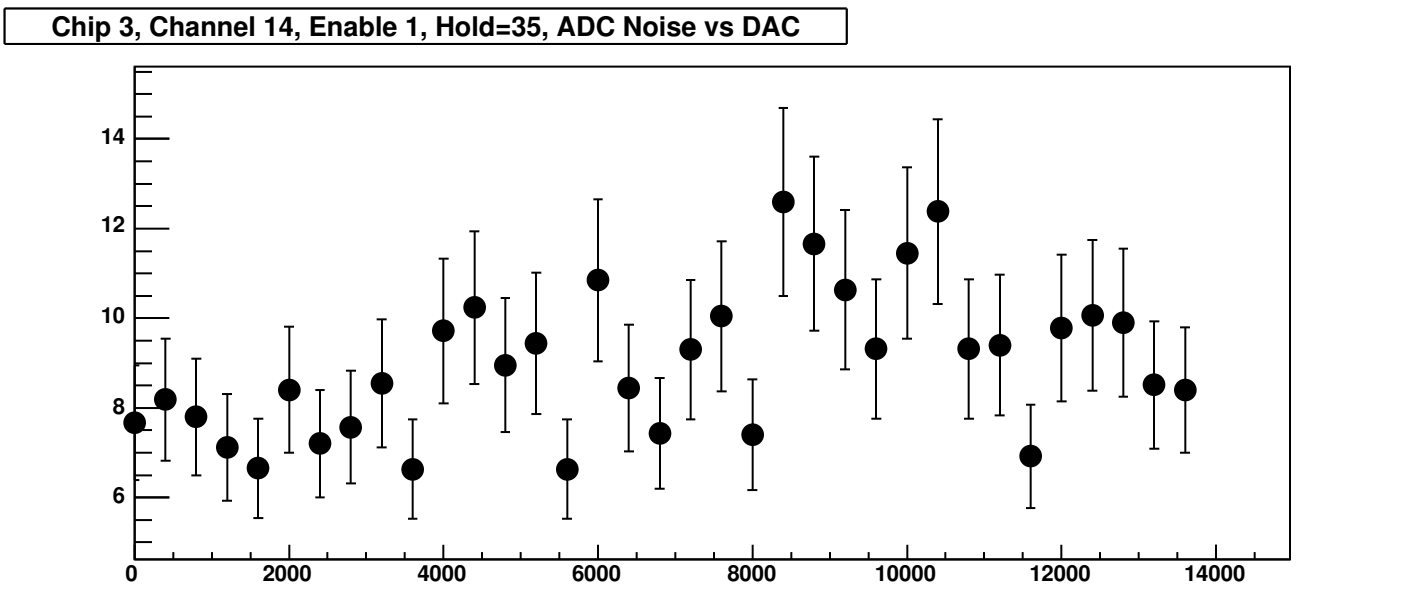
Chip 3, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

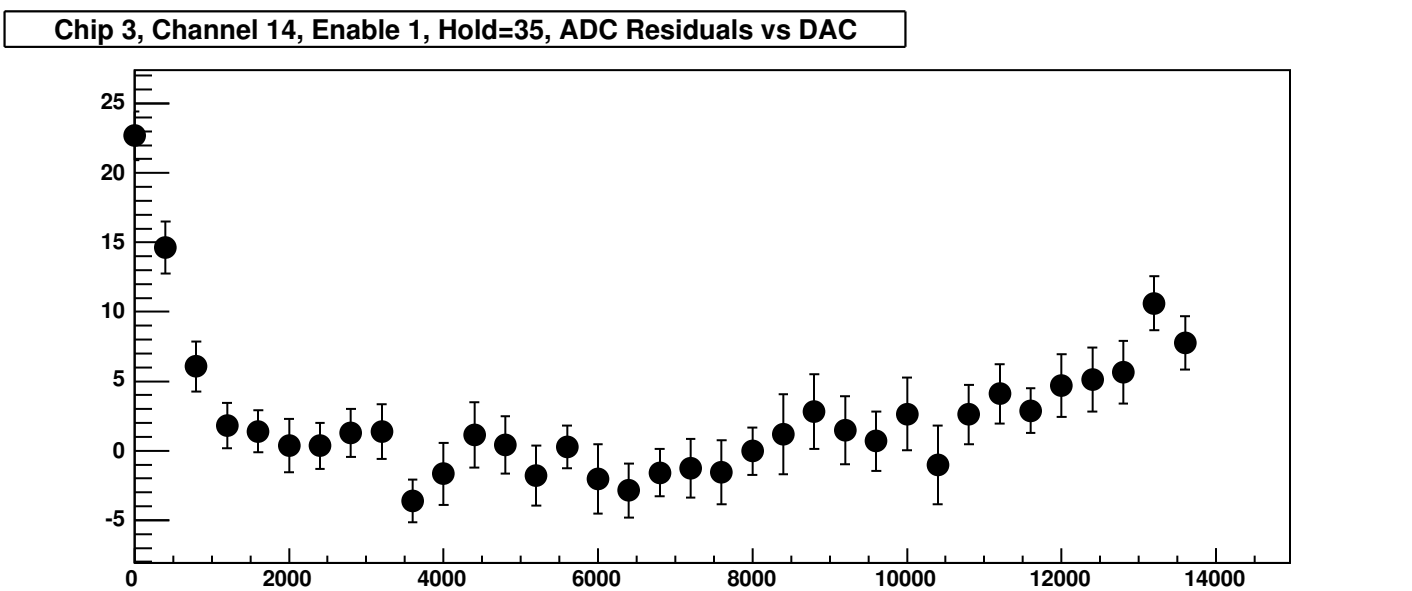


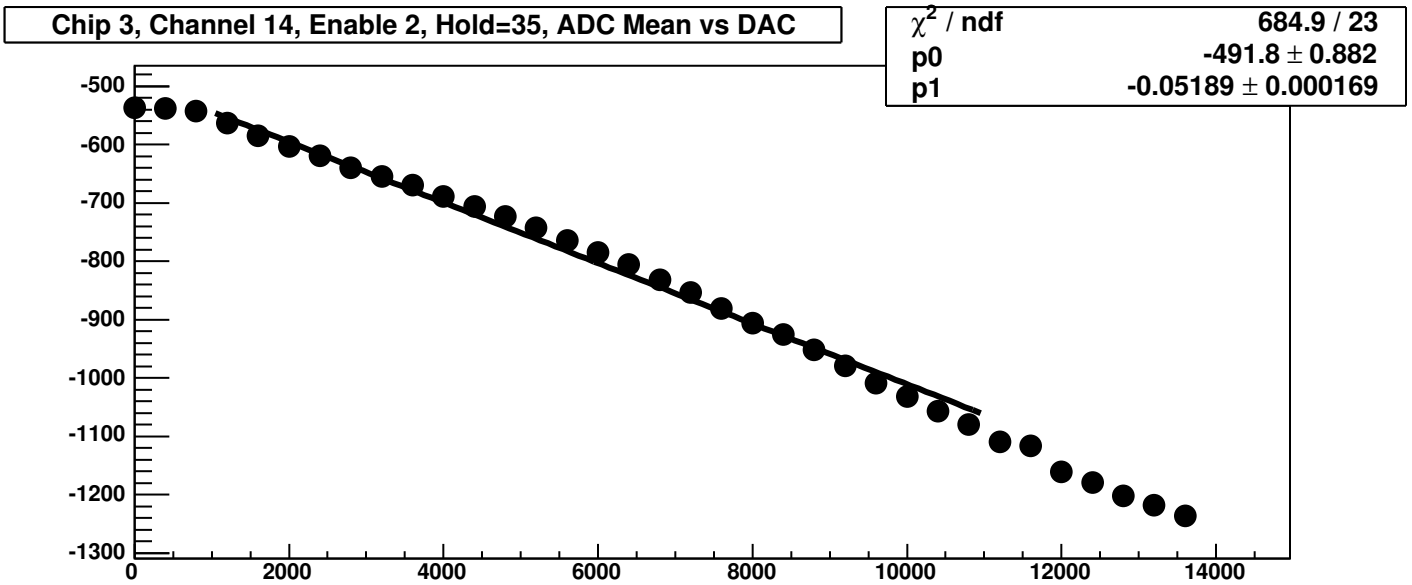
Chip 3, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

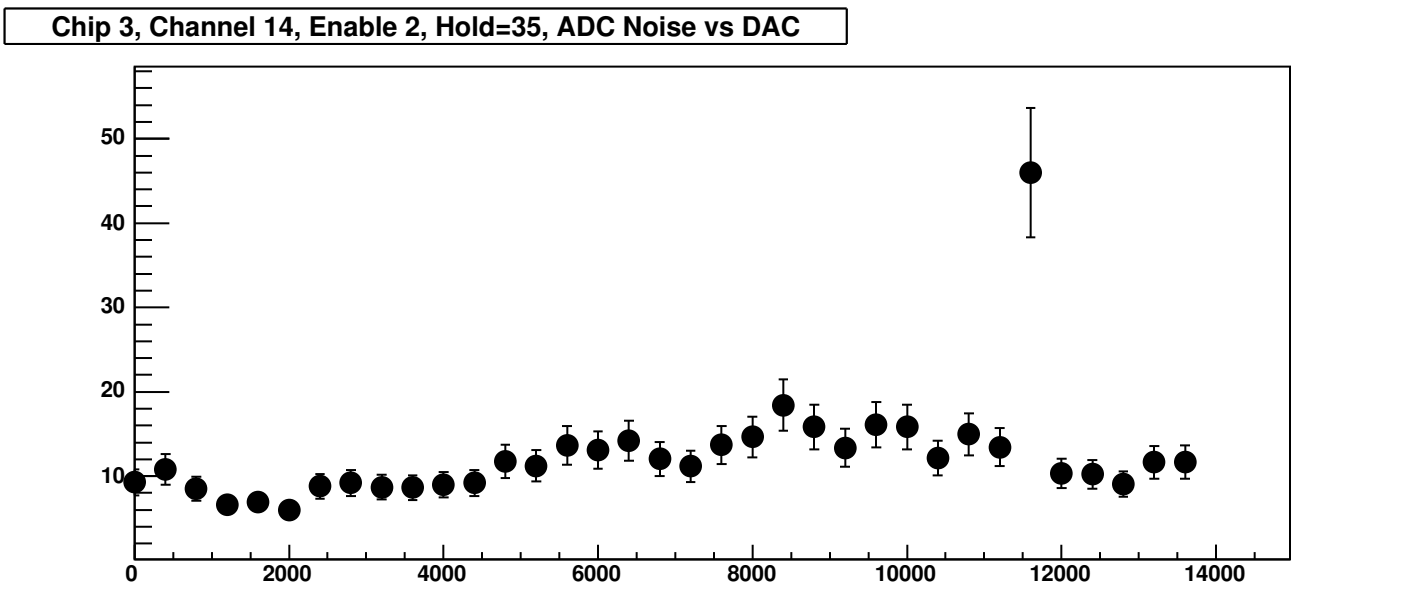


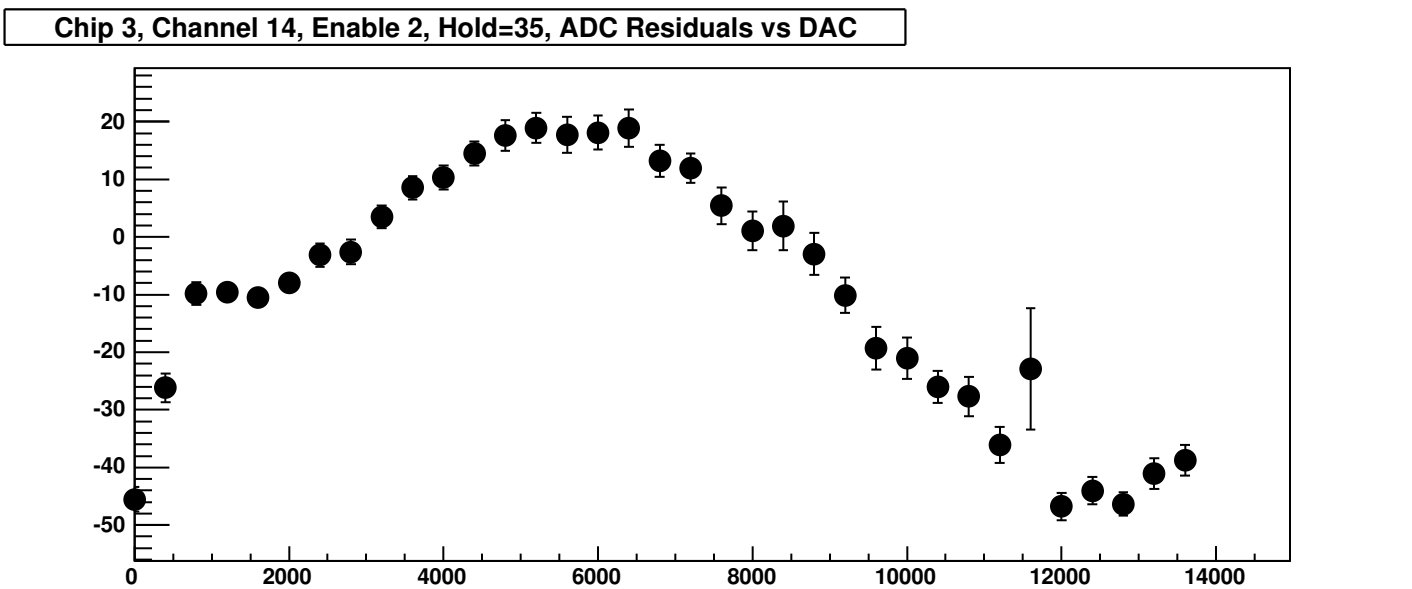

Chip 3, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 19.25 / 23
p0 -522.4 ± 0.8407
p1 0.01656 ± 0.0001397
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

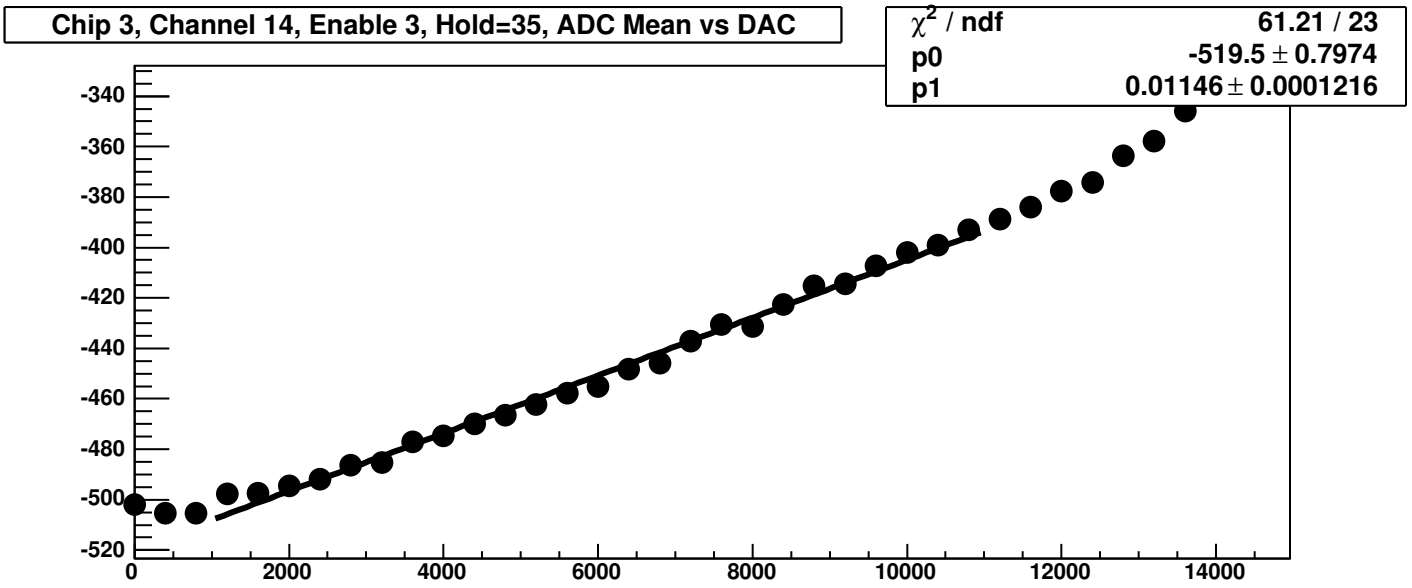

Chip 3, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

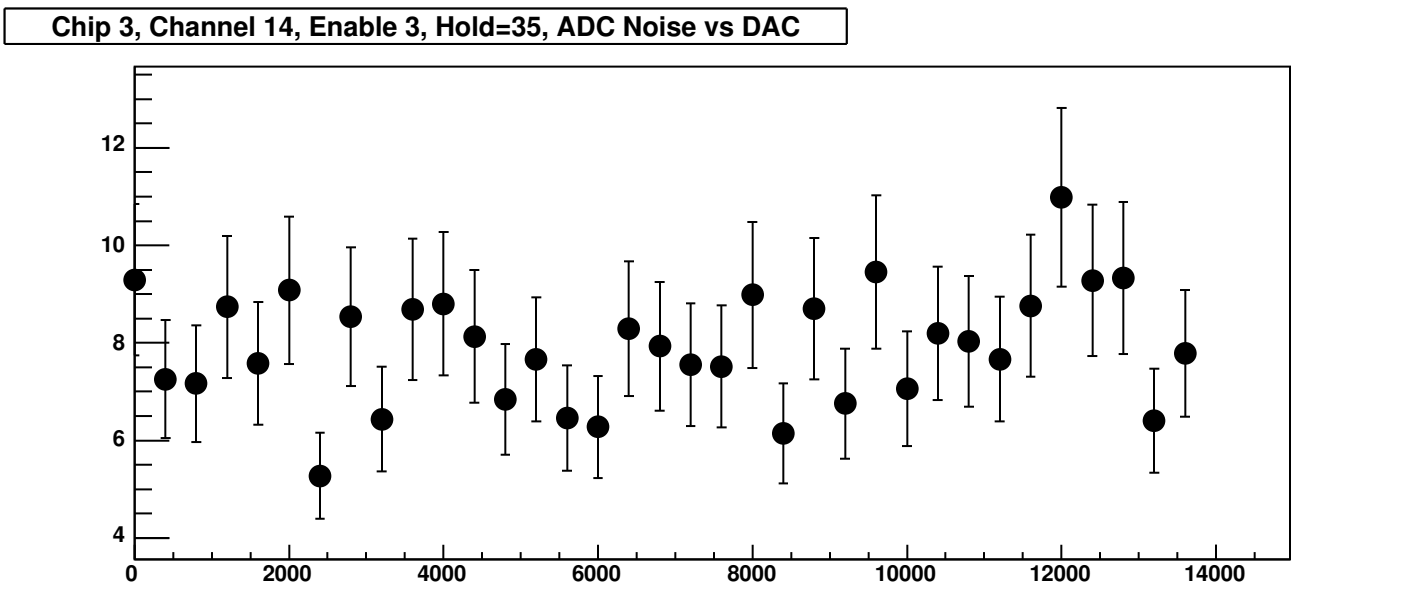

Chip 3, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

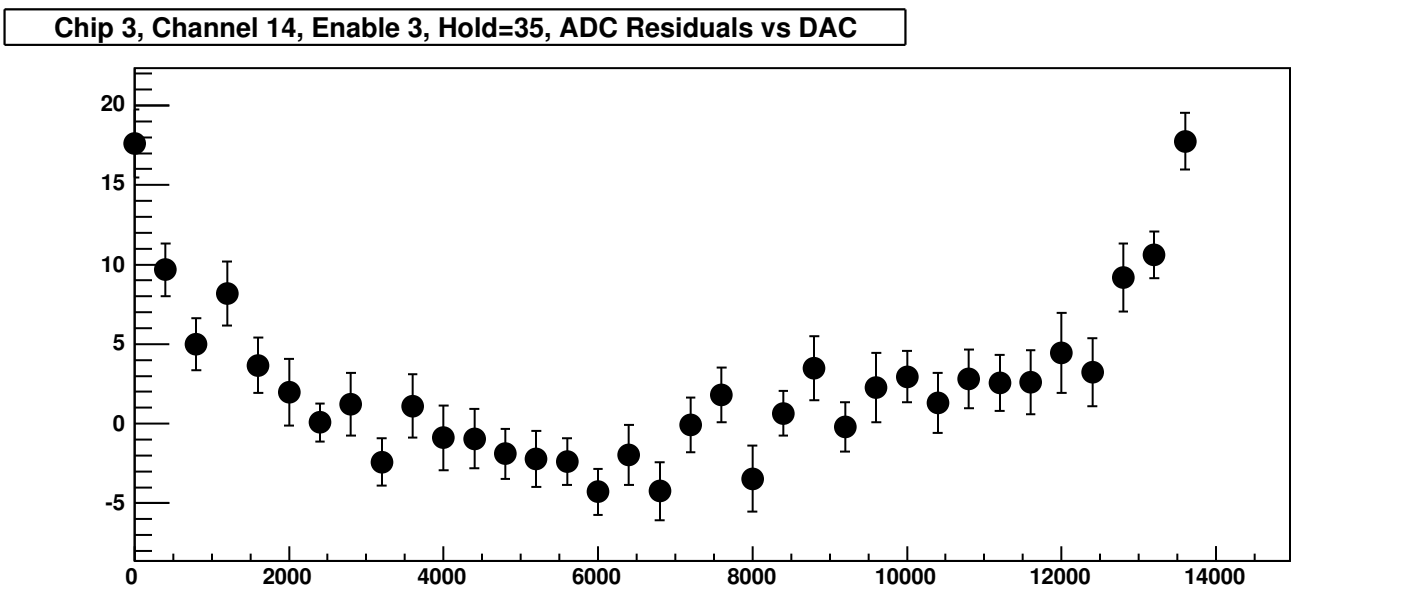

Chip 3, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 684.9 / 23
p0 -491.8 ± 0.882
p1 -0.05189 ± 0.000169
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
2000
4000
6000
8000
10000
12000
14000

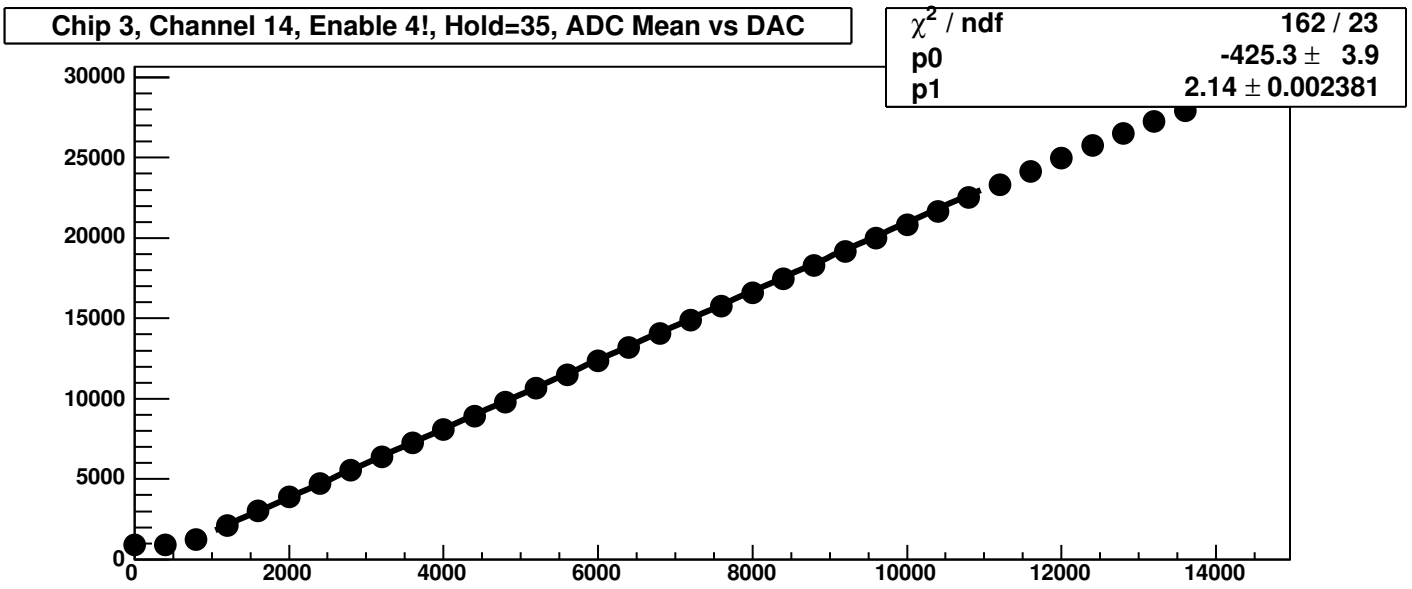

Chip 3, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

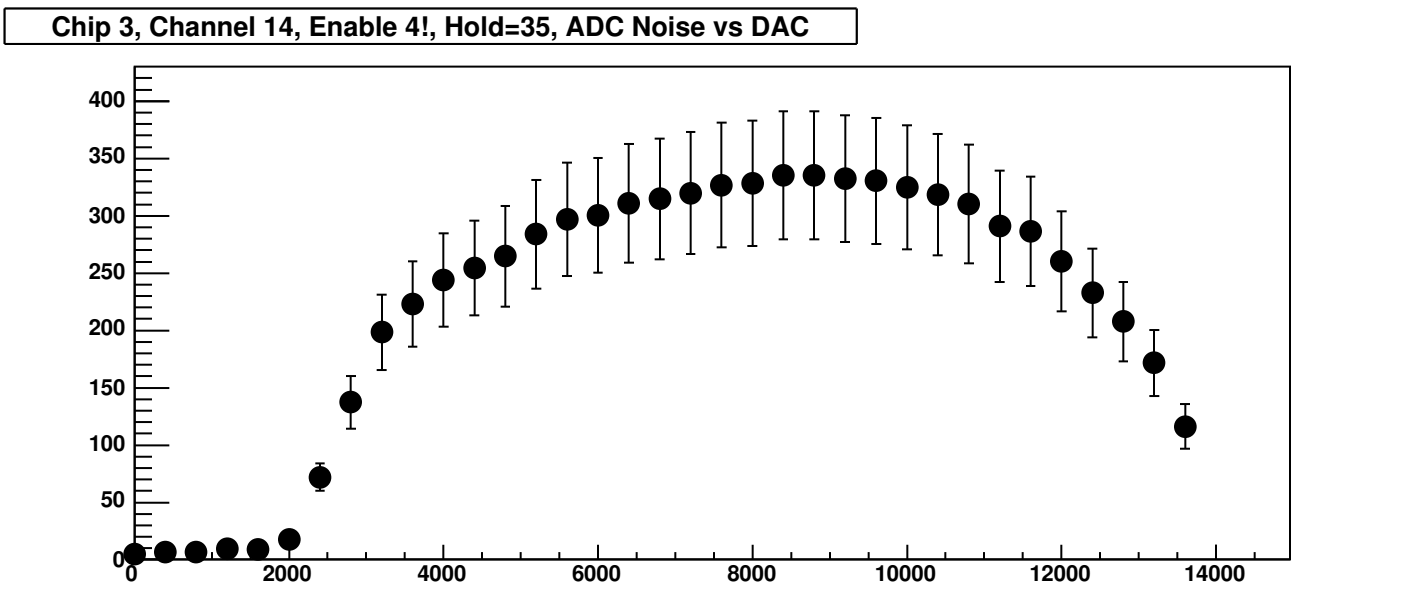

Chip 3, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

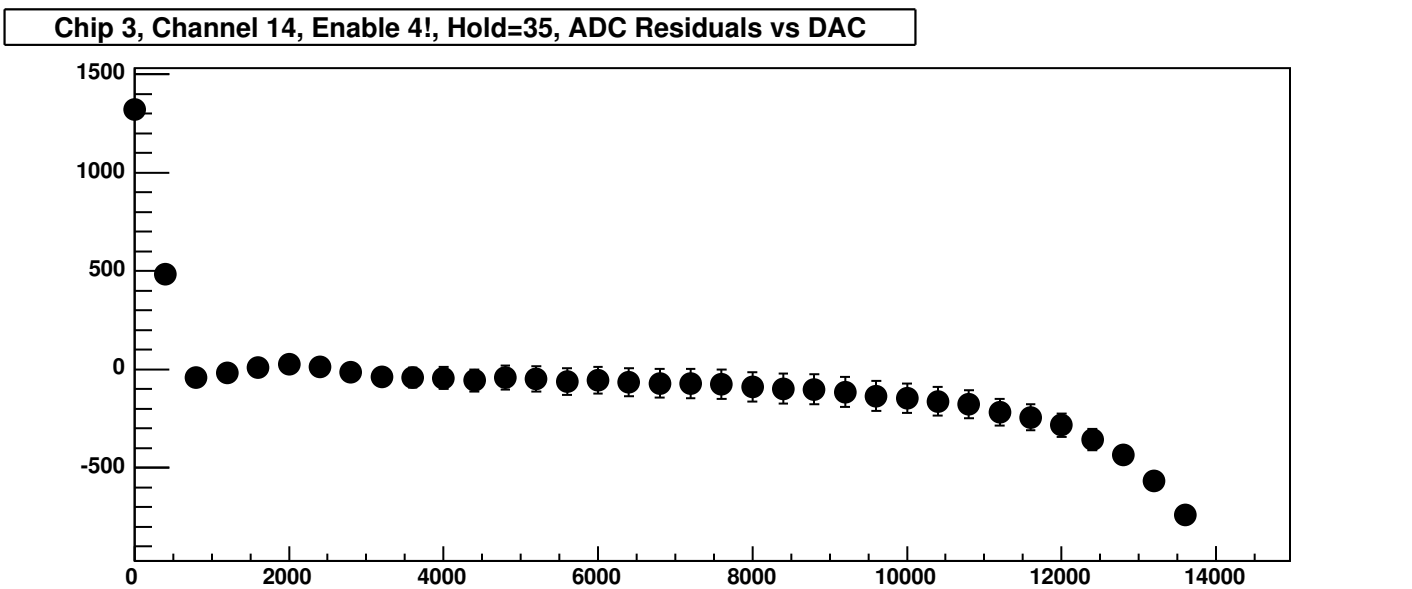

Chip 3, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 61.21 / 23
p0 -519.5 ± 0.7974
p1 0.01146 ± 0.0001216
-340
-360
-380
-400
-420
-440
-460
-480
-500
-520
0
2000
4000
6000
8000
10000
12000
14000

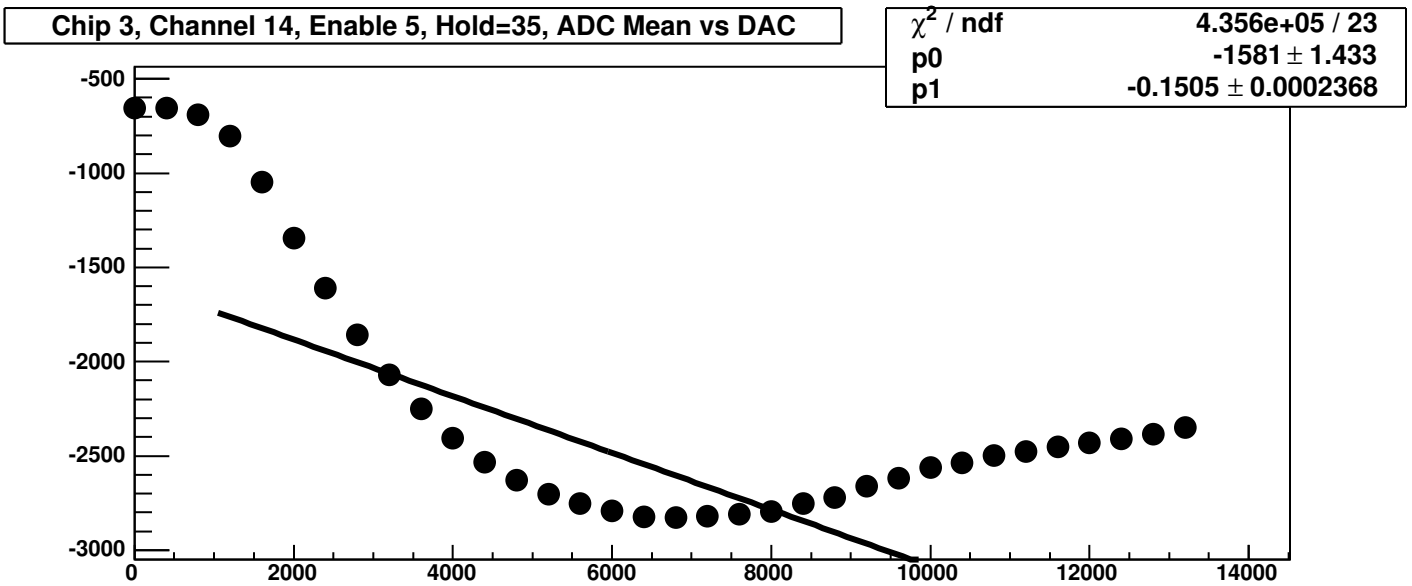

Chip 3, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

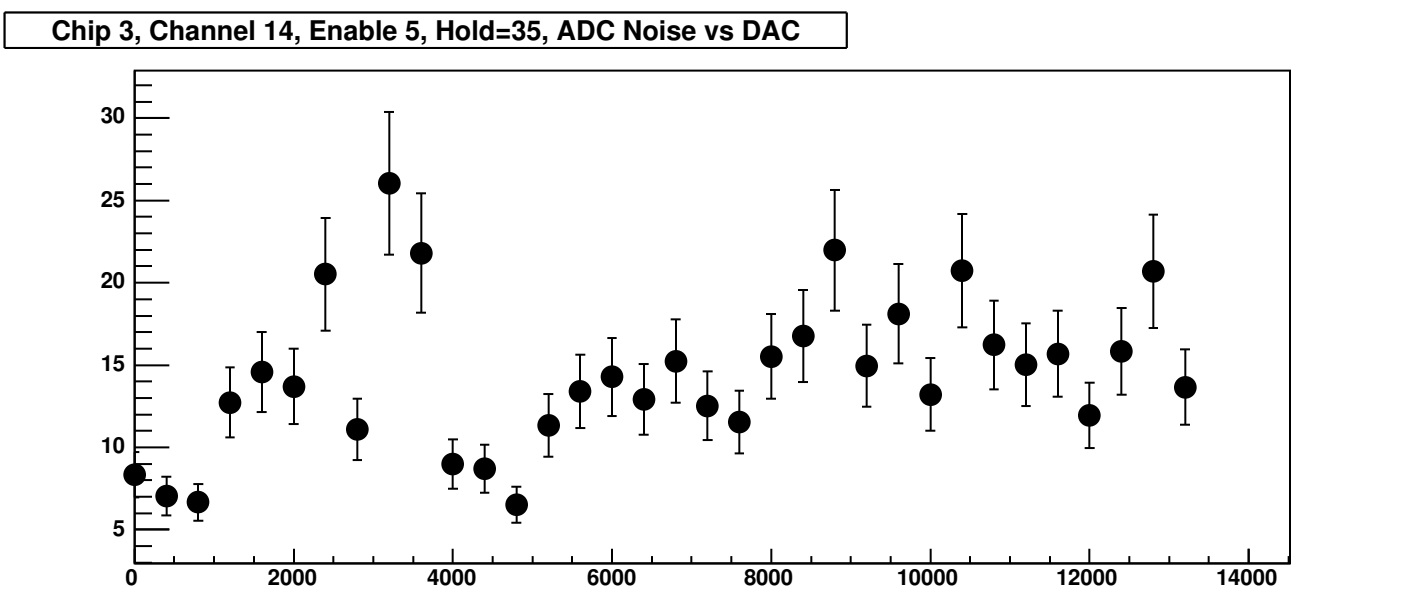

Chip 3, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

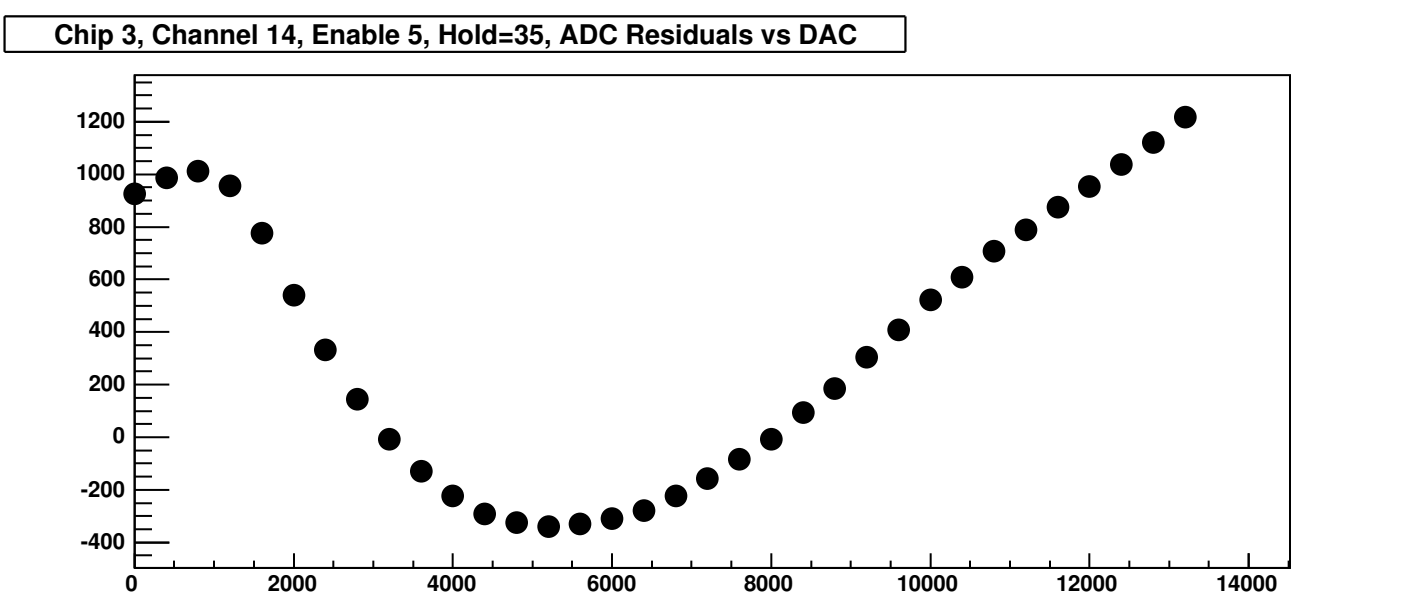

Chip 3, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 162 / 23
p0 -425.3 ± 3.9
p1 2.14 ± 0.002381
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

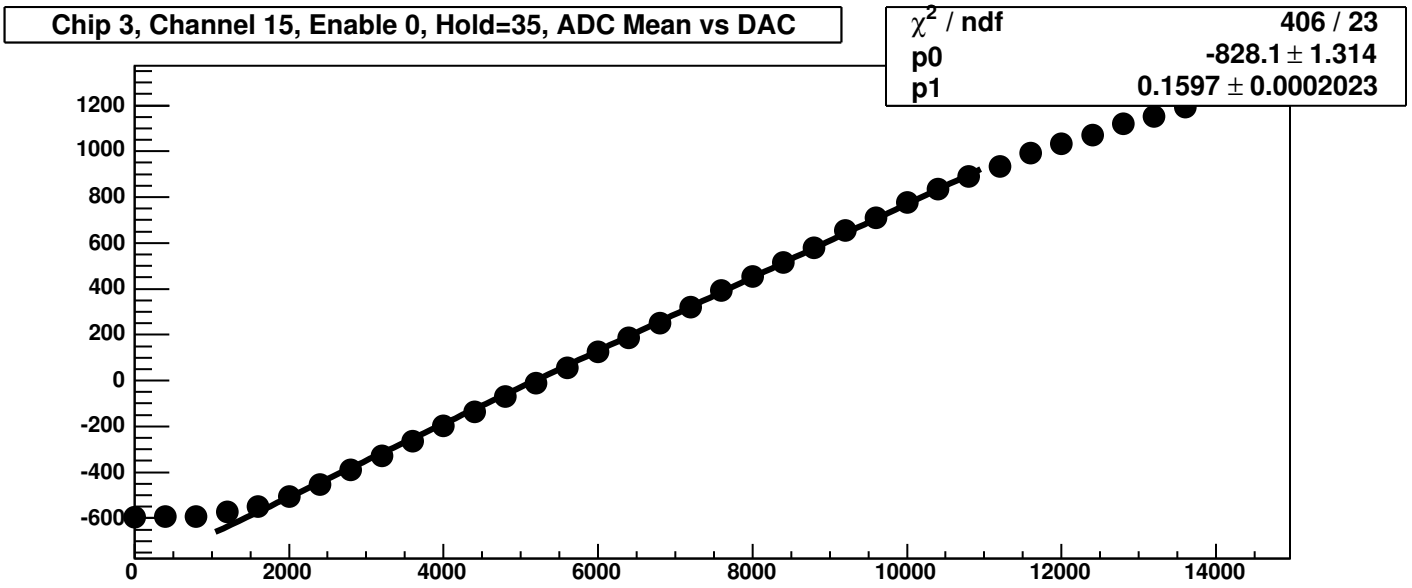

Chip 3, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

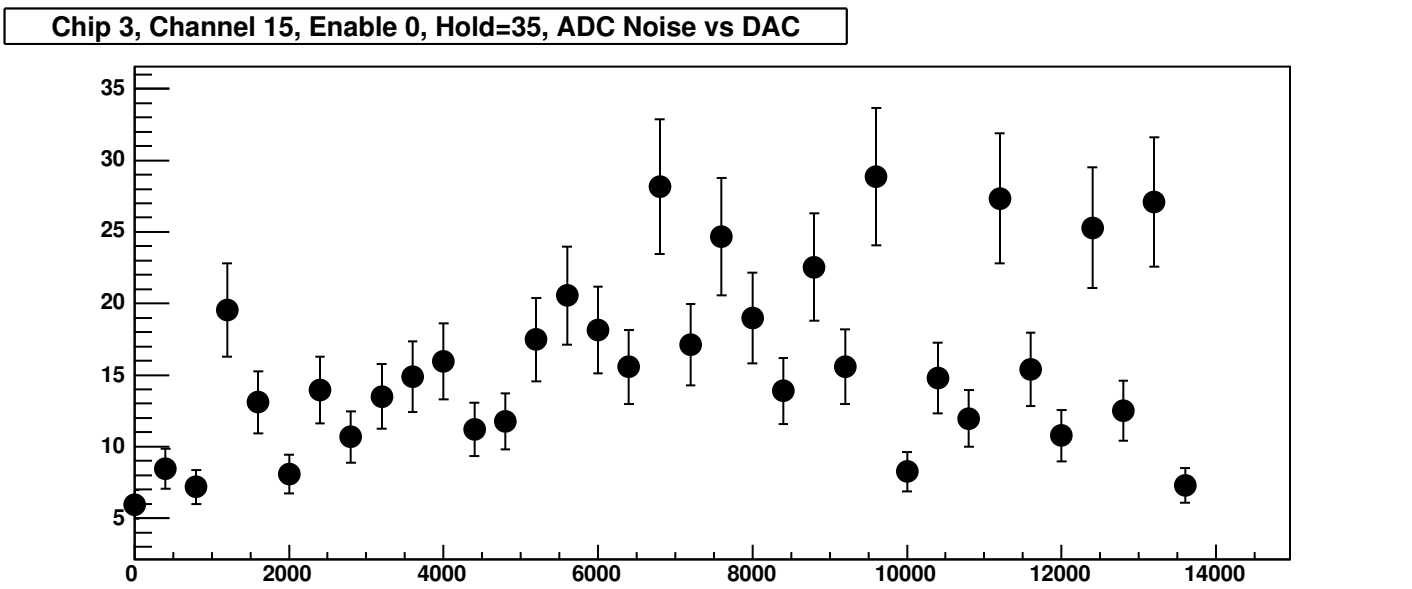

Chip 3, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

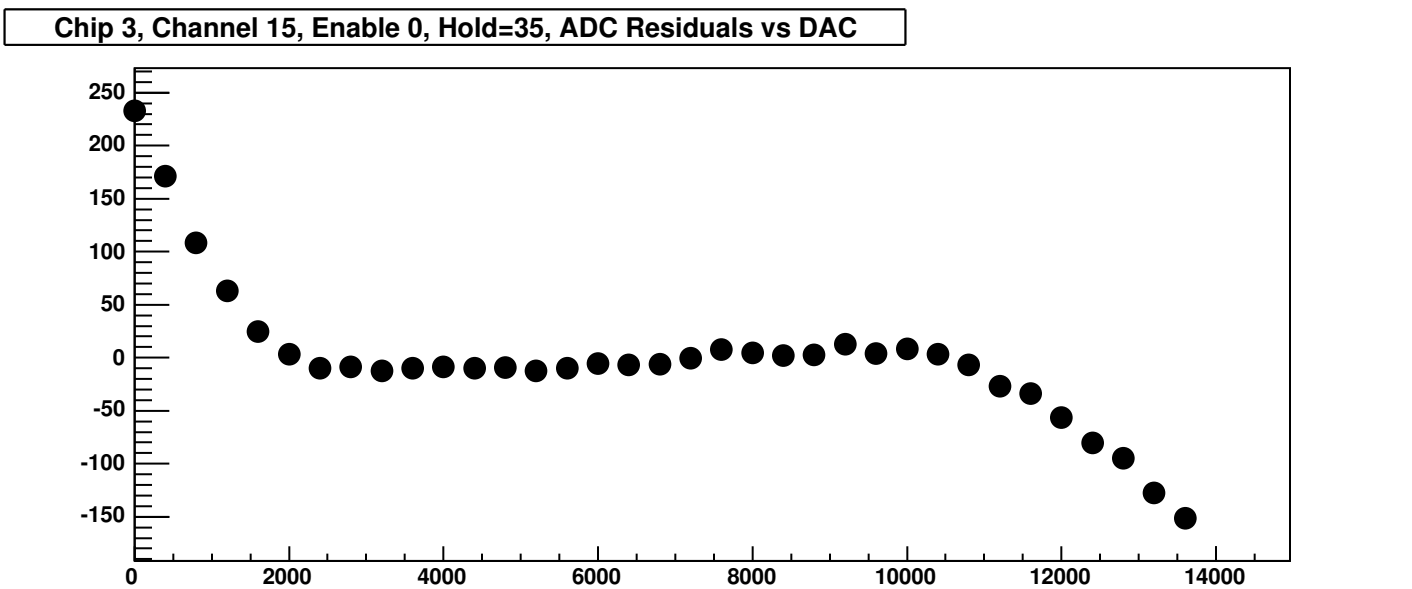

Chip 3, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4.356e+05 / 23
p0 -1581 ± 1.433
p1 -0.1505 ± 0.0002368
-500
-1000
-1500
-2000
-2500
-3000
0
2000
4000
6000
8000
10000
12000
14000

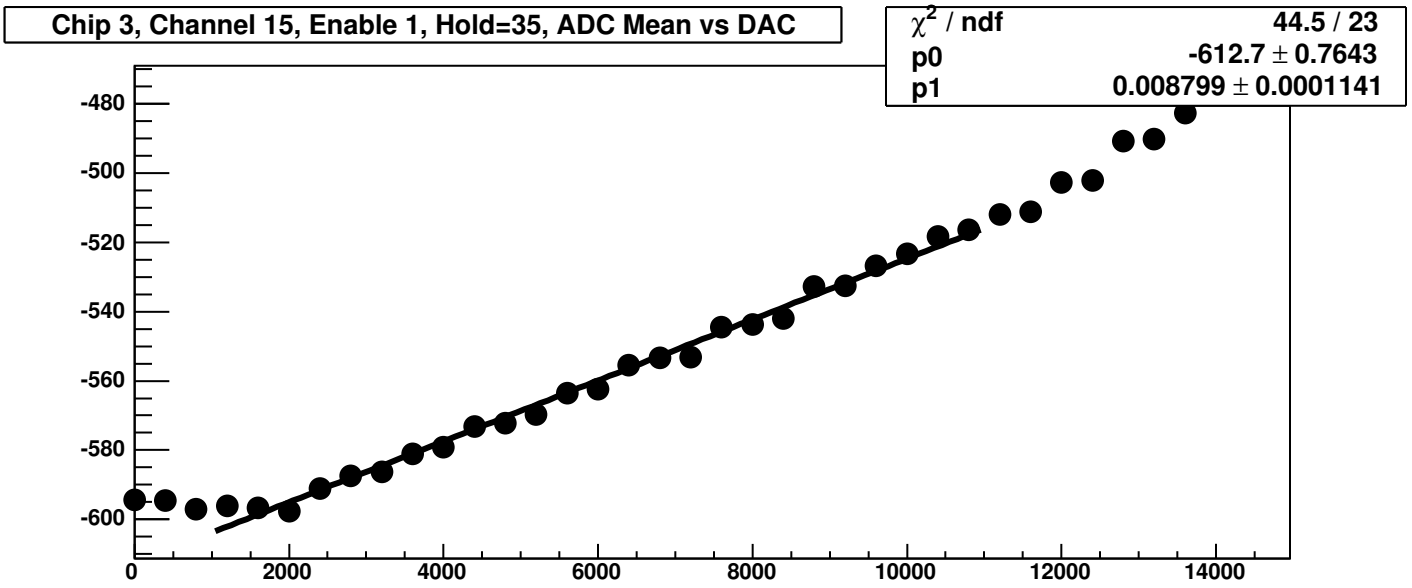

Chip 3, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

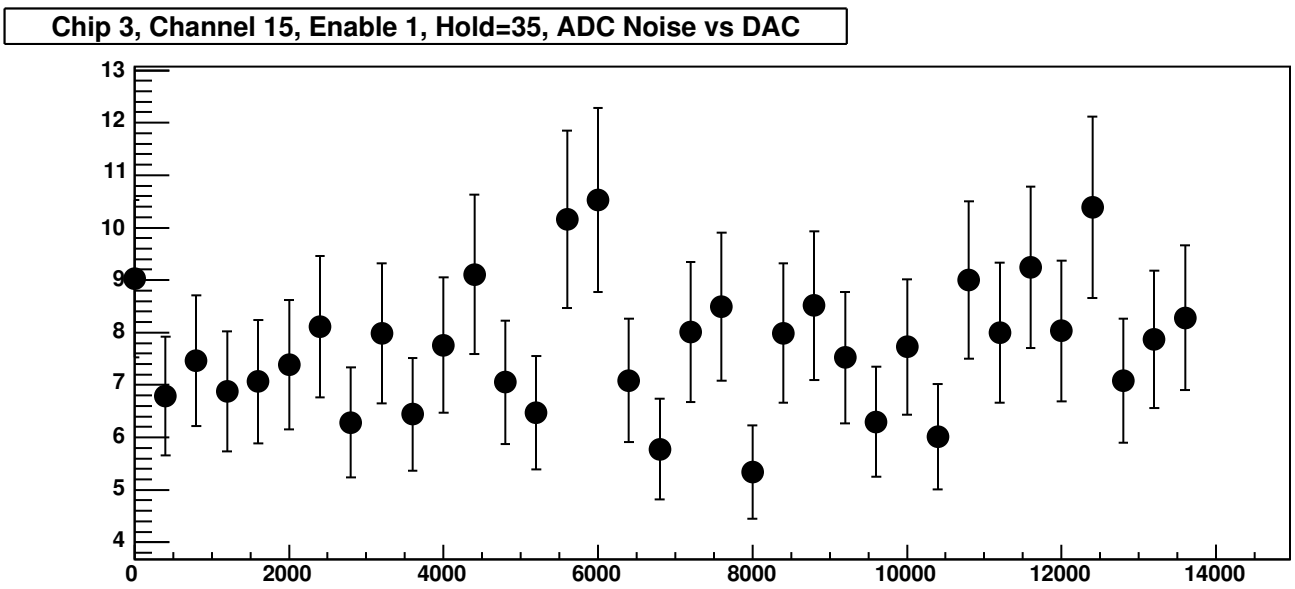

Chip 3, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

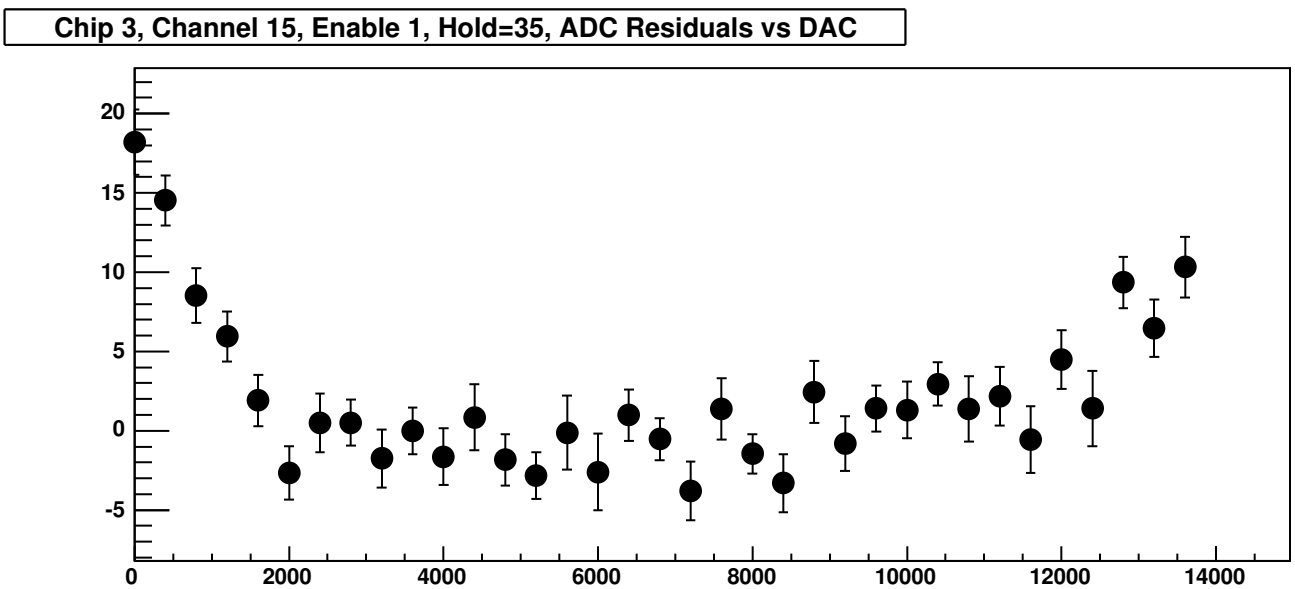

Chip 3, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 406 / 23
p0 -828.1 ± 1.314
p1 0.1597 ± 0.0002023
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

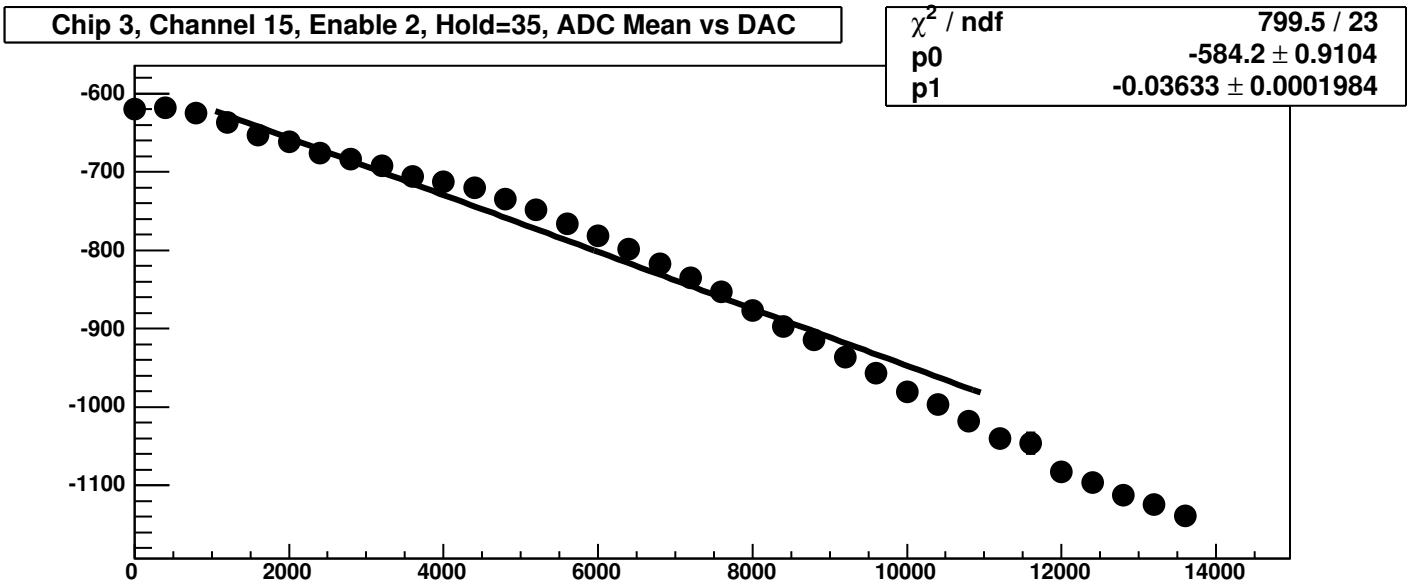

Chip 3, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

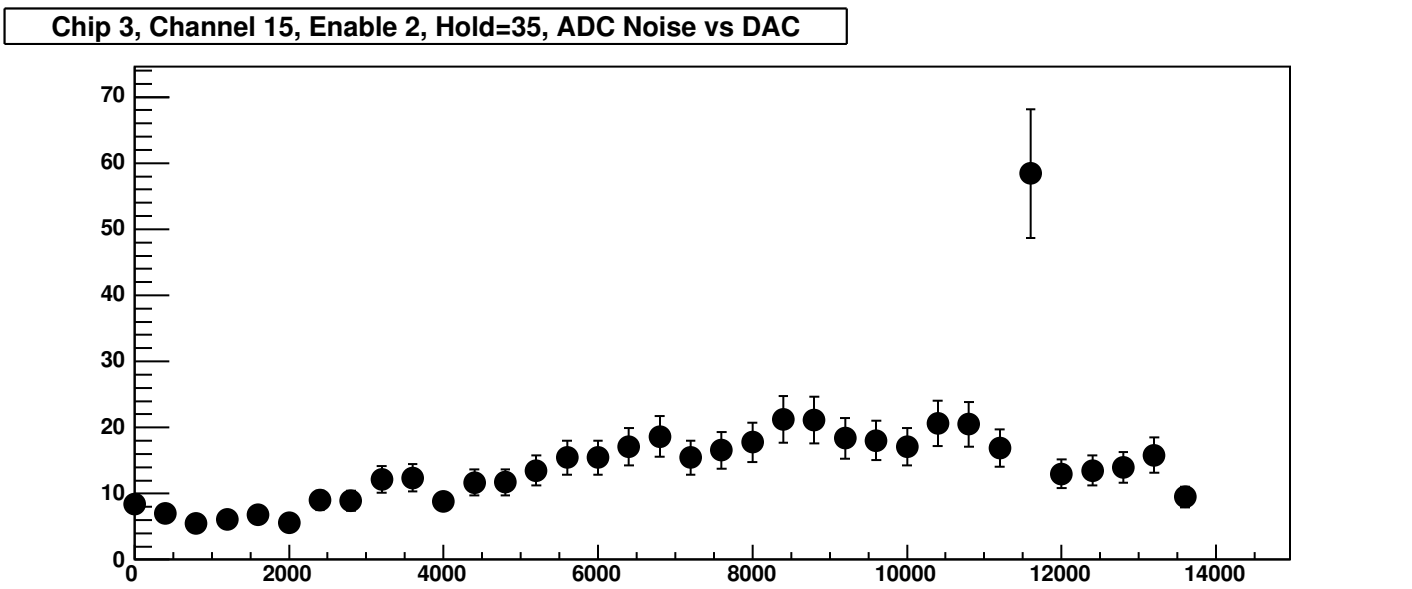

Chip 3, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

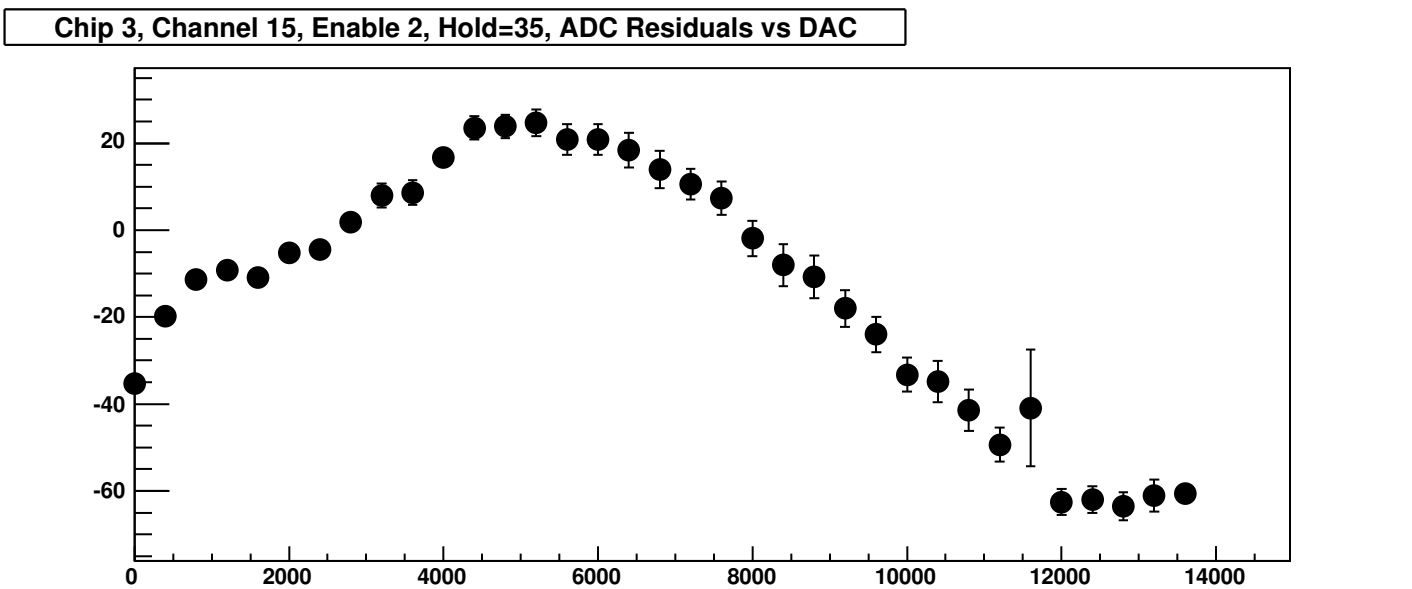

Chip 3, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 44.5 / 23
p0 -612.7 ± 0.7643
p1 0.008799 ± 0.0001141
-480
-500
-520
-540
-560
-580
-600
0
2000
4000
6000
8000
10000
12000
14000

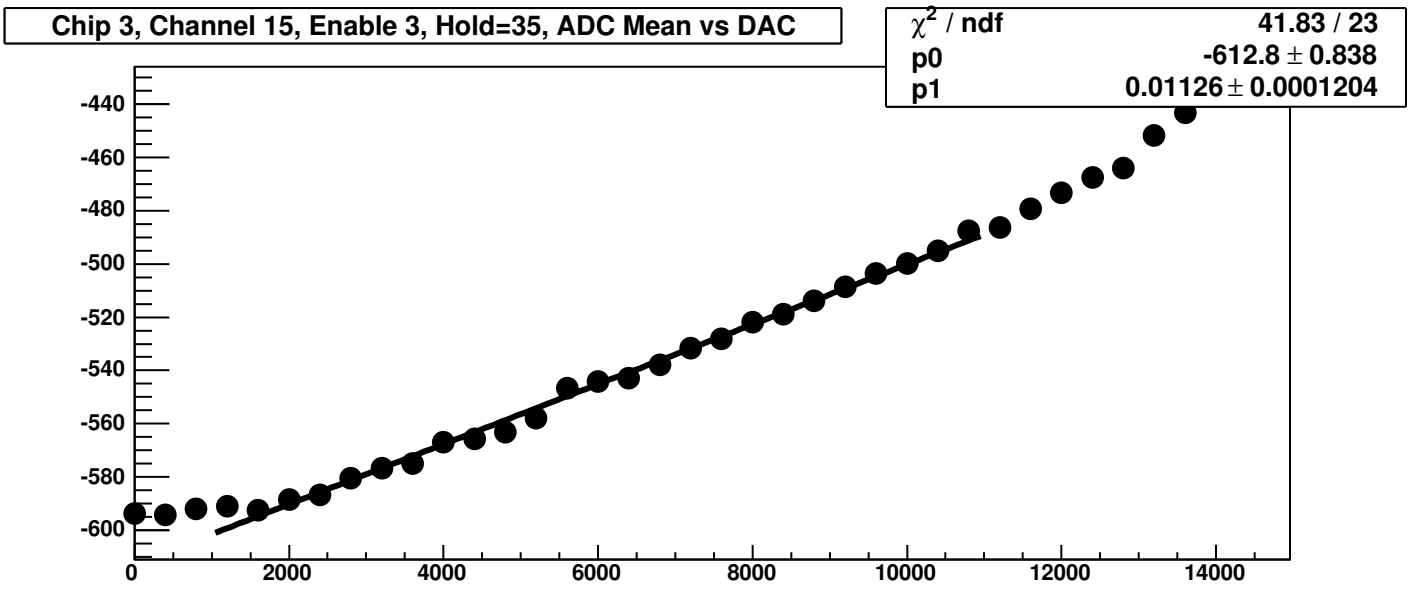

Chip 3, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

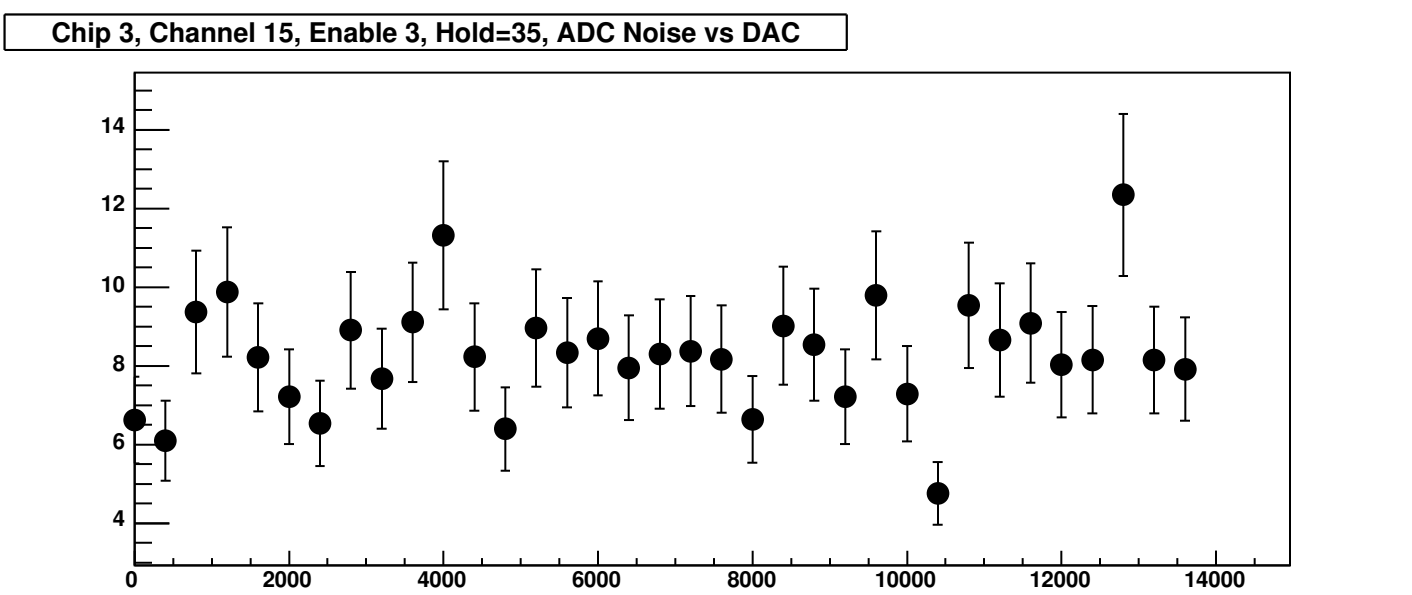

Chip 3, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

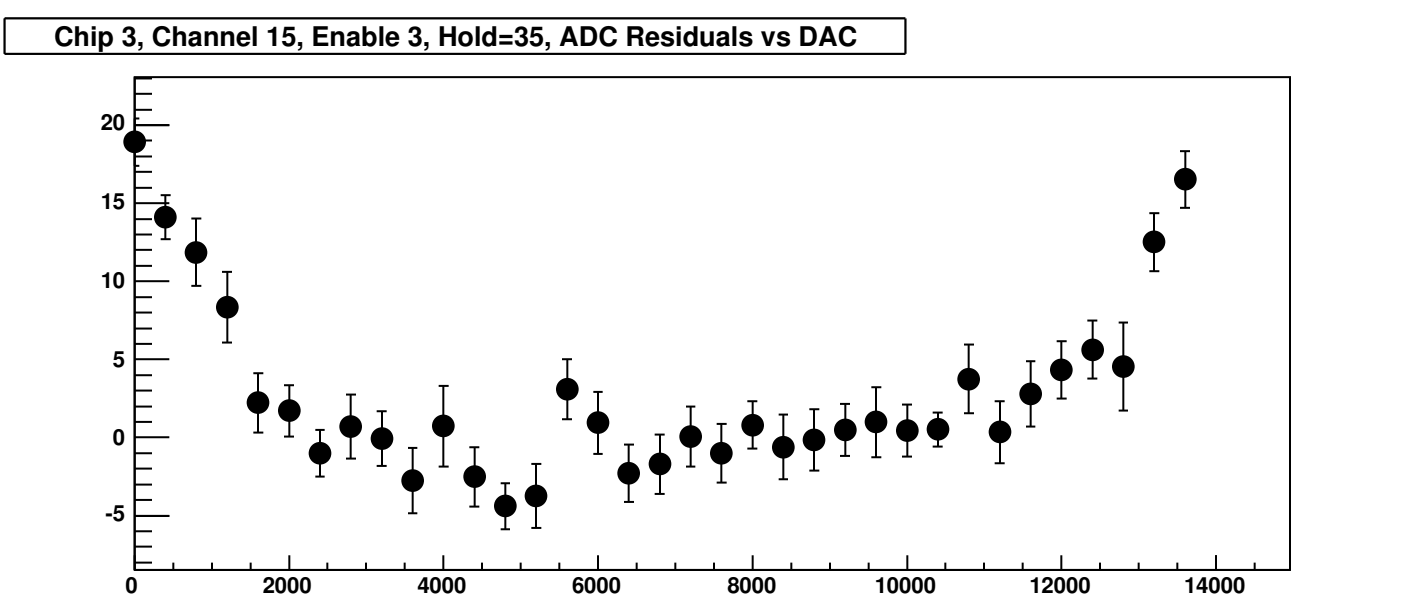

Chip 3, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 799.5 / 23
p0 -584.2 ± 0.9104
p1 -0.03633 ± 0.0001984
-600
-700
-800
-900
-1000
-1100
0
2000
4000
6000
8000
10000
12000
14000

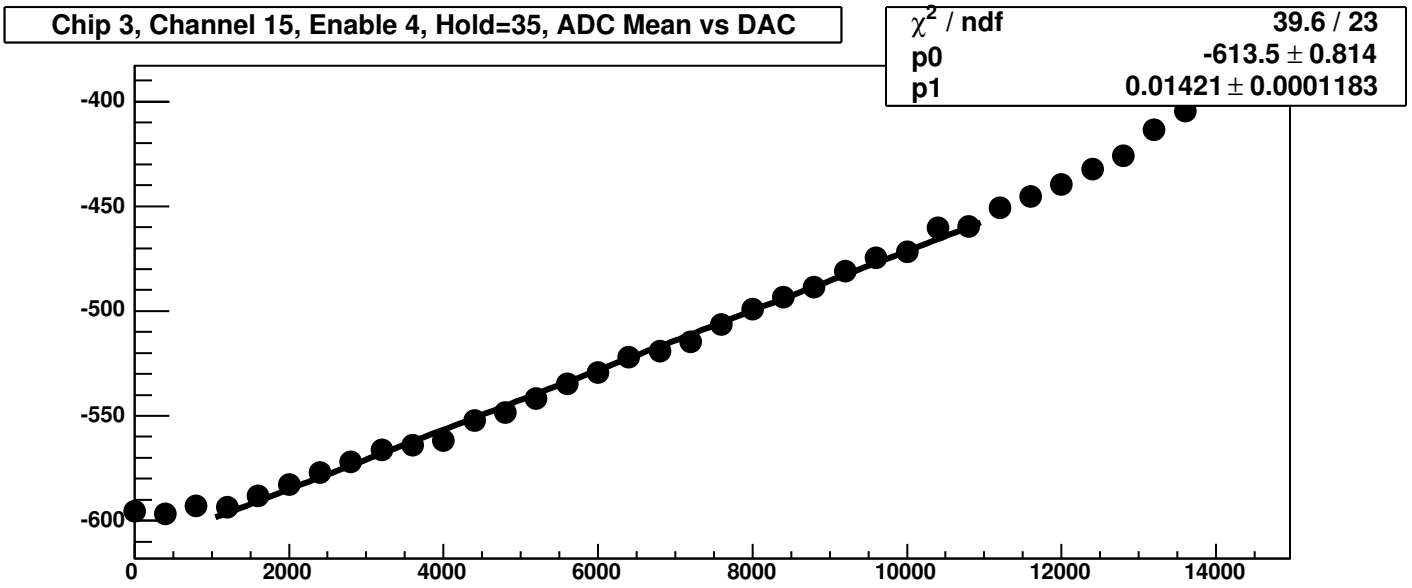

Chip 3, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

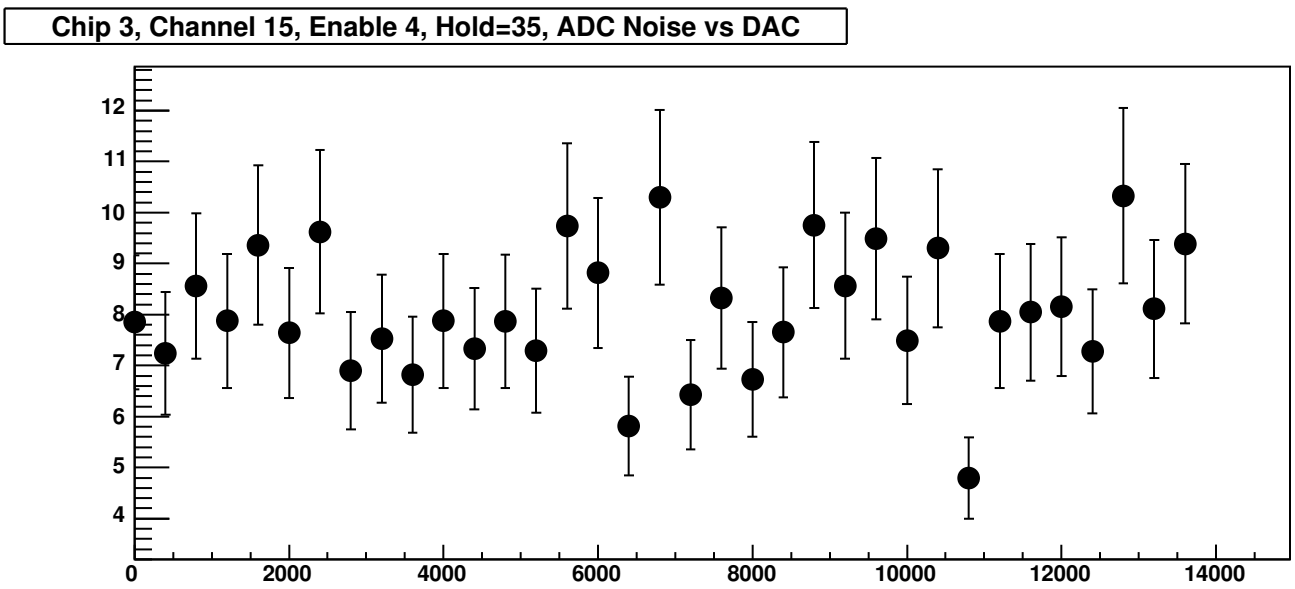

Chip 3, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
0
2000
4000
6000
8000
10000
12000
14000

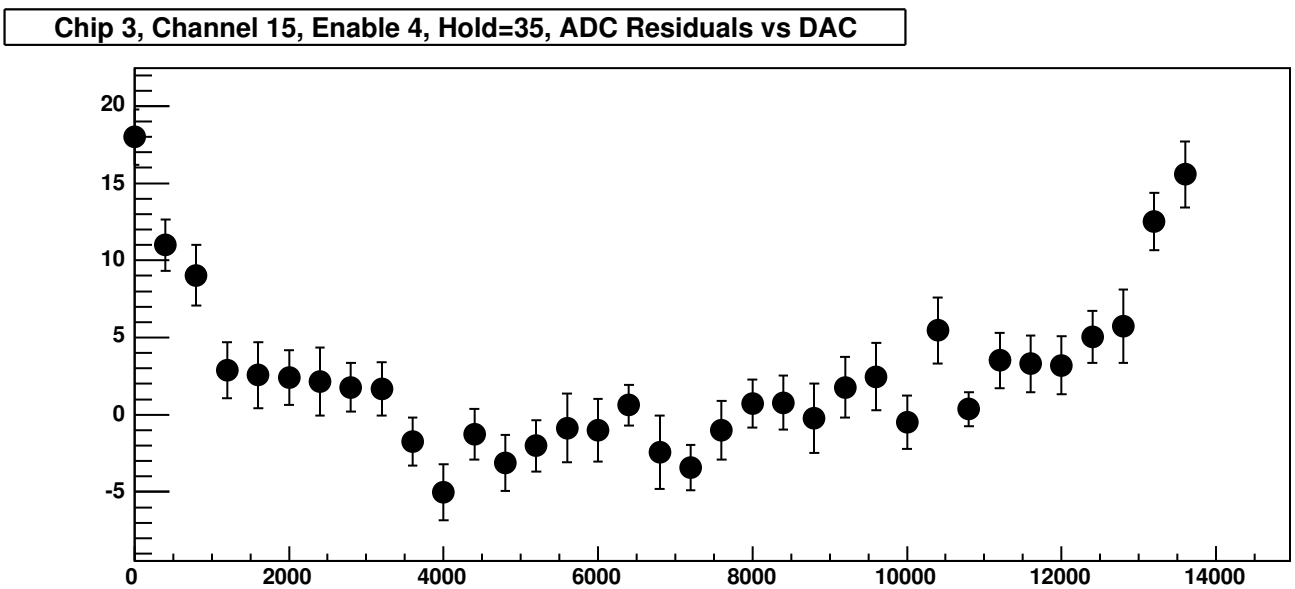

Chip 3, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 41.83 / 23
p0 -612.8 ± 0.838
p1 0.01126 ± 0.0001204
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
2000
4000
6000
8000
10000
12000
14000

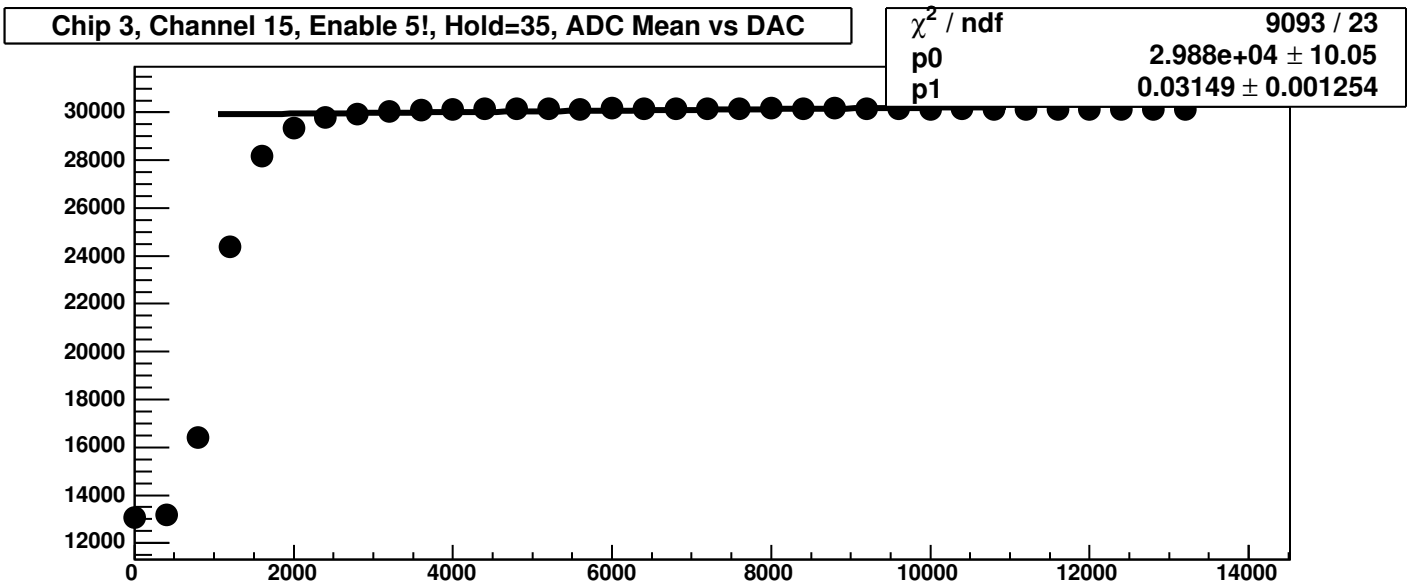

Chip 3, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

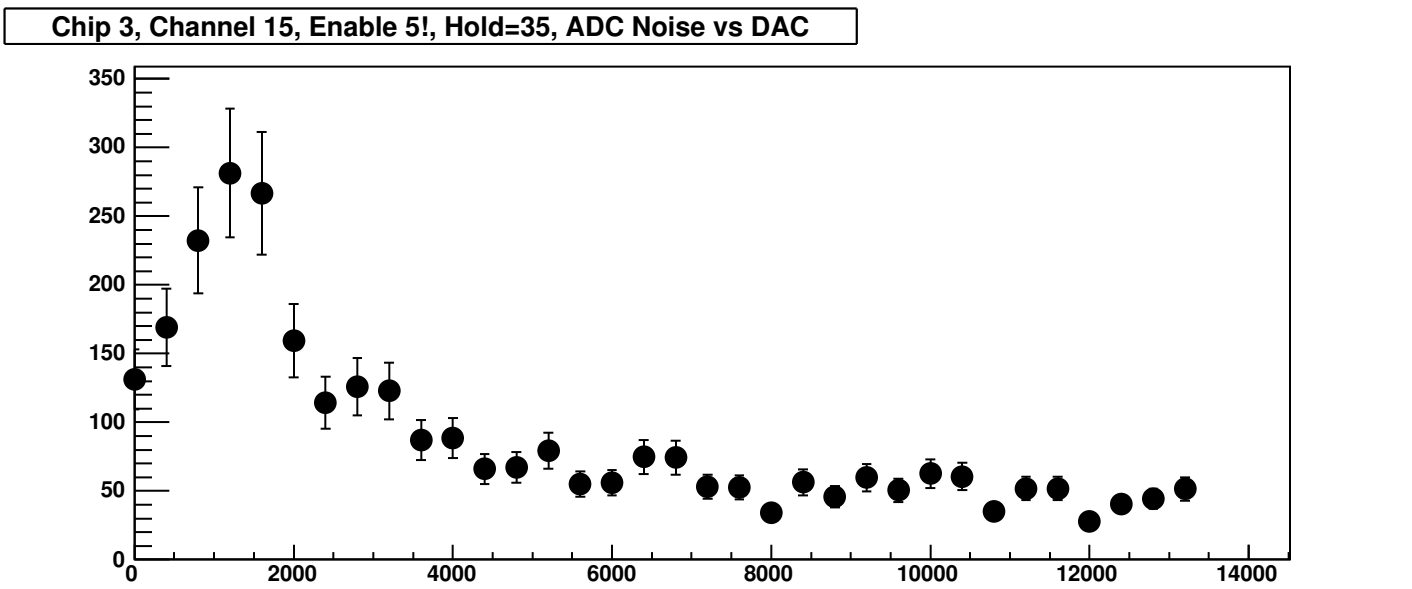

Chip 3, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

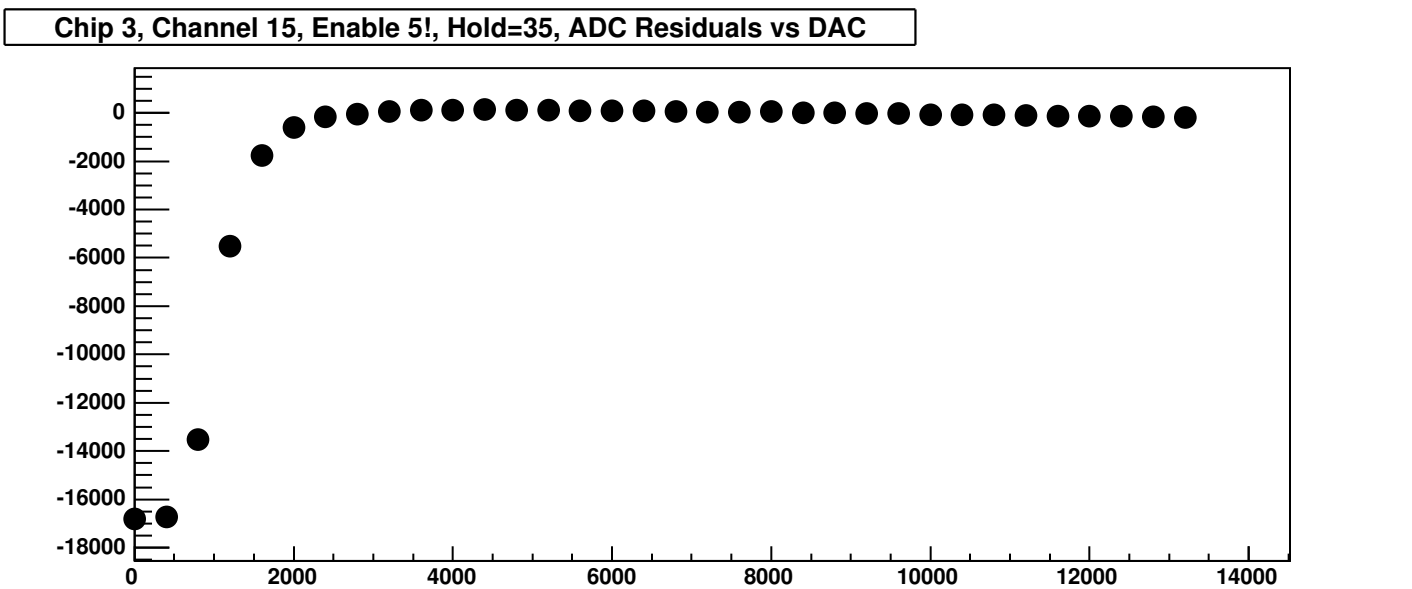

Chip 3, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 39.6 / 23
p0 -613.5 ± 0.814
p1 0.01421 ± 0.0001183
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

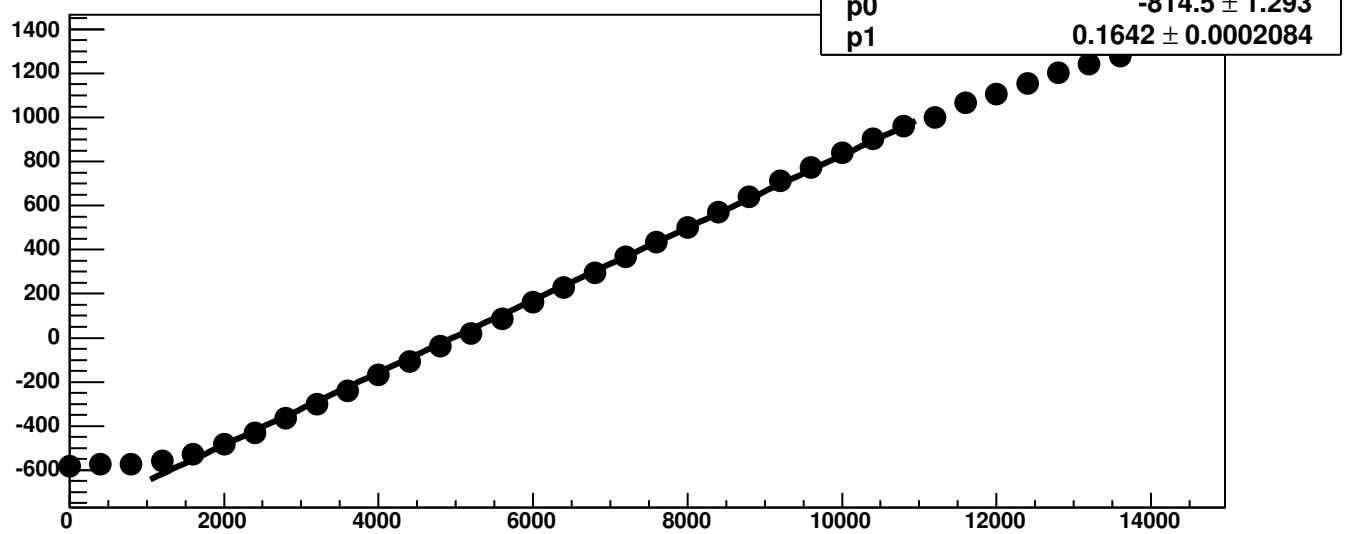

Chip 3, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ^2 / ndf 9093 / 23
p0 2.988e+04 ± 10.05
p1 0.03149 ± 0.001254
30000
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
2000
4000
6000
8000
10000
12000
14000

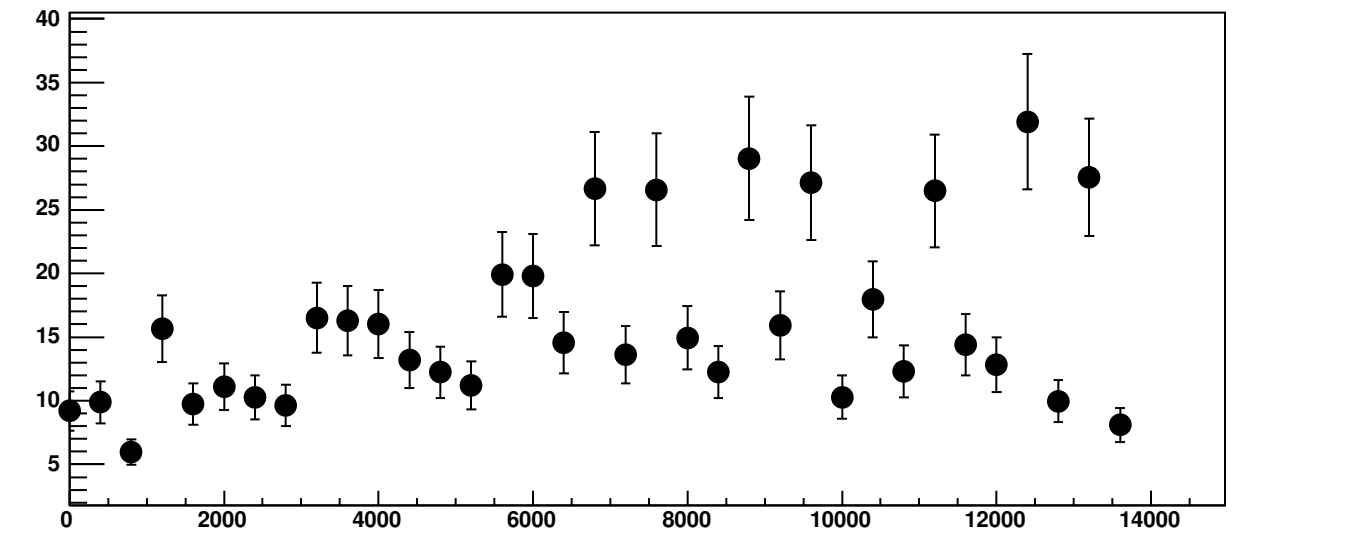

Chip 3, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 3, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
-18000
0
2000
4000
6000
8000
10000
12000
14000

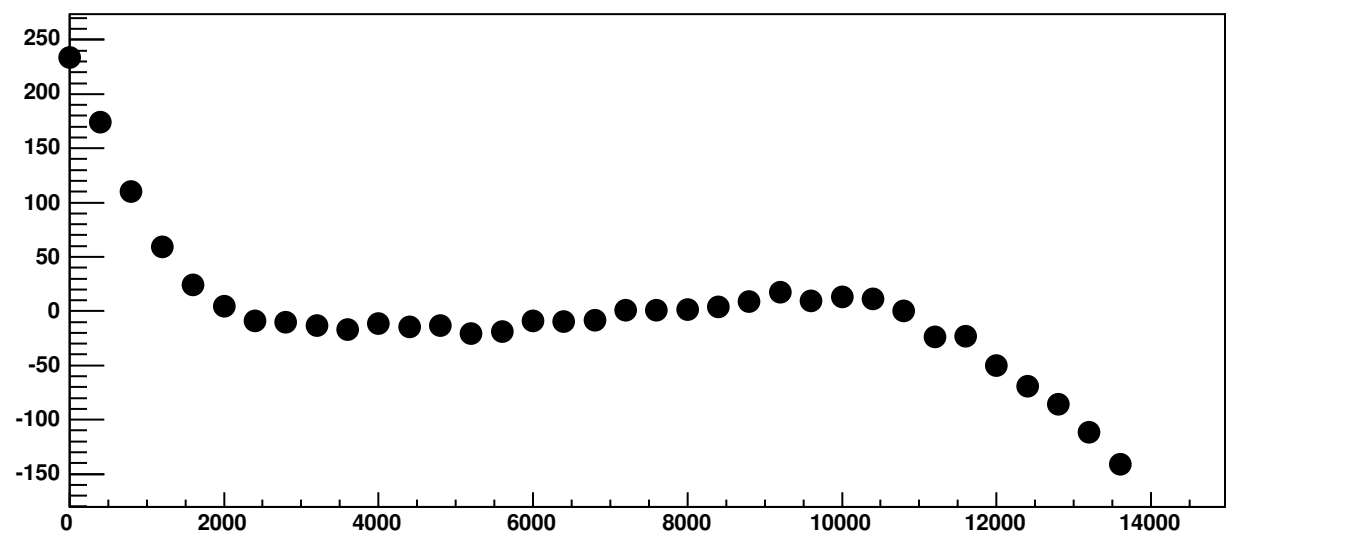
Chip 3, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



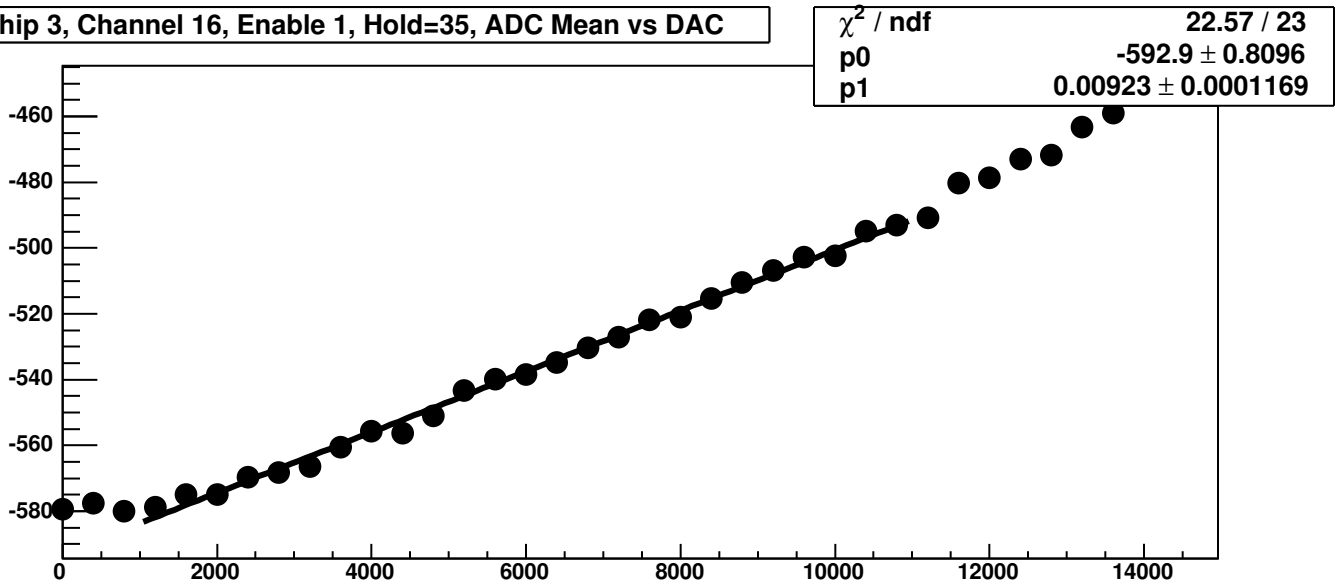
Chip 3, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



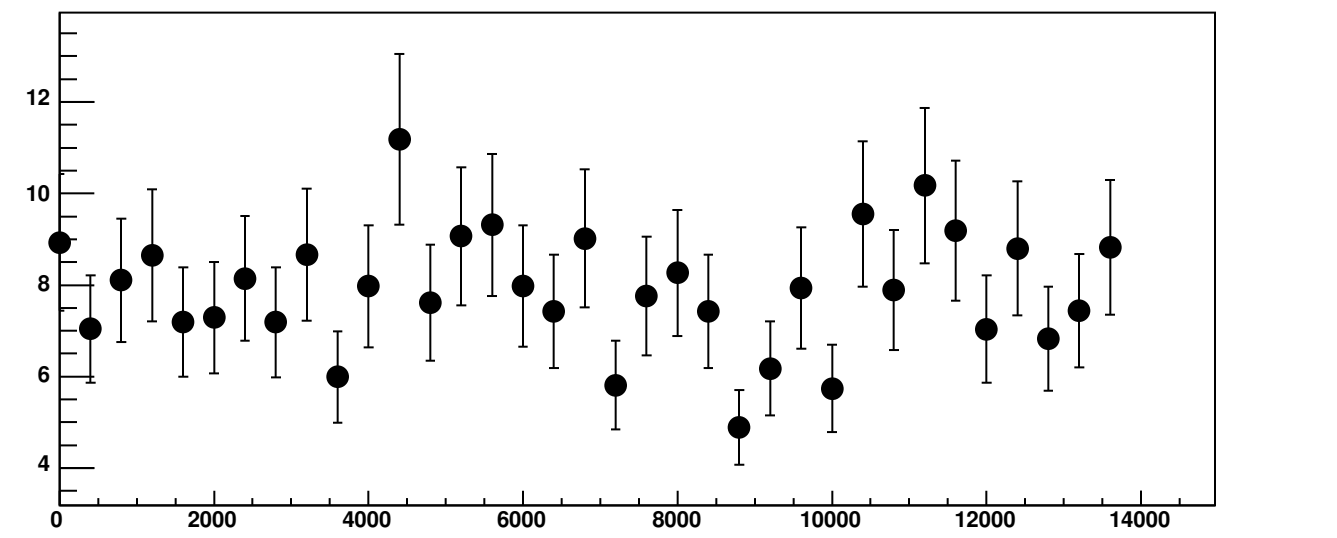
Chip 3, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



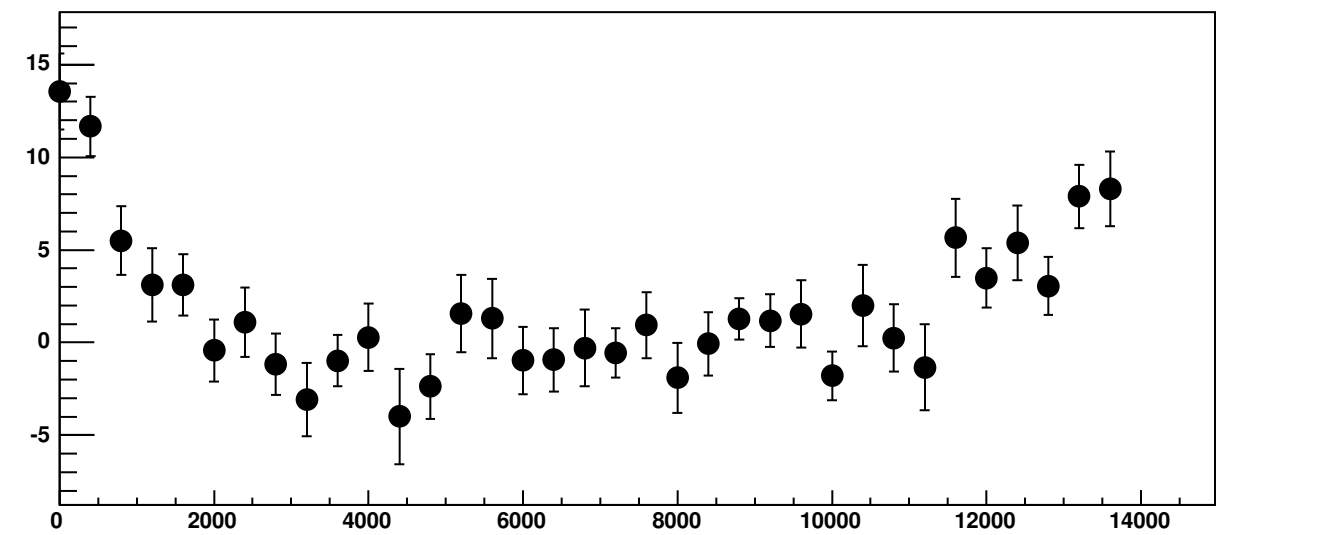
Chip 3, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

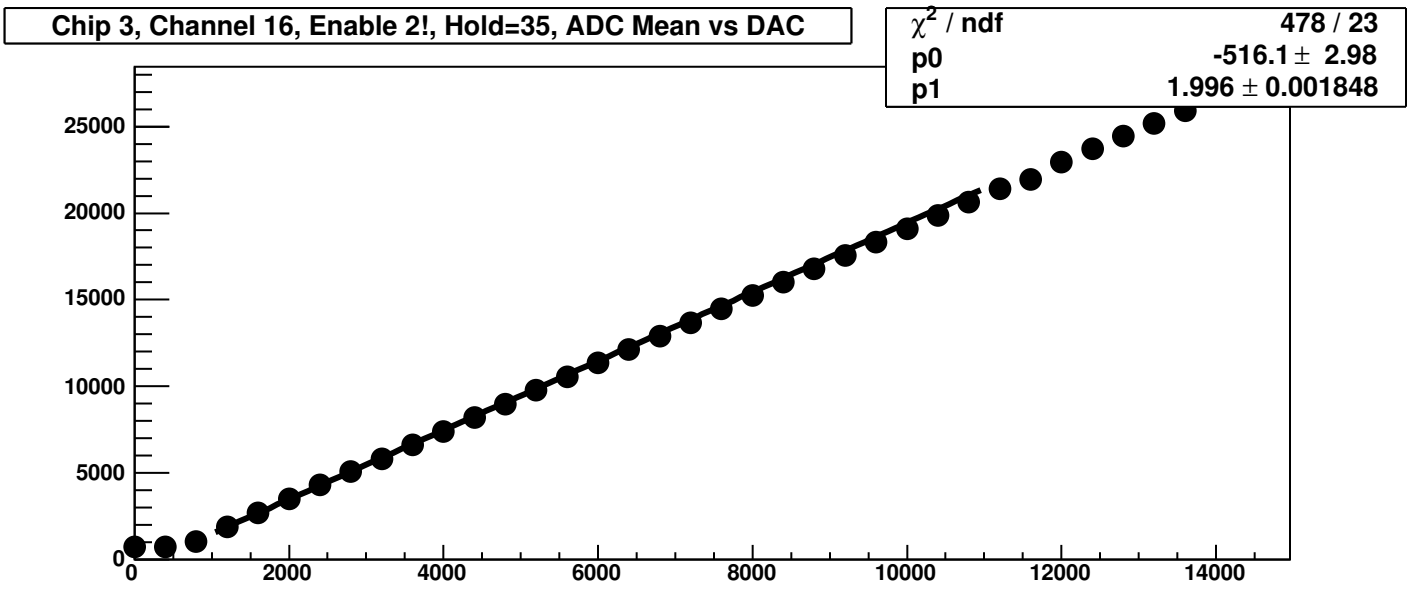


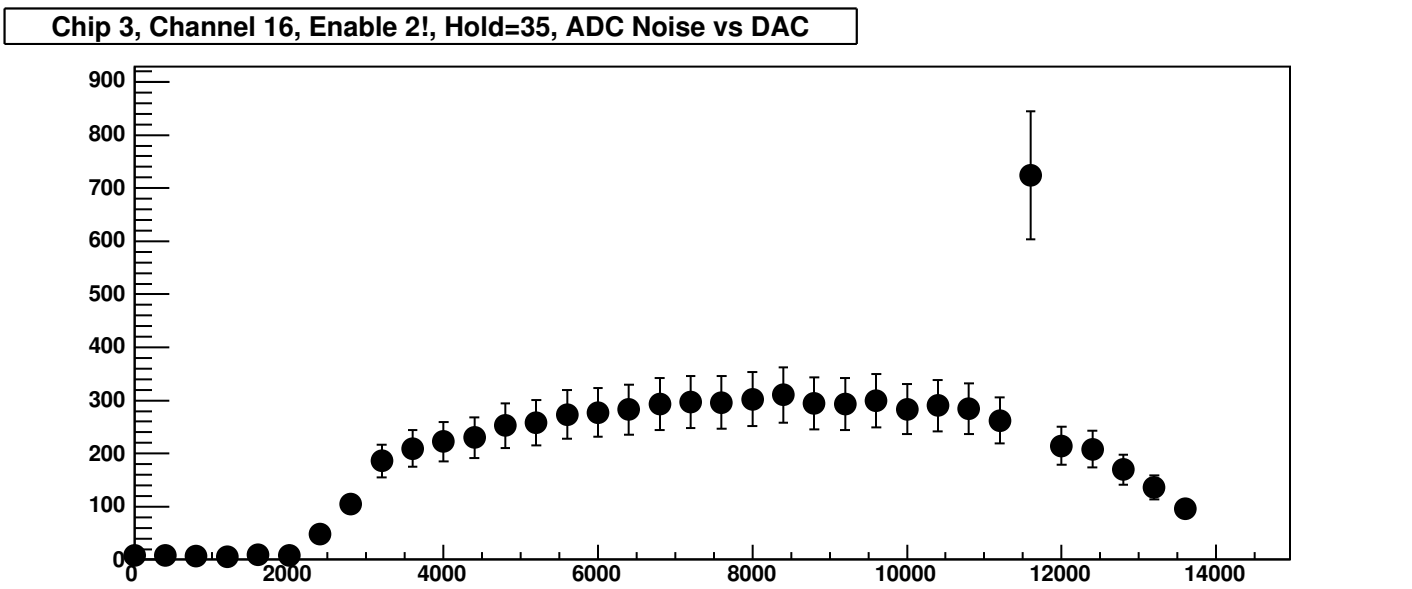
Chip 3, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC

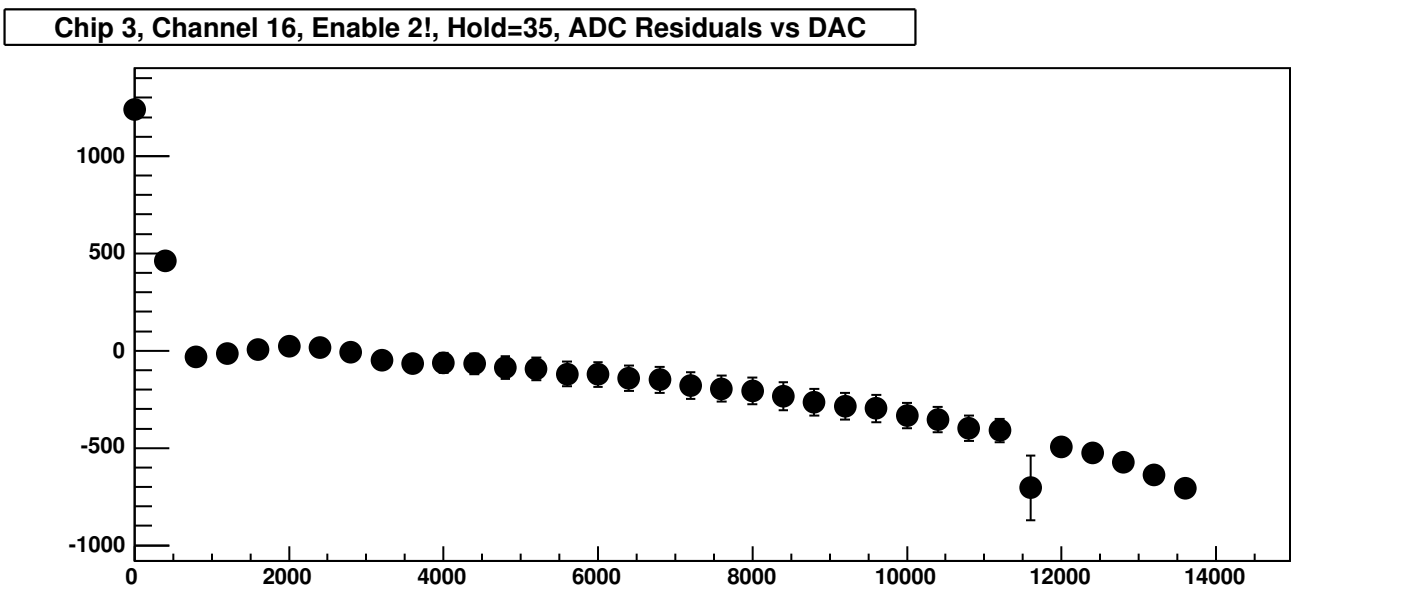


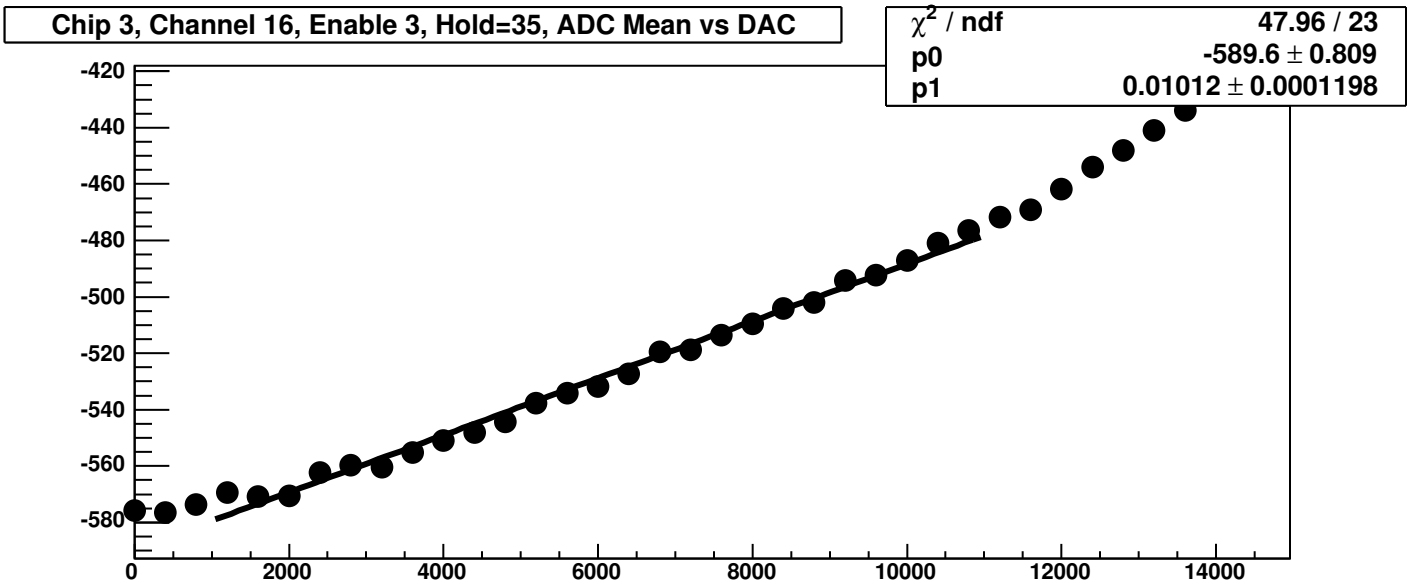
Chip 3, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

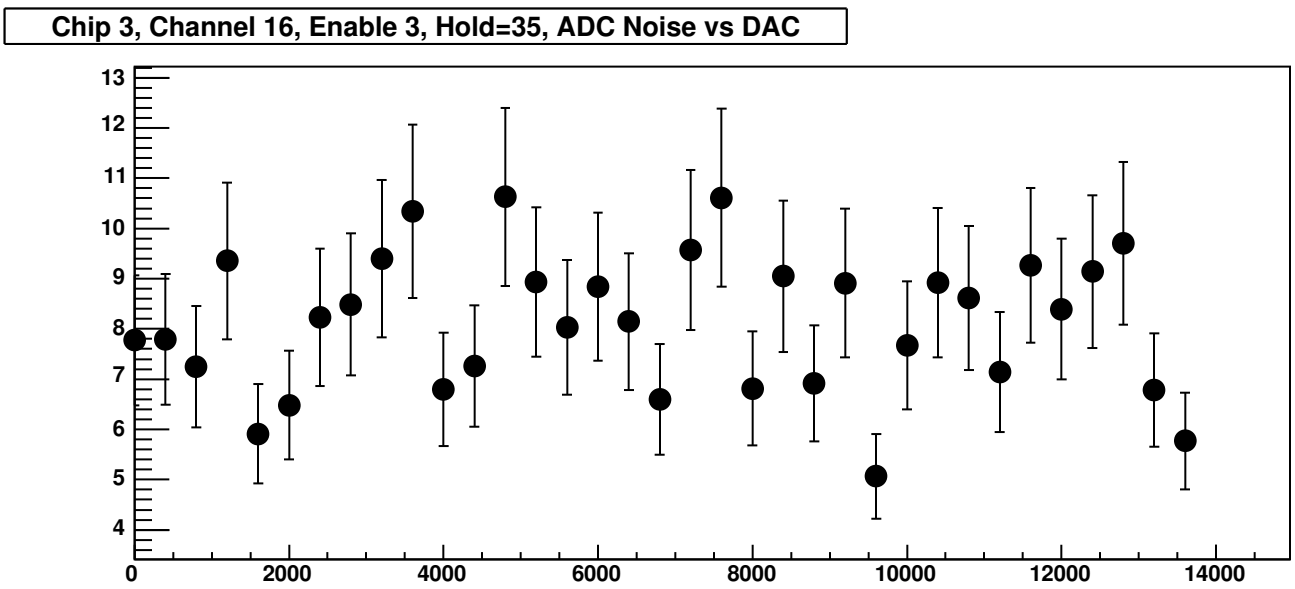


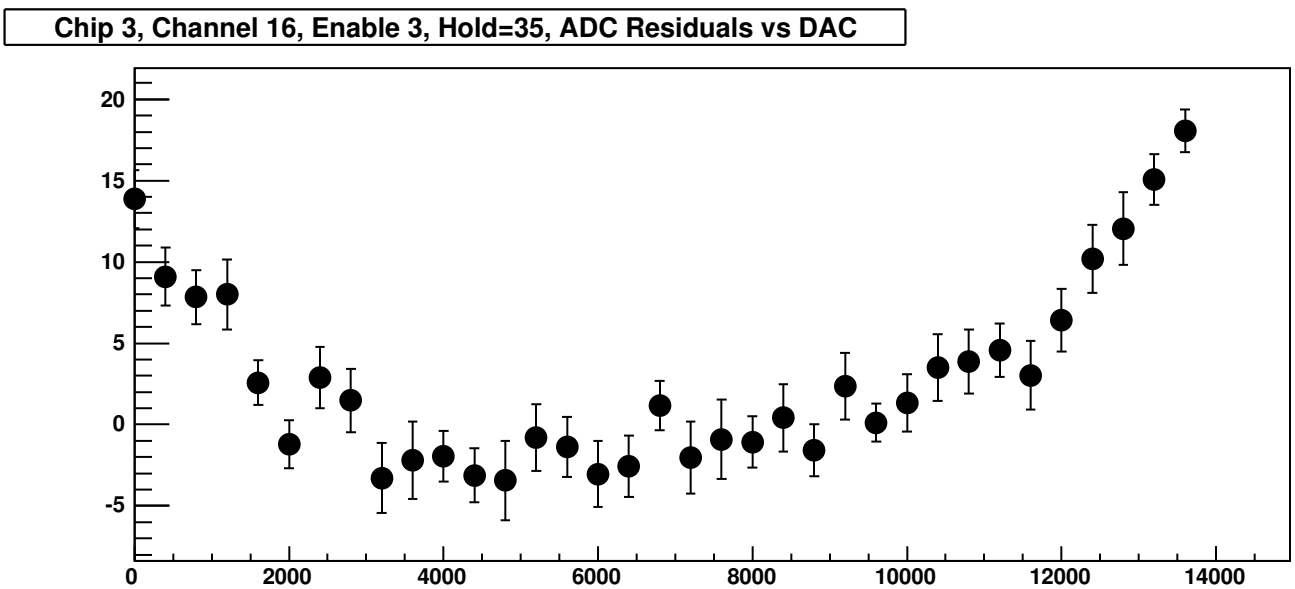

Chip 3, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 478 / 23
p0 -516.1 ± 2.98
p1 1.996 ± 0.001848
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

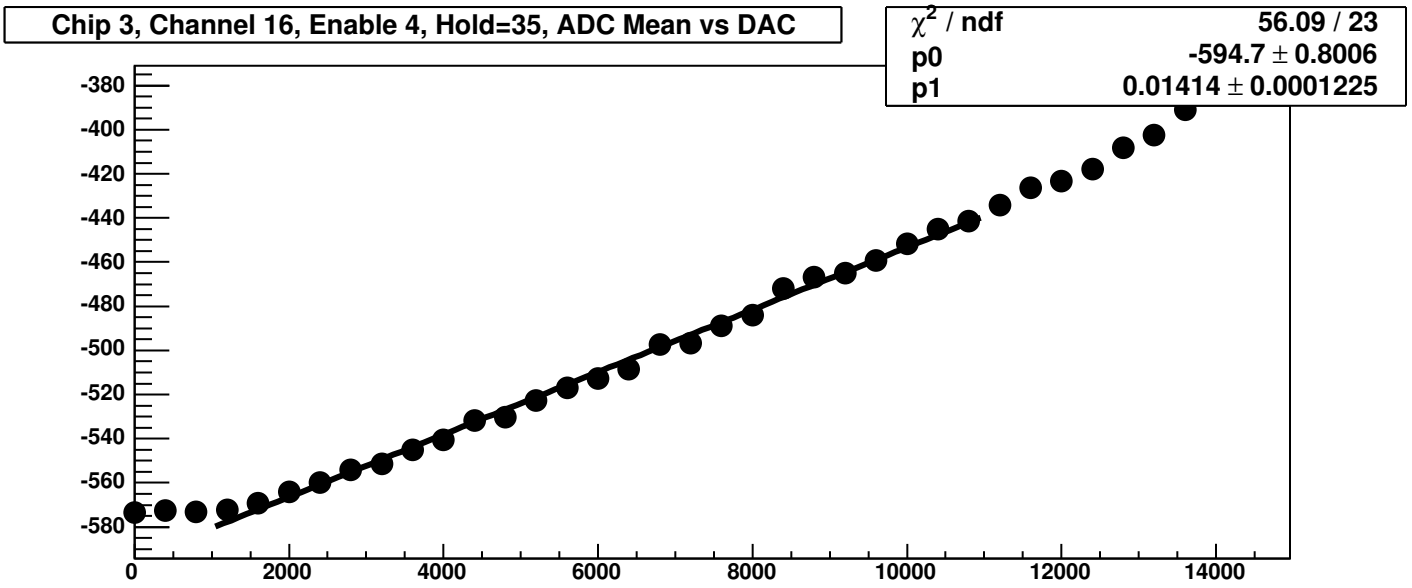

Chip 3, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
900
800
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

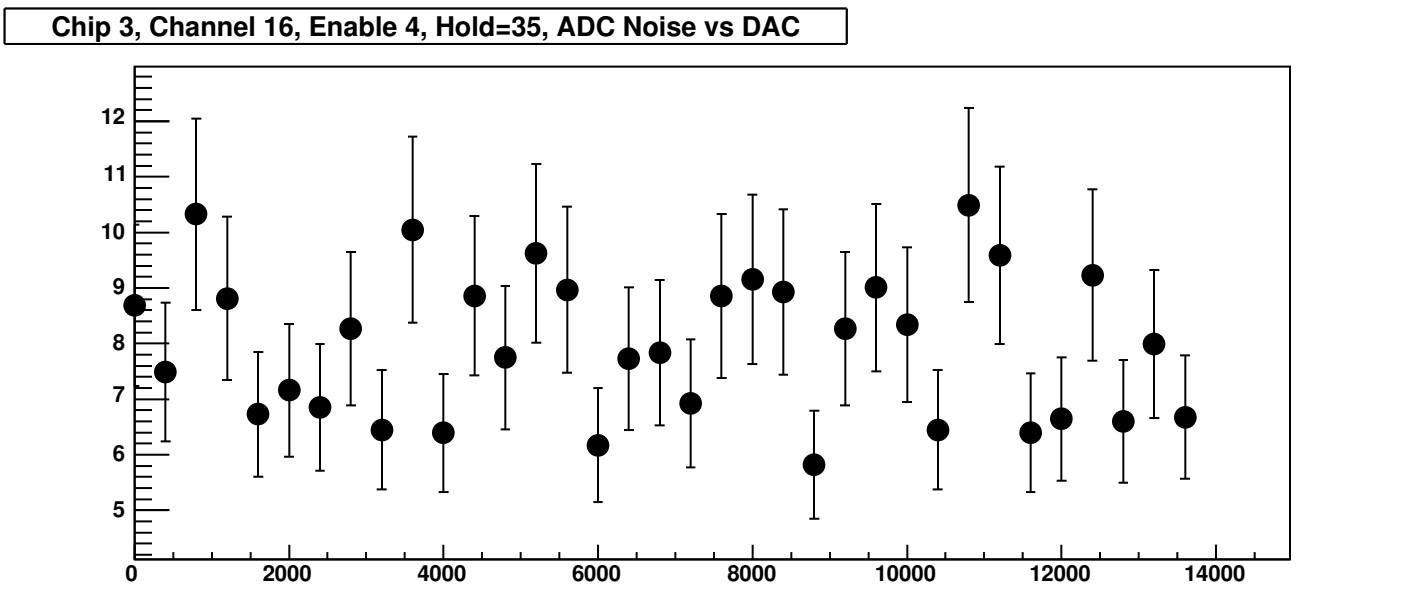

Chip 3, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

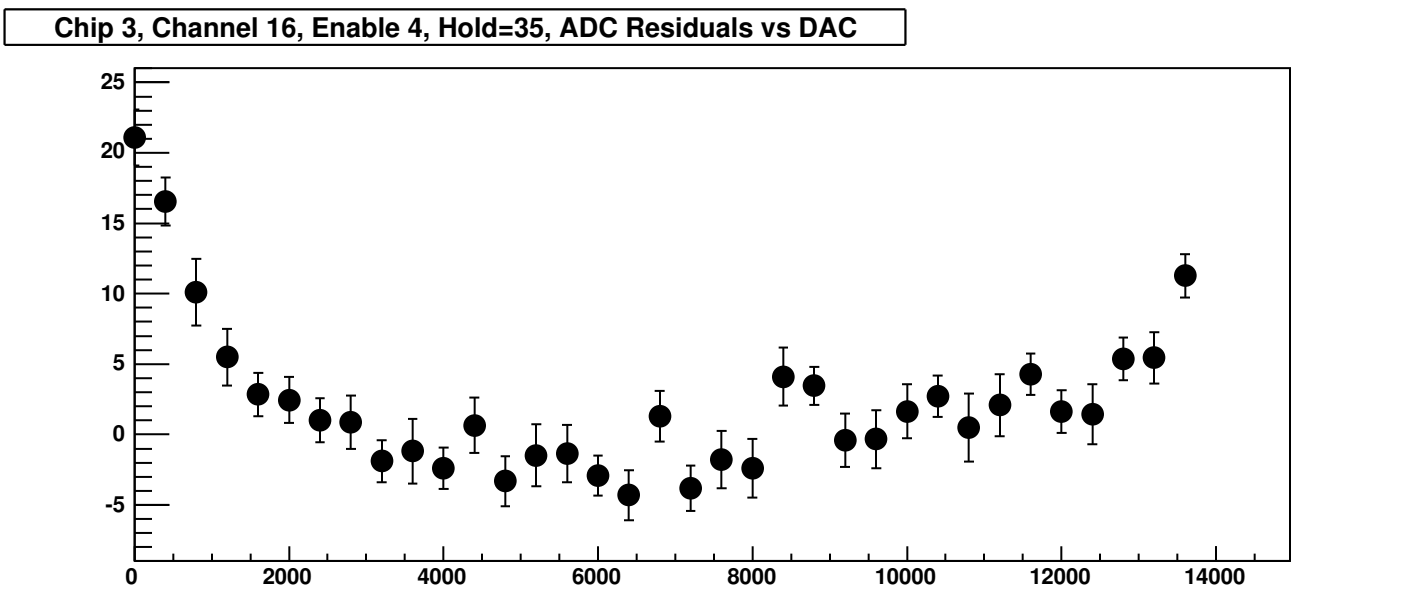

Chip 3, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 47.96 / 23
p0 -589.6 ± 0.809
p1 0.01012 ± 0.0001198
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000

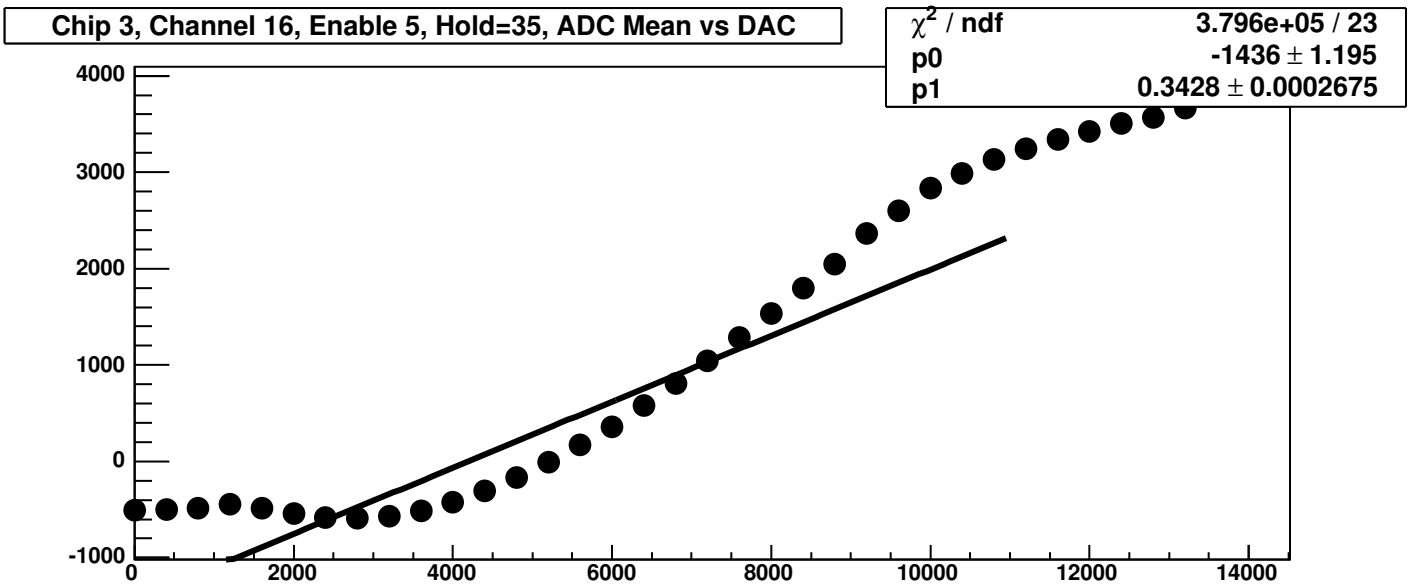

Chip 3, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

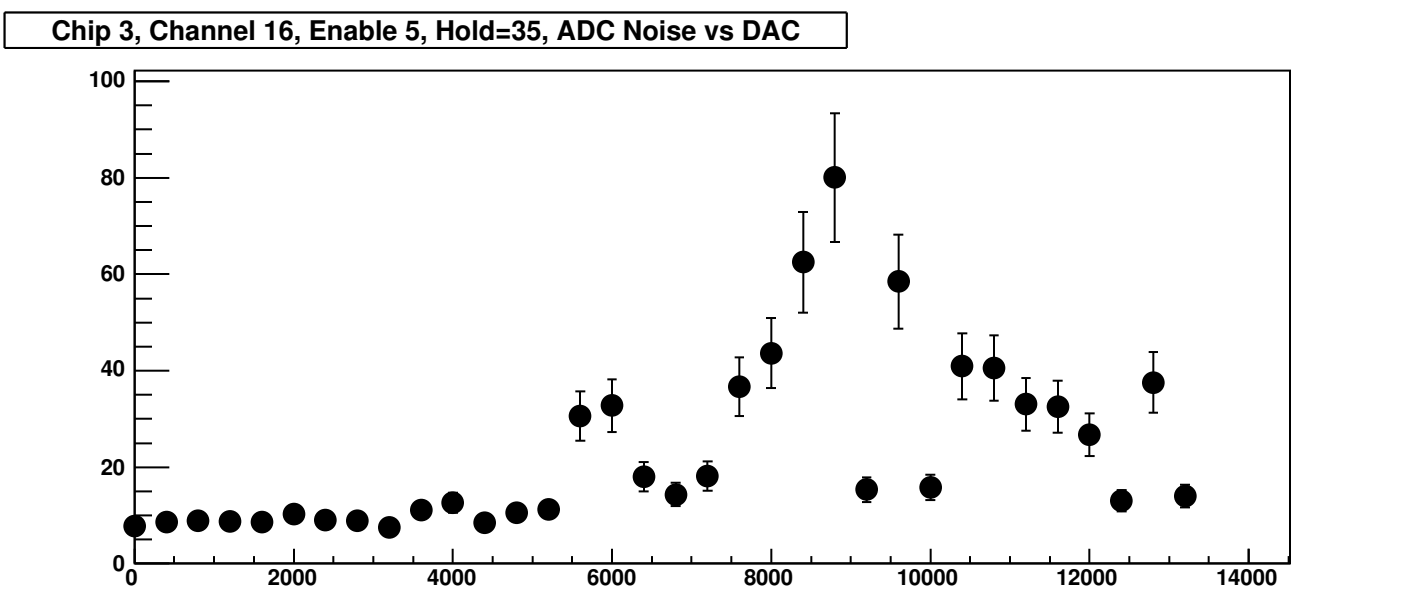

Chip 3, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

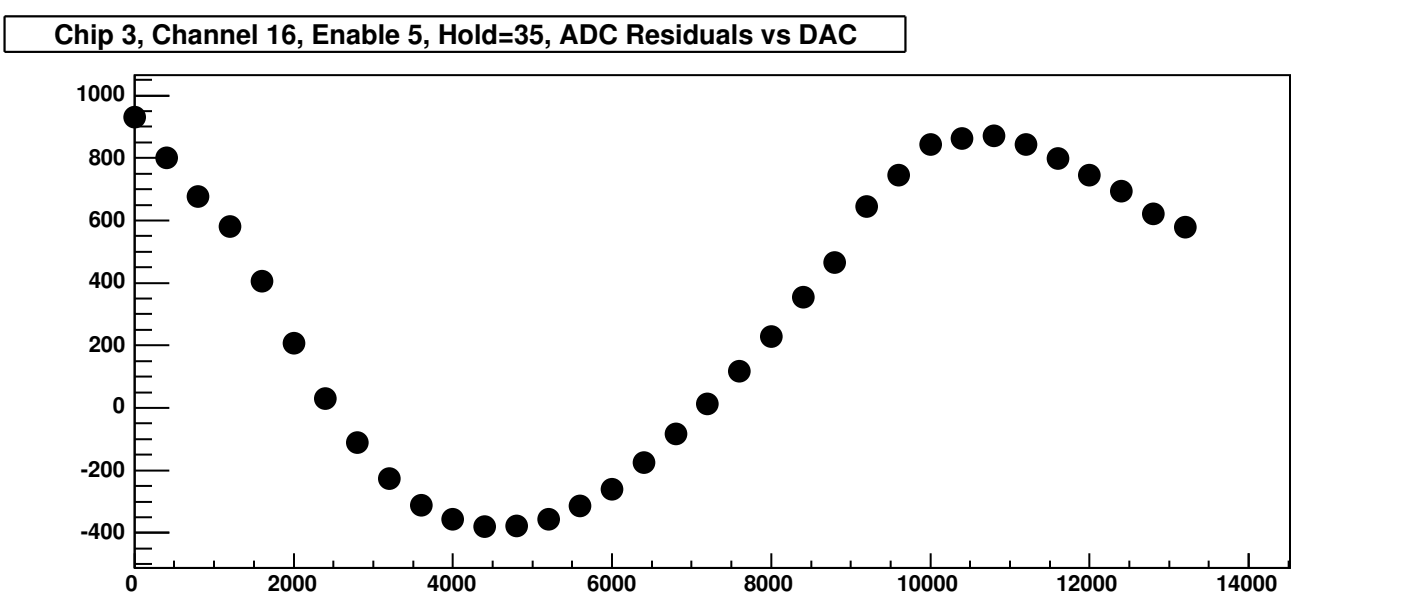

Chip 3, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 56.09 / 23
p0 -594.7 ± 0.8006
p1 0.01414 ± 0.0001225
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
2000
4000
6000
8000
10000
12000
14000

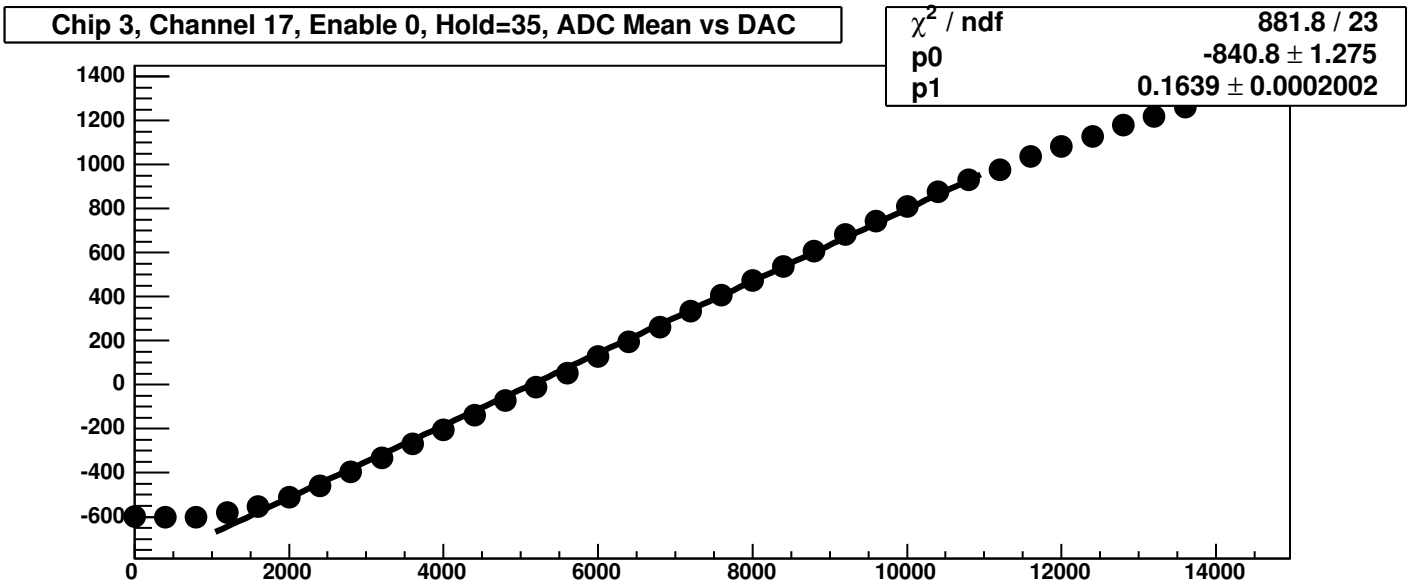

Chip 3, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

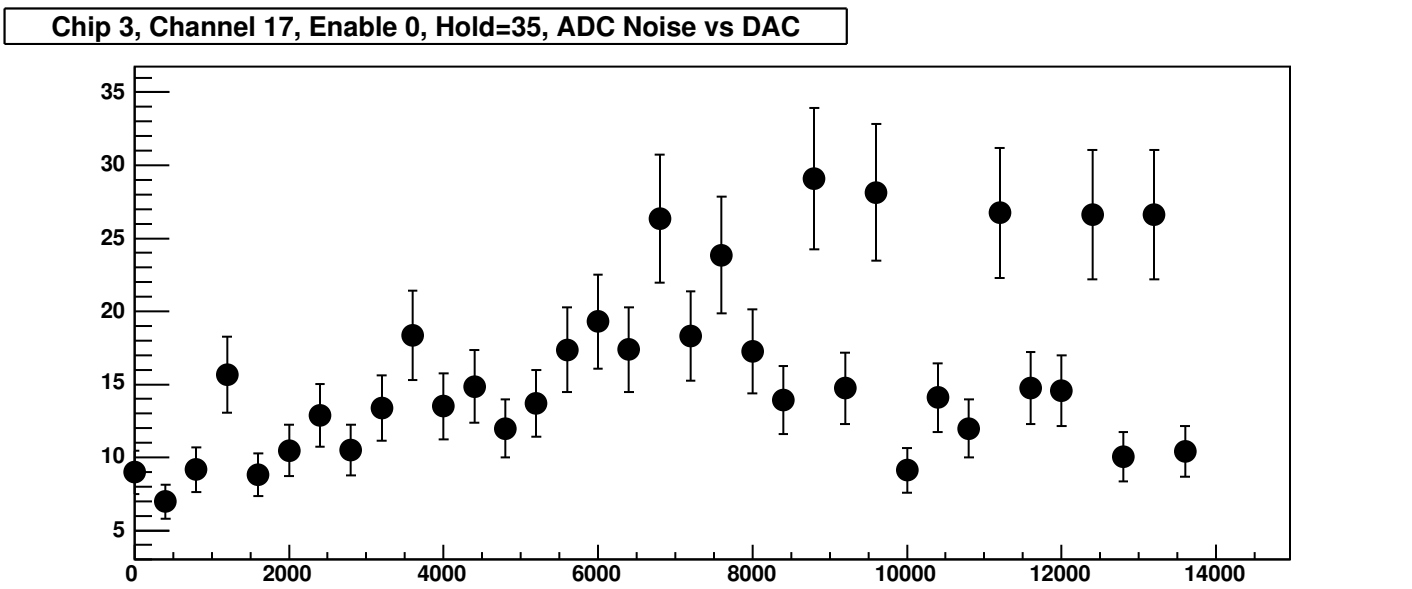

Chip 3, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

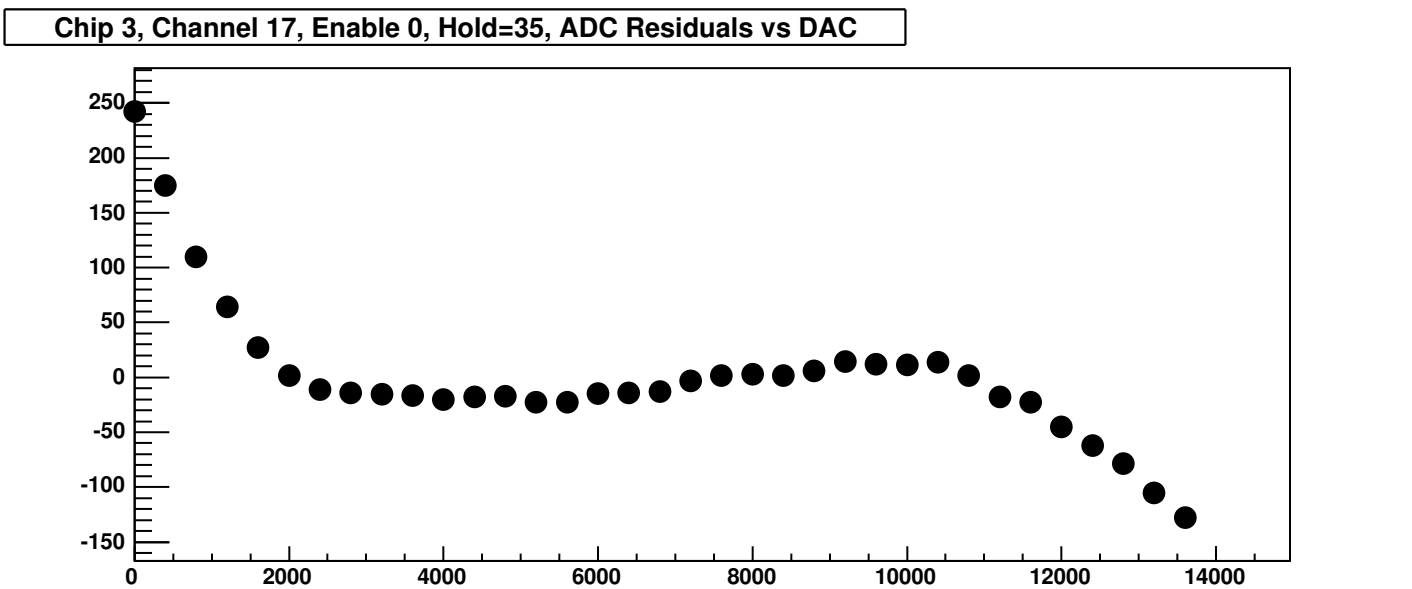

Chip 3, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.796e+05 / 23
p0 -1436 ± 1.195
p1 0.3428 ± 0.0002675
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

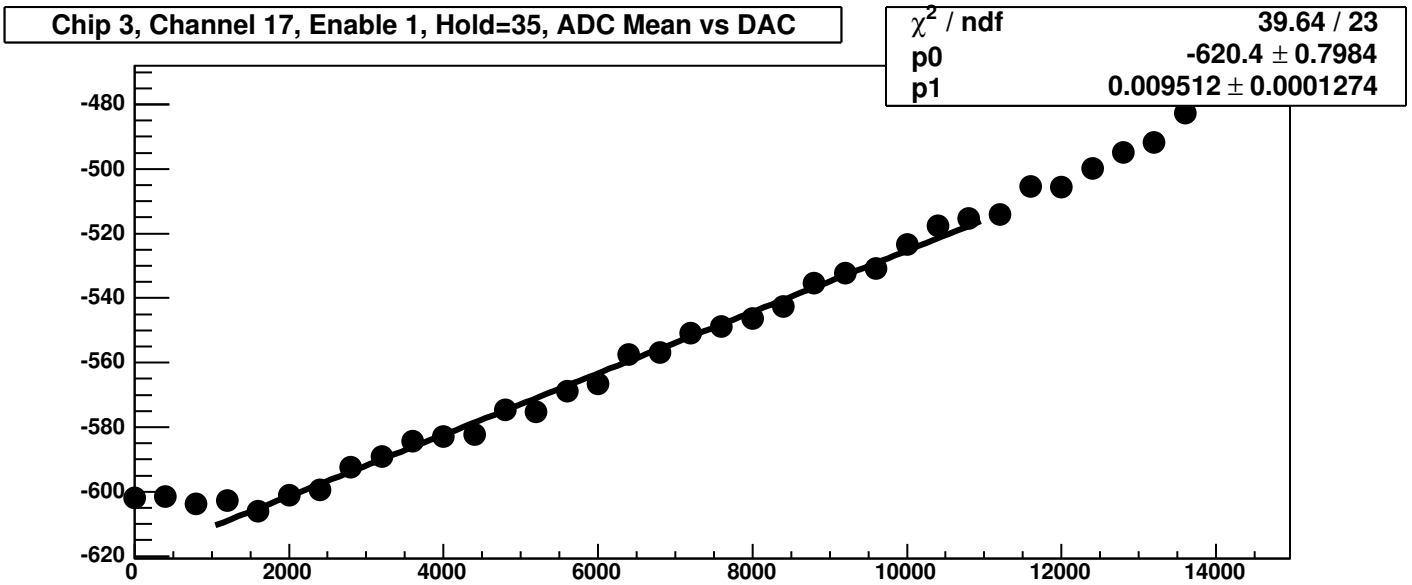

Chip 3, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

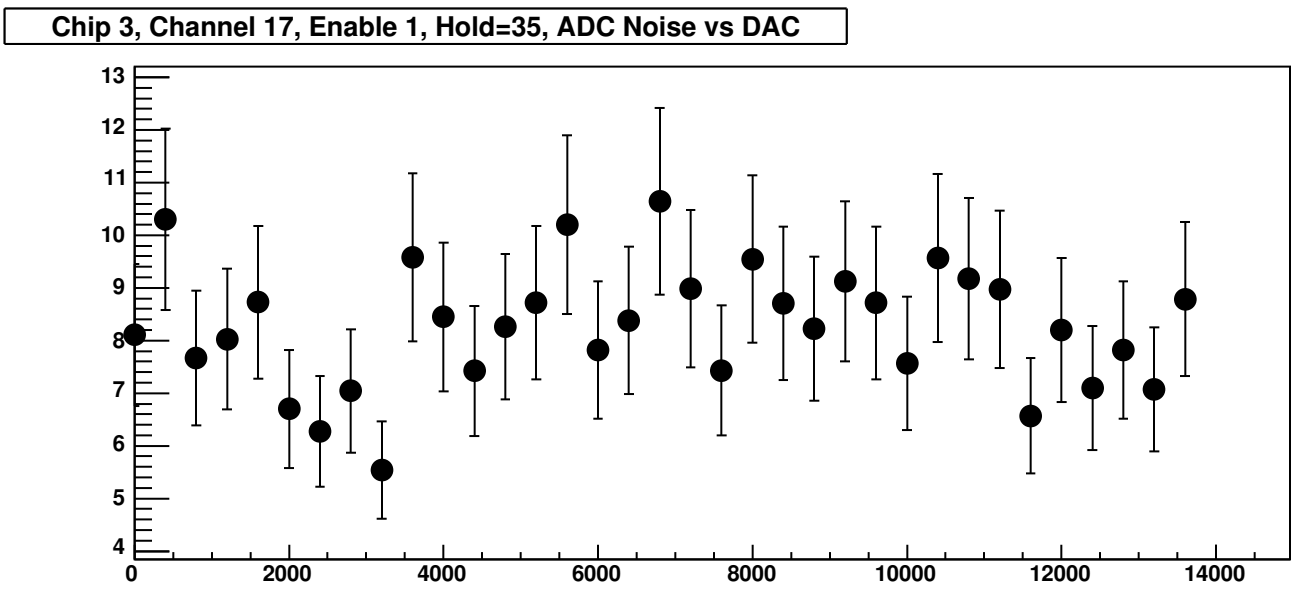

Chip 3, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

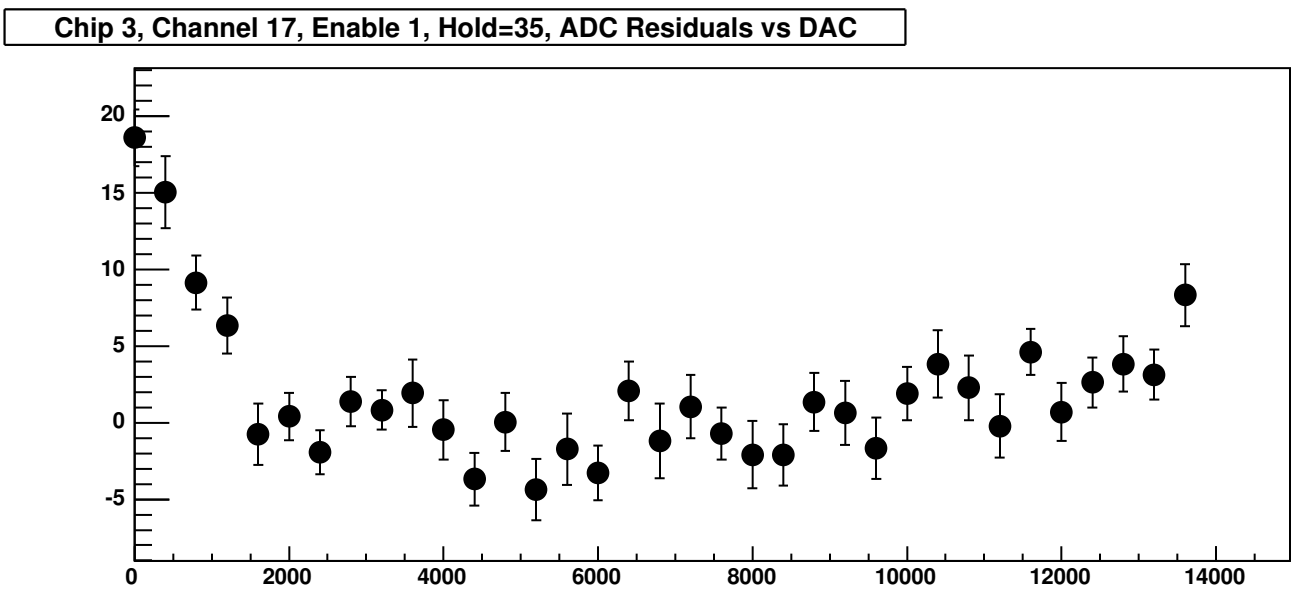

Chip 3, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 881.8 / 23
p0 -840.8 ± 1.275
p1 0.1639 ± 0.0002002
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

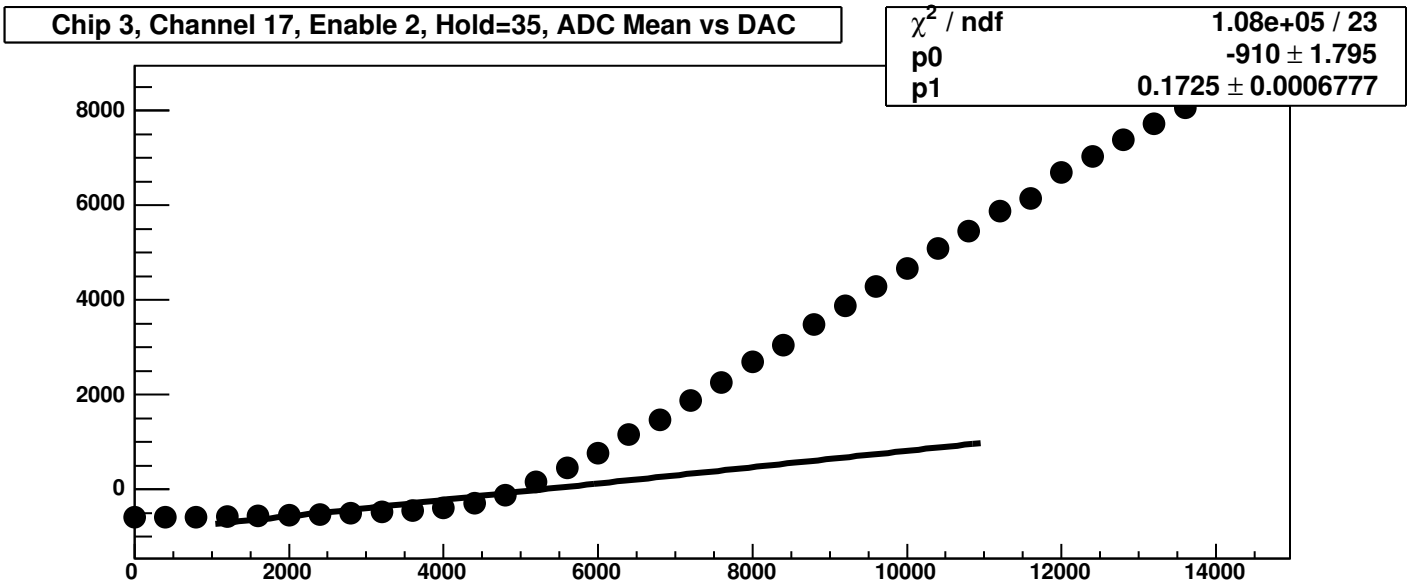

Chip 3, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

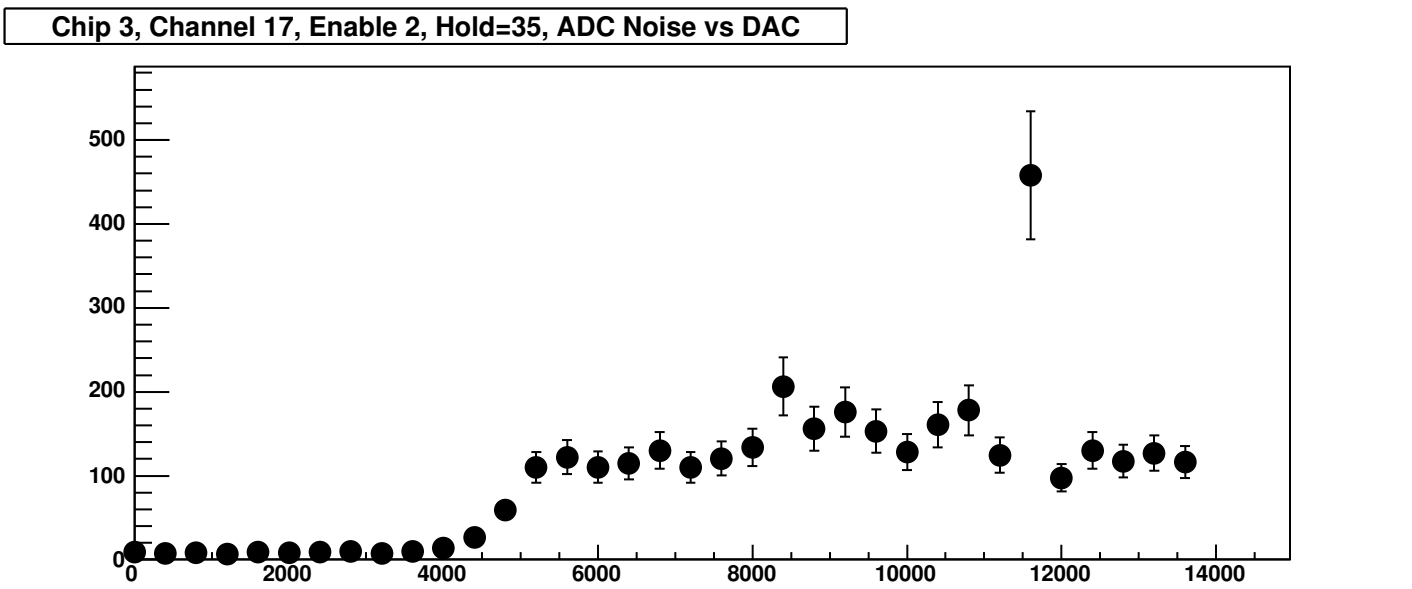

Chip 3, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

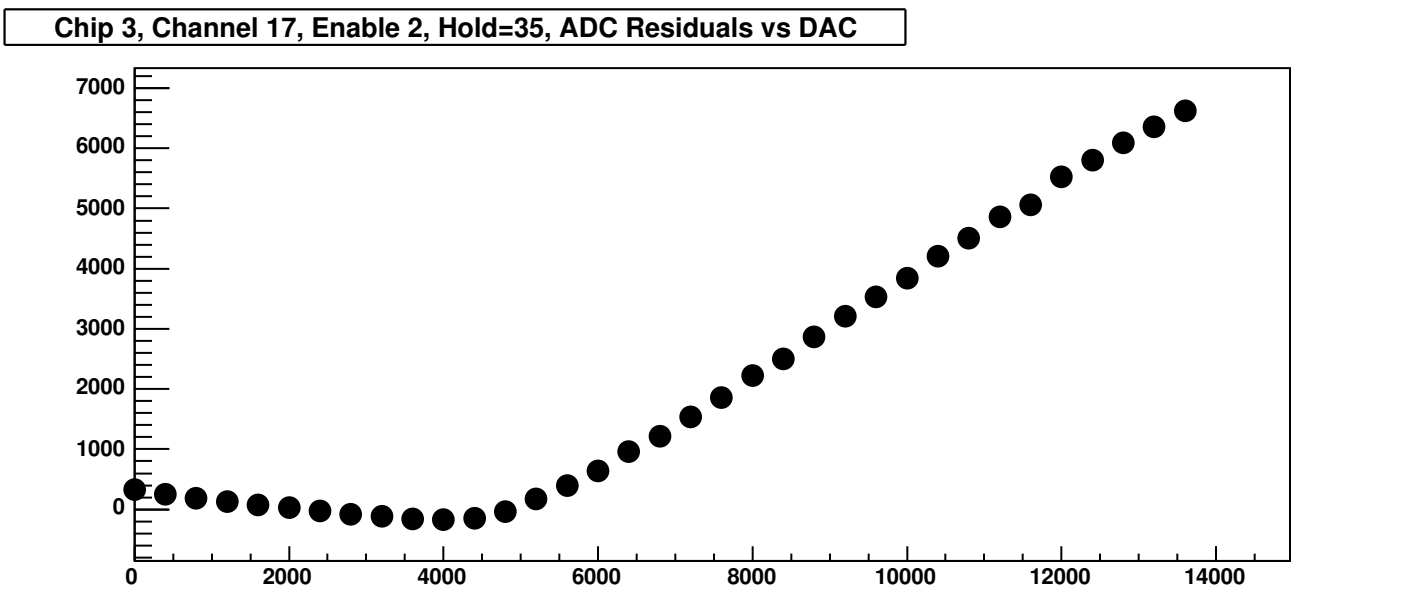

Chip 3, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 39.64 / 23
p0 -620.4 ± 0.7984
p1 0.009512 ± 0.0001274
-480
-500
-520
-540
-560
-580
-600
-620
0
2000
4000
6000
8000
10000
12000
14000

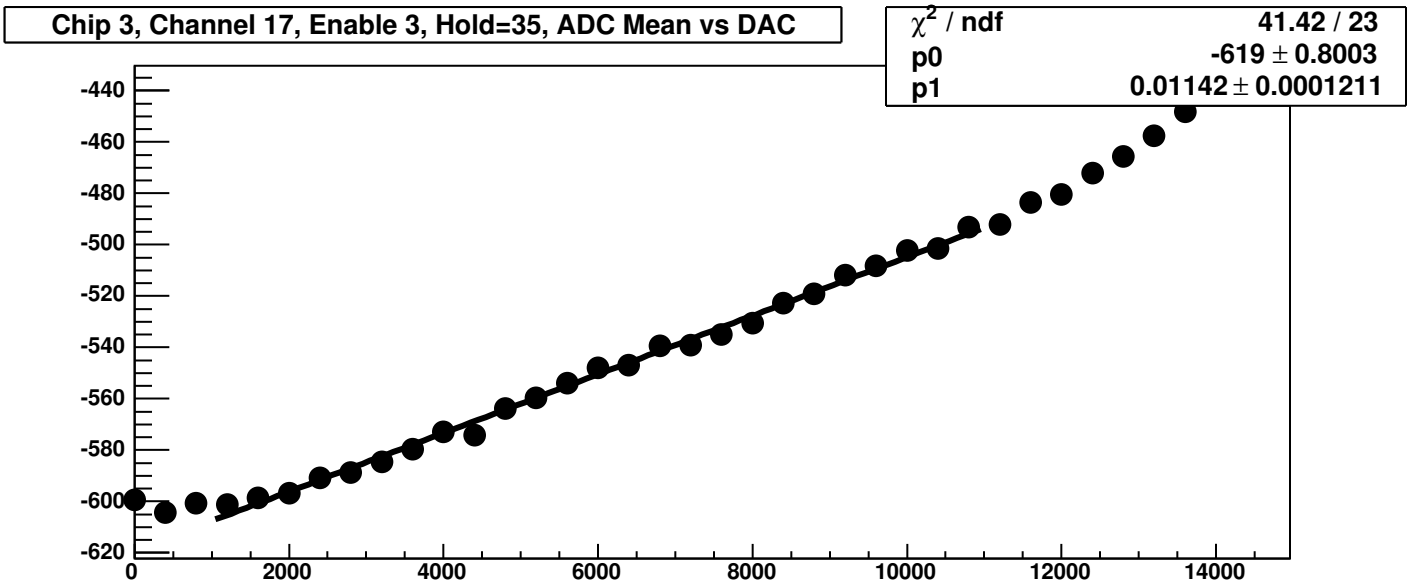

Chip 3, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

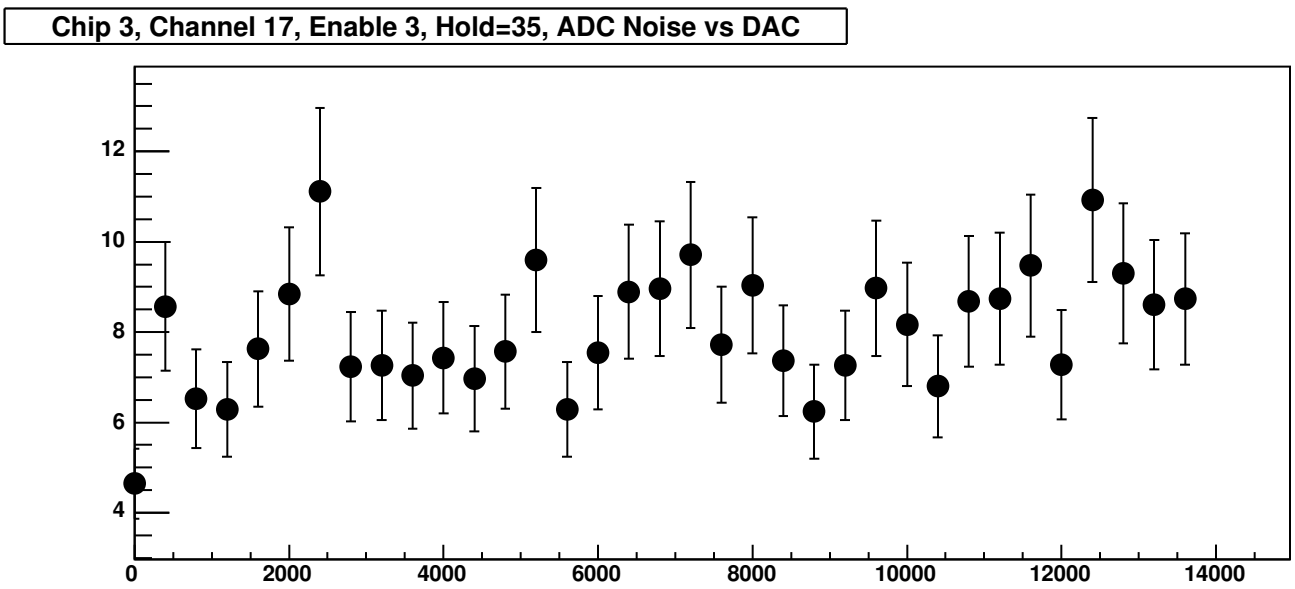

Chip 3, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

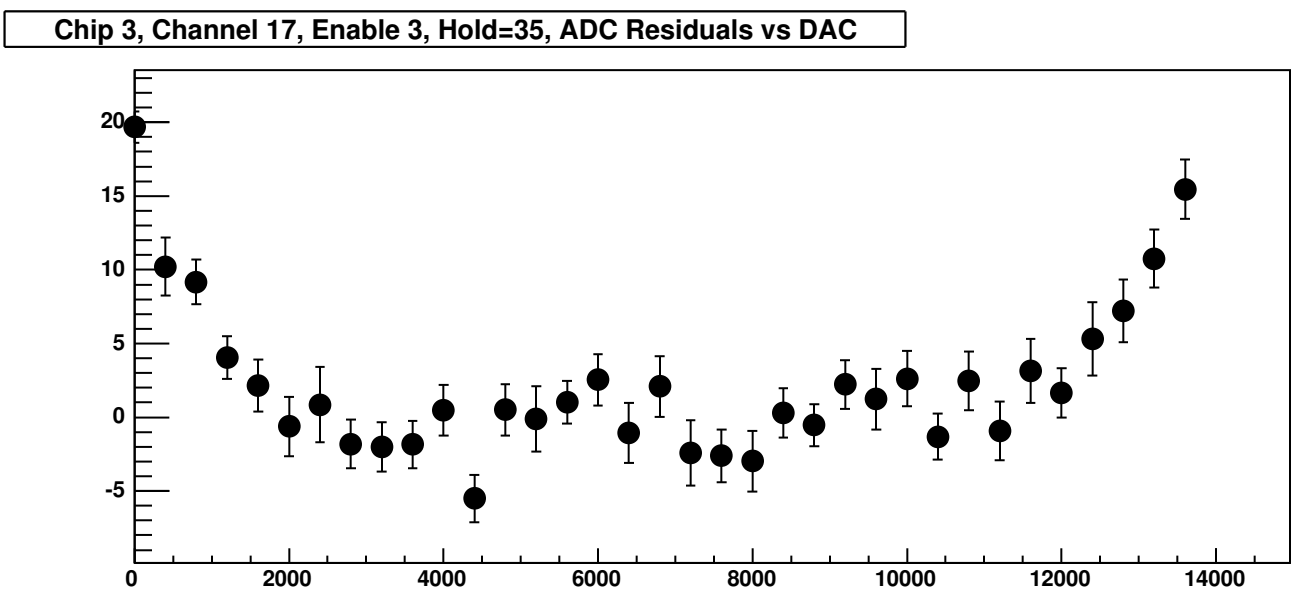

Chip 3, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1.08e+05 / 23
p0 -910 ± 1.795
p1 0.1725 ± 0.0006777
8000
6000
4000
2000
0
0
2000
4000
6000
8000
10000
12000
14000

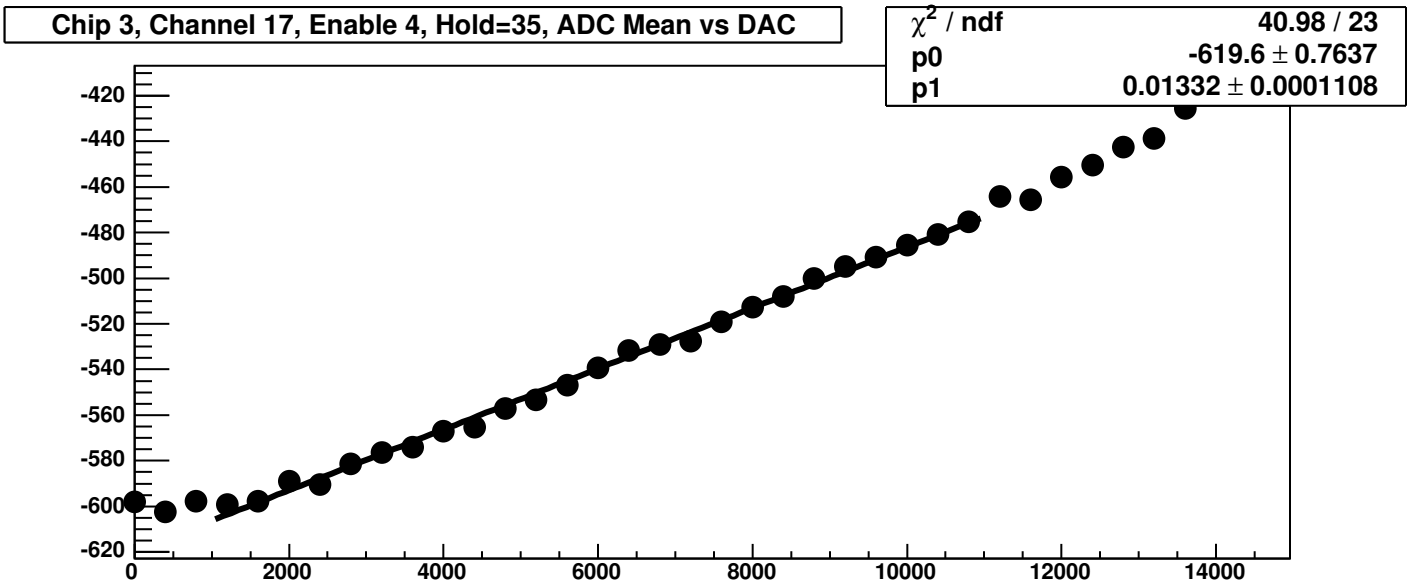

Chip 3, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

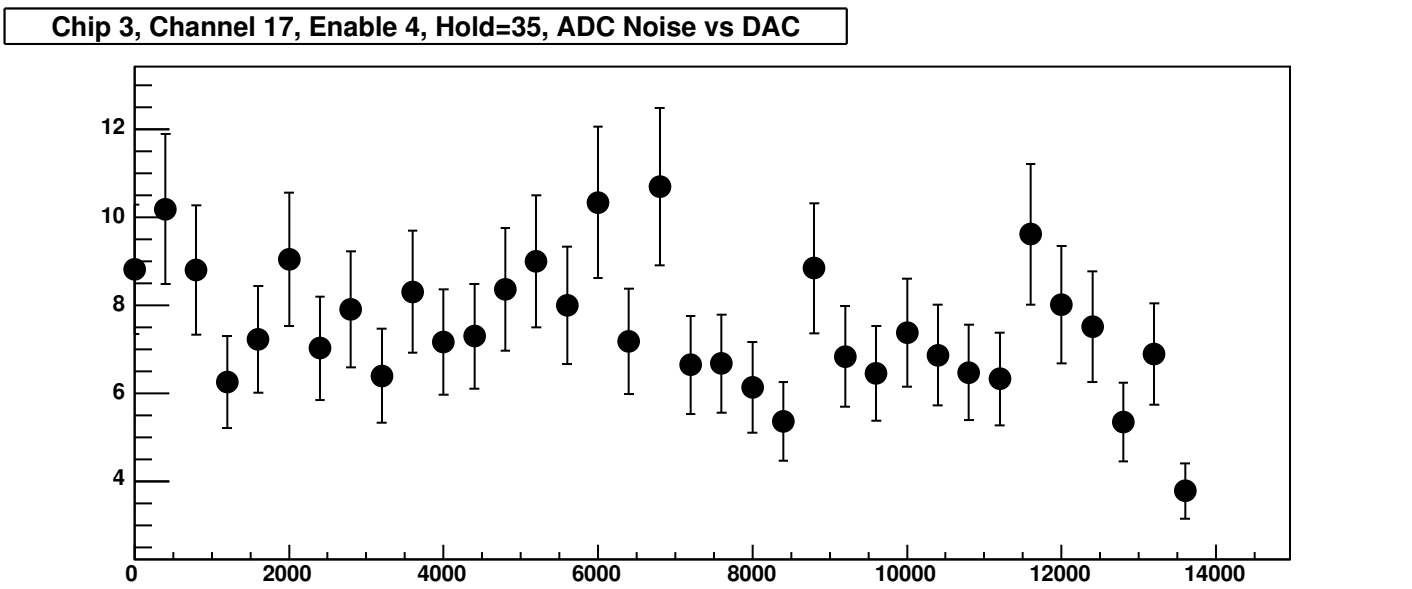

Chip 3, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
7000
6000
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

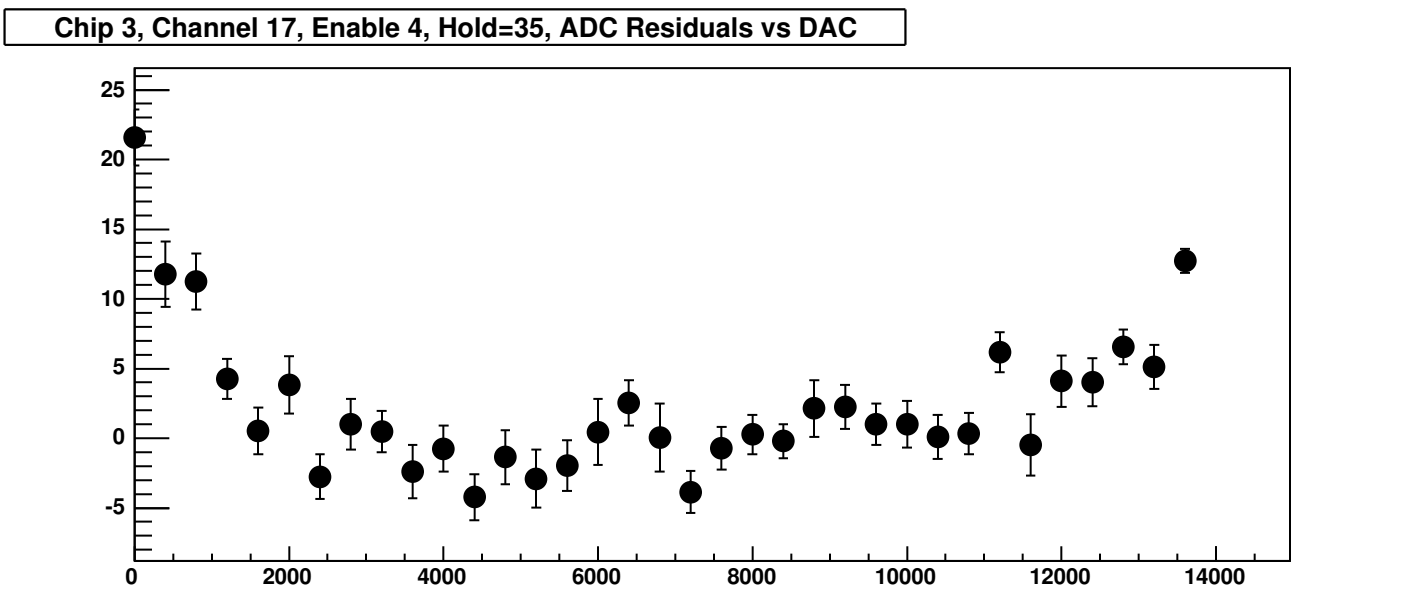

Chip 3, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 41.42 / 23
p0 -619 ± 0.8003
p1 0.01142 ± 0.0001211
-440
-460
-480
-500
-520
-540
-560
-580
-600
-620
0
2000
4000
6000
8000
10000
12000
14000

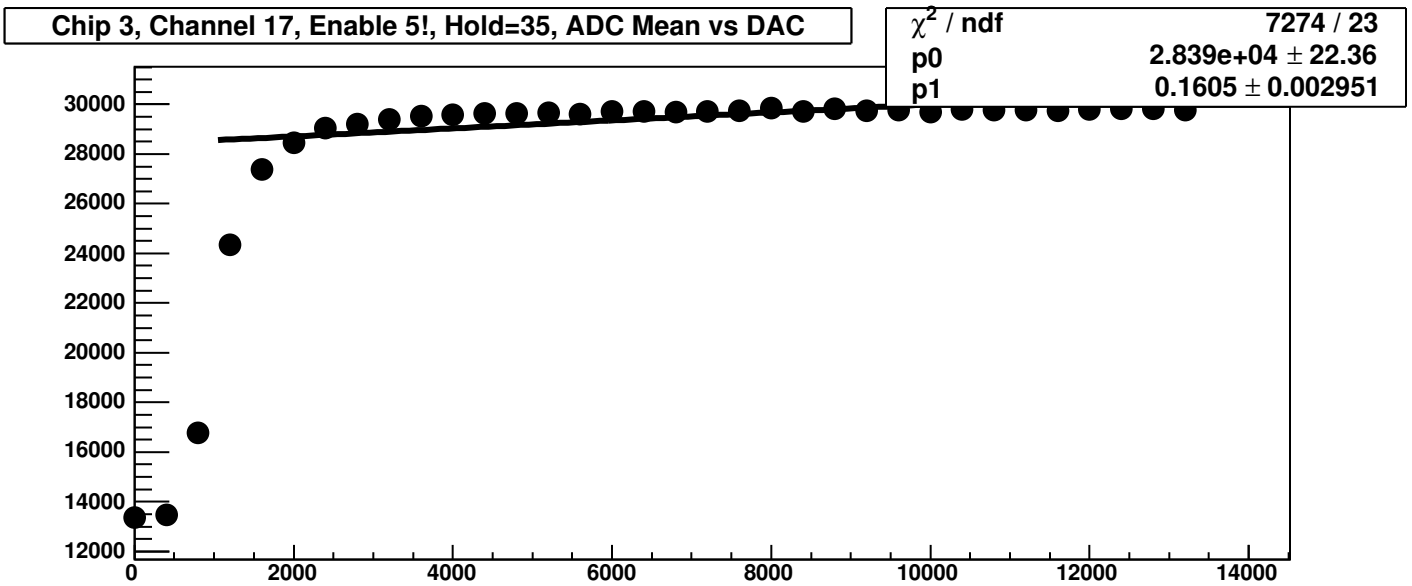

Chip 3, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

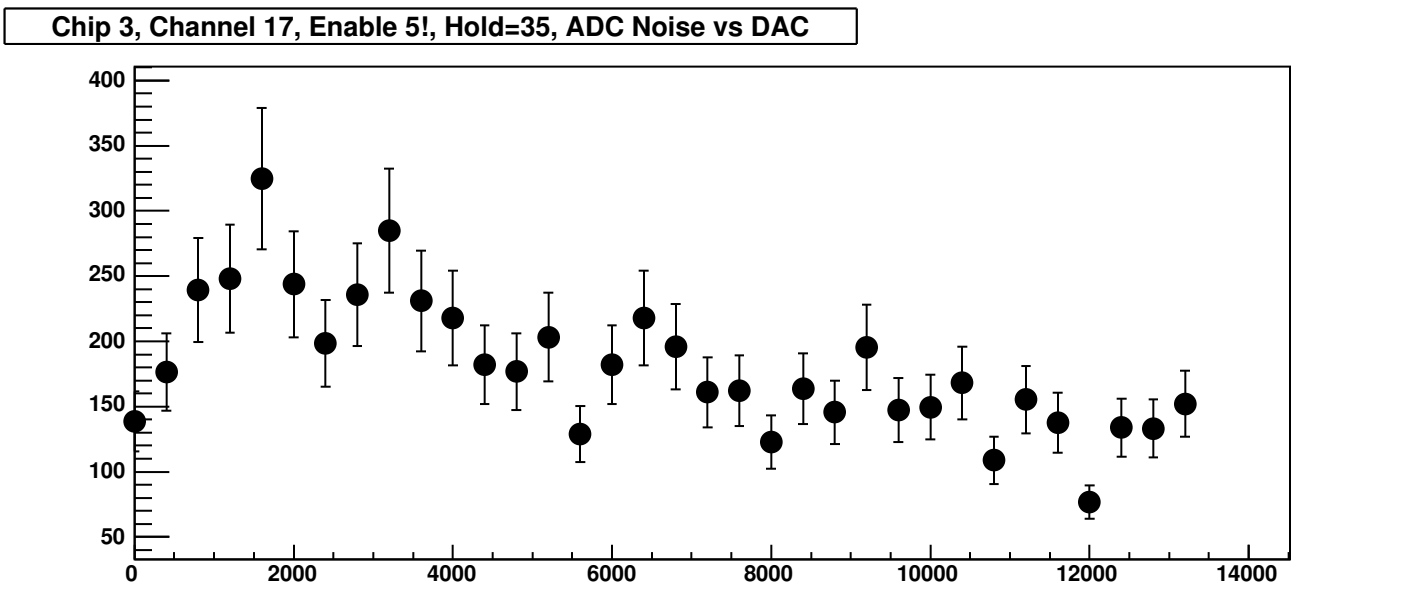

Chip 3, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

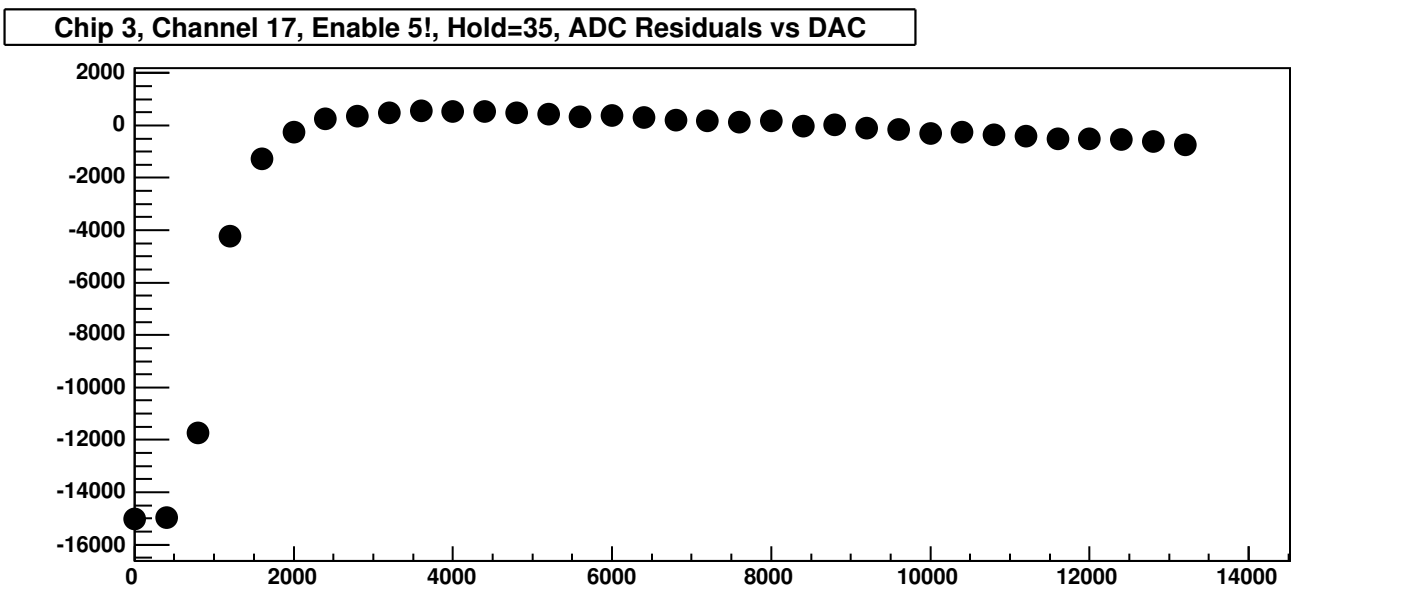

Chip 3, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 40.98 / 23
p0 -619.6 ± 0.7637
p1 0.01332 ± 0.0001108
-420
-440
-460
-480
-500
-520
-540
-560
-580
-600
-620
0
2000
4000
6000
8000
10000
12000
14000

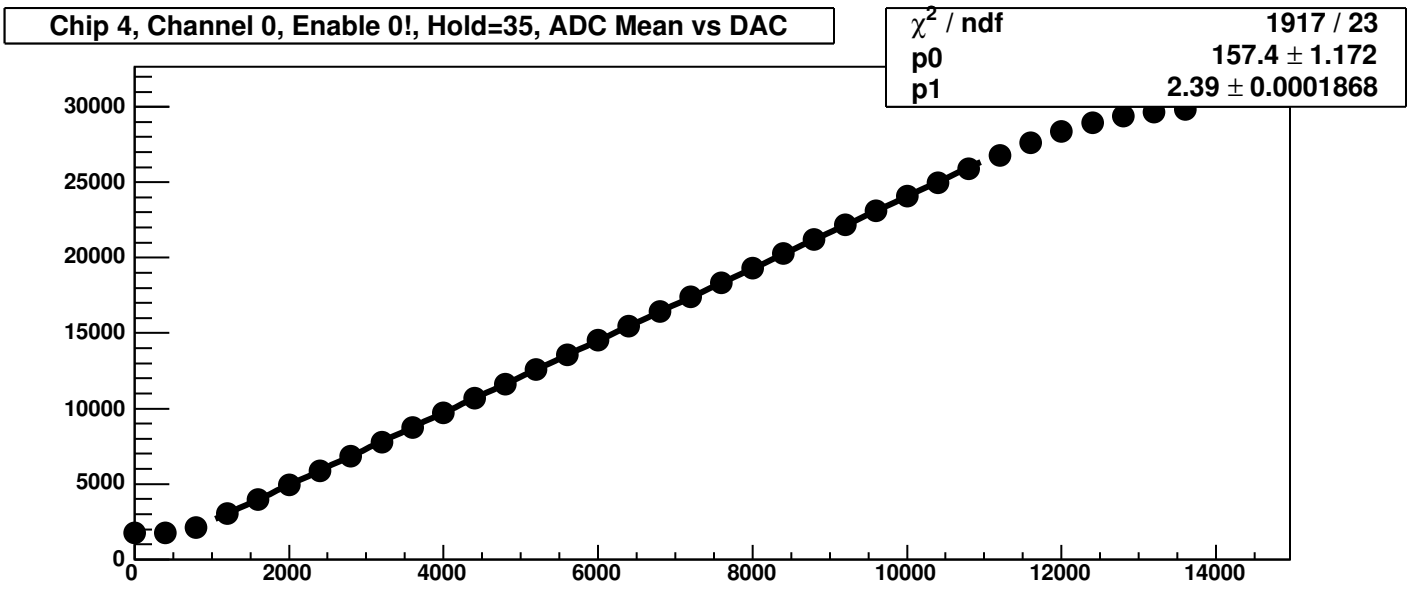

Chip 3, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

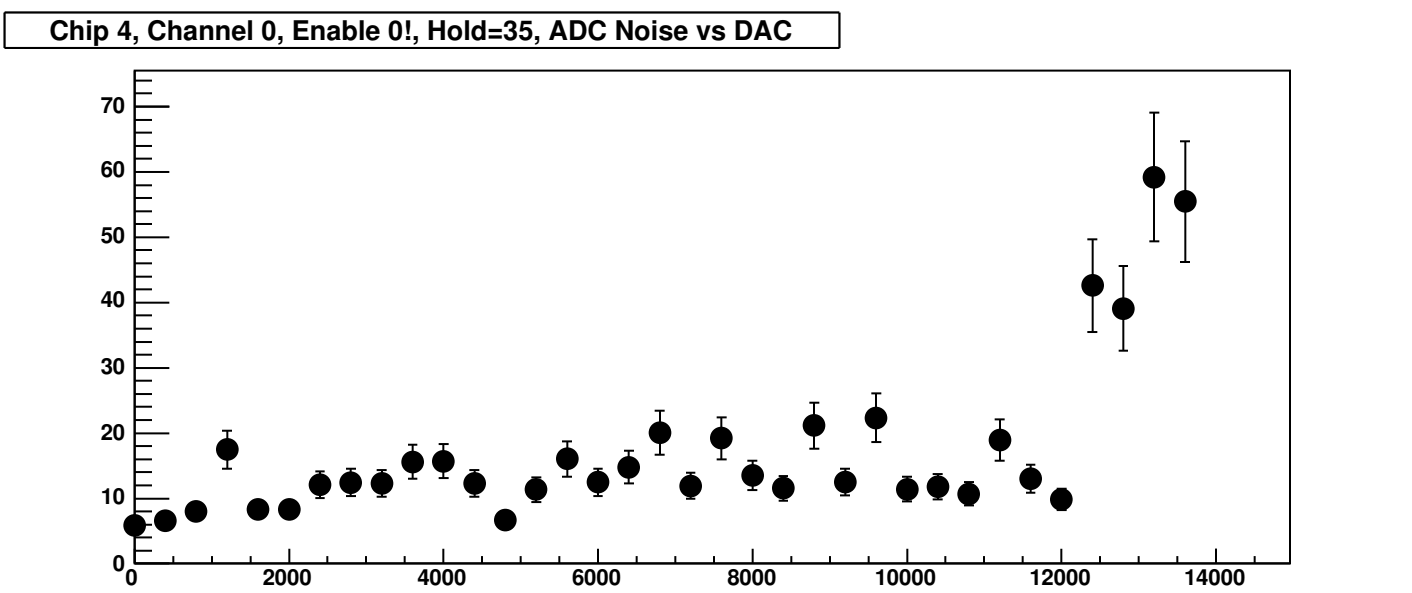

Chip 3, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

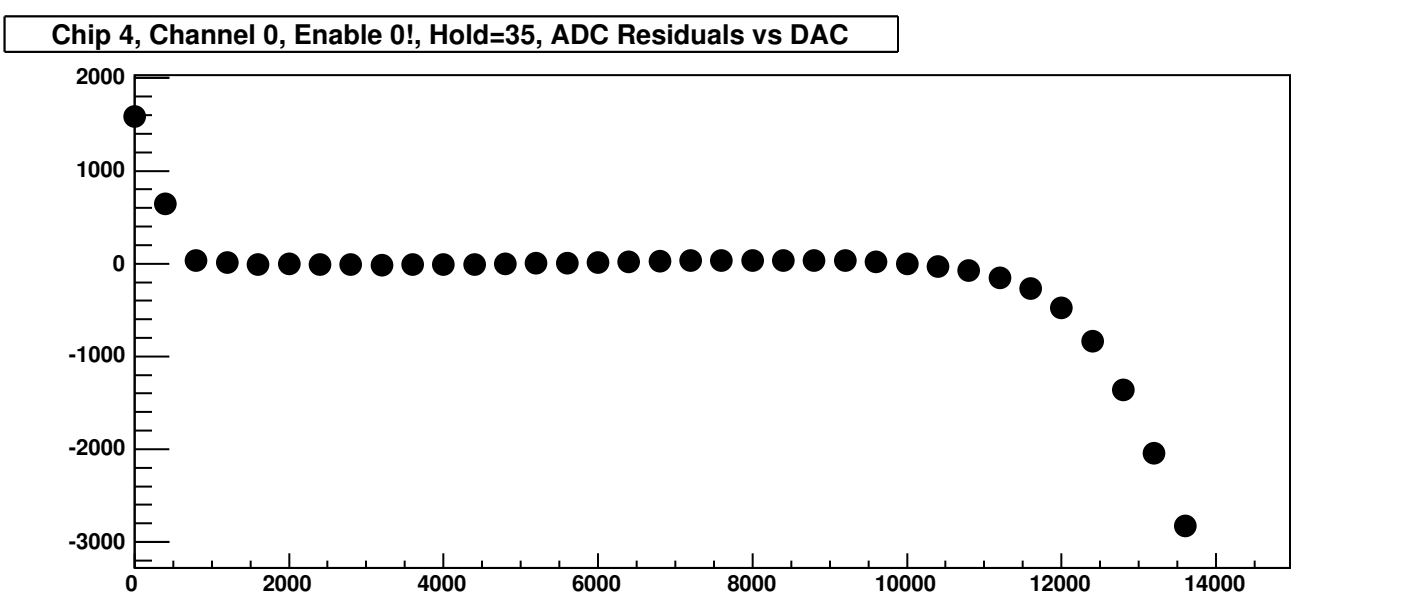

Chip 3, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 7274 / 23
p0 2.839e+04 ± 22.36
p1 0.1605 ± 0.002951
30000
28000
26000
24000
22000
20000
18000
16000
14000
12000
0
2000
4000
6000
8000
10000
12000
14000

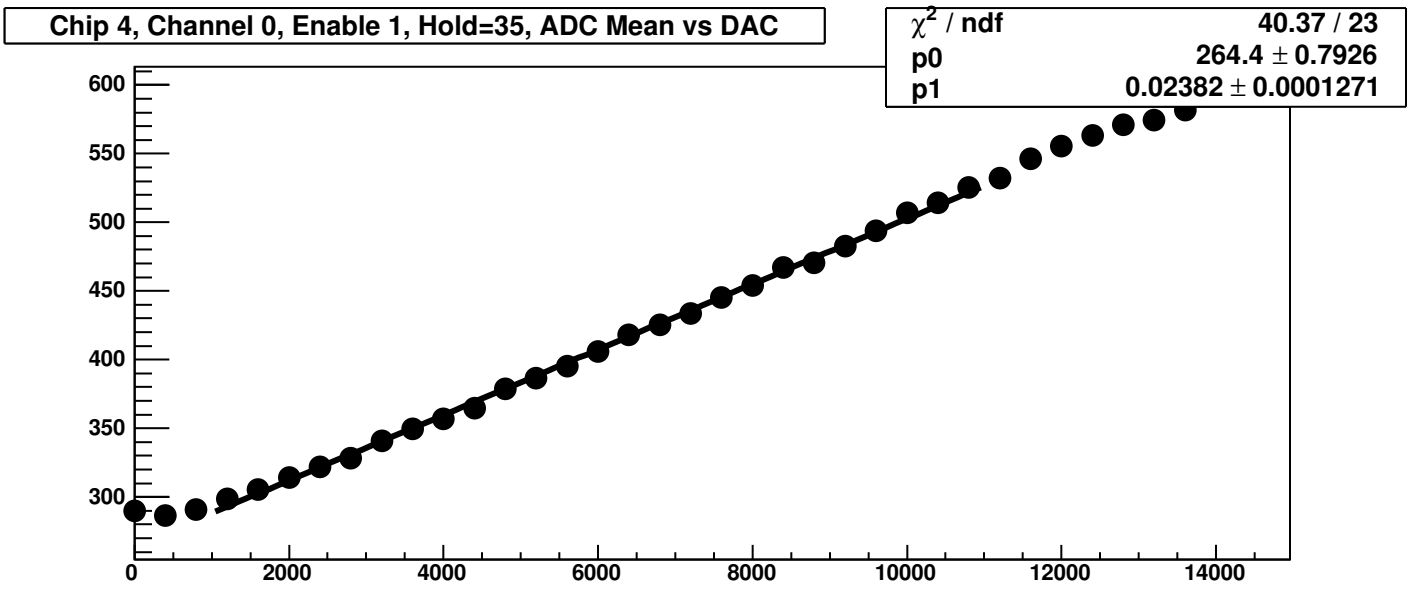

Chip 3, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

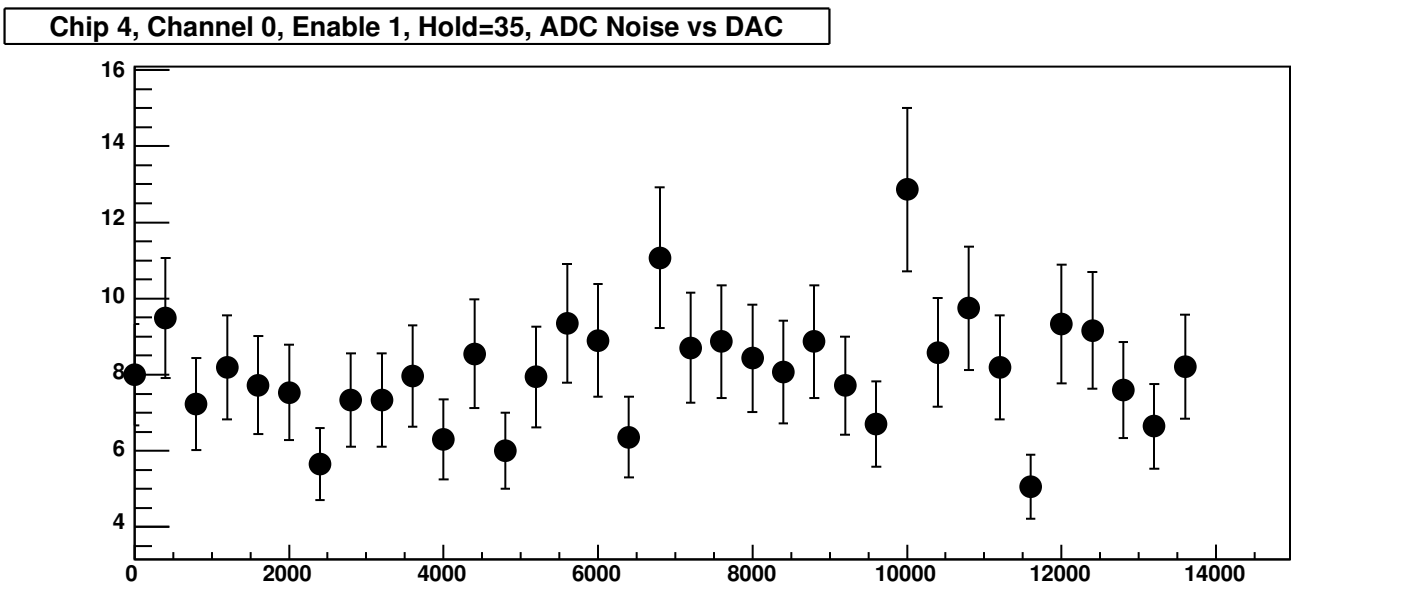

Chip 3, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

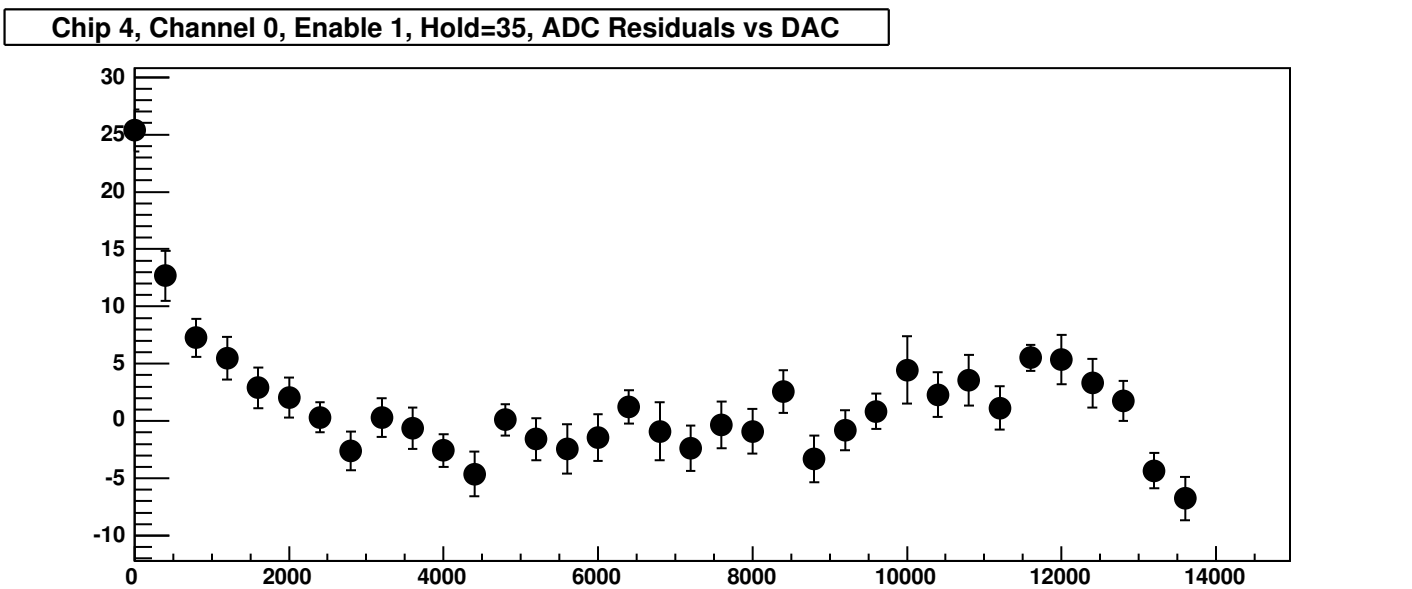

Chip 4, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1917 / 23
p0 157.4 ± 1.172
p1 2.39 ± 0.0001868
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

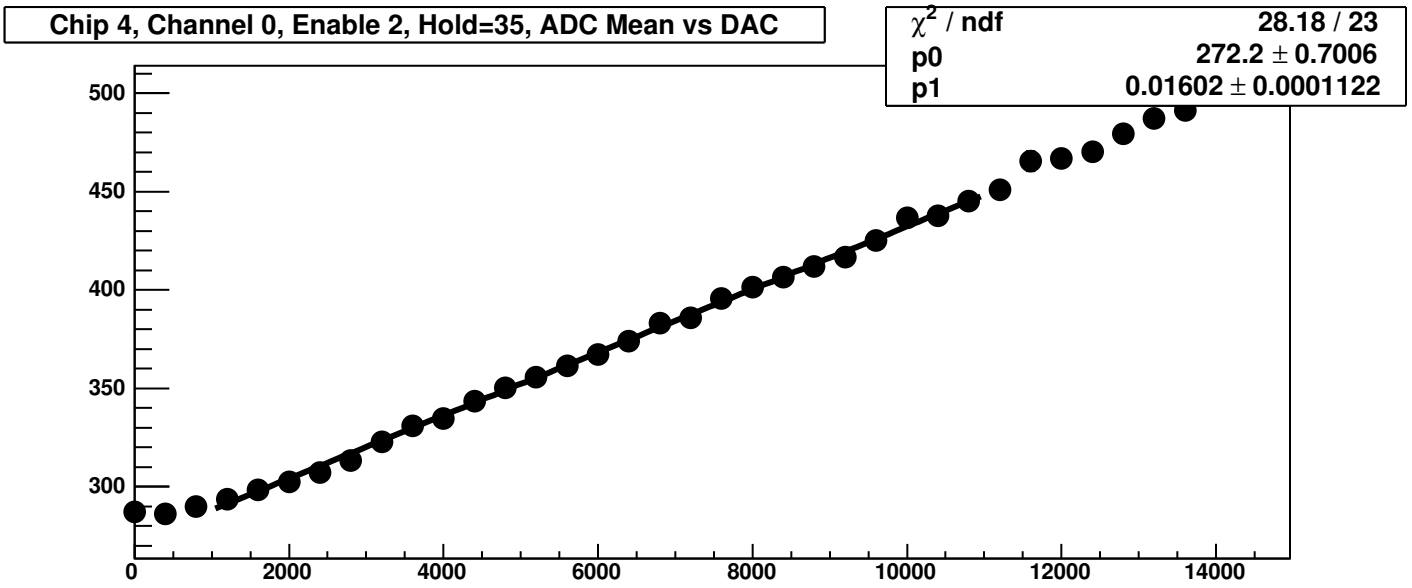

Chip 4, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

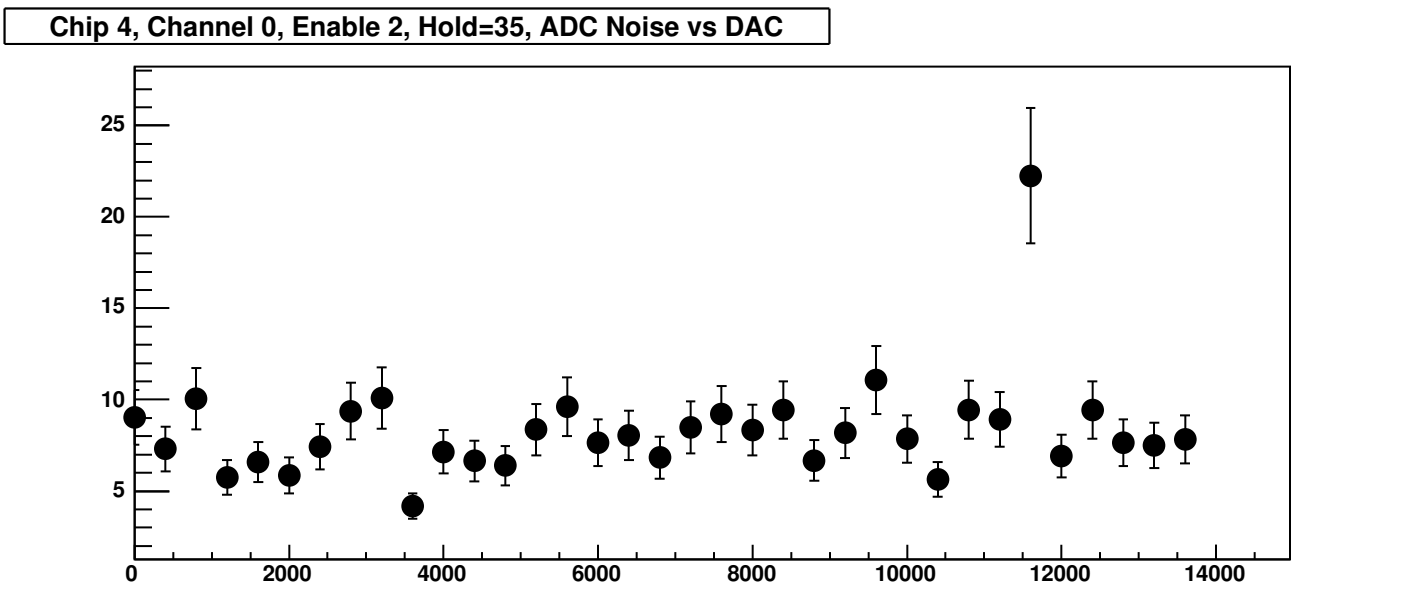

Chip 4, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

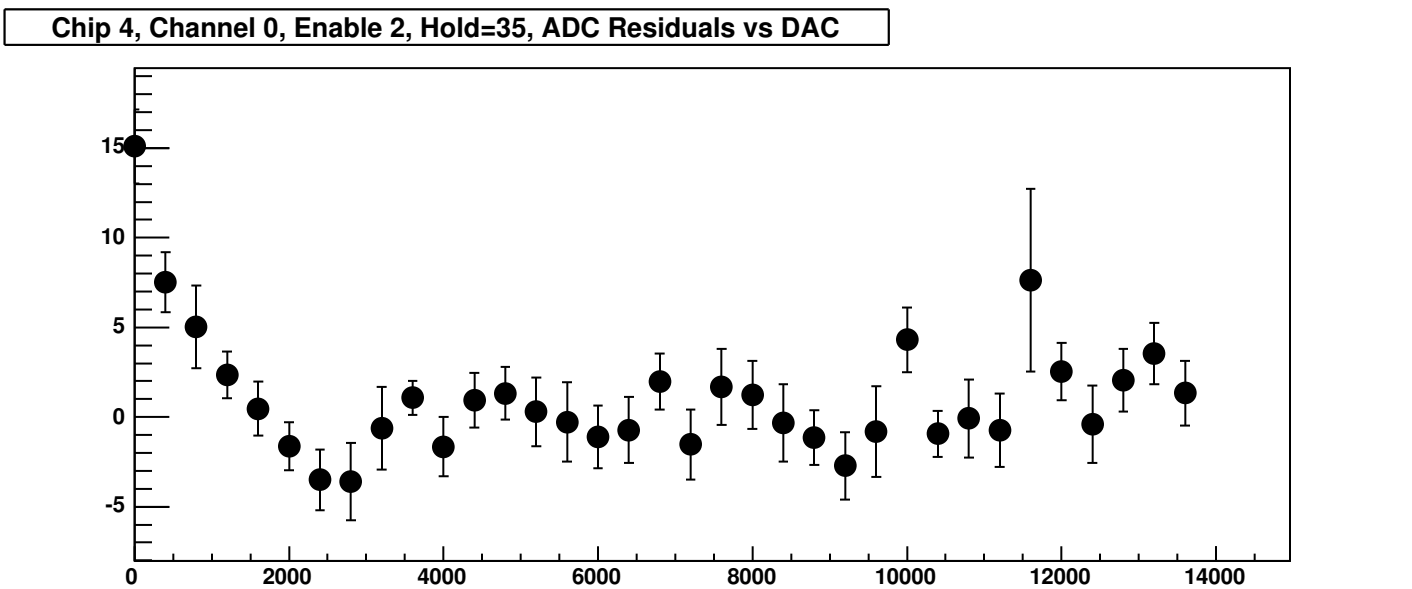

Chip 4, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 40.37 / 23
p0 264.4 ± 0.7926
p1 0.02382 ± 0.0001271
600
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

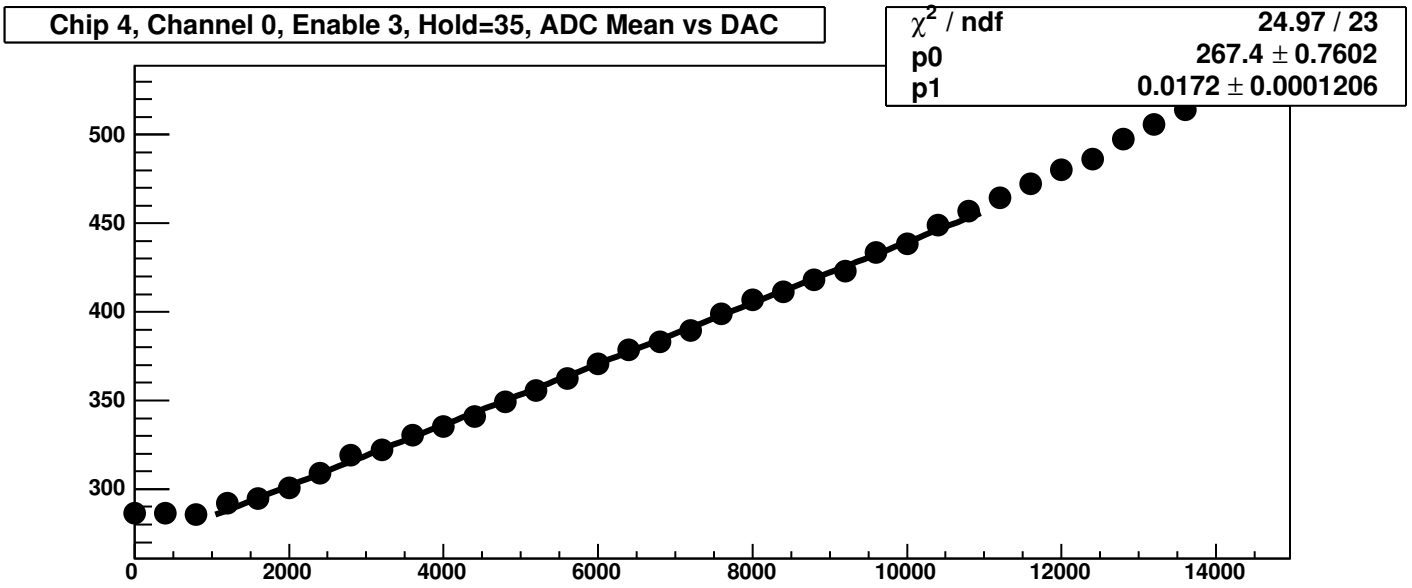

Chip 4, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

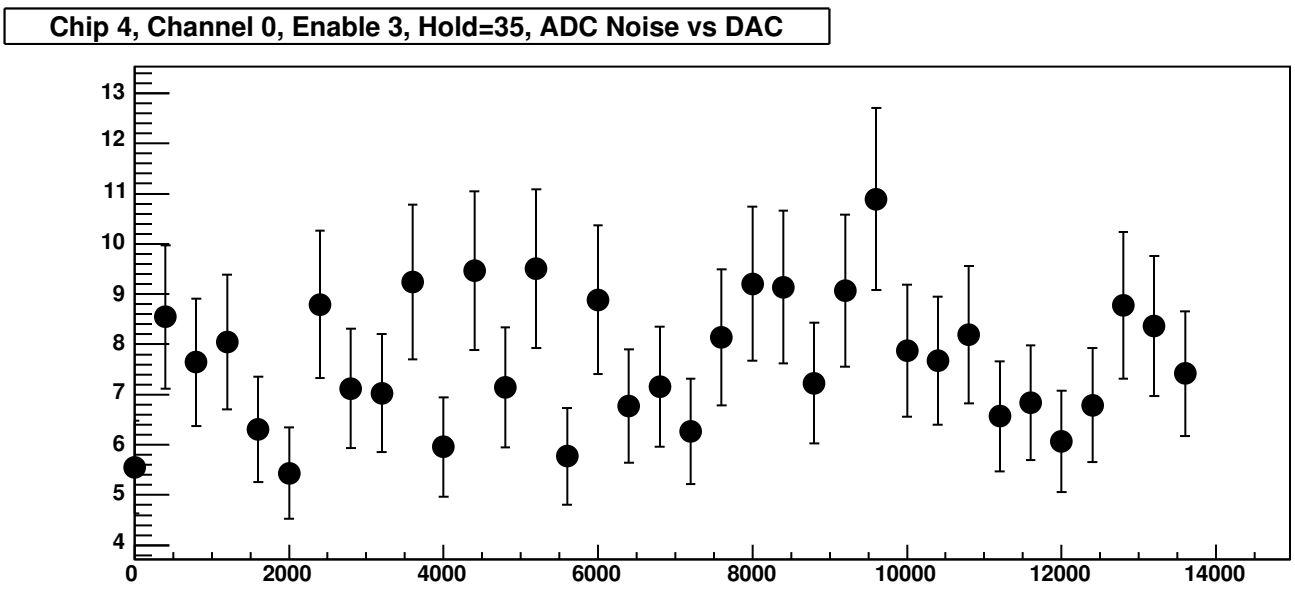

Chip 4, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

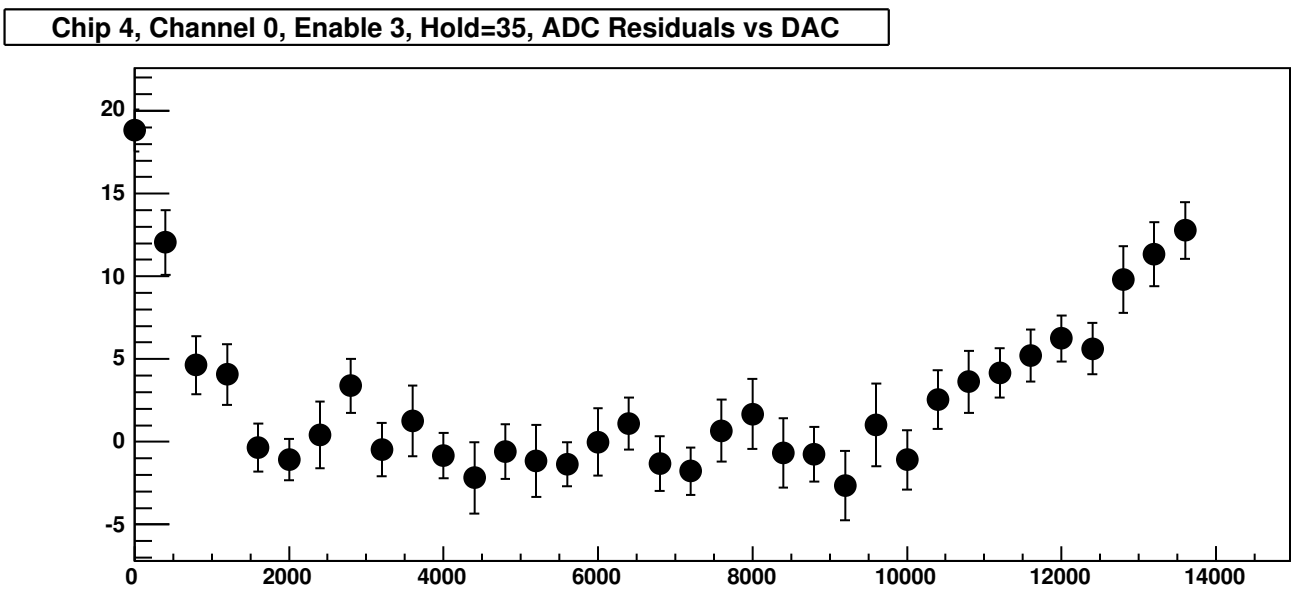

Chip 4, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 28.18 / 23
p0 272.2 ± 0.7006
p1 0.01602 ± 0.0001122
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

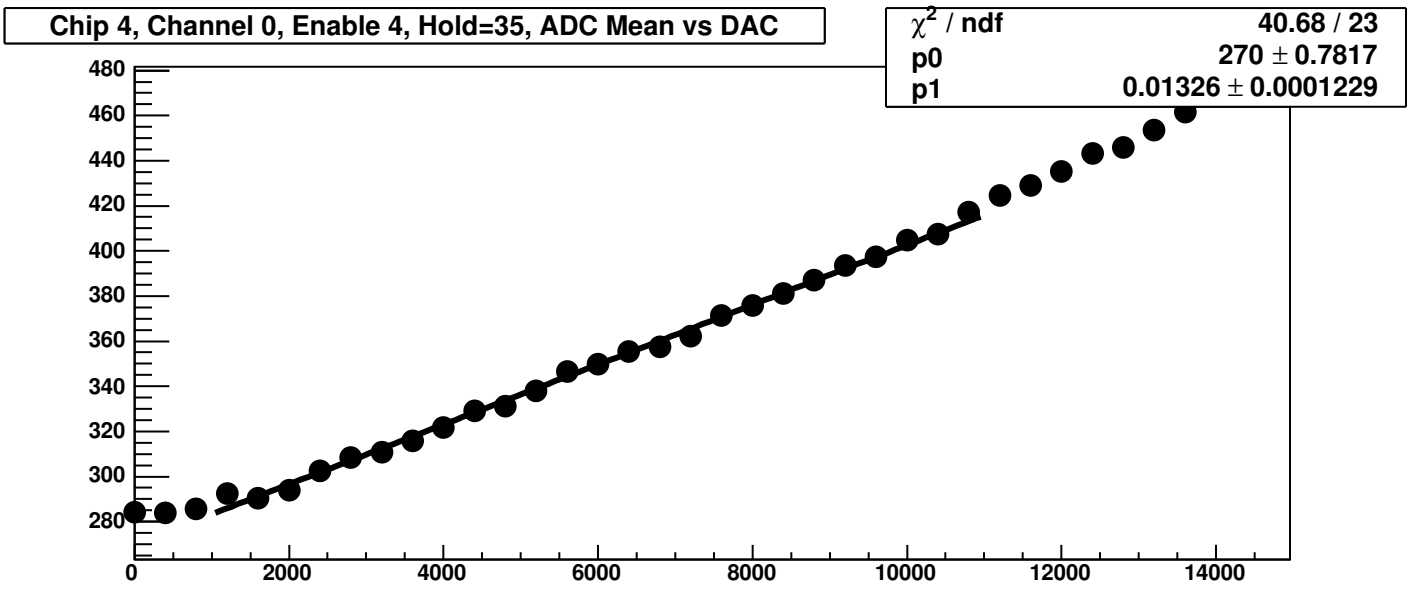

Chip 4, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

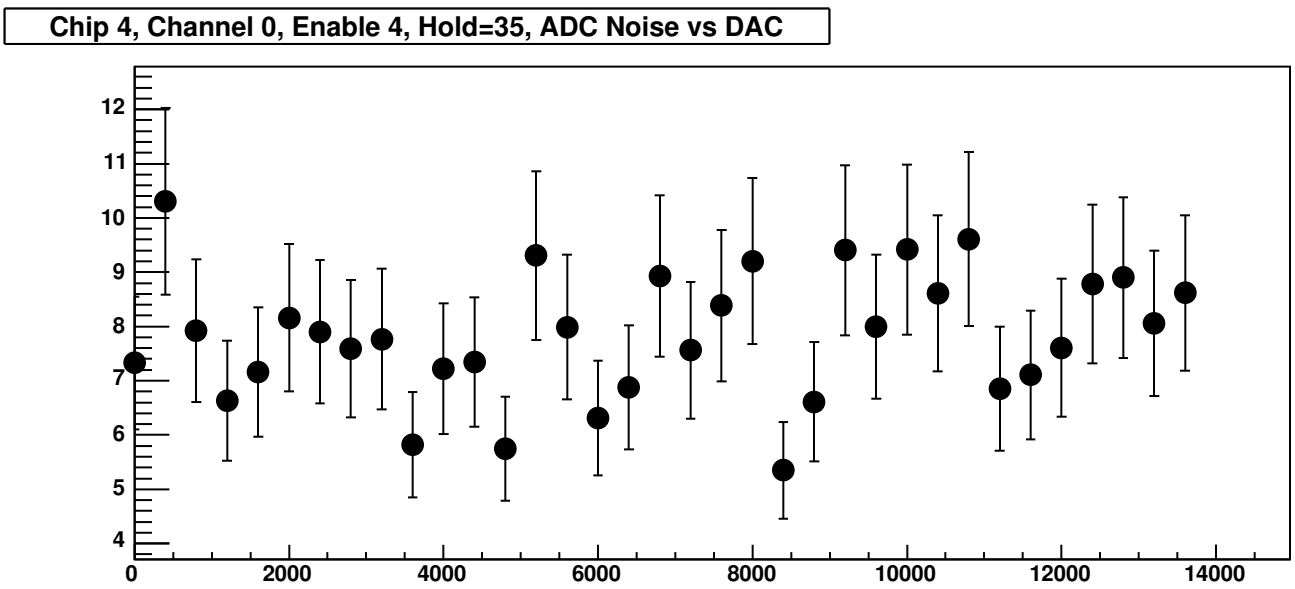

Chip 4, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

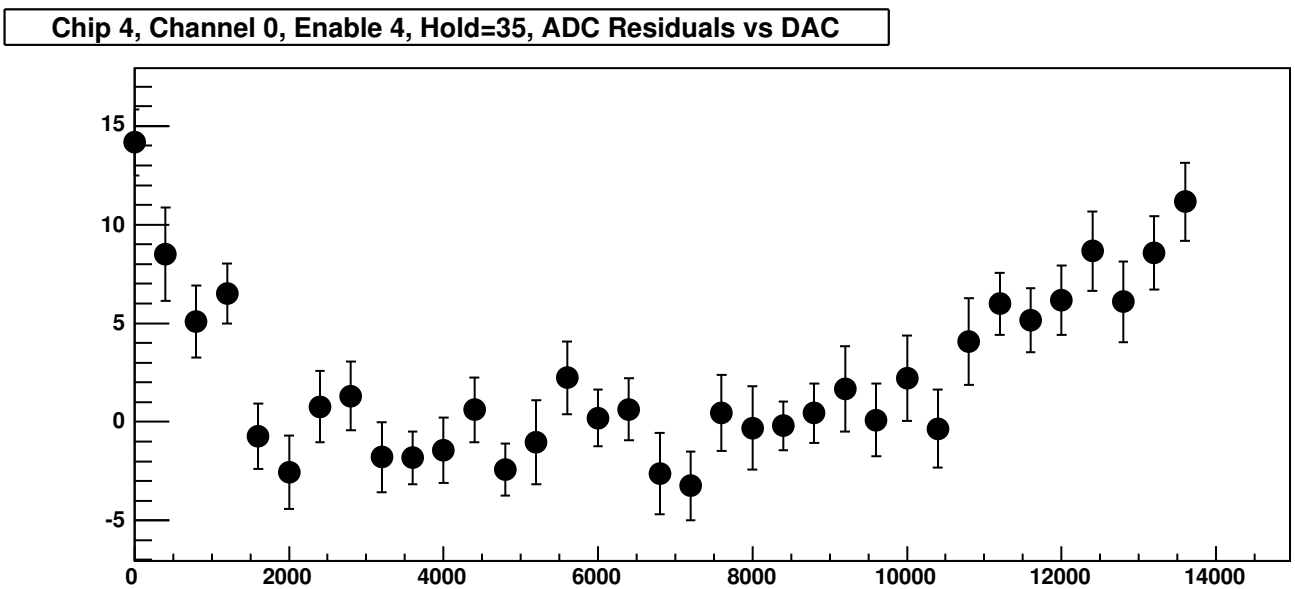

Chip 4, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.97 / 23
p0 267.4 ± 0.7602
p1 0.0172 ± 0.0001206
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

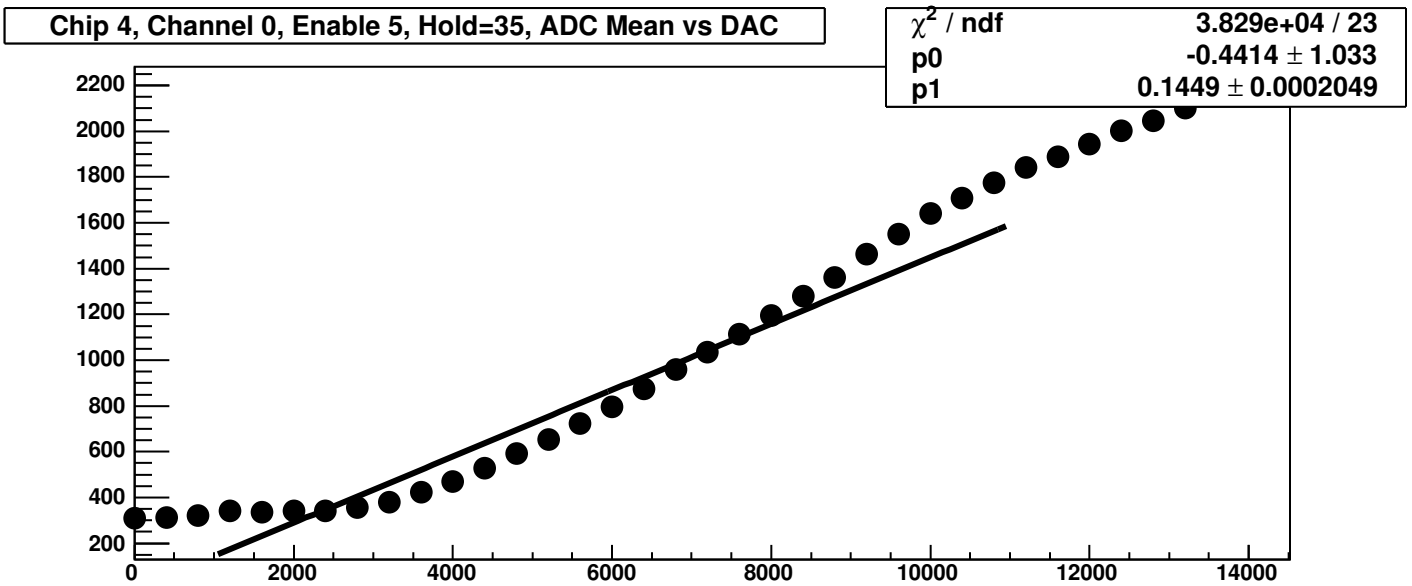

Chip 4, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

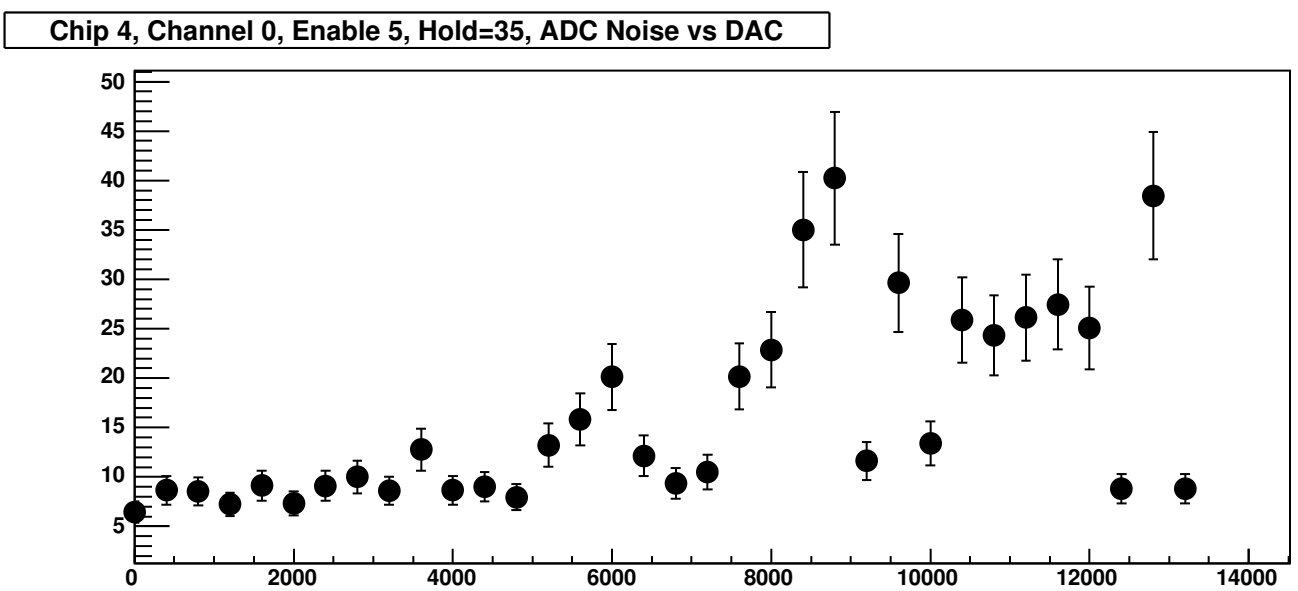

Chip 4, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

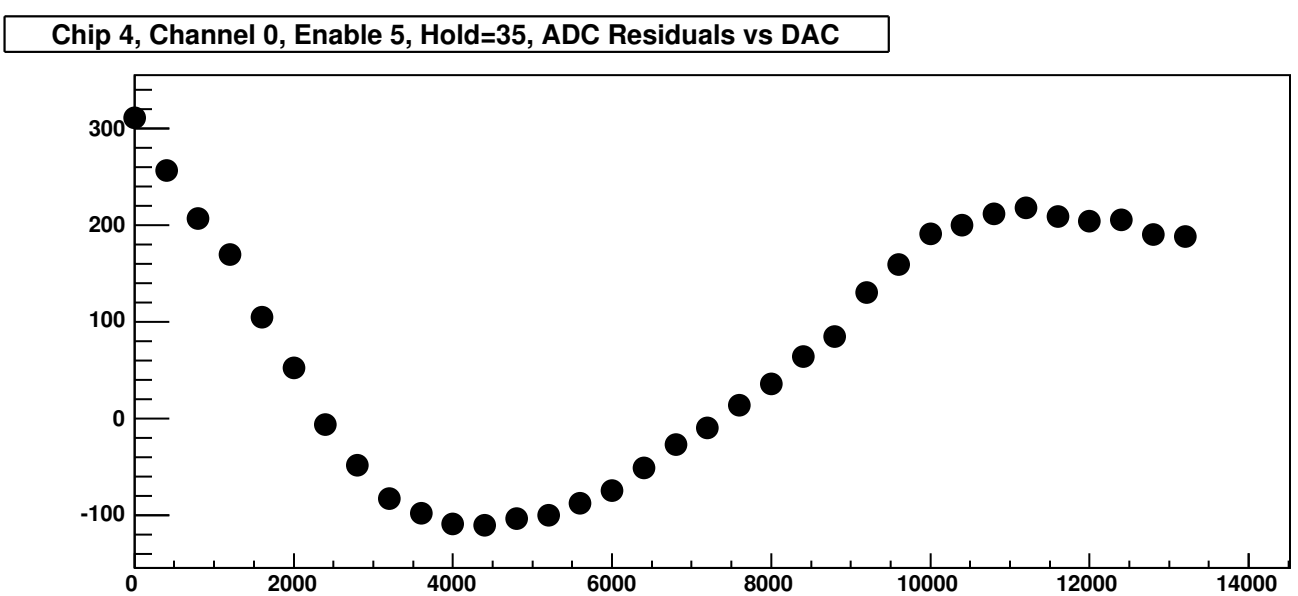

Chip 4, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 40.68 / 23
p0 270 ± 0.7817
p1 0.01326 ± 0.0001229
480
460
440
420
400
380
360
340
320
300
280
0
2000
4000
6000
8000
10000
12000
14000

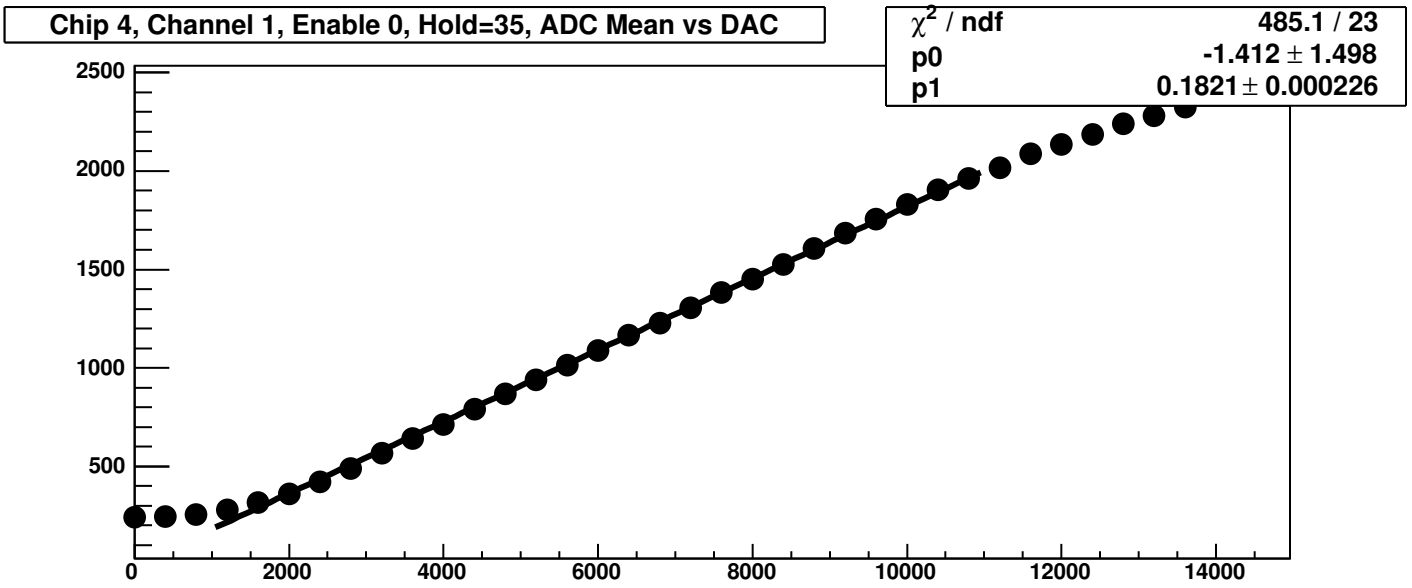

Chip 4, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

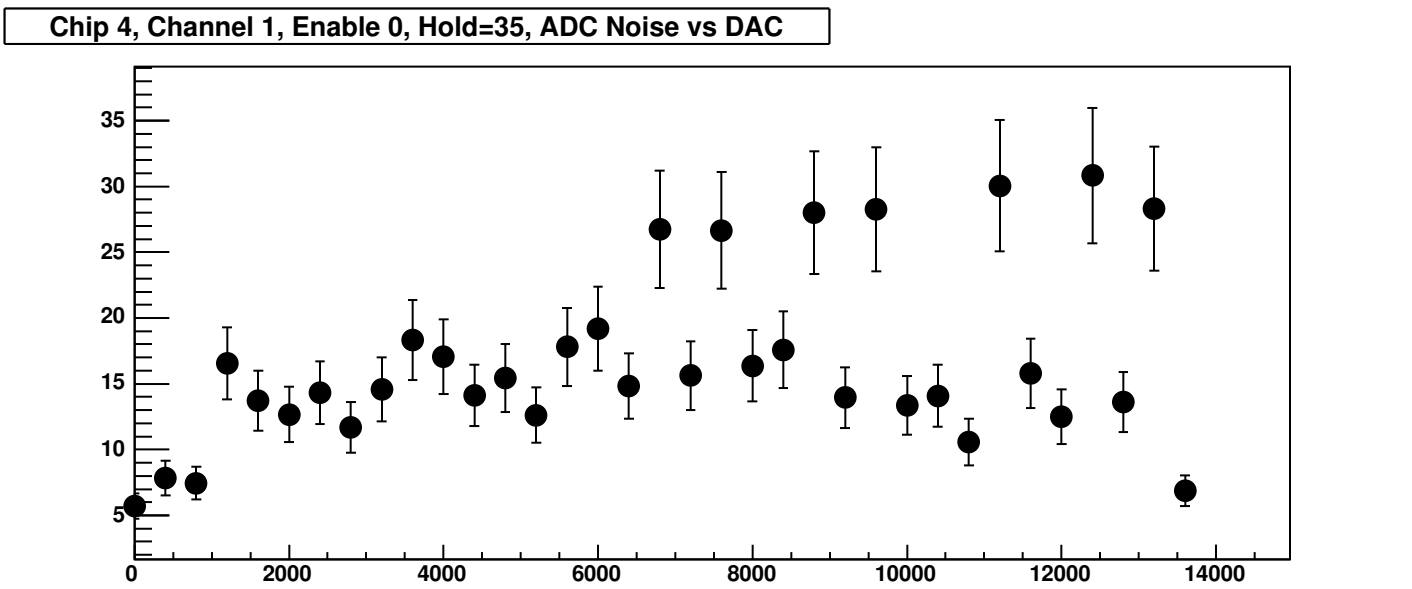

Chip 4, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

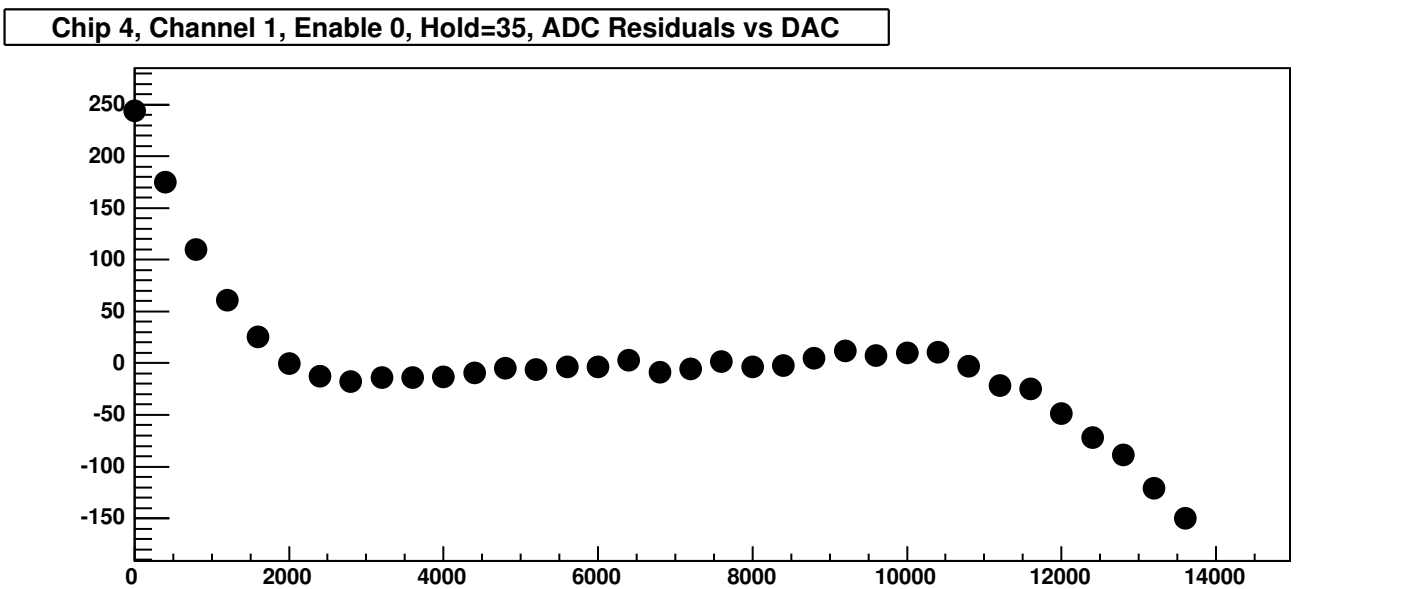

Chip 4, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.829e+04 / 23
p0 -0.4414 ± 1.033
p1 0.1449 ± 0.0002049
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

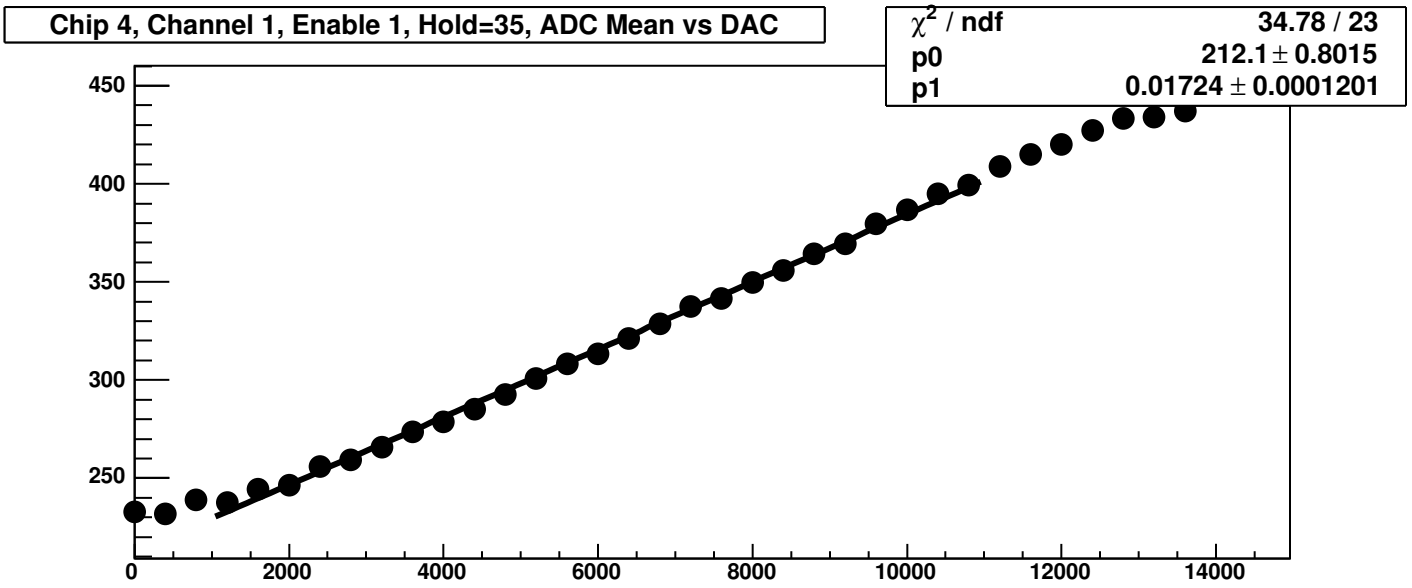

Chip 4, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

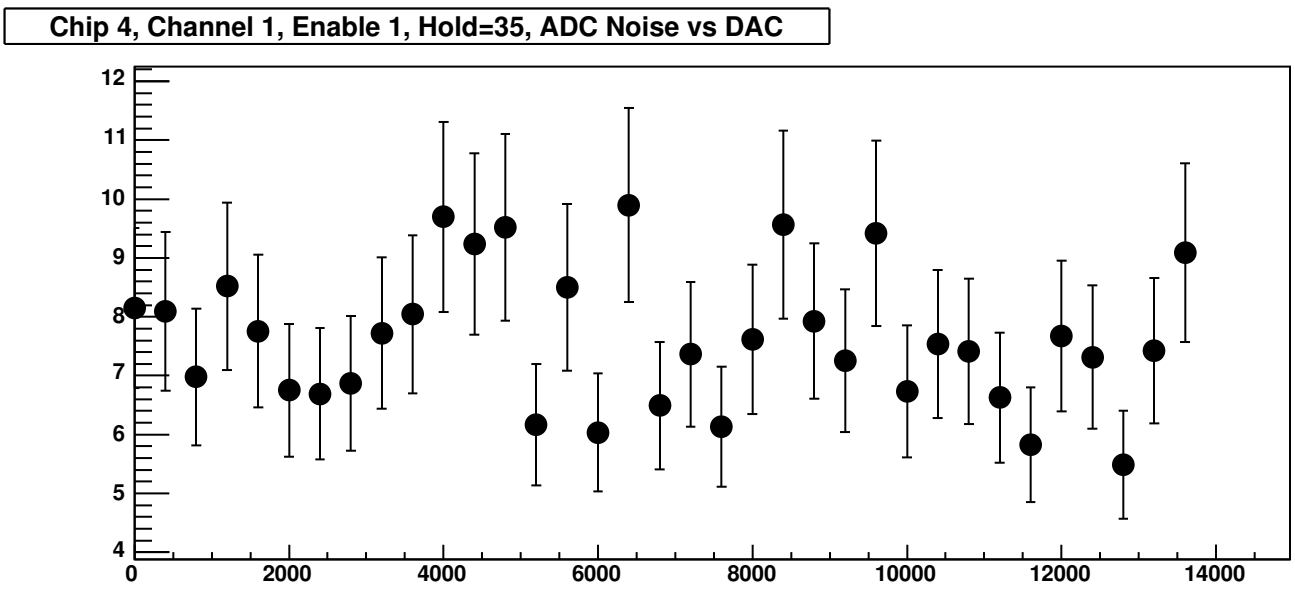

Chip 4, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

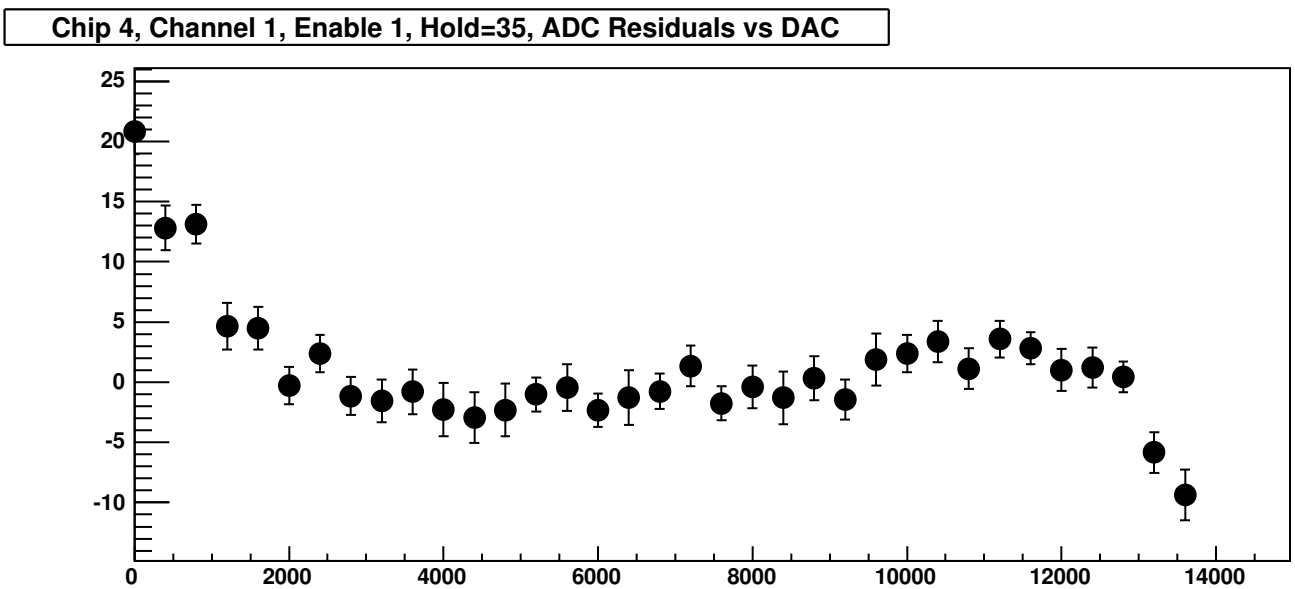

Chip 4, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 485.1 / 23
p0 -1.412 ± 1.498
p1 0.1821 ± 0.000226
2500
2000
1500
1000
500
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

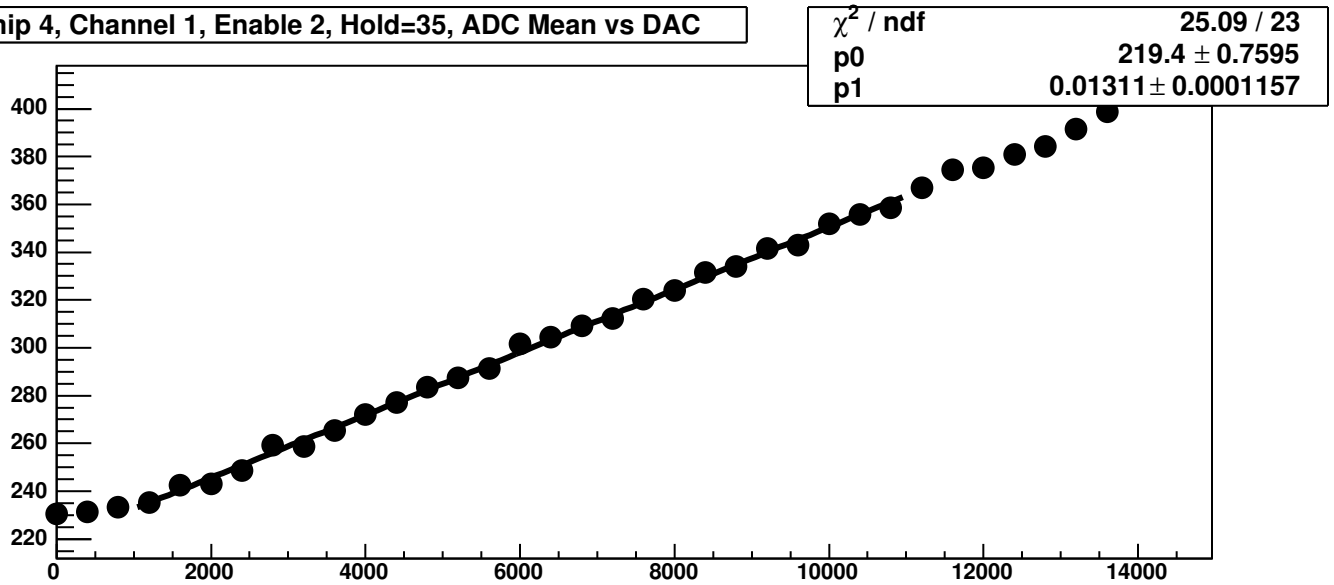

Chip 4, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 34.78 / 23
p0 212.1 ± 0.8015
p1 0.01724 ± 0.0001201
450
400
350
300
250
0
2000
4000
6000
8000
10000
12000
14000

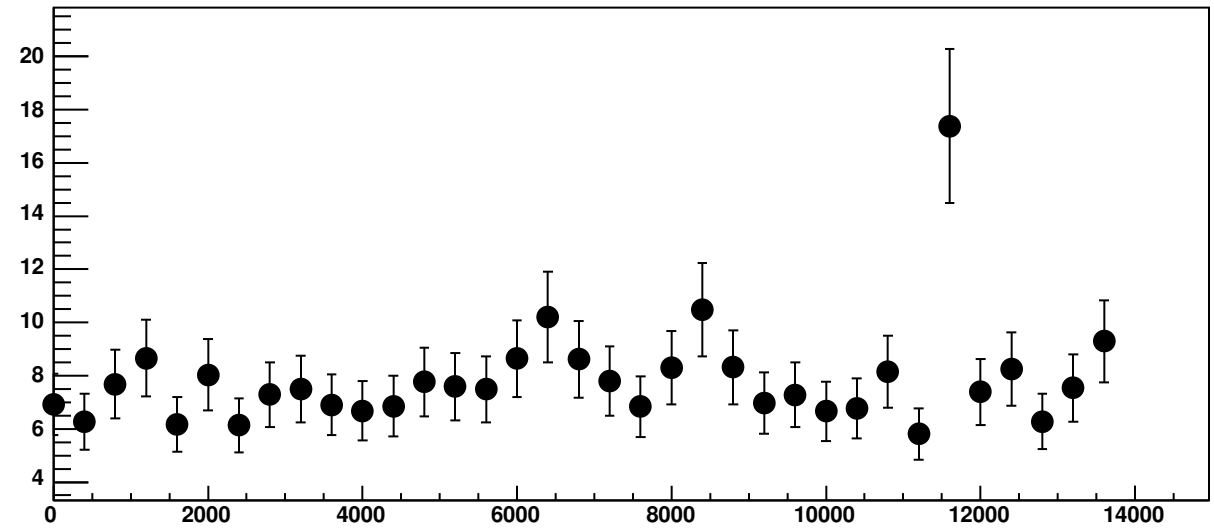

Chip 4, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

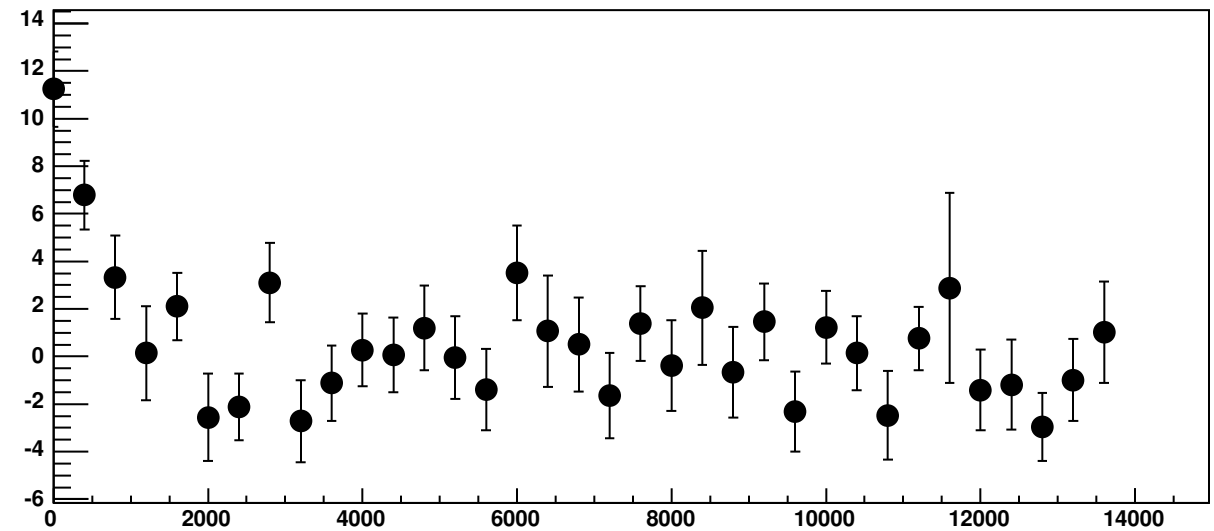
Chip 4, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

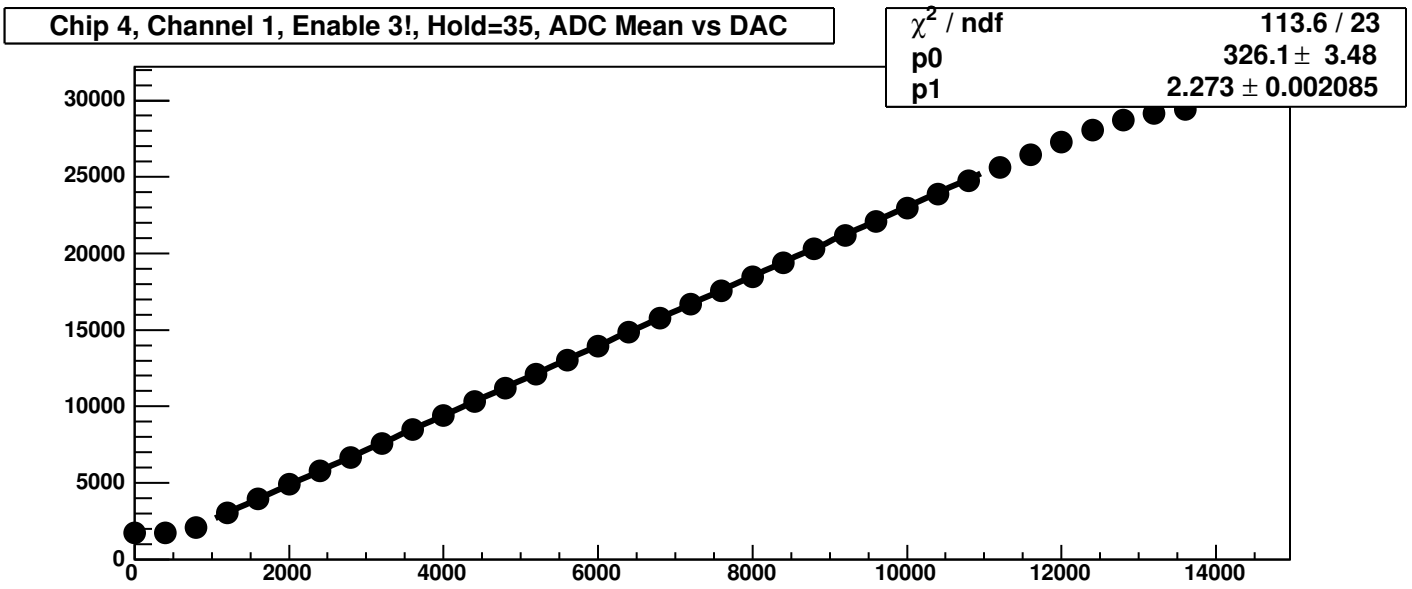


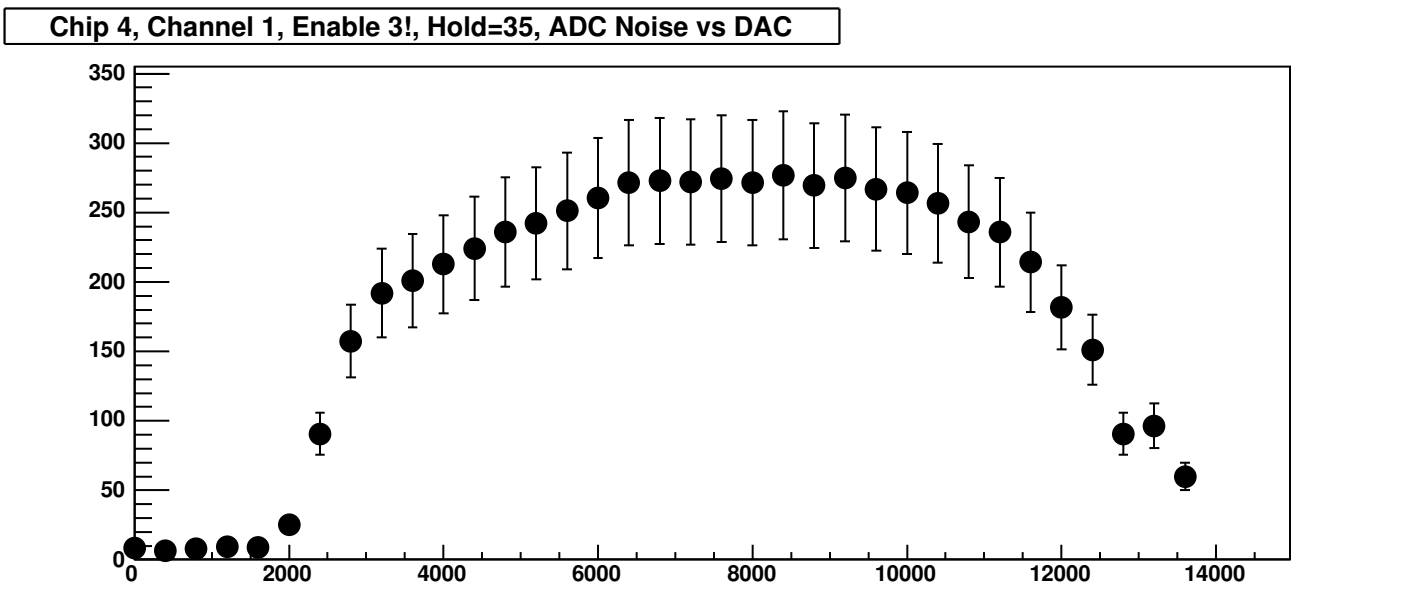
Chip 4, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC

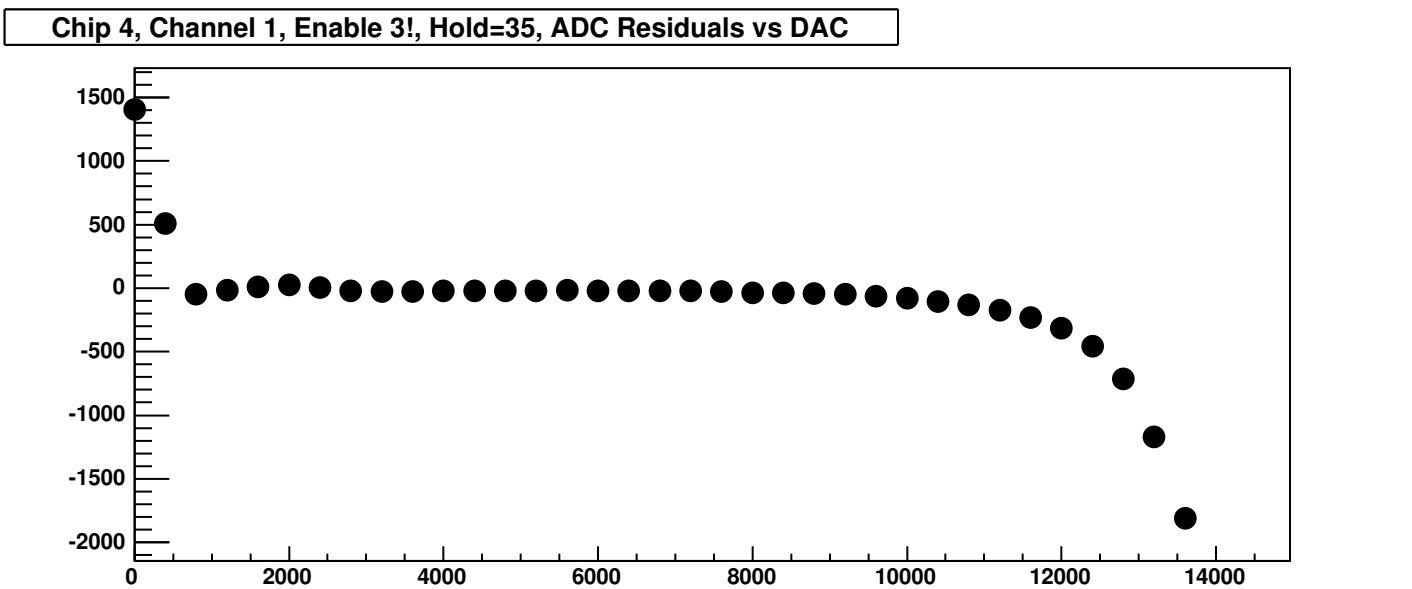


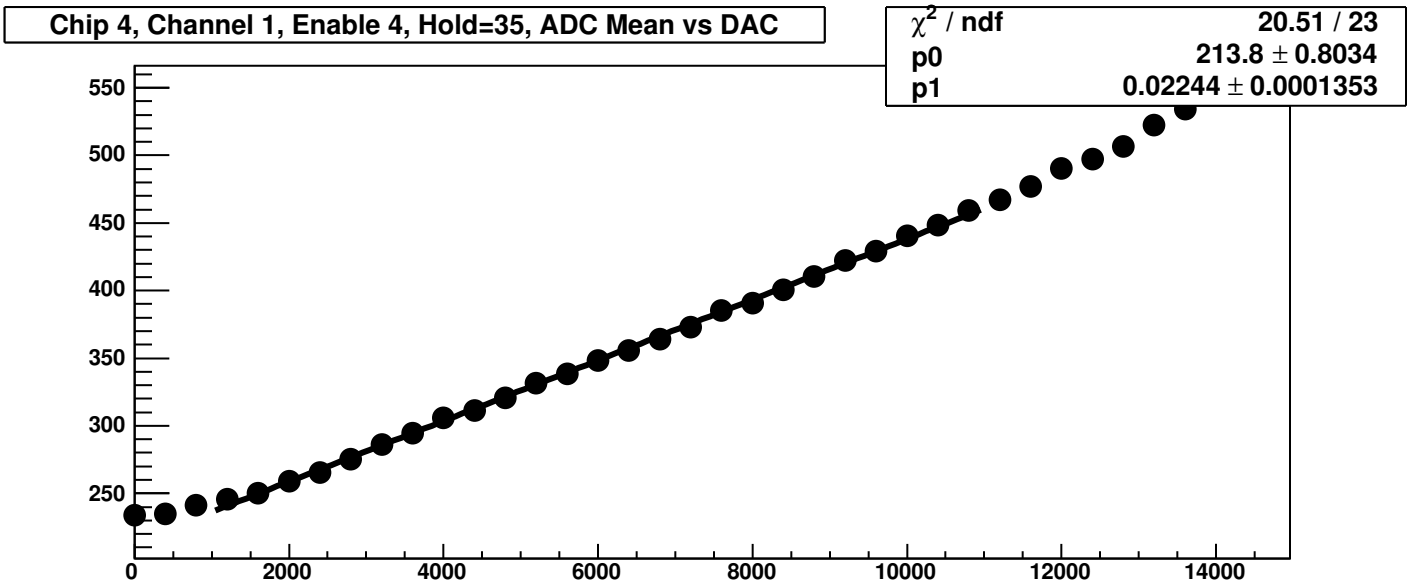
Chip 4, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

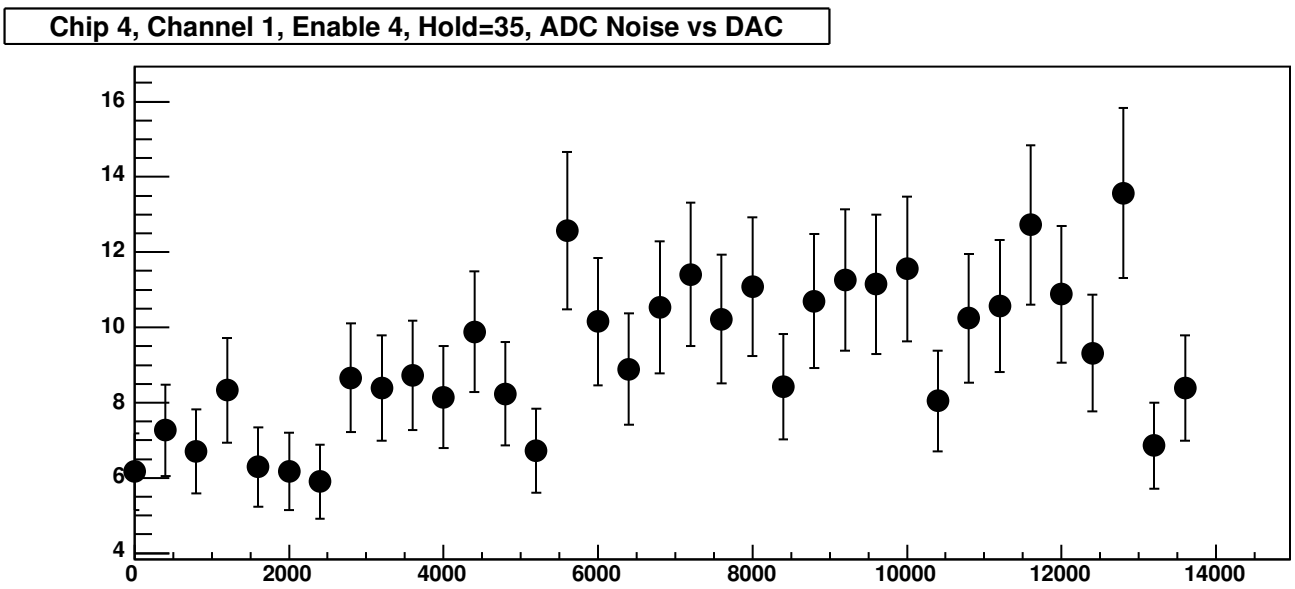


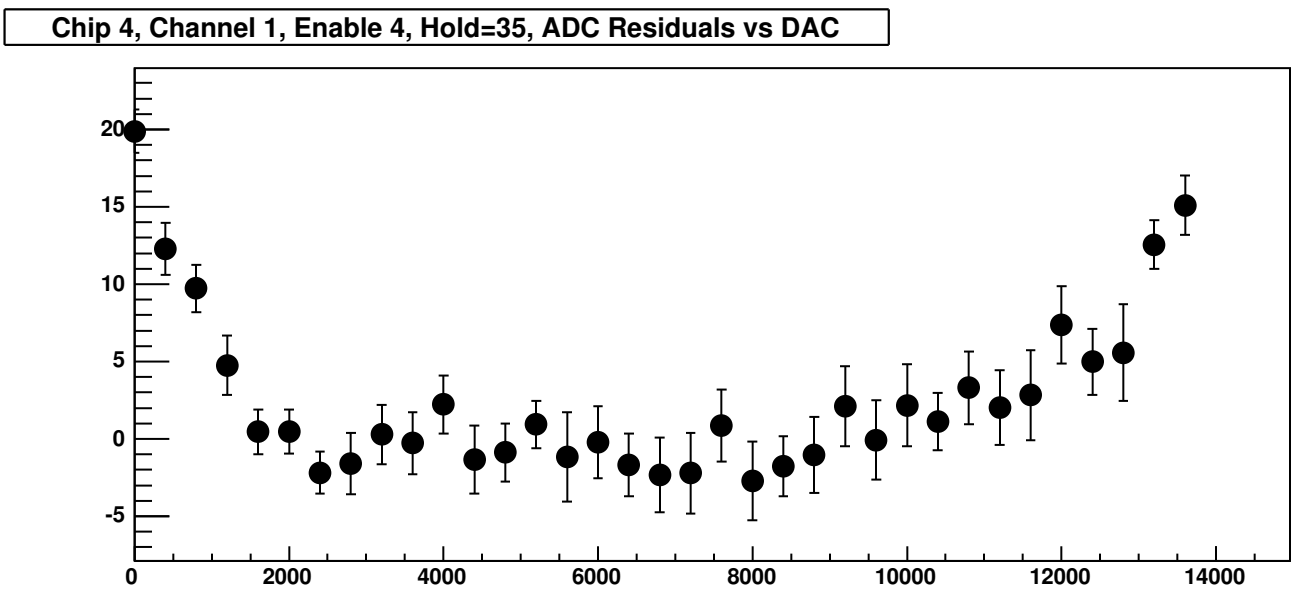

Chip 4, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 113.6 / 23
p0 326.1 ± 3.48
p1 2.273 ± 0.002085
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

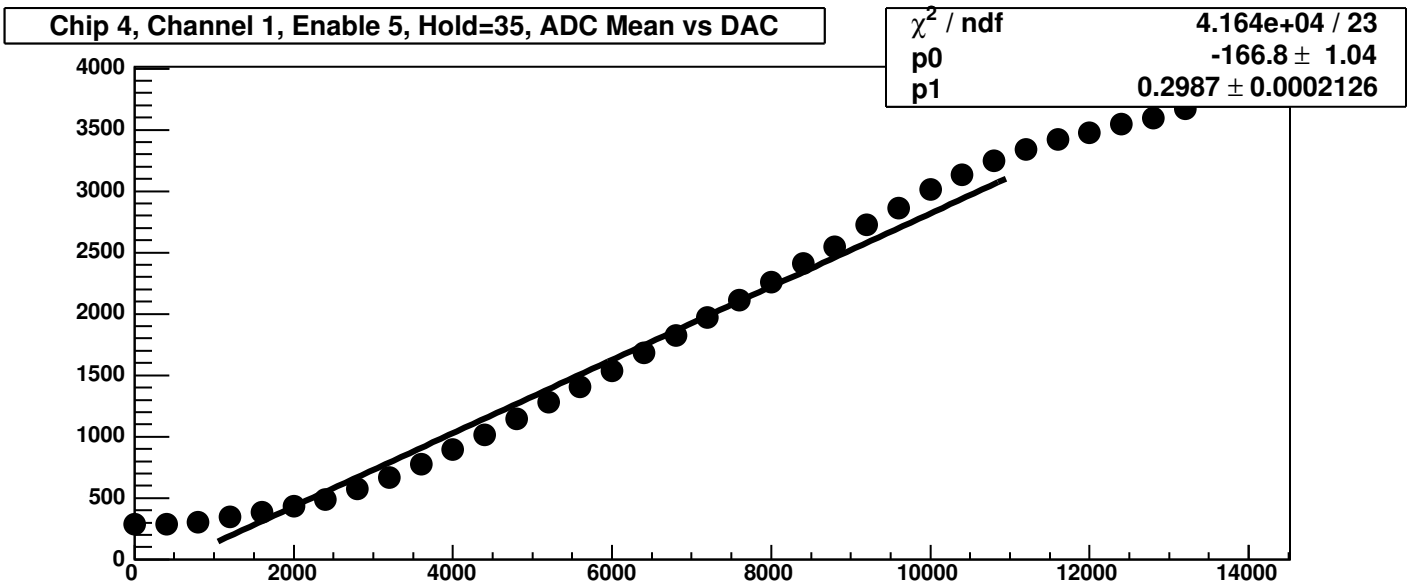

Chip 4, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

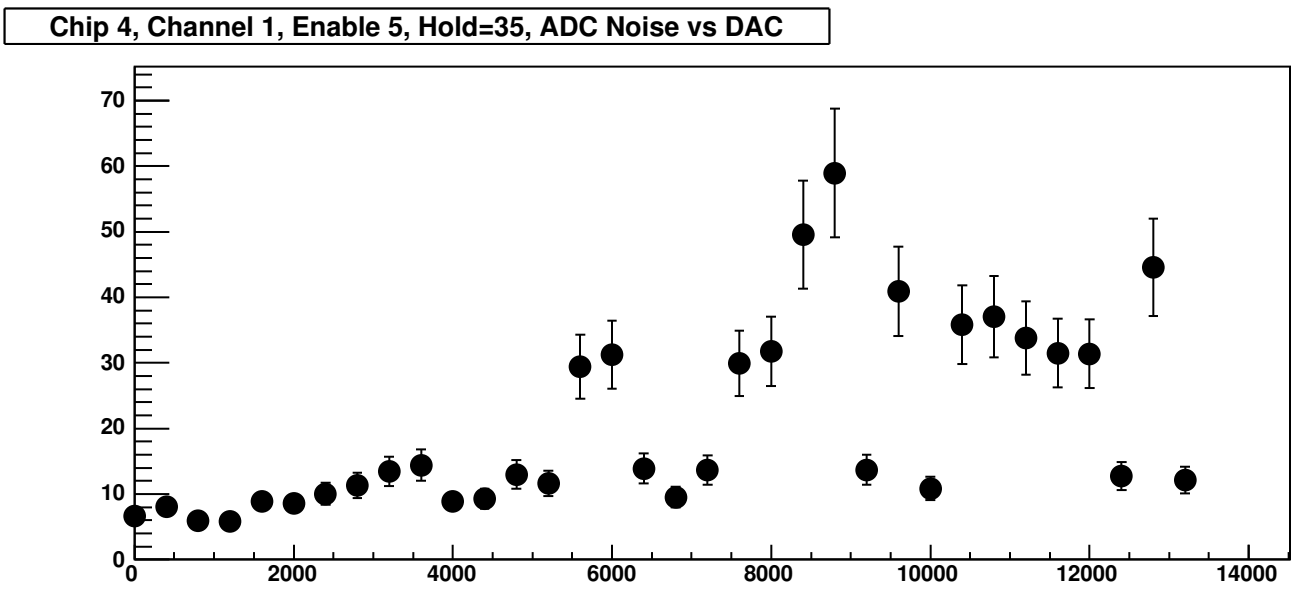

Chip 4, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

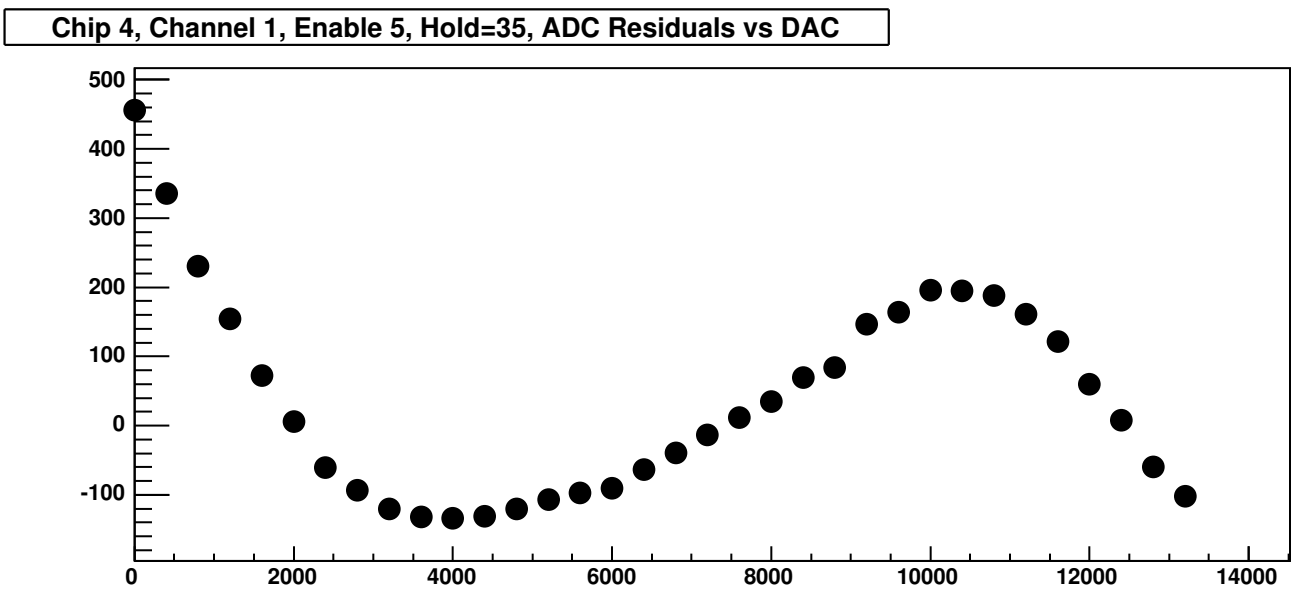

Chip 4, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 20.51 / 23
p0 213.8 ± 0.8034
p1 0.02244 ± 0.0001353
550
500
450
400
350
300
250
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

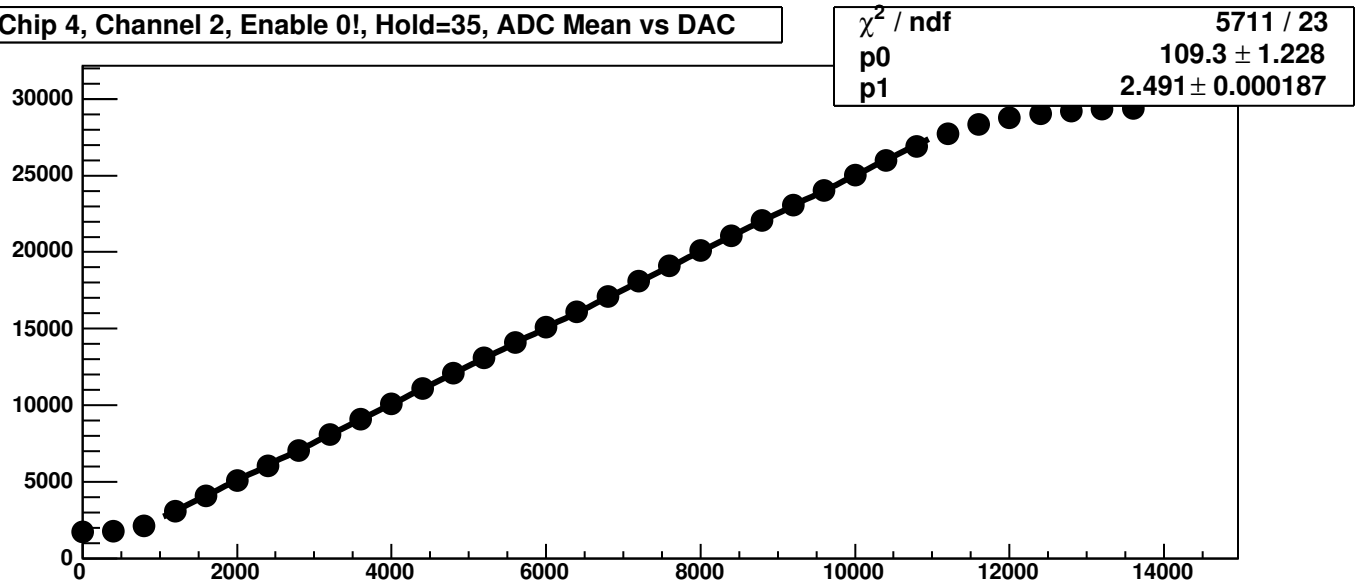

Chip 4, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4.164e+04 / 23
p0 -166.8 ± 1.04
p1 0.2987 ± 0.0002126
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

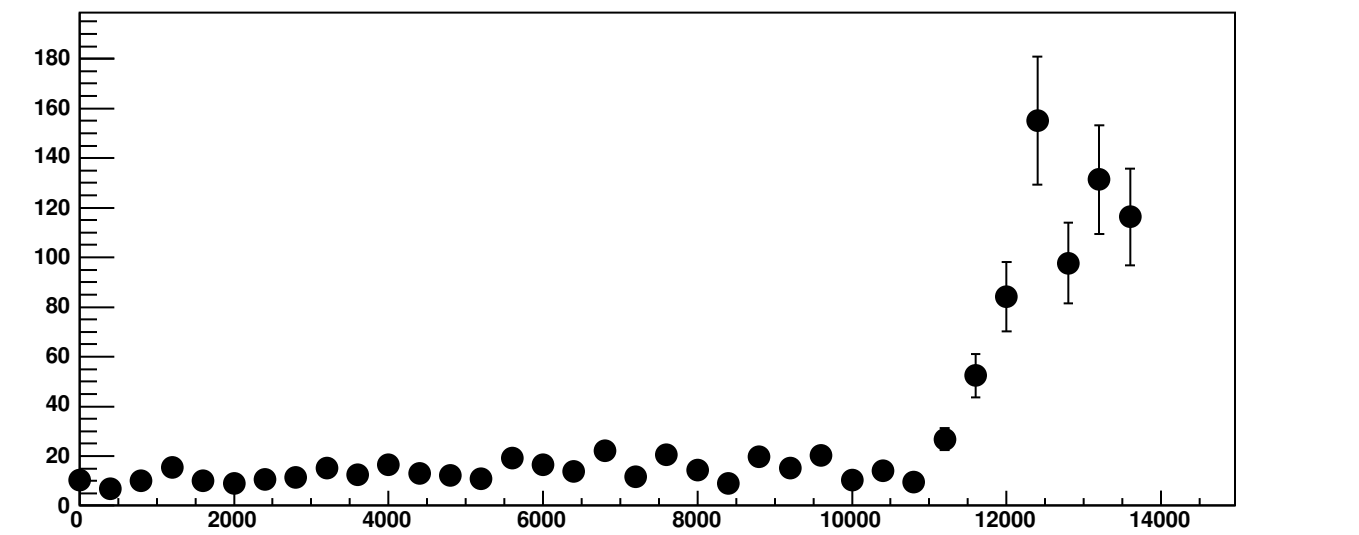

Chip 4, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

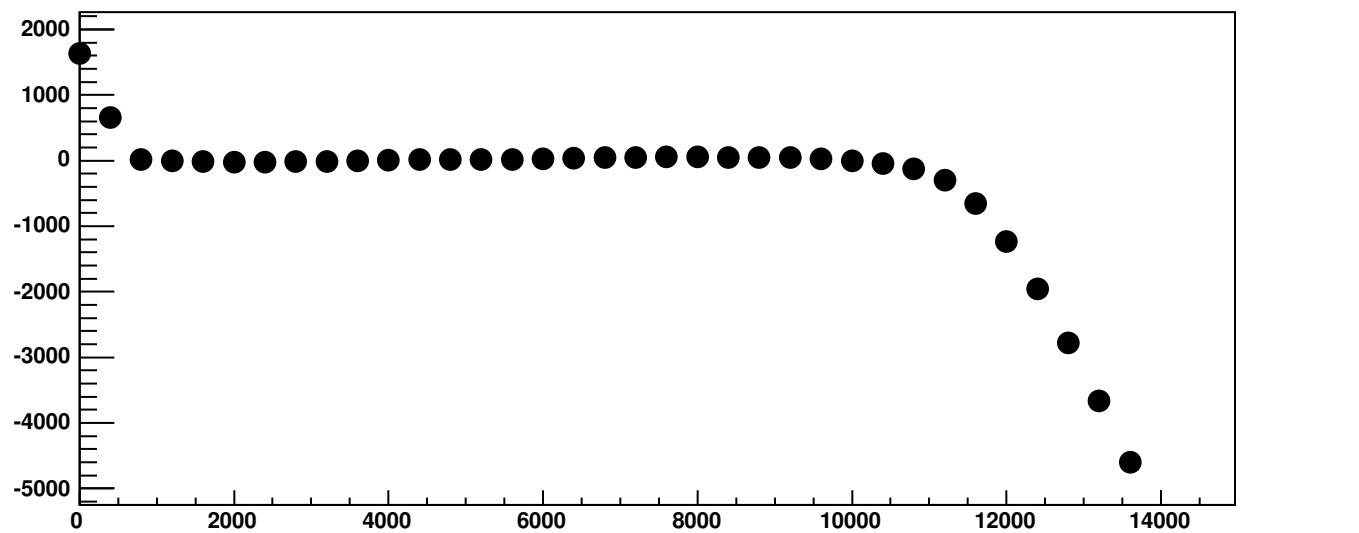
Chip 4, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC



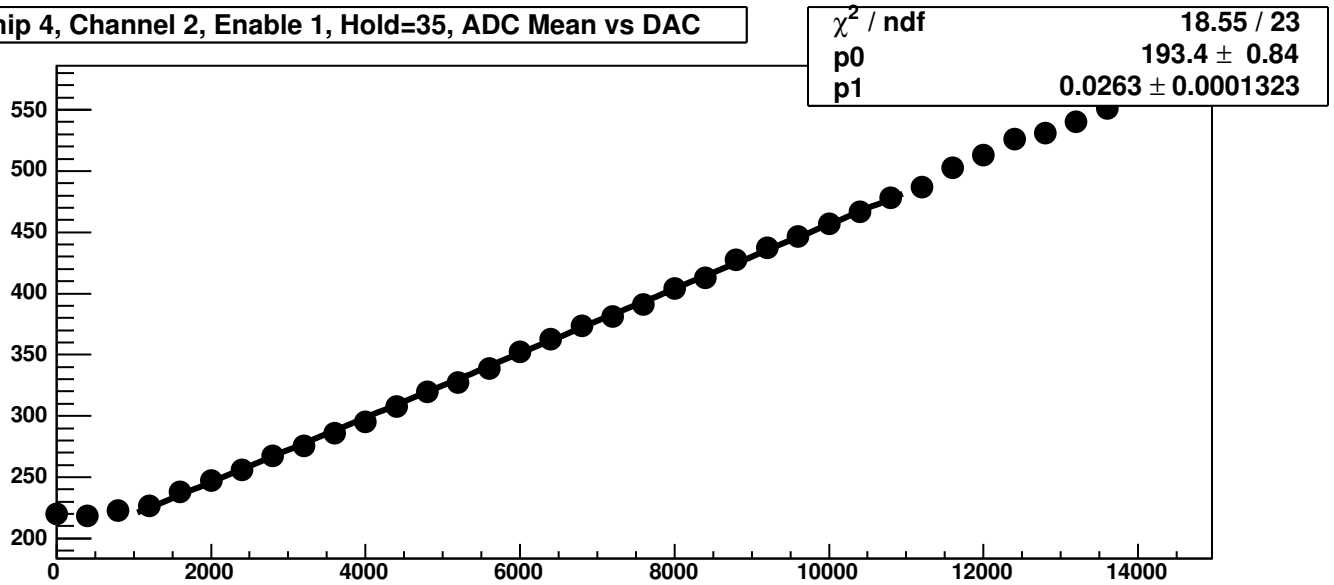
Chip 4, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



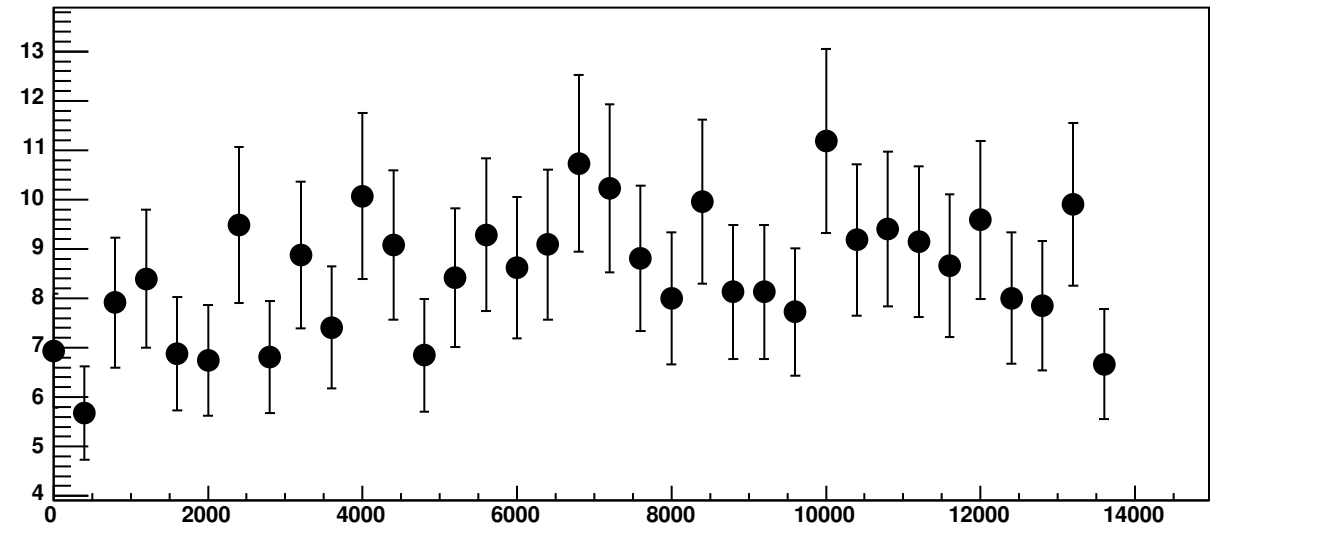
Chip 4, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC



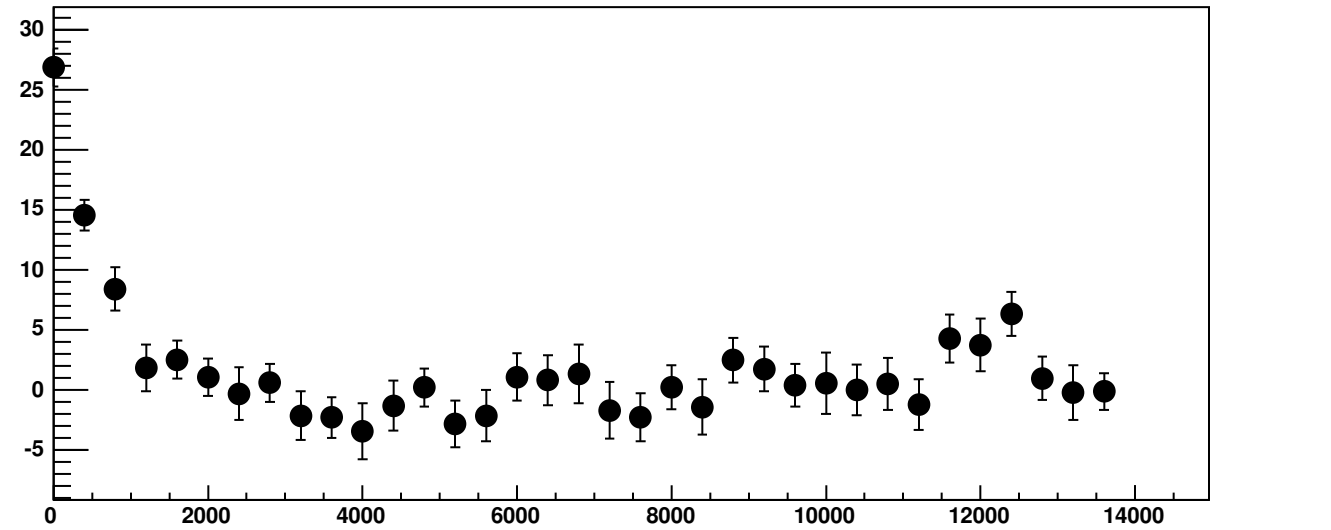
Chip 4, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



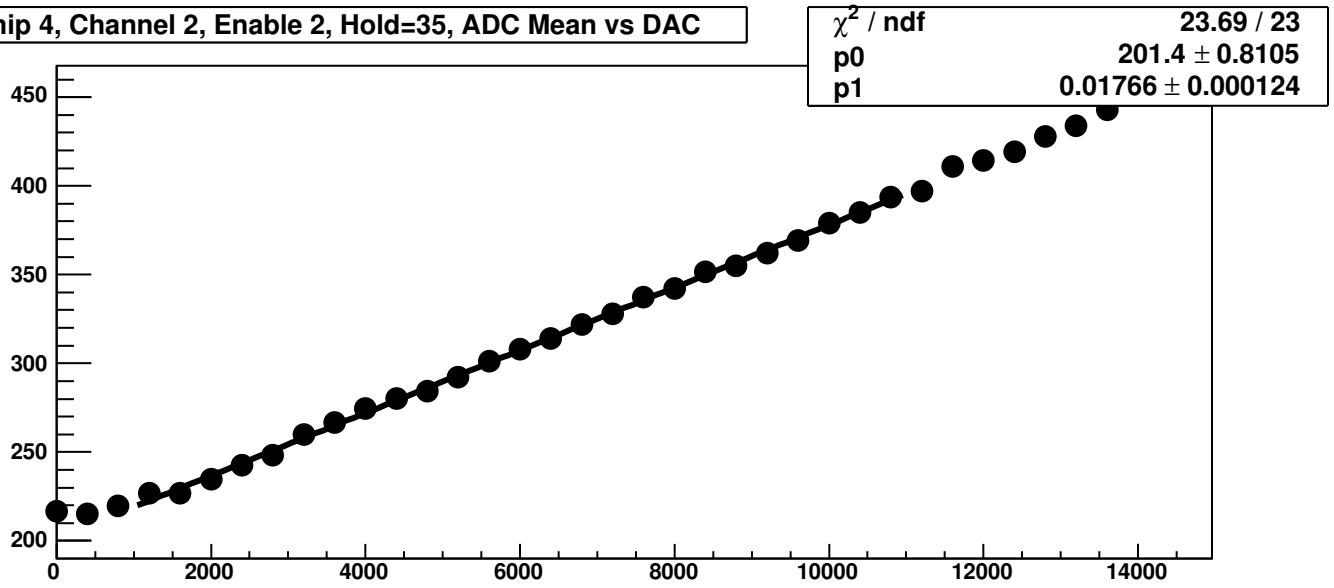
Chip 4, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



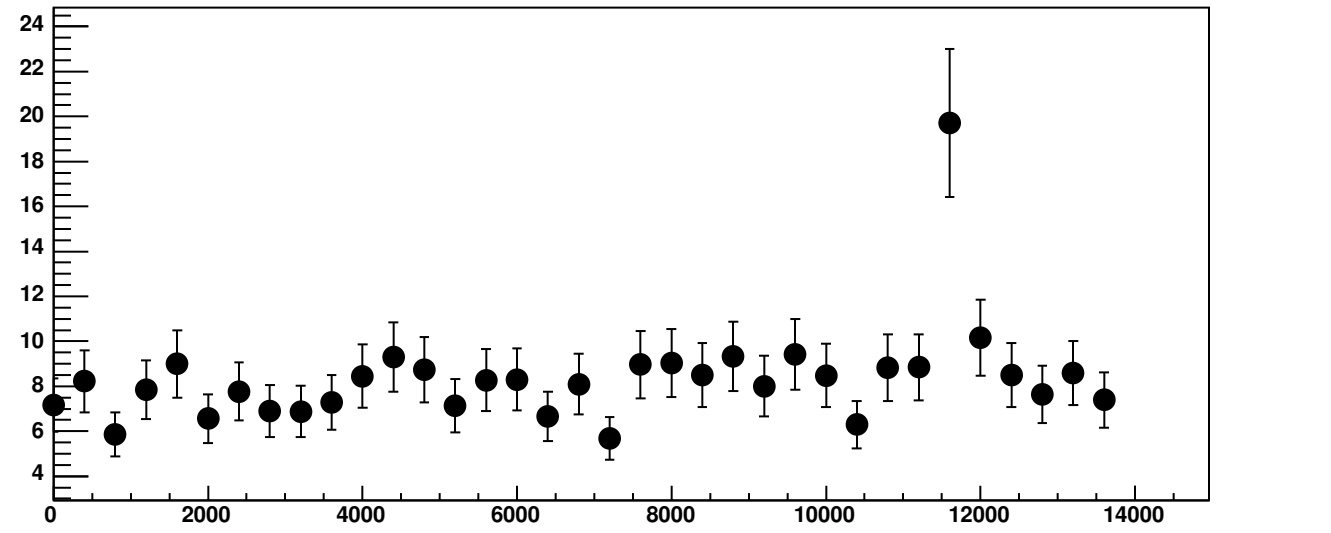
Chip 4, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



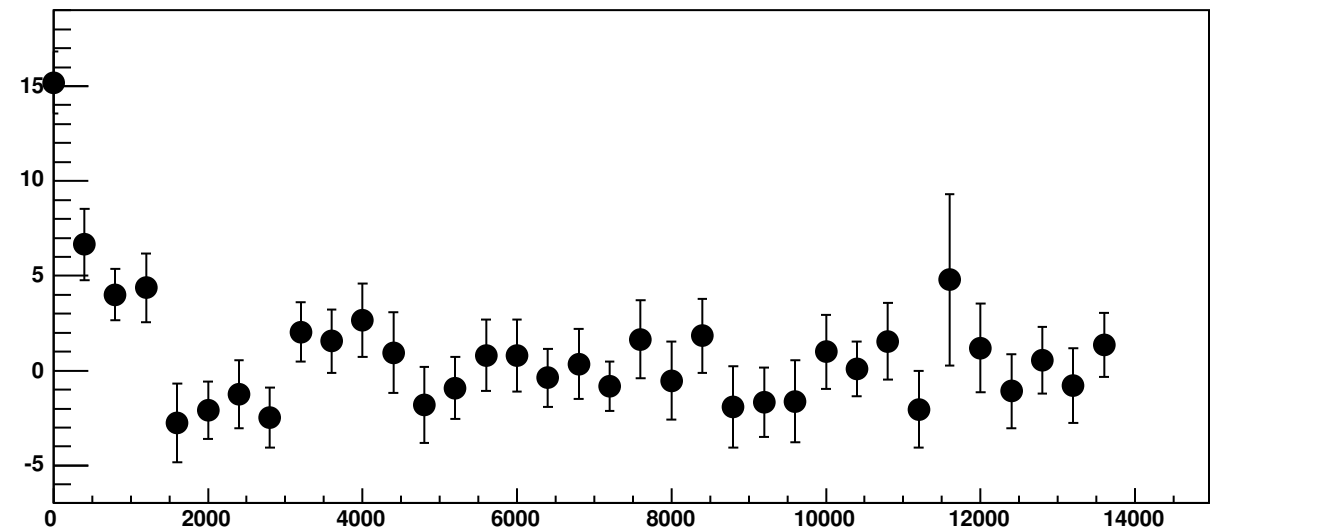
Chip 4, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



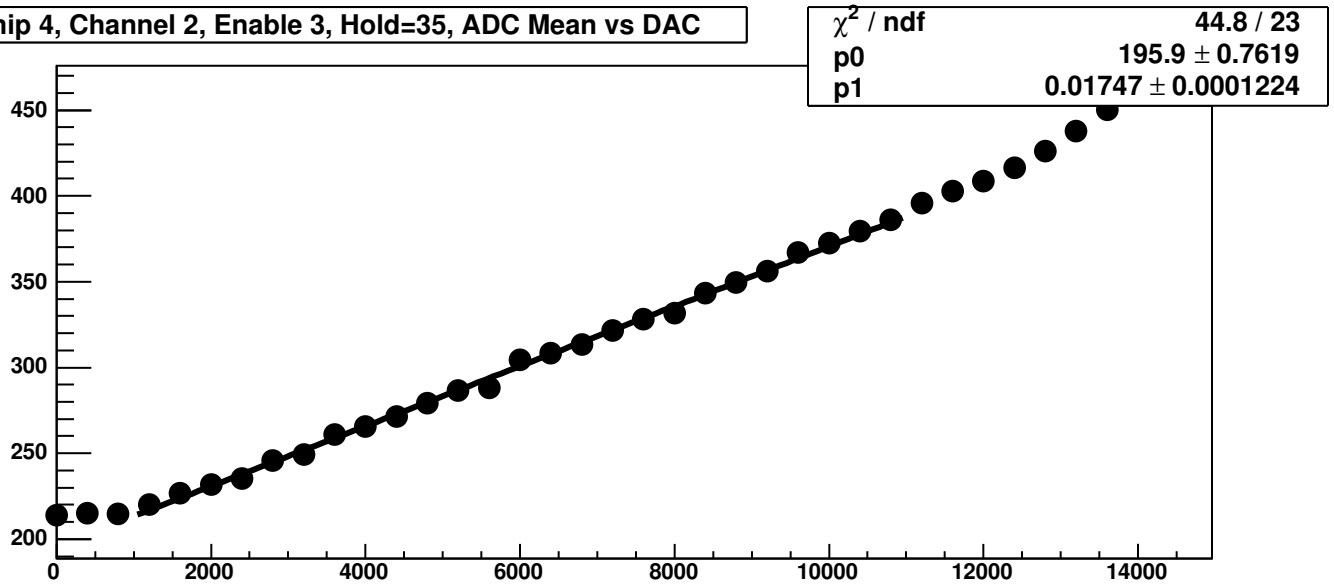
Chip 4, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



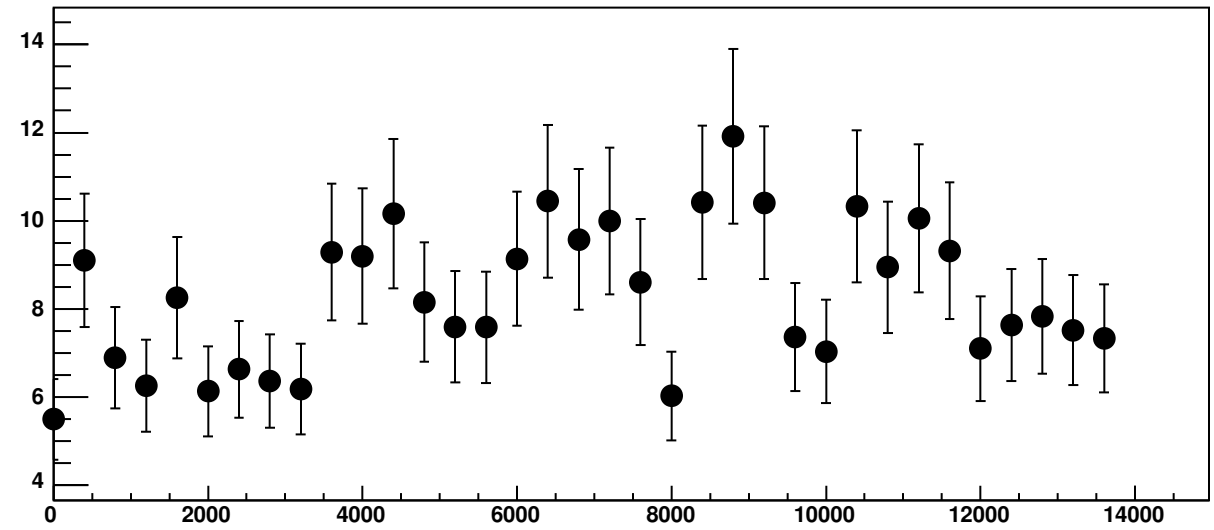
Chip 4, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



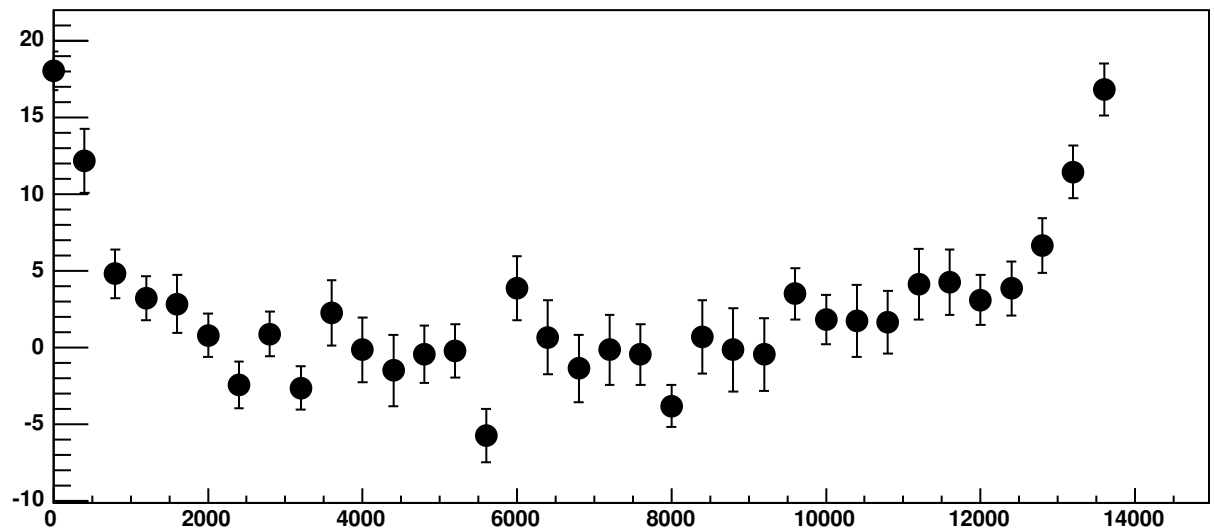
Chip 4, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

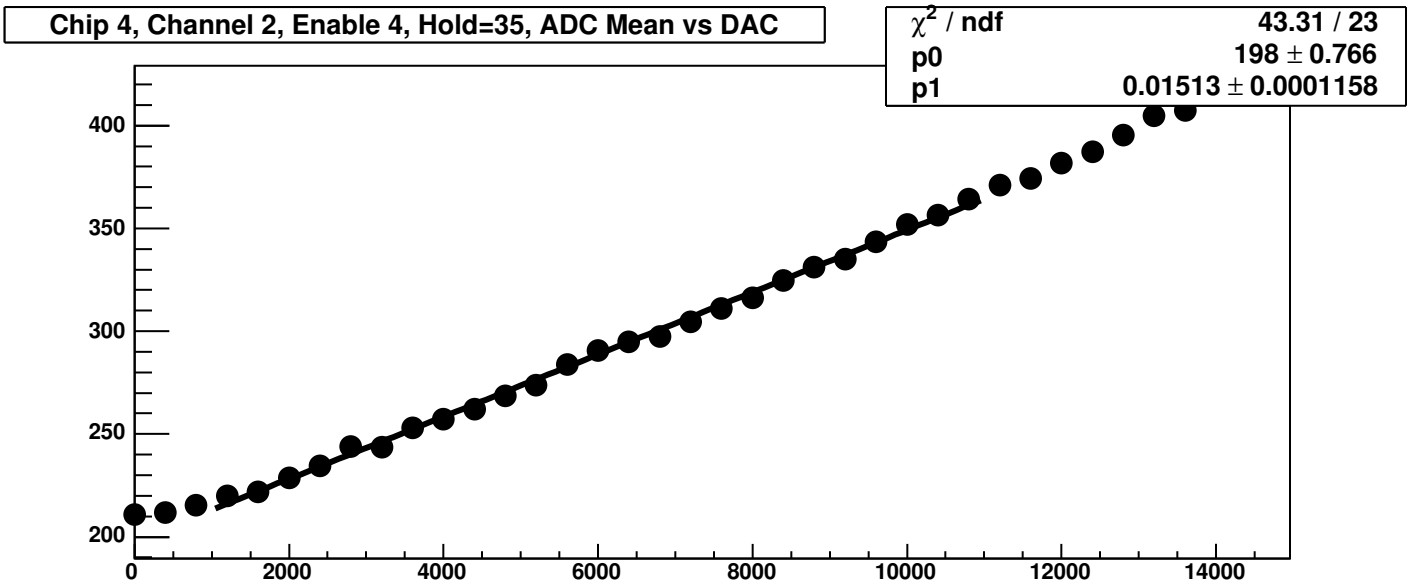


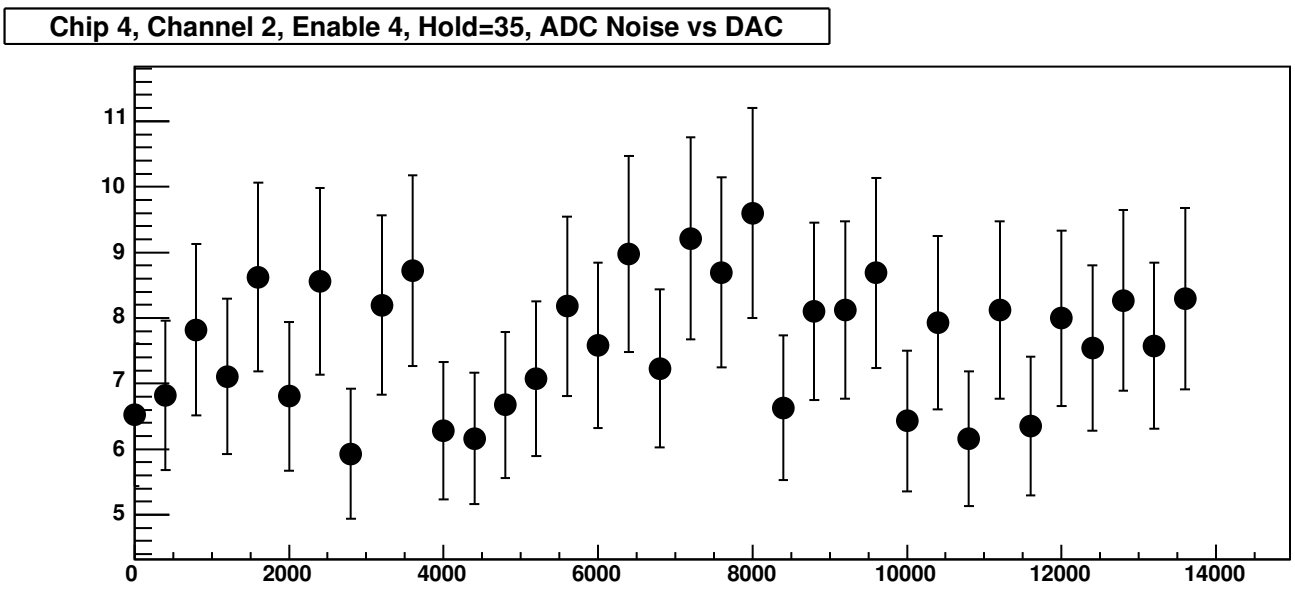
Chip 4, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC

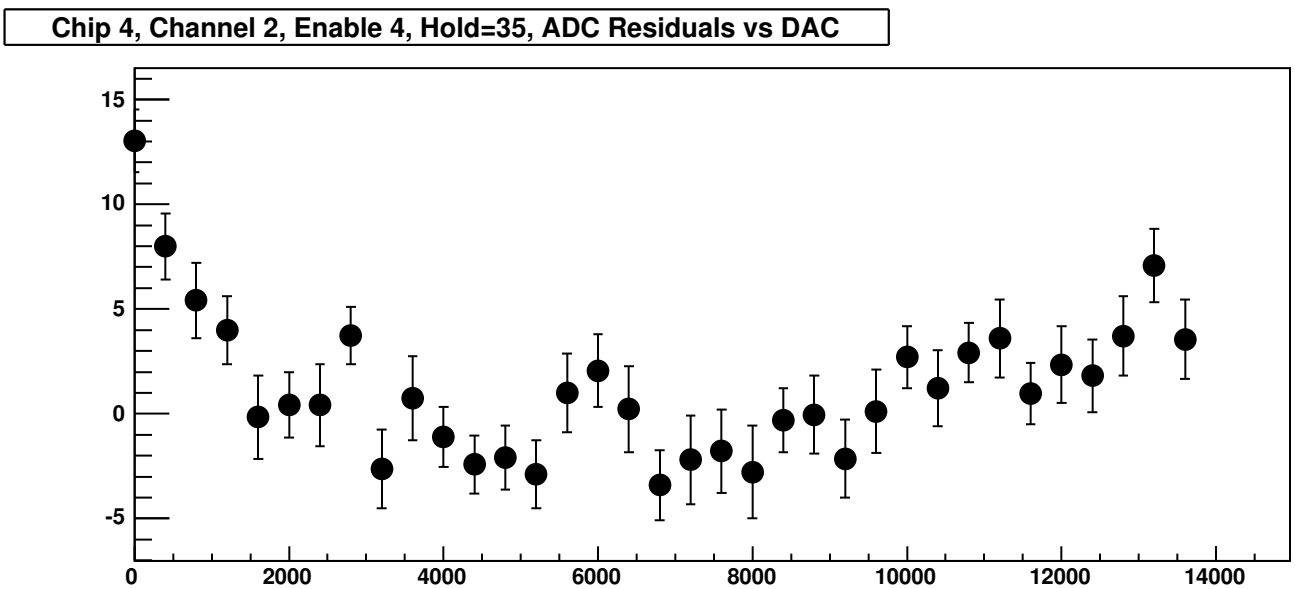


Chip 4, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

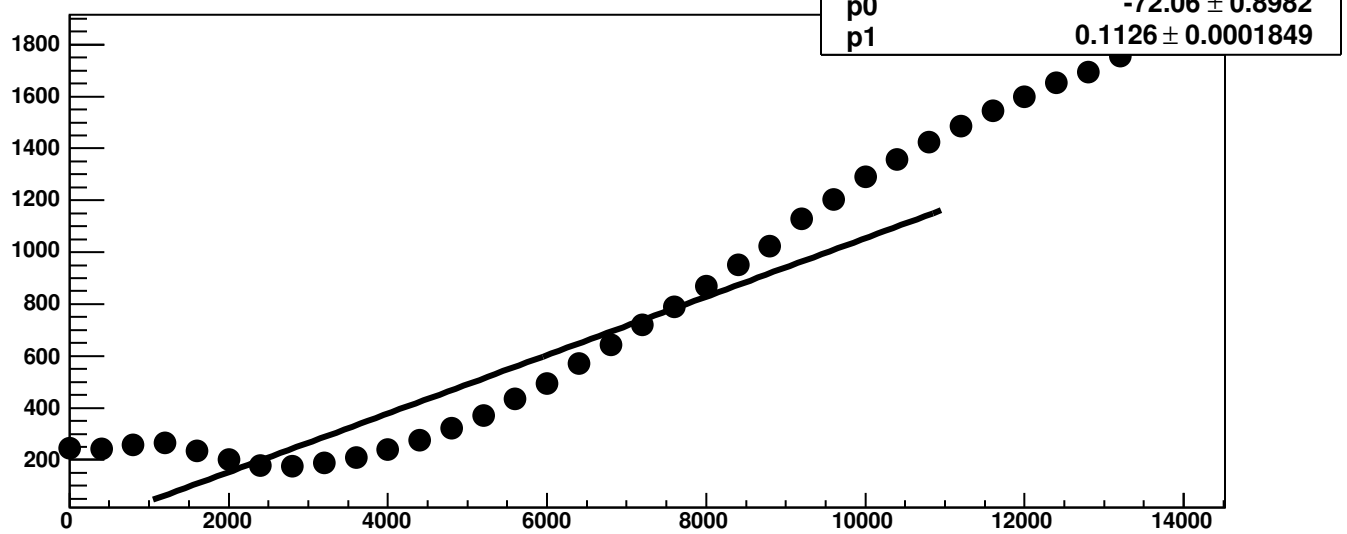



Chip 4, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 43.31 / 23
p0 198 ± 0.766
p1 0.01513 ± 0.0001158
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

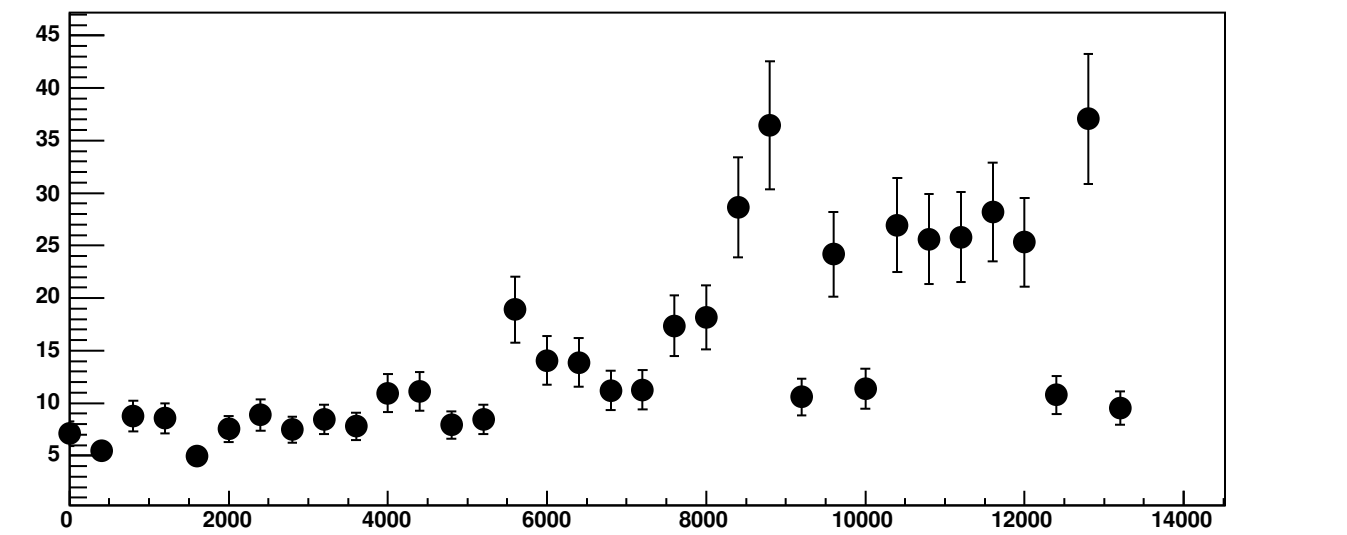

Chip 4, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

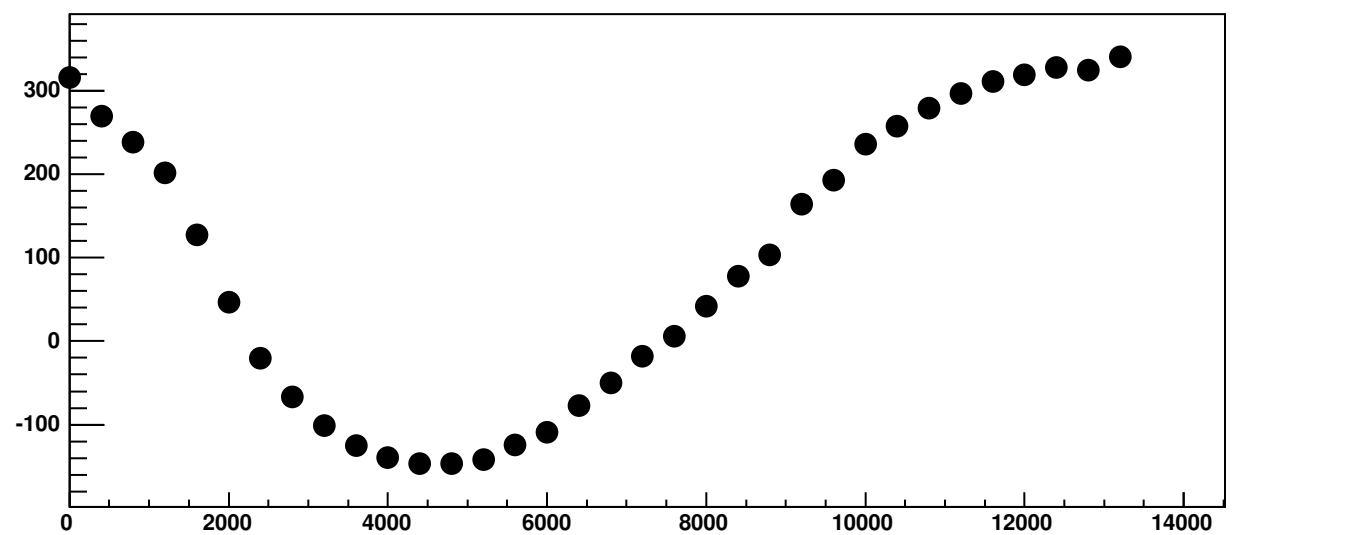
Chip 4, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC



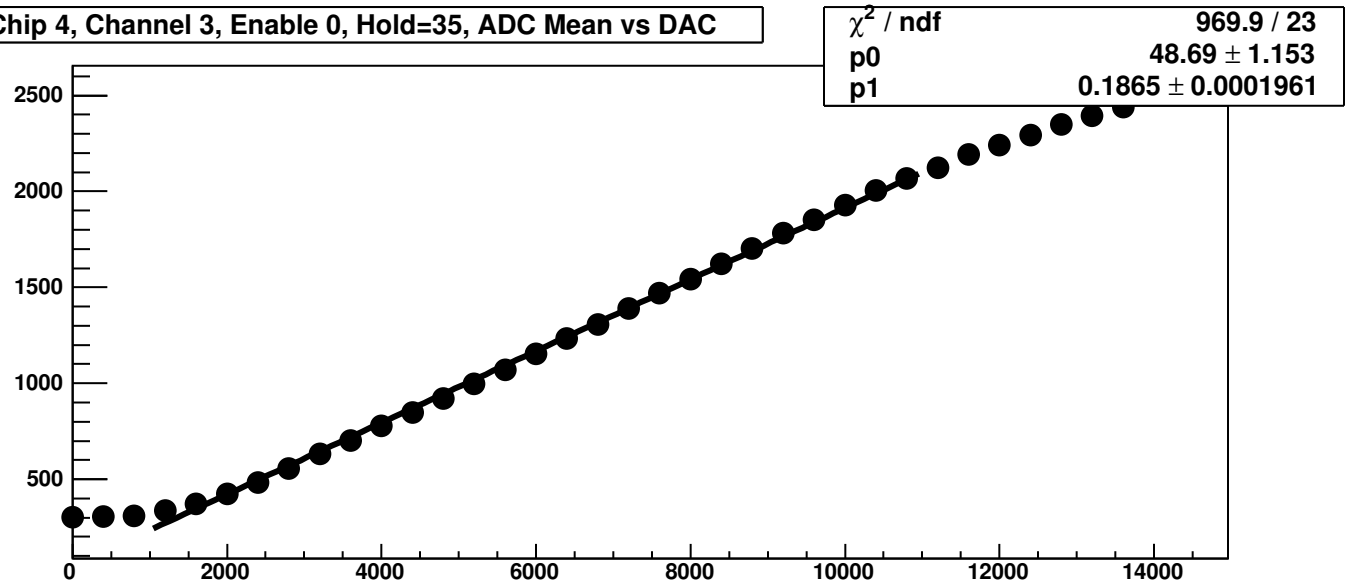
Chip 4, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC



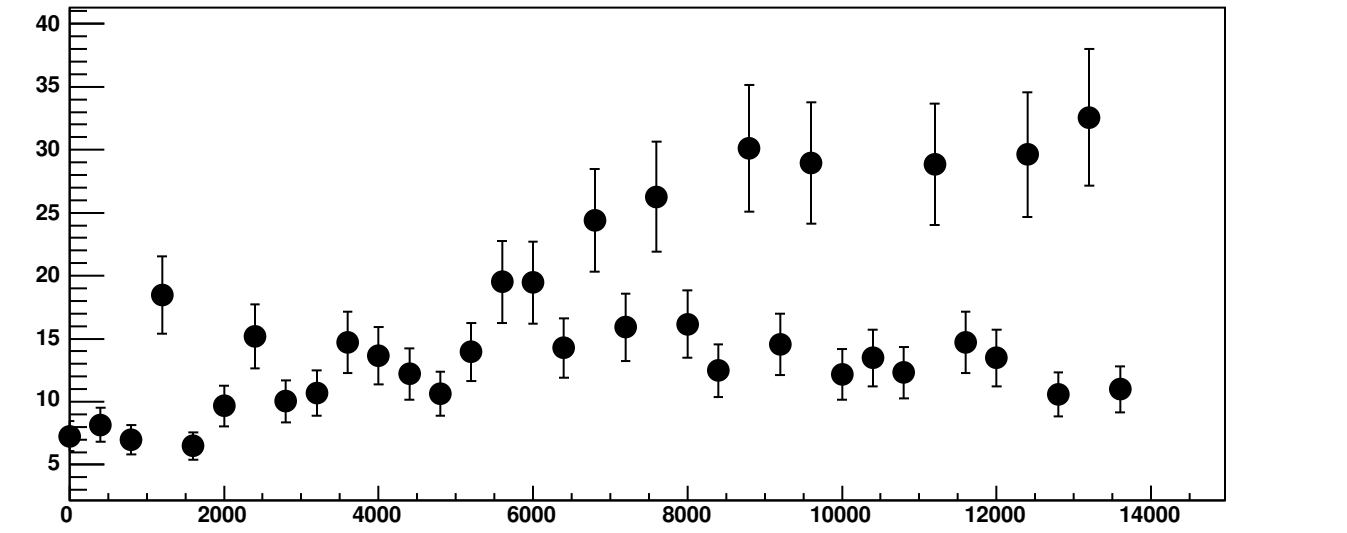
Chip 4, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC



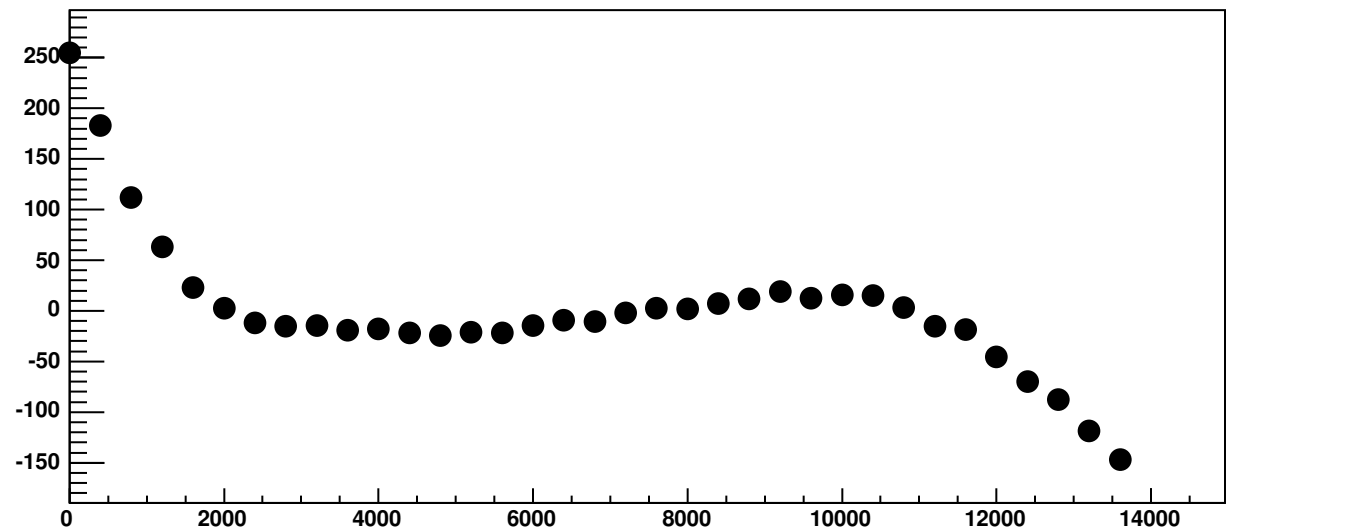
Chip 4, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



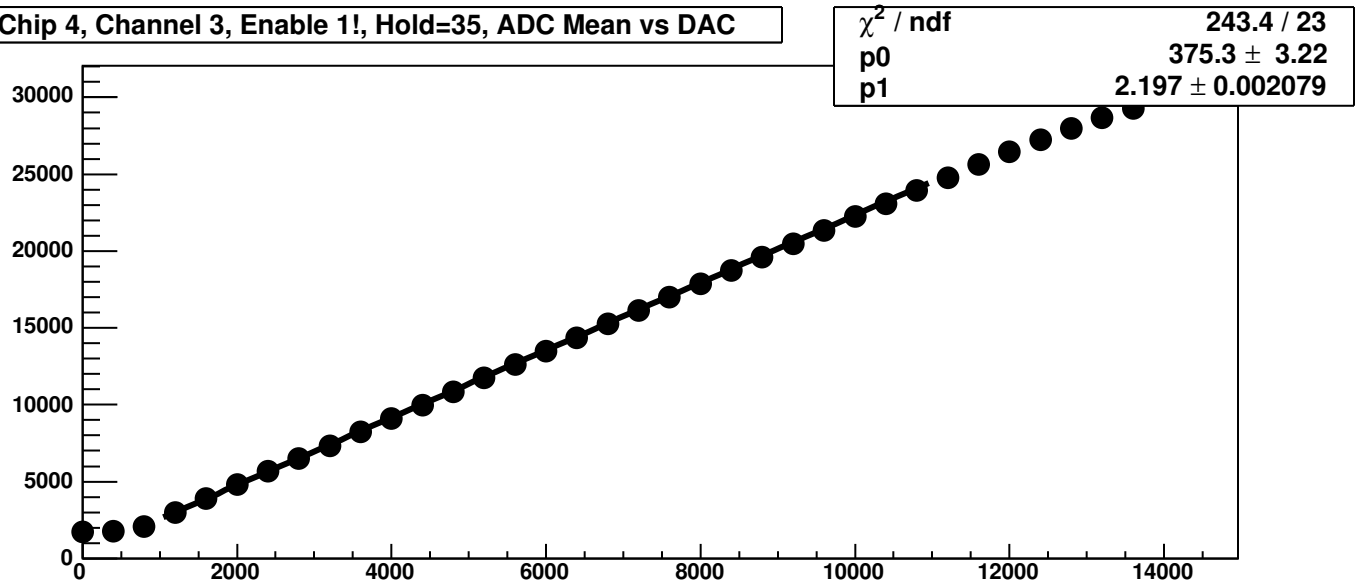
Chip 4, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



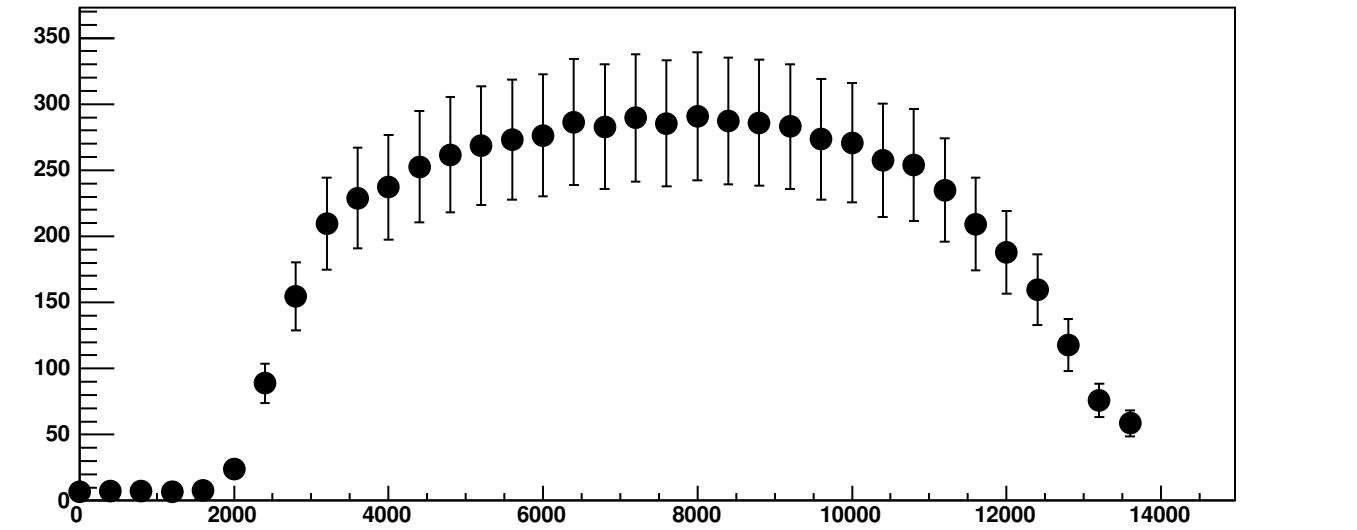
Chip 4, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



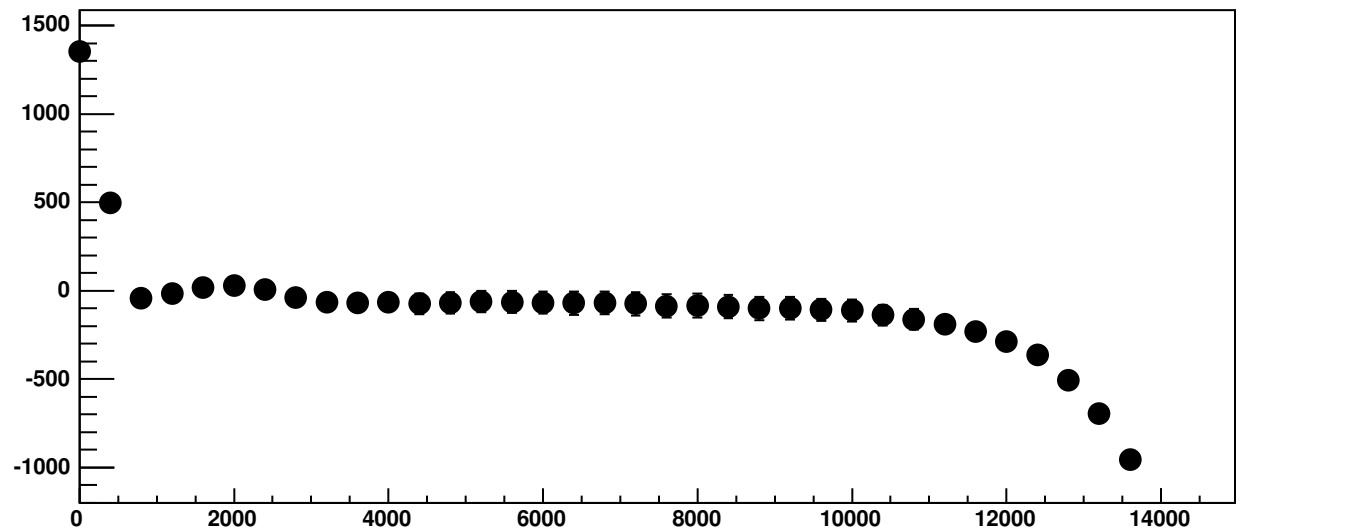
Chip 4, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC

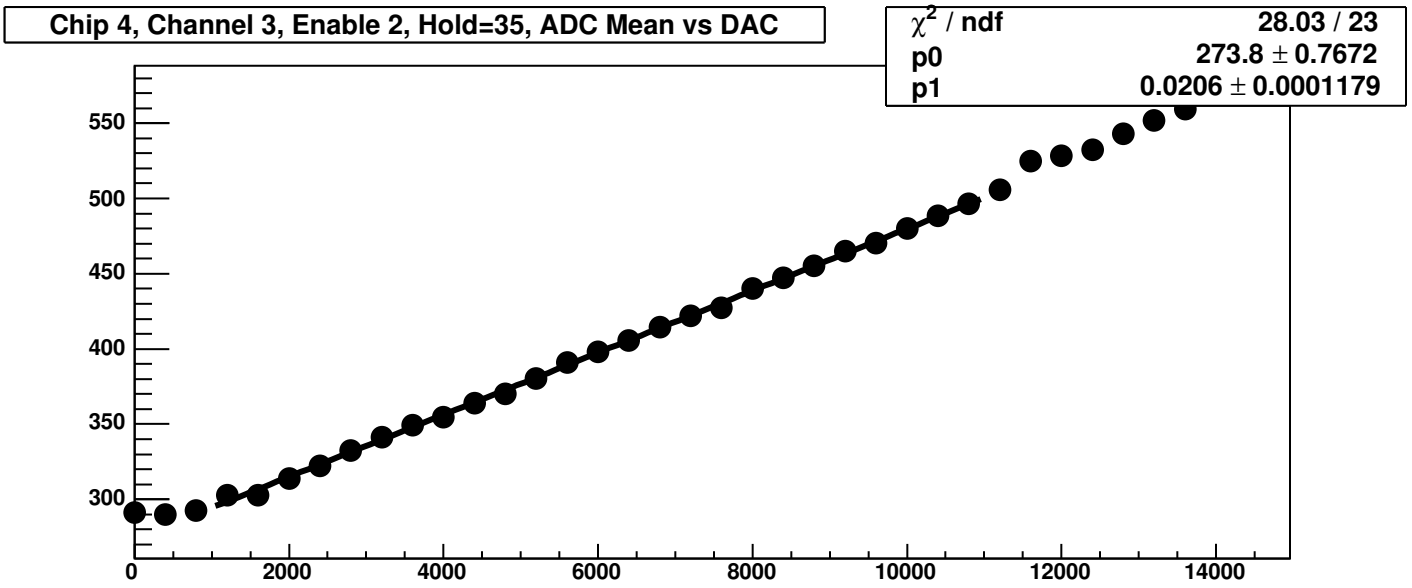


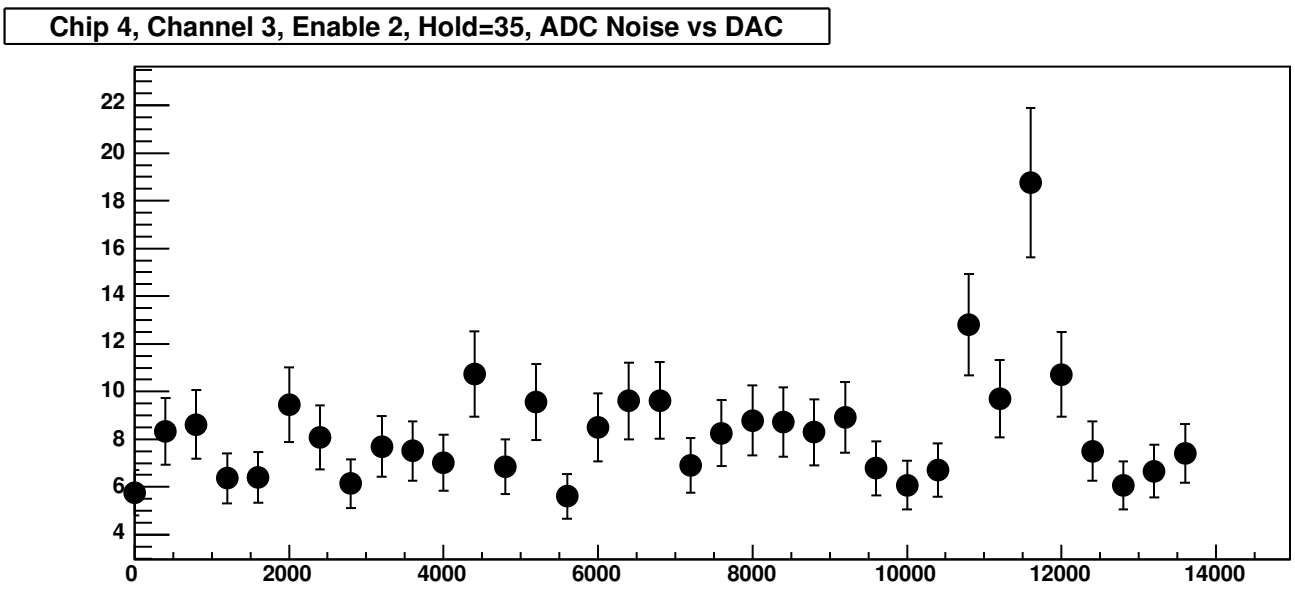
Chip 4, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC

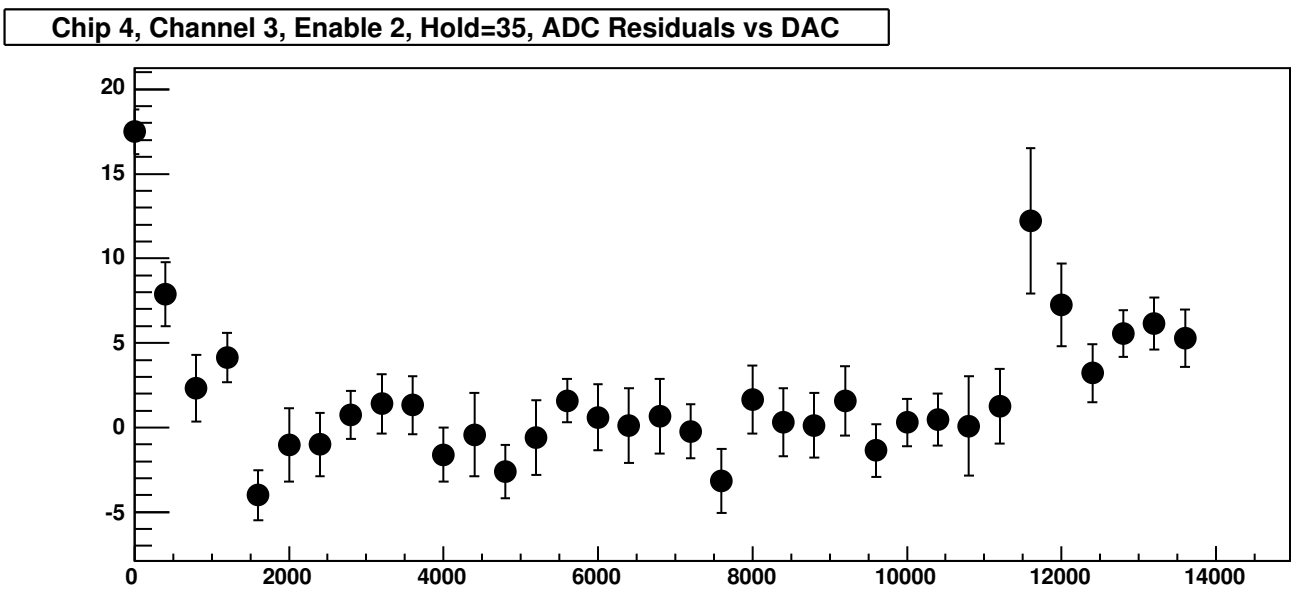


Chip 4, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC

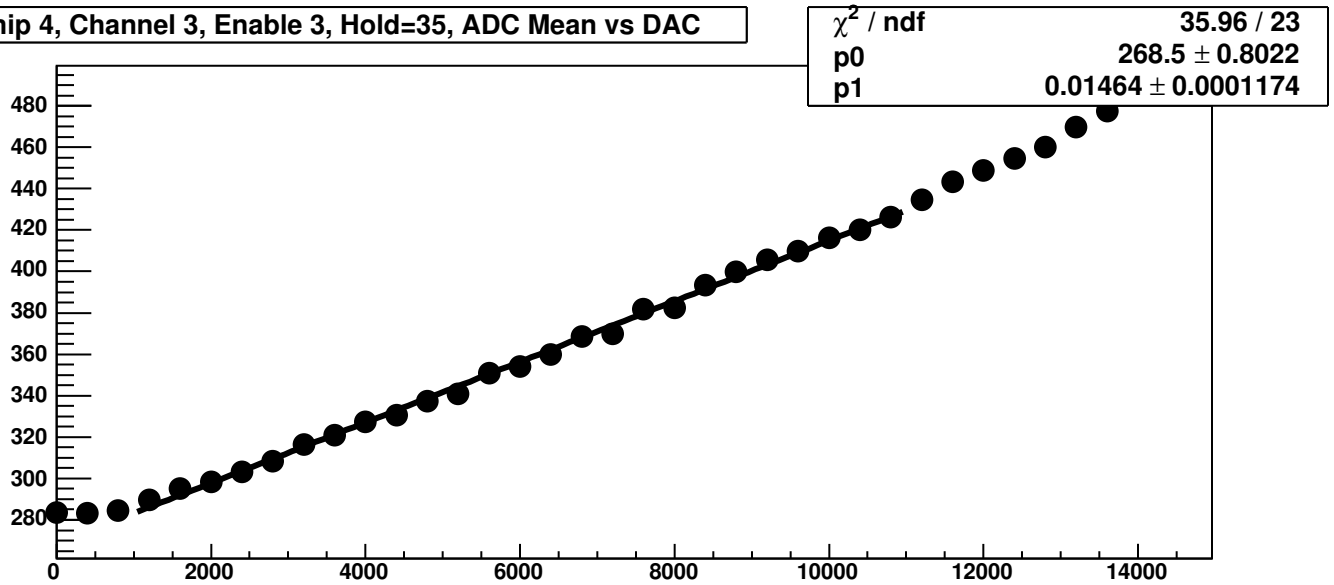



Chip 4, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 28.03 / 23
p0 273.8 ± 0.7672
p1 0.0206 ± 0.0001179
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

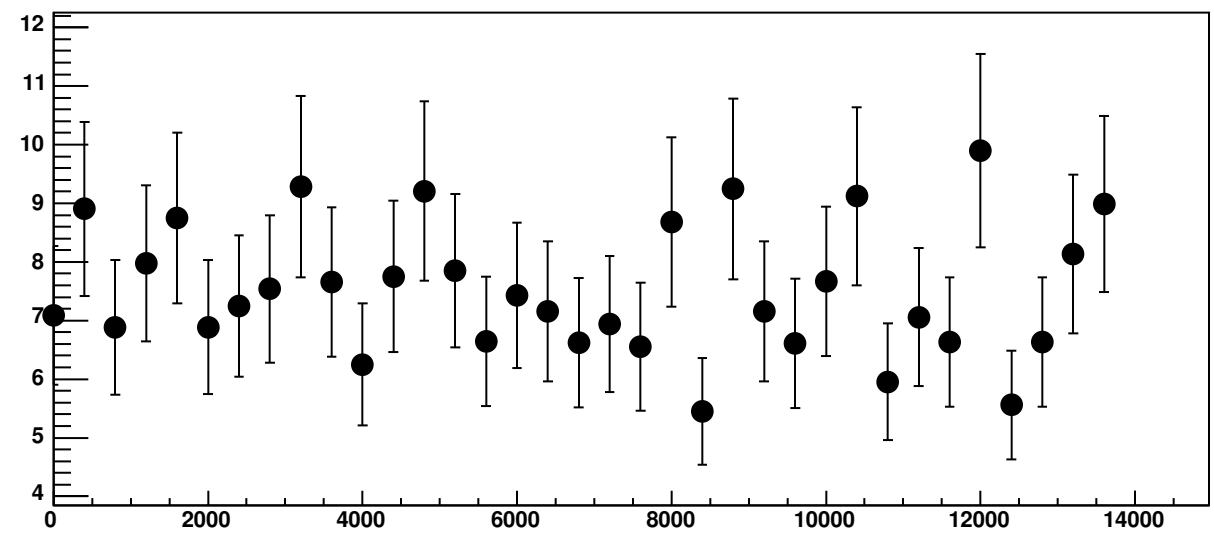

Chip 4, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

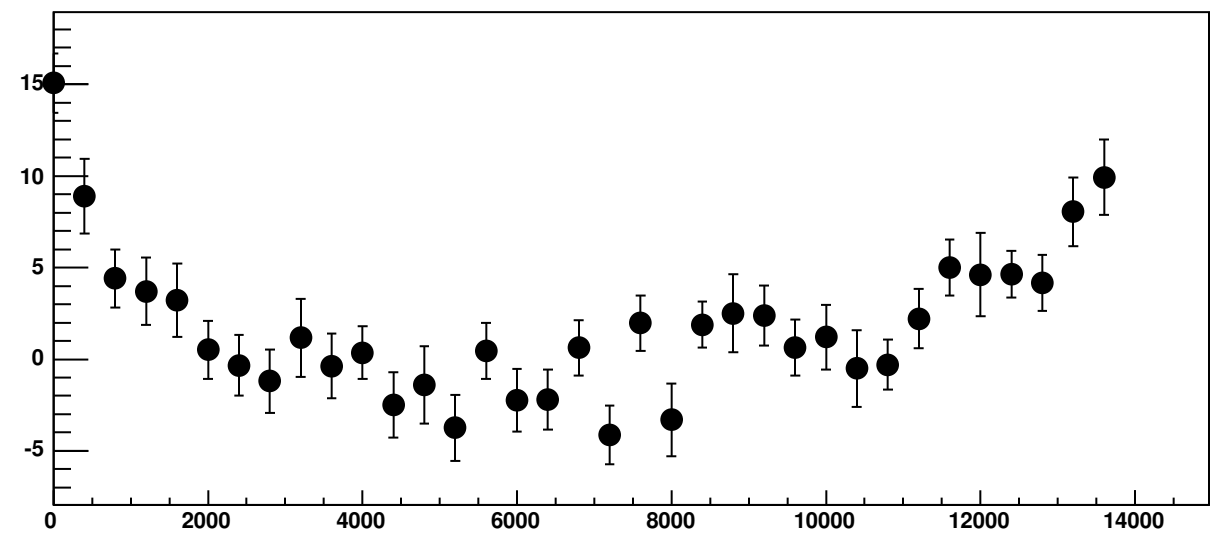
Chip 4, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC

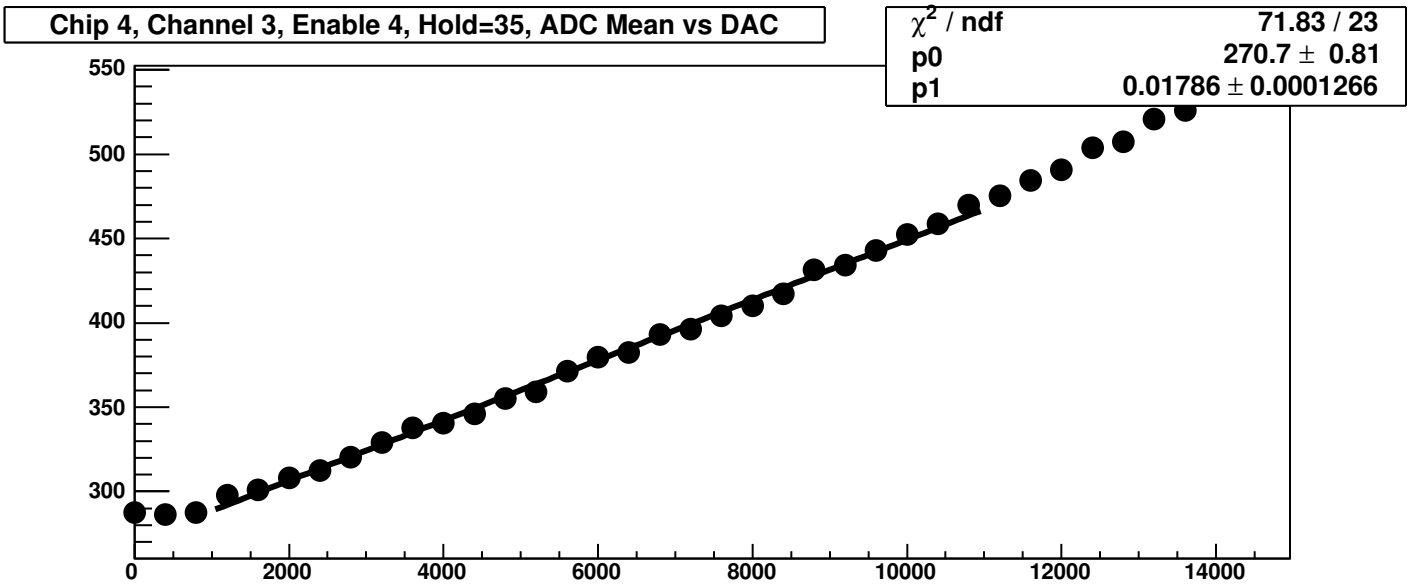


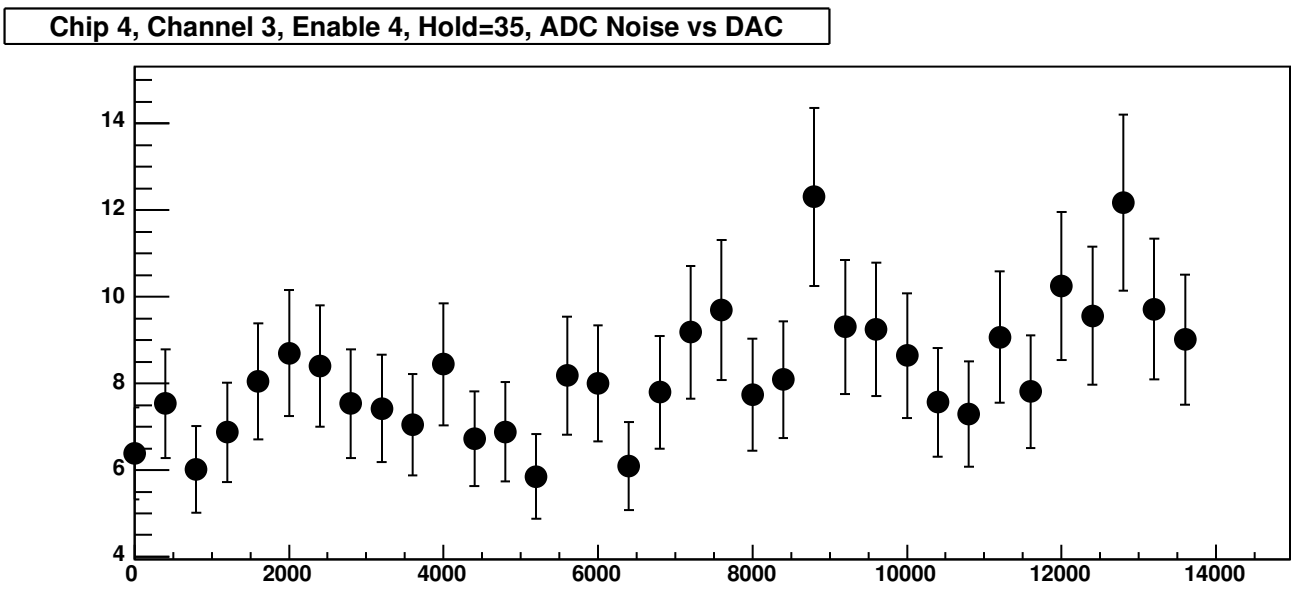
Chip 4, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC

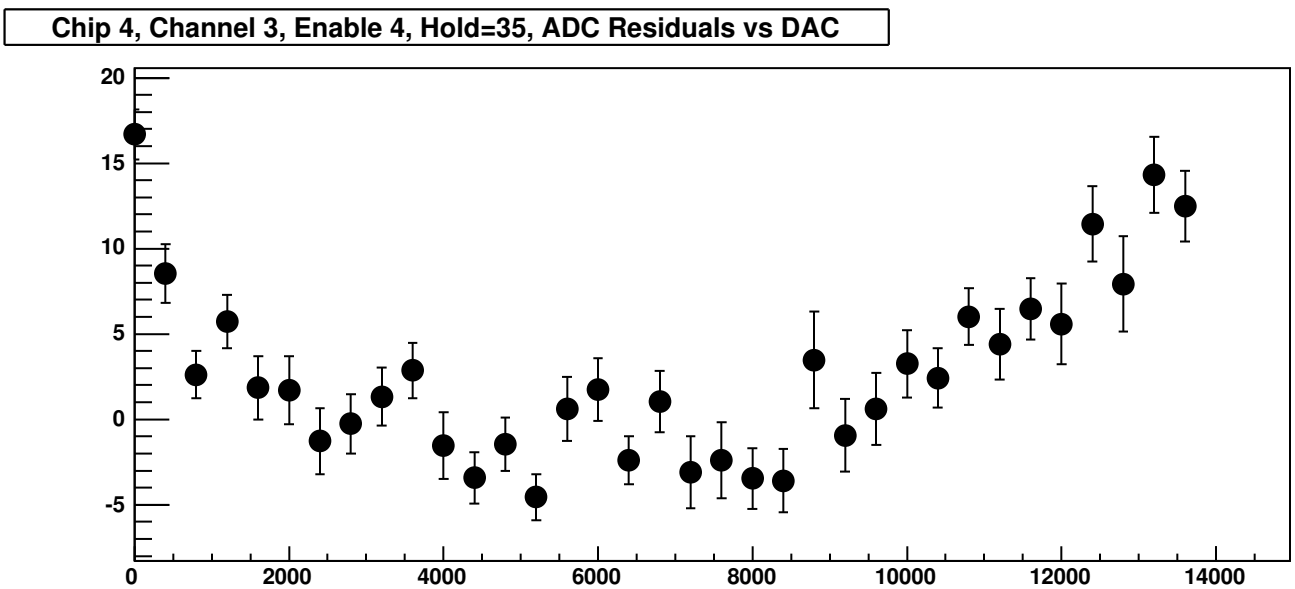


Chip 4, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC

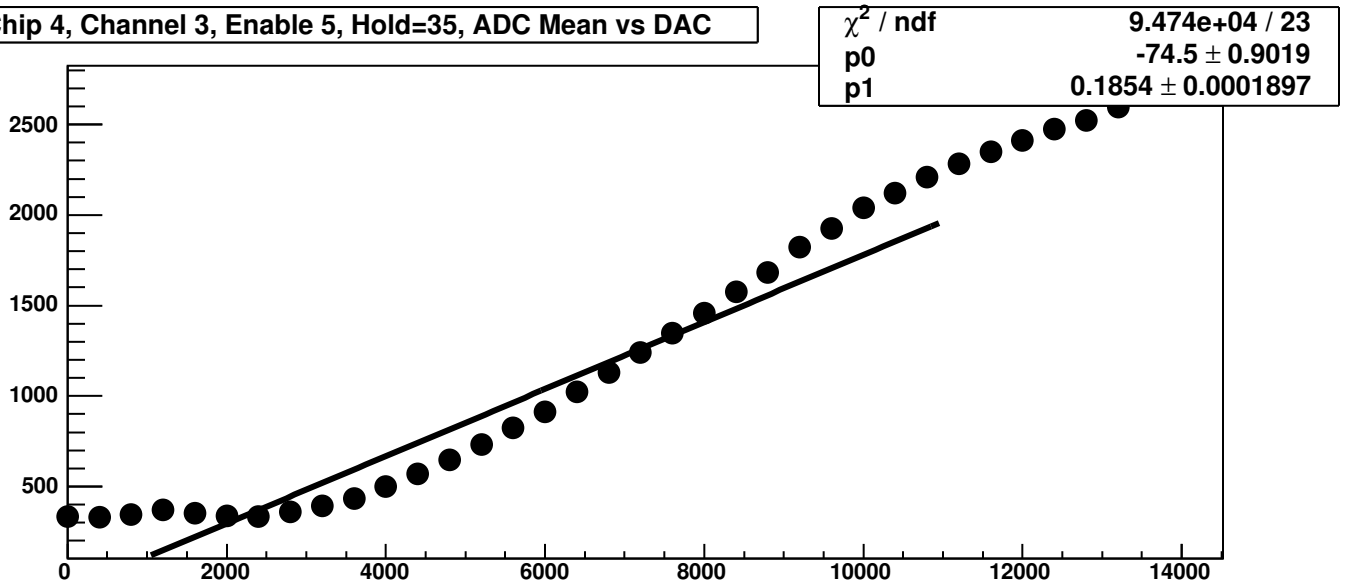



Chip 4, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 71.83 / 23
p0 270.7 ± 0.81
p1 0.01786 ± 0.0001266
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

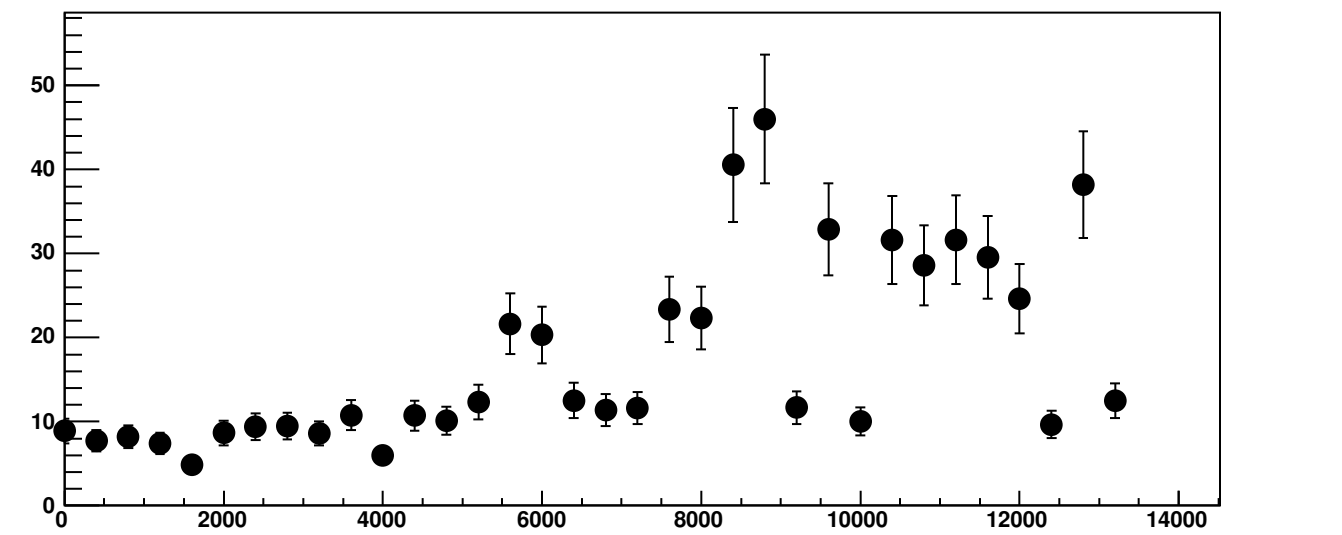

Chip 4, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

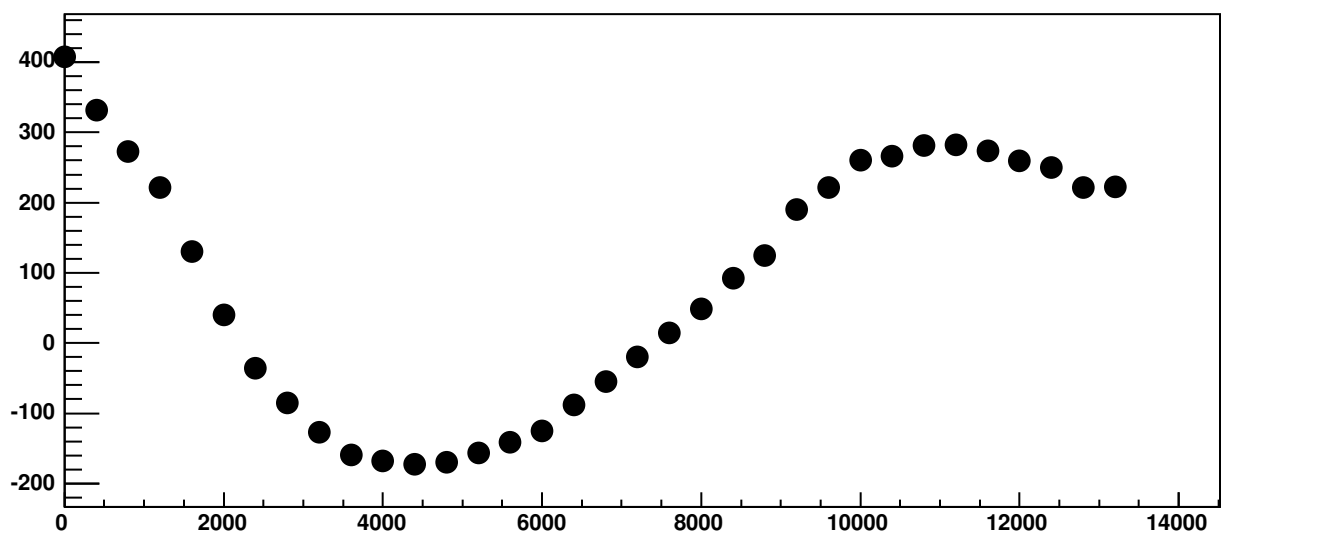
Chip 4, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

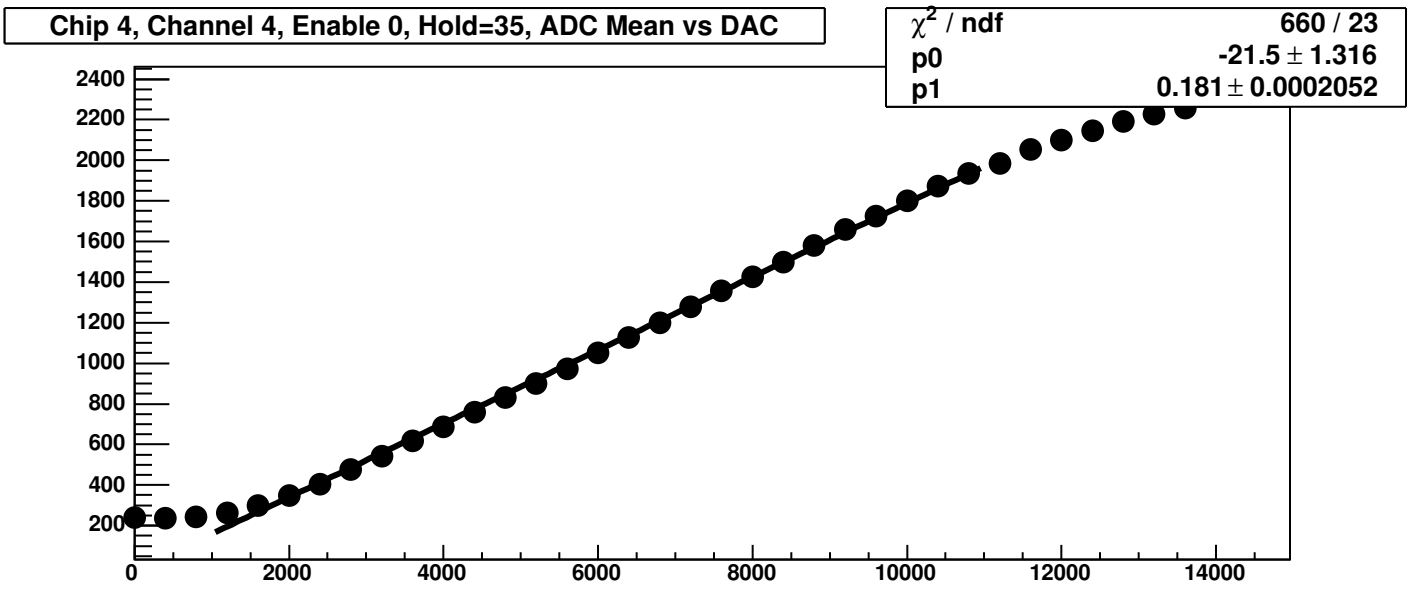


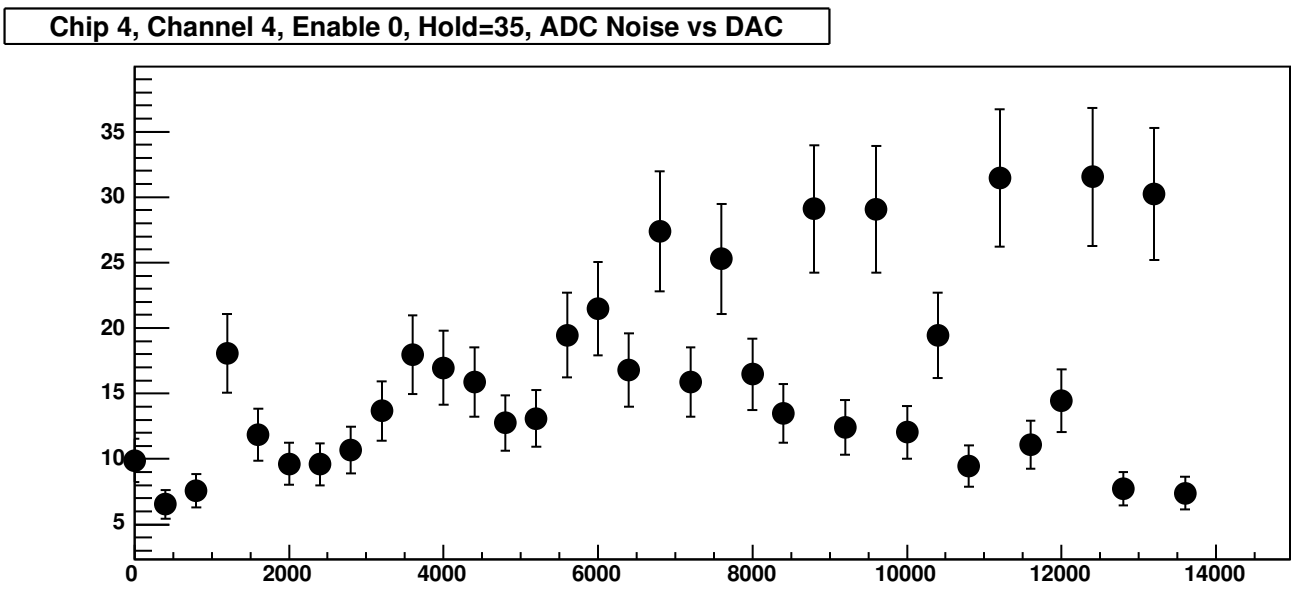
Chip 4, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC

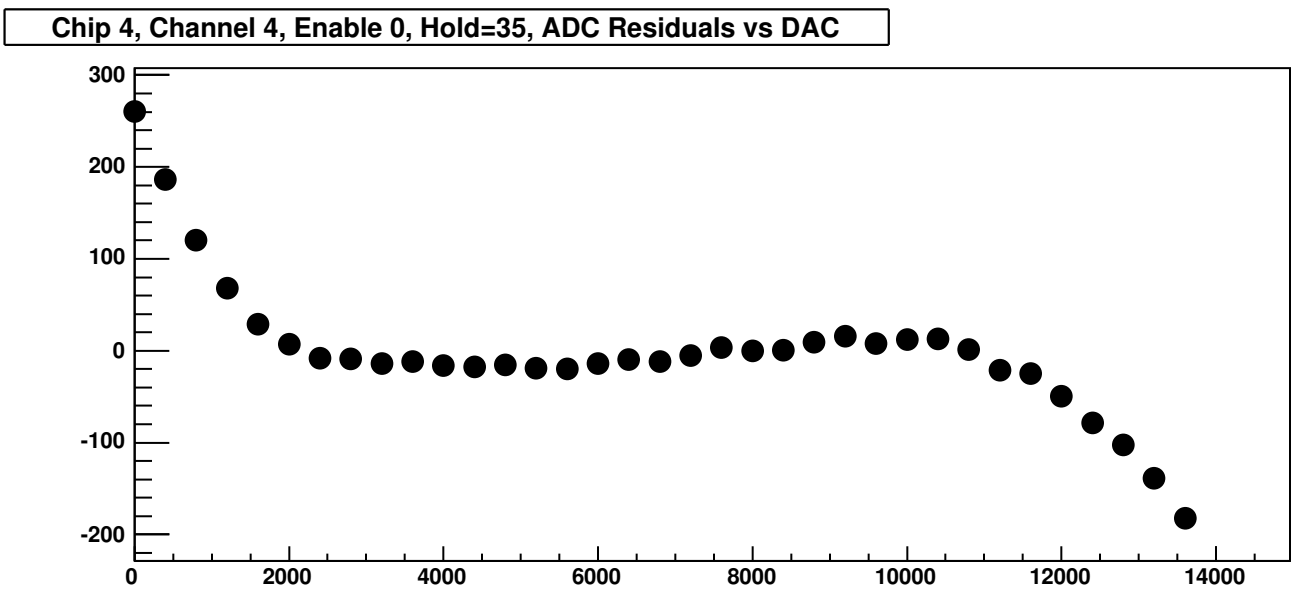


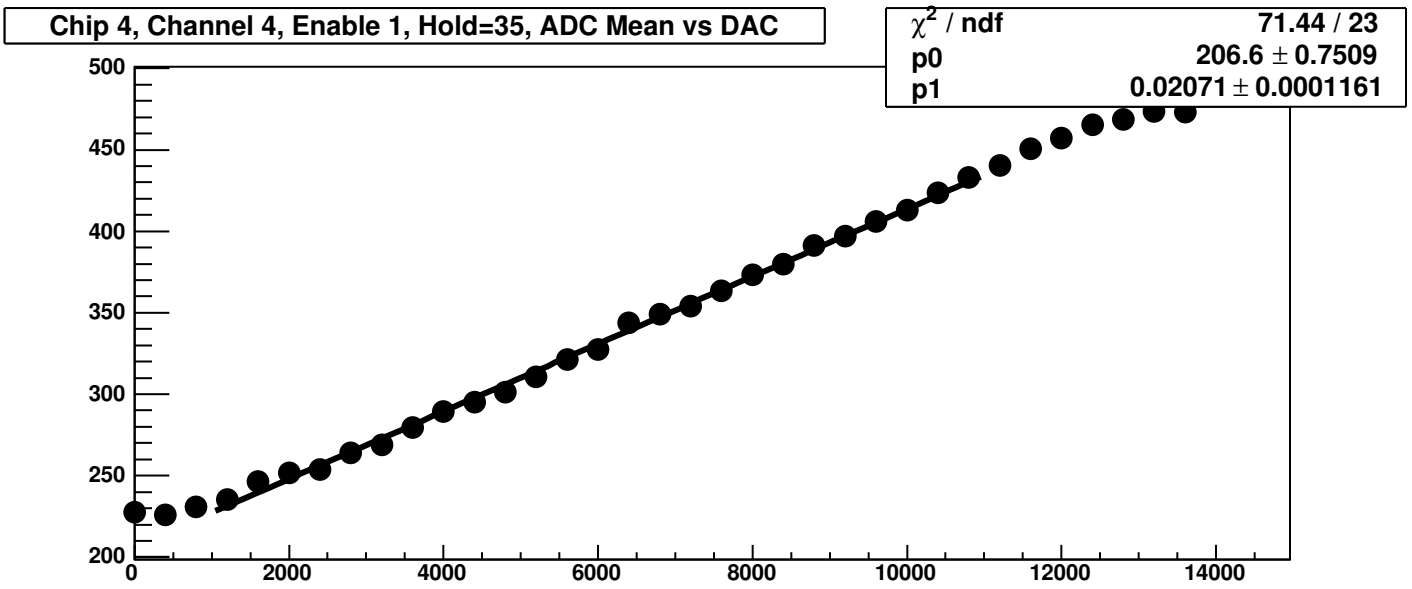
Chip 4, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

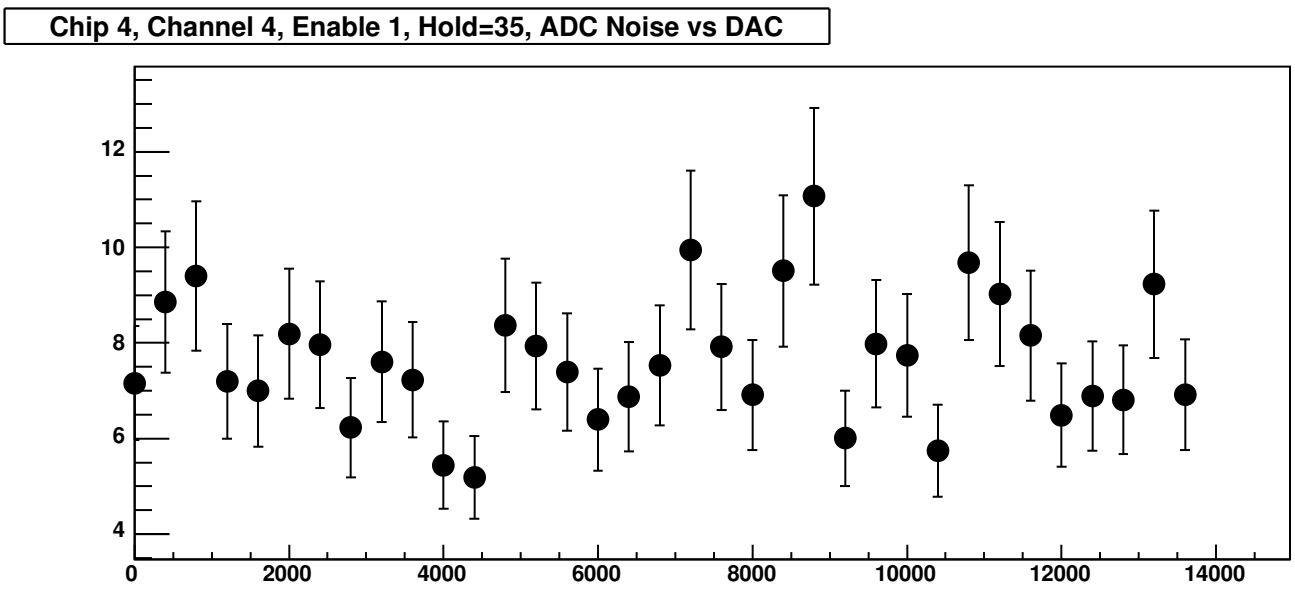


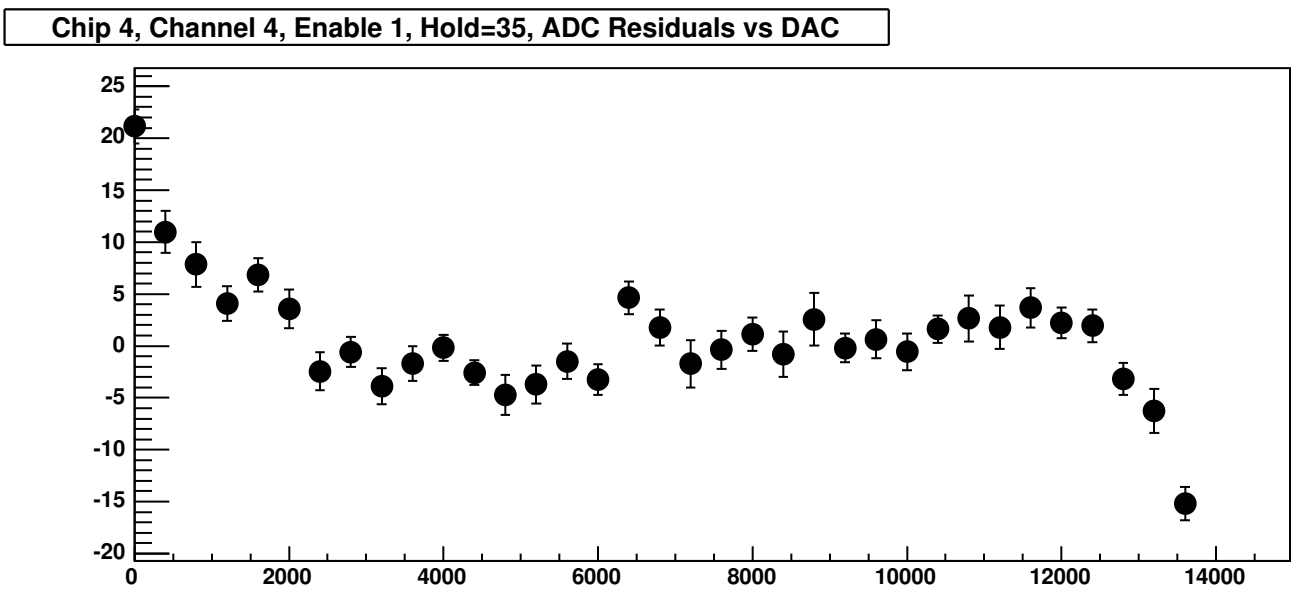

Chip 4, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 660 / 23
p0 -21.5 ± 1.316
p1 0.181 ± 0.0002052
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

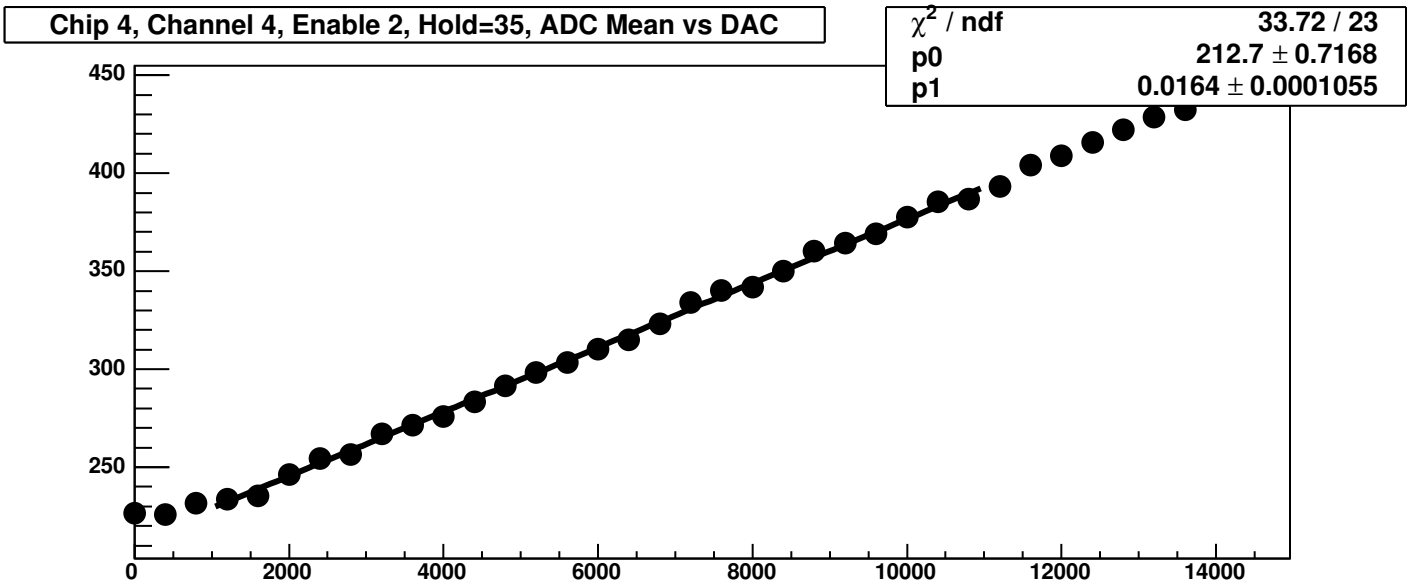

Chip 4, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

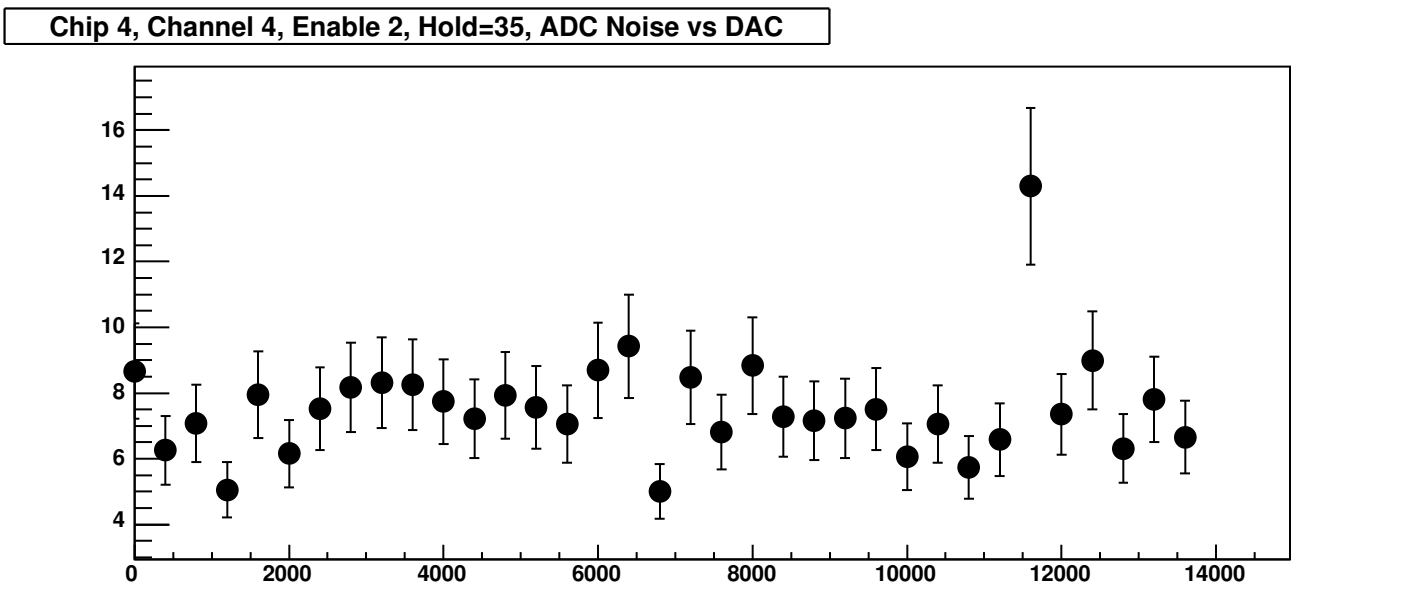

Chip 4, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

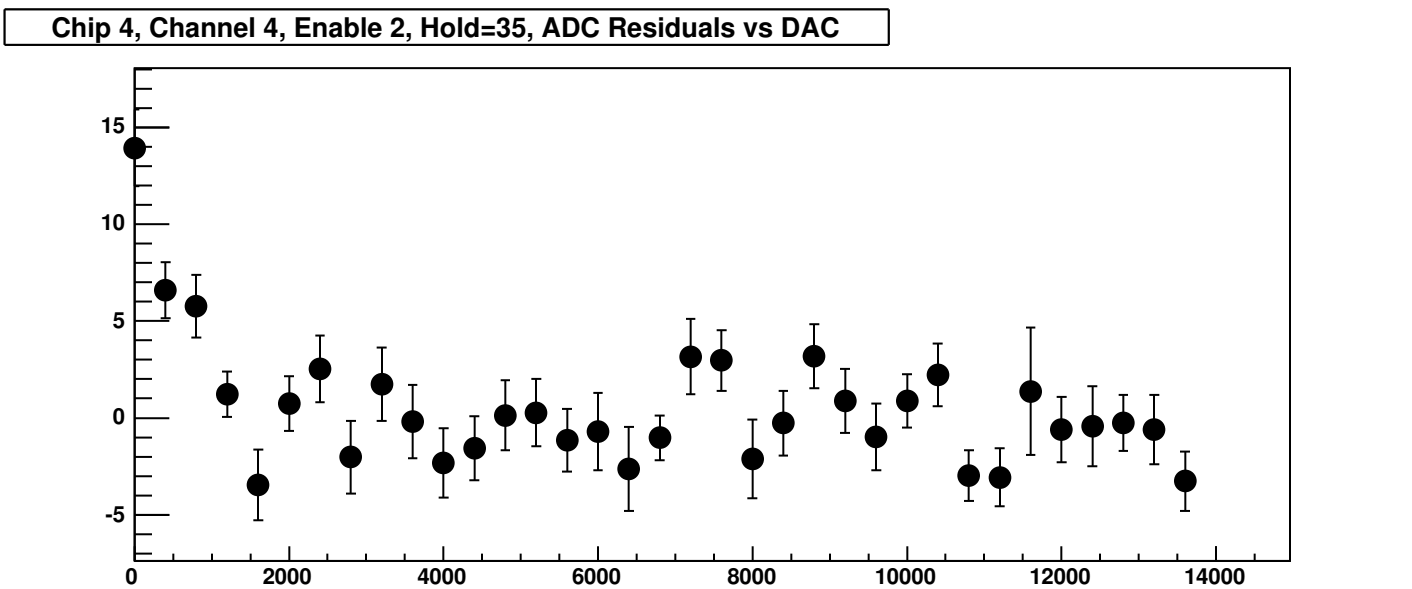

Chip 4, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 71.44 / 23
p0 206.6 ± 0.7509
p1 0.02071 ± 0.0001161
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

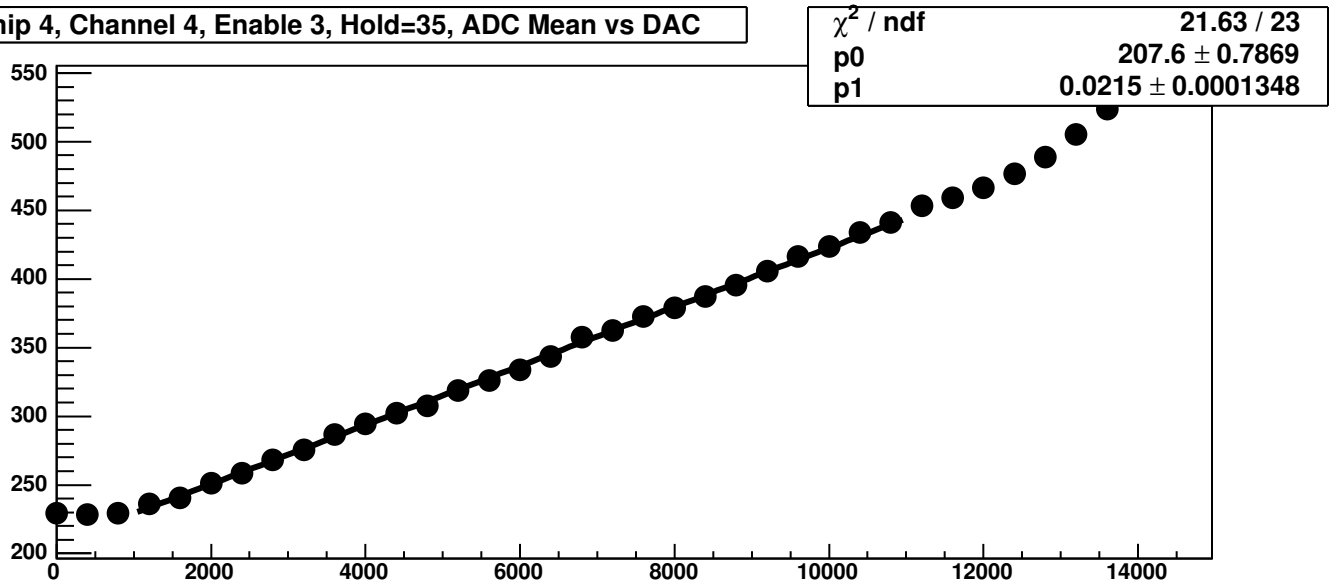

Chip 4, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 33.72 / 23
p0 212.7 ± 0.7168
p1 0.0164 ± 0.0001055
450
400
350
300
250
0
2000
4000
6000
8000
10000
12000
14000

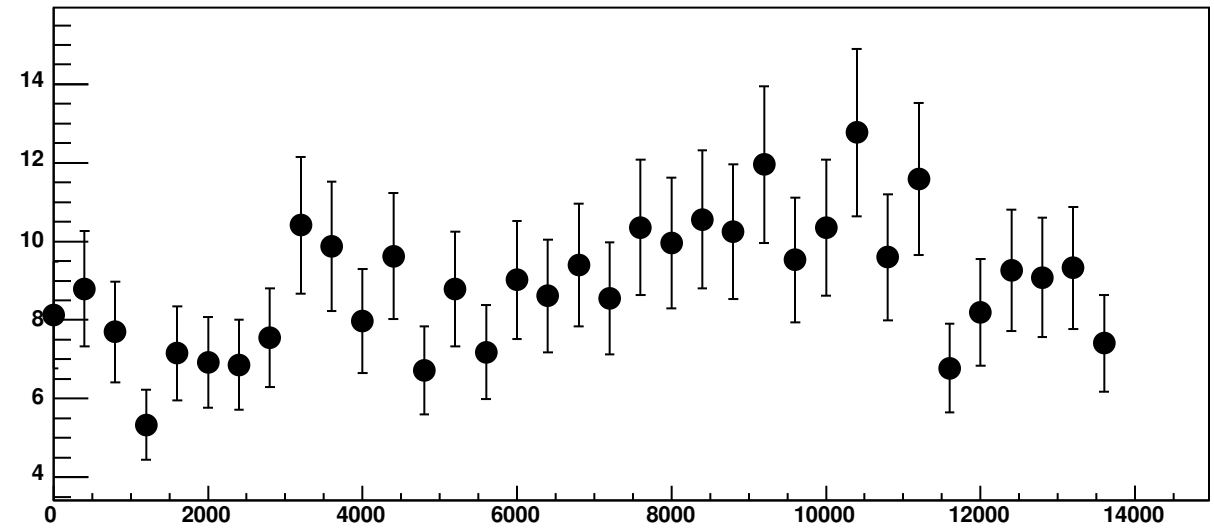

Chip 4, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

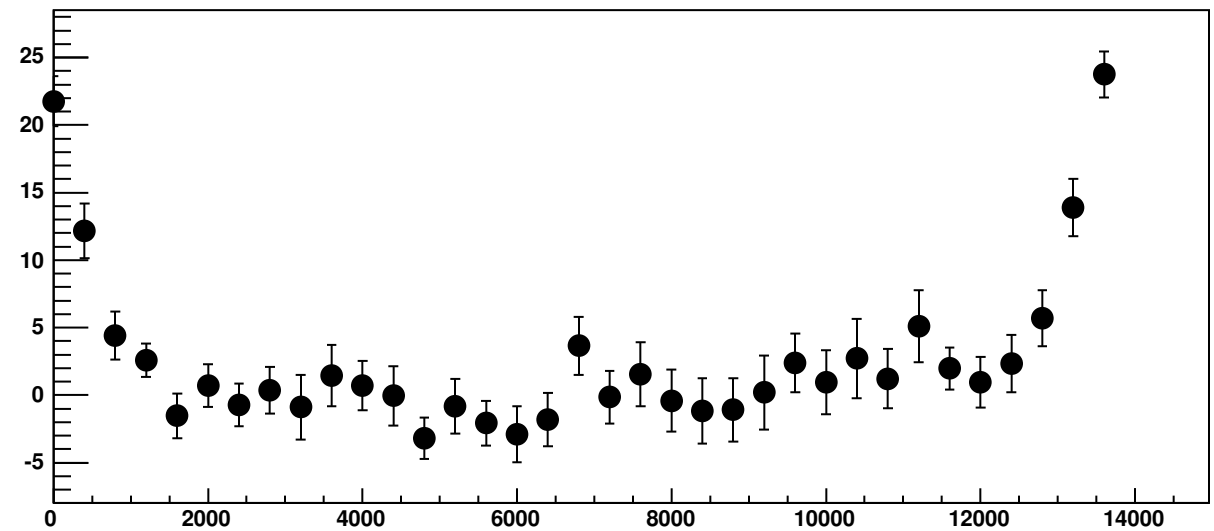
Chip 4, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

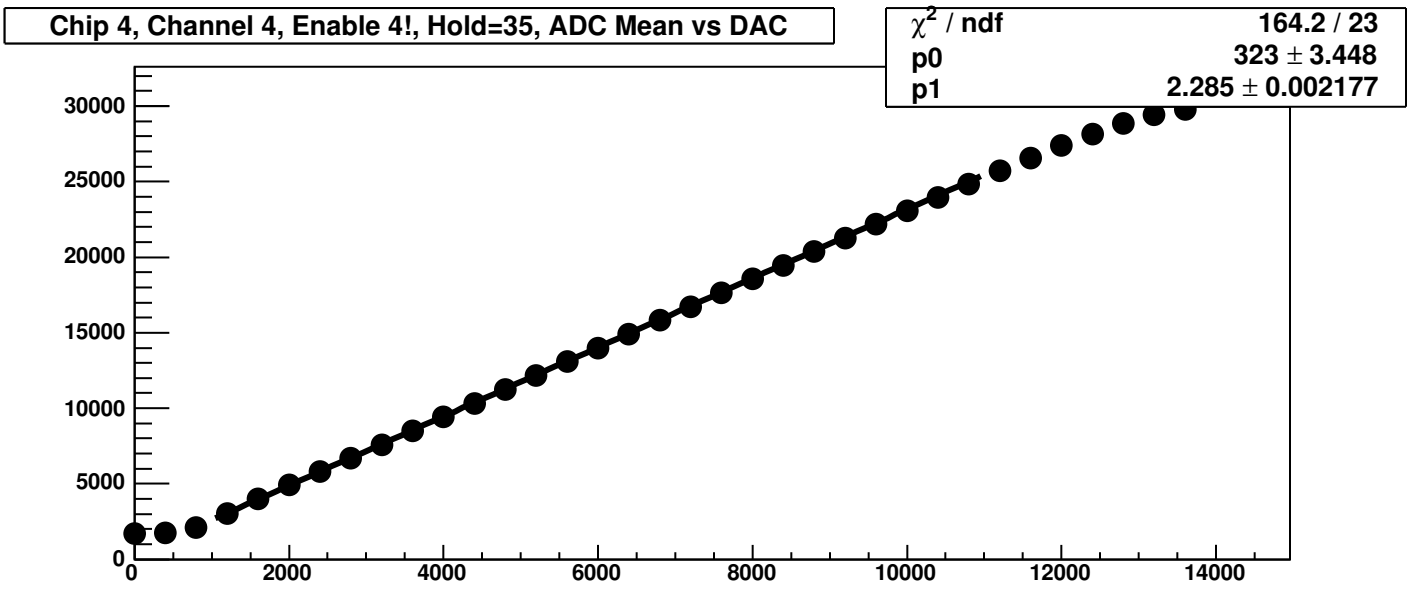


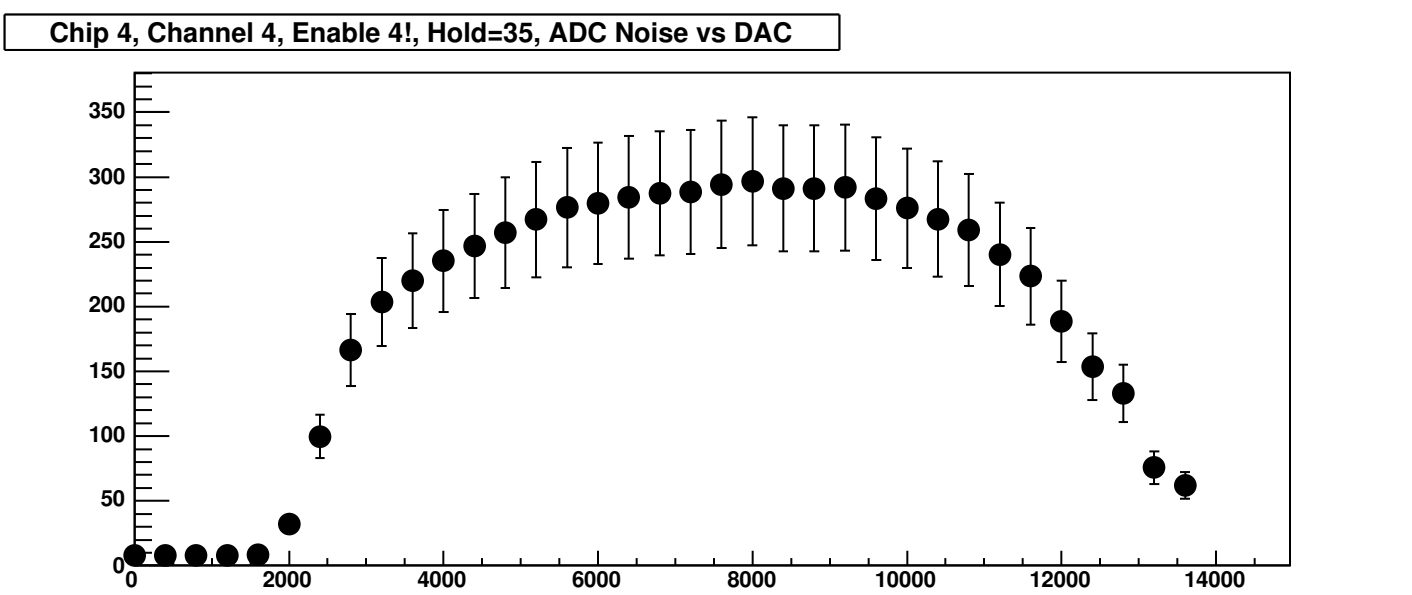
Chip 4, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC

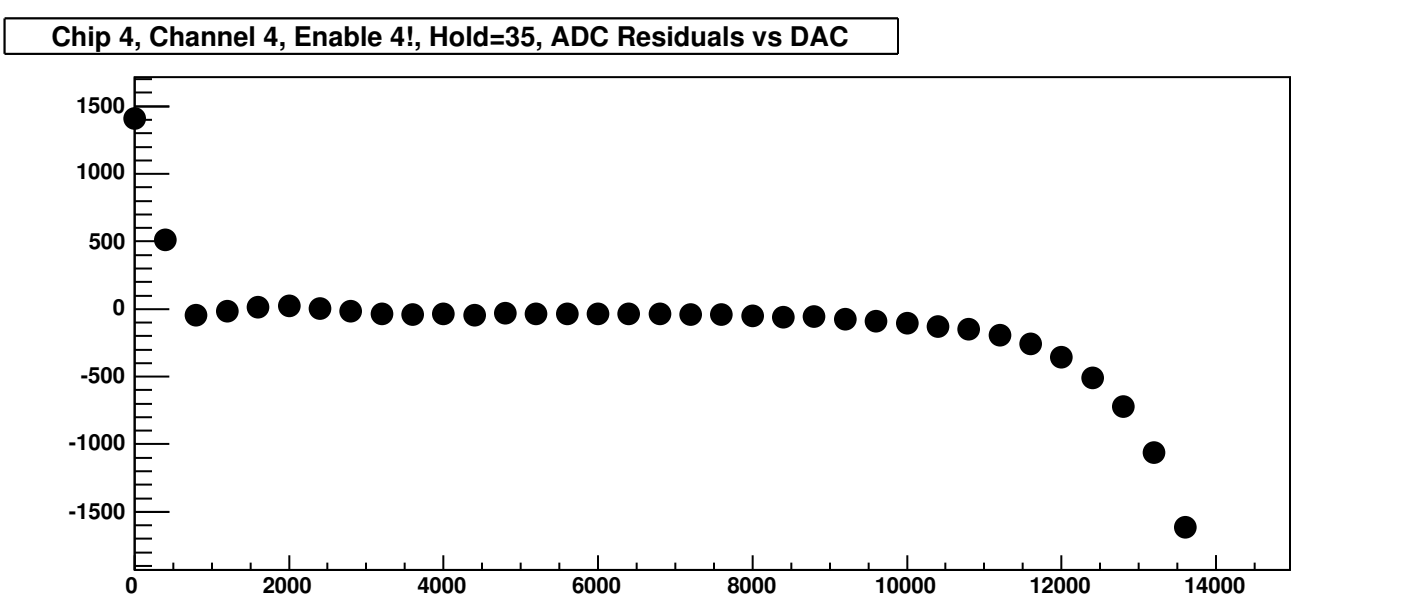


Chip 4, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

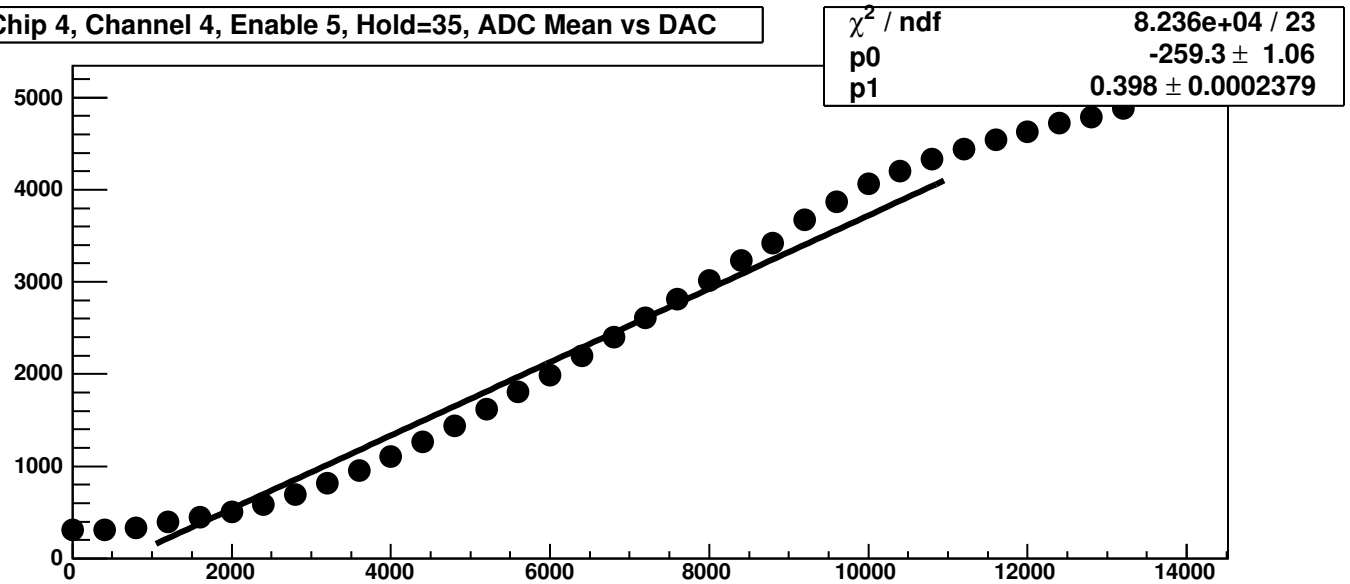



Chip 4, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 164.2 / 23
p0 323 ± 3.448
p1 2.285 ± 0.002177
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

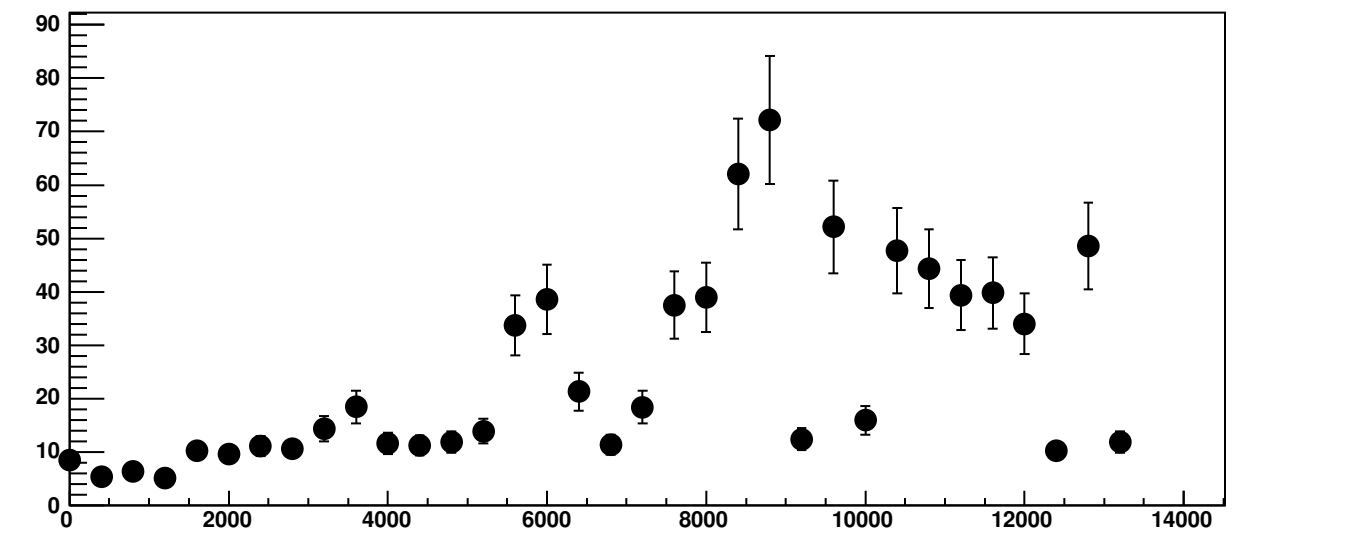

Chip 4, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

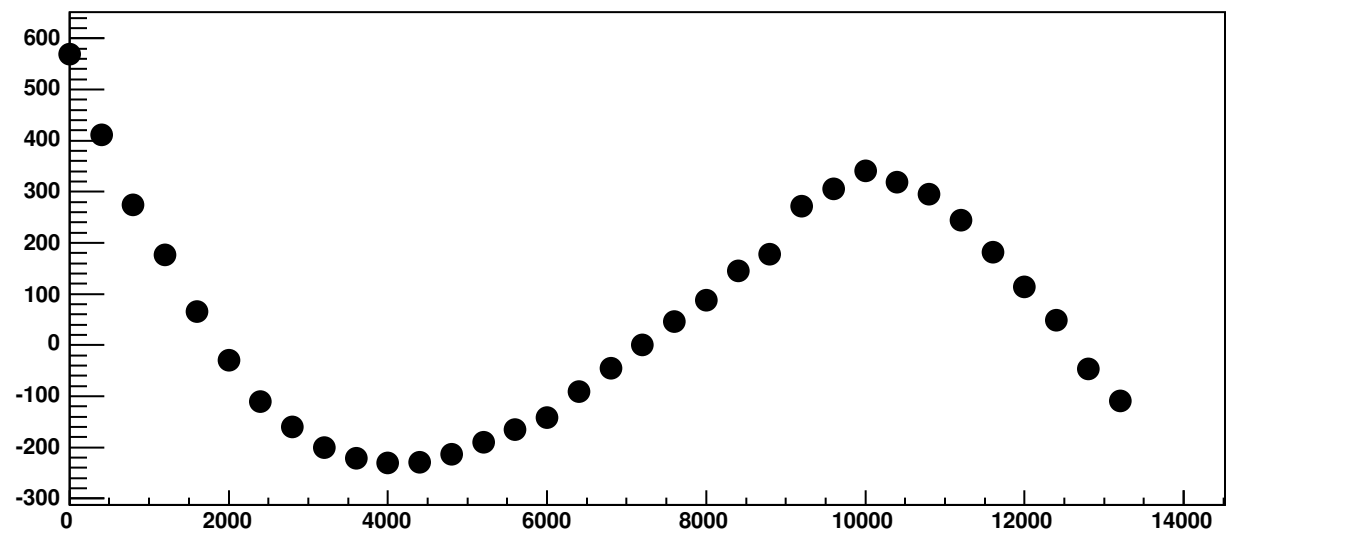
Chip 4, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

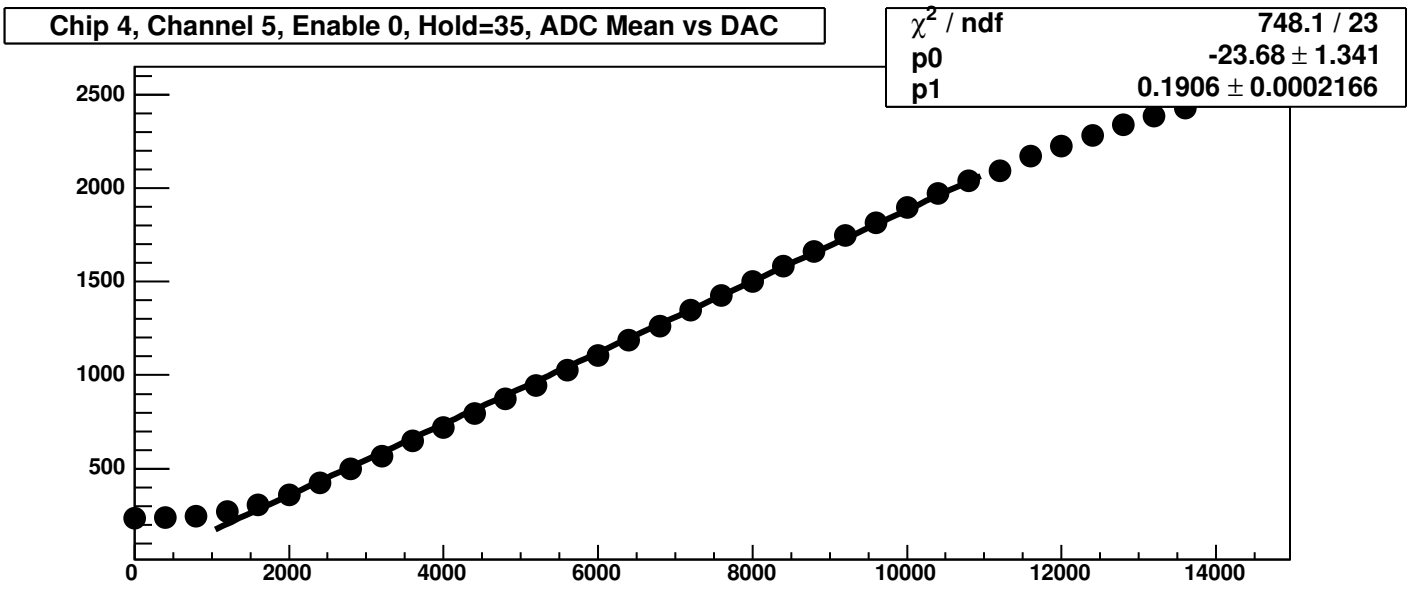


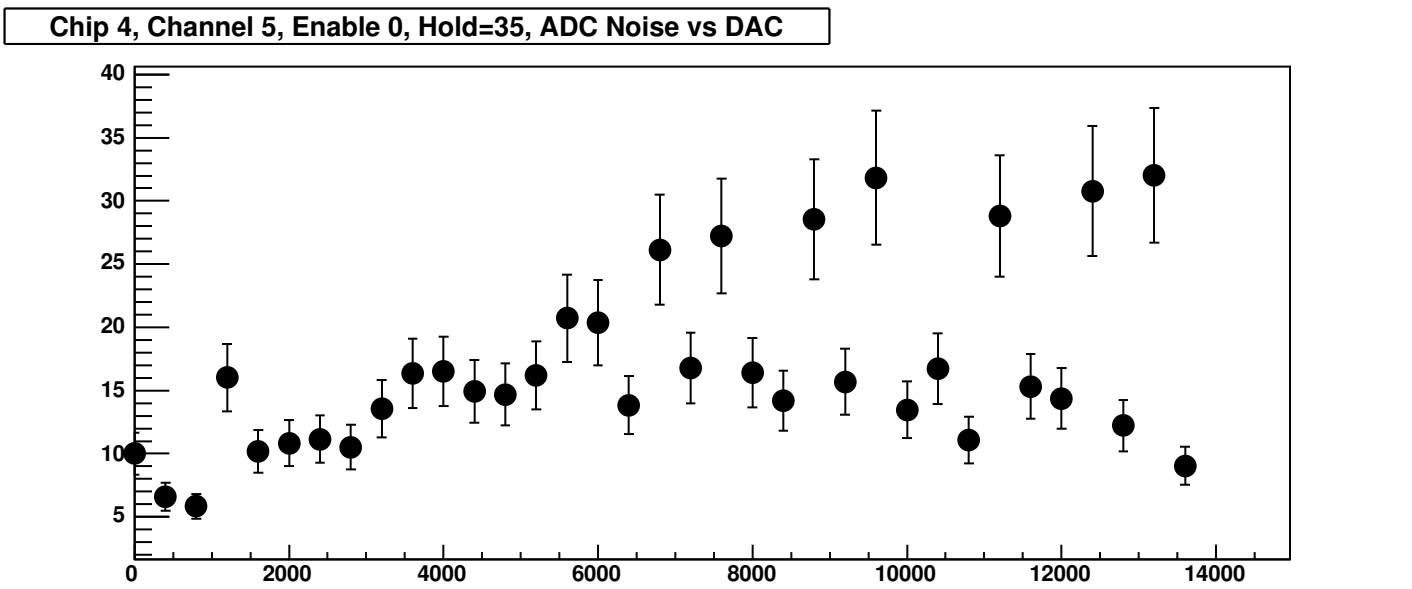
Chip 4, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC

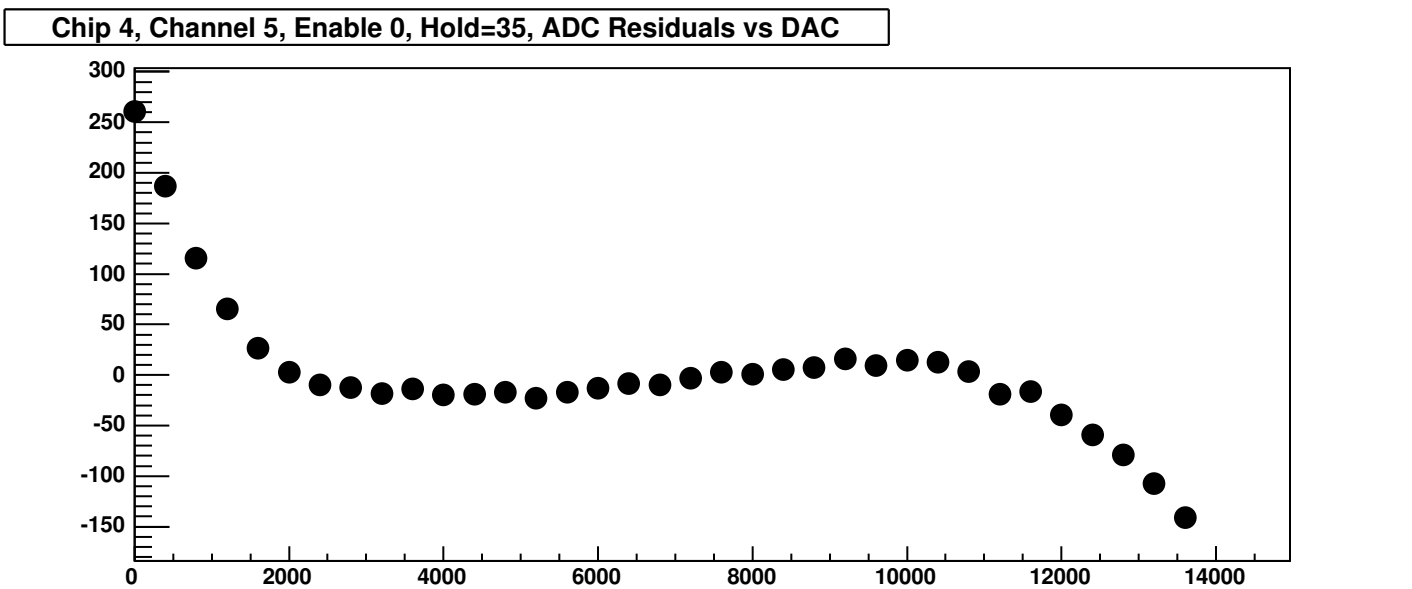


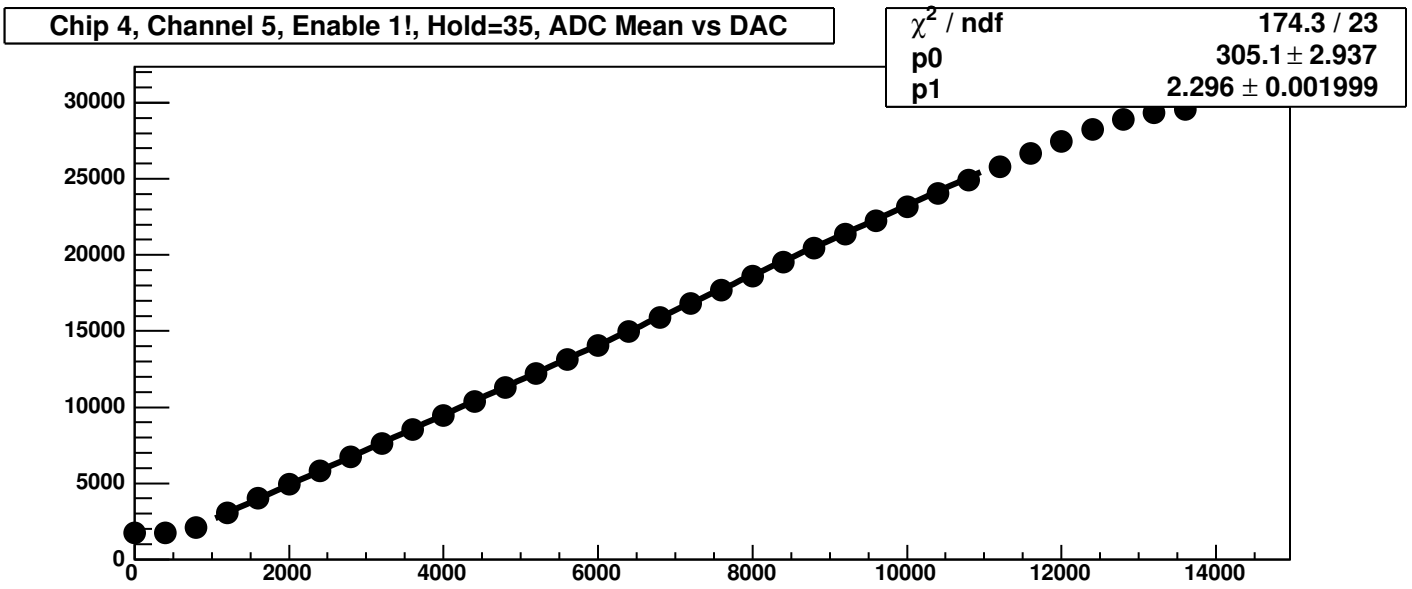
Chip 4, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

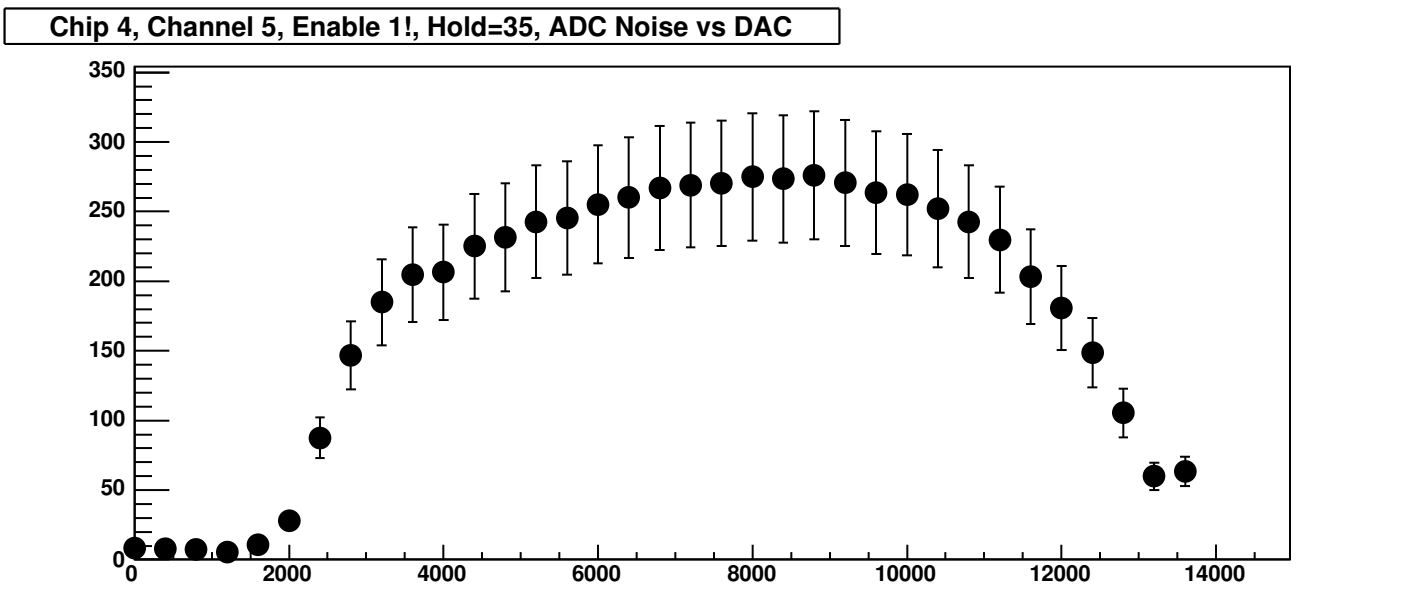


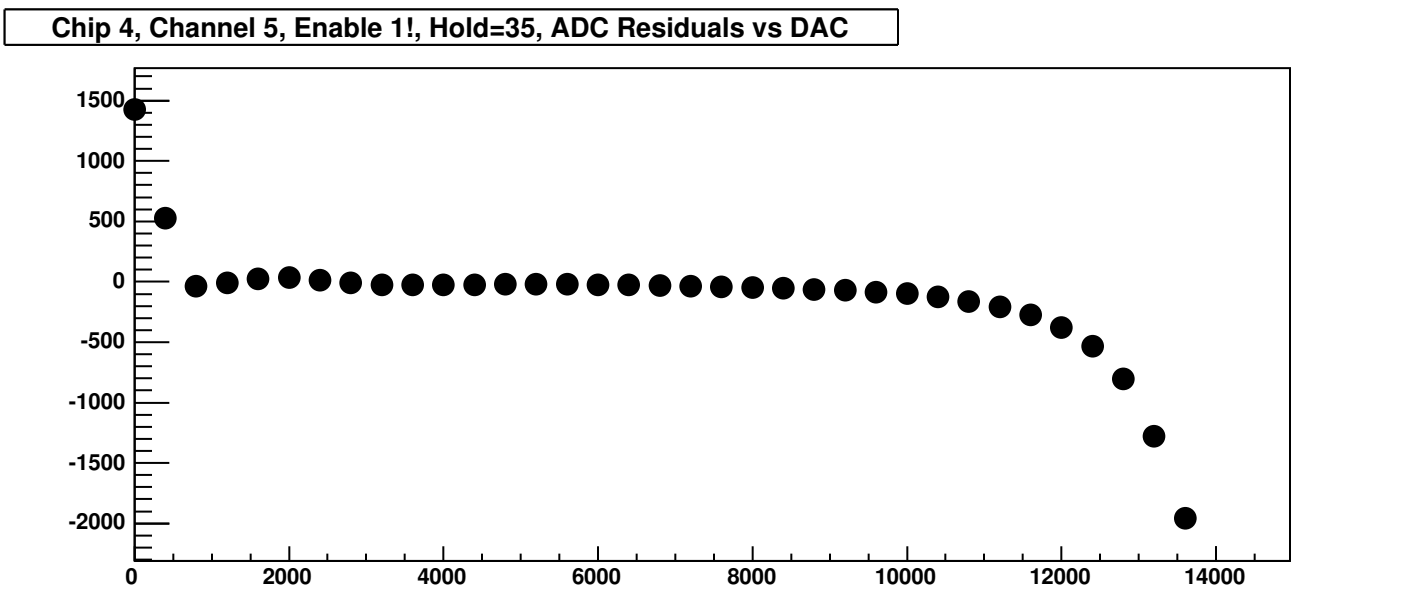

Chip 4, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 748.1 / 23
p0 -23.68 ± 1.341
p1 0.1906 ± 0.0002166
2500
2000
1500
1000
500
0
2000
4000
6000
8000
10000
12000
14000

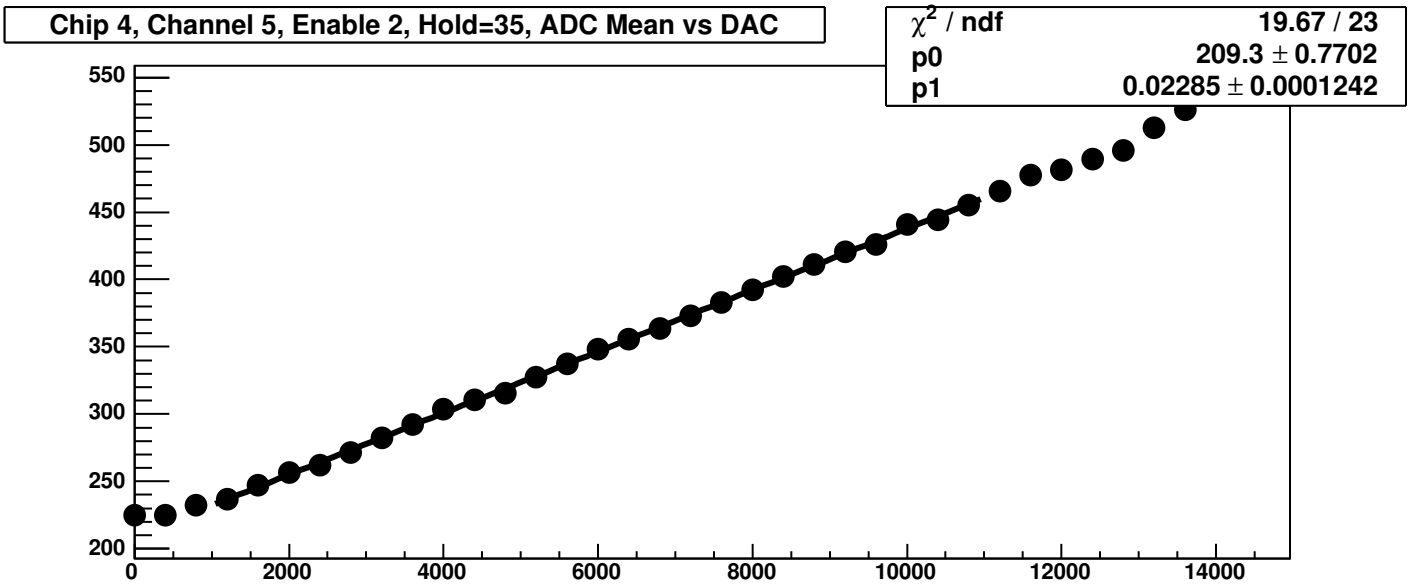

Chip 4, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

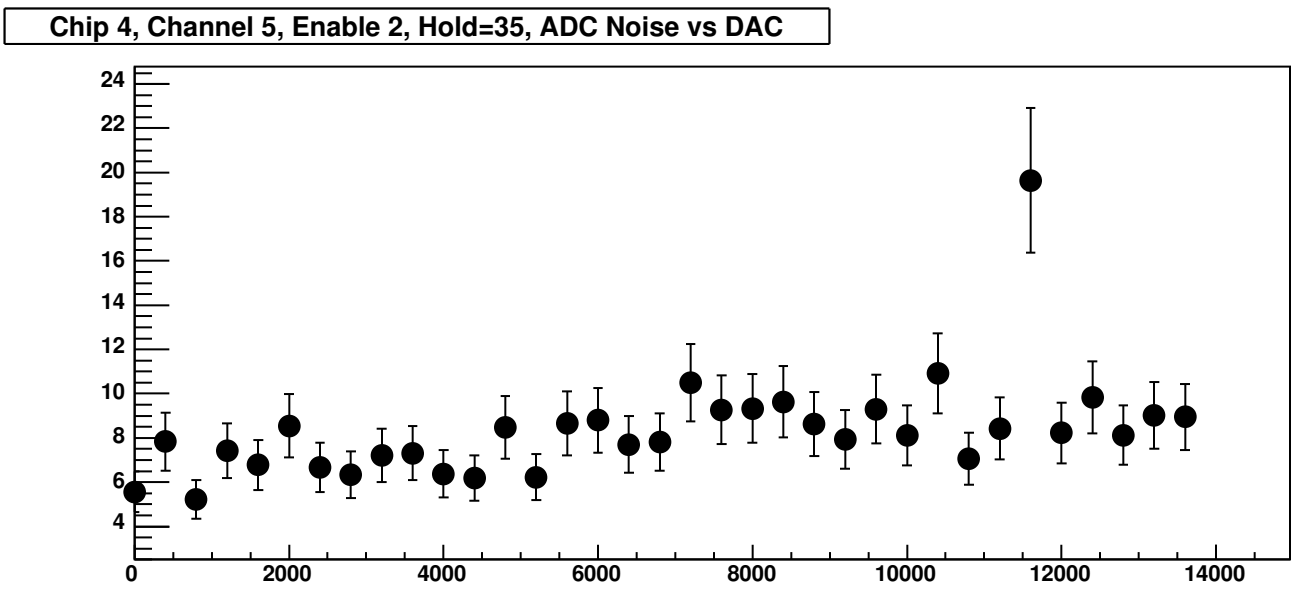

Chip 4, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

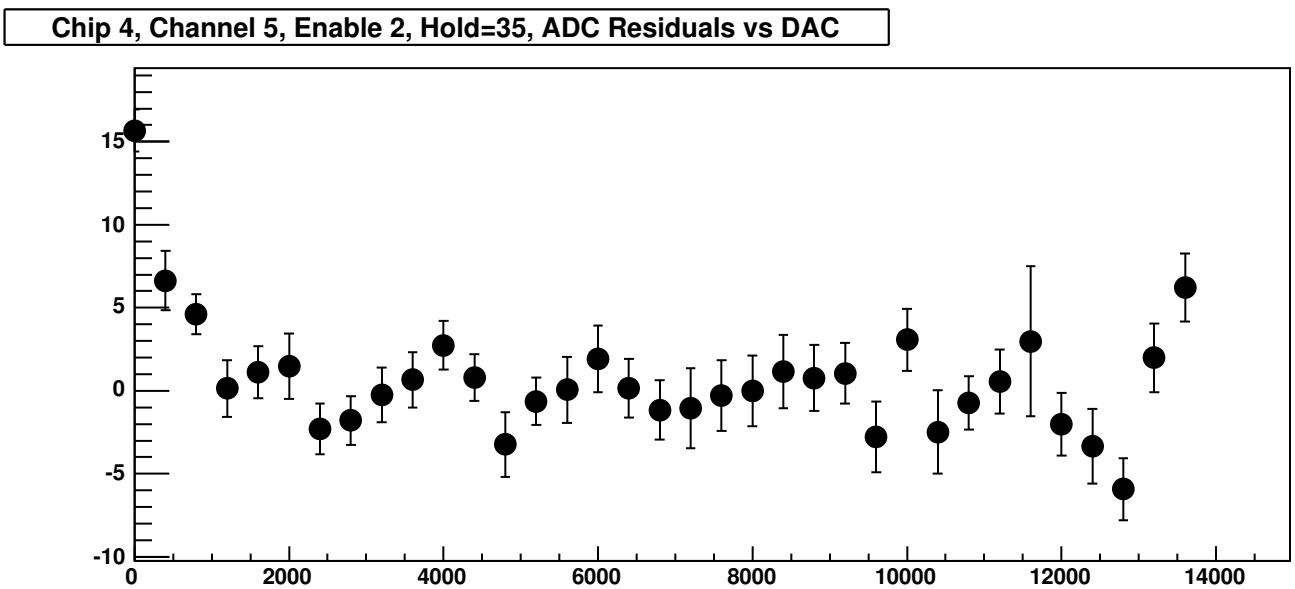

Chip 4, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 174.3 / 23
p0 305.1 ± 2.937
p1 2.296 ± 0.001999
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

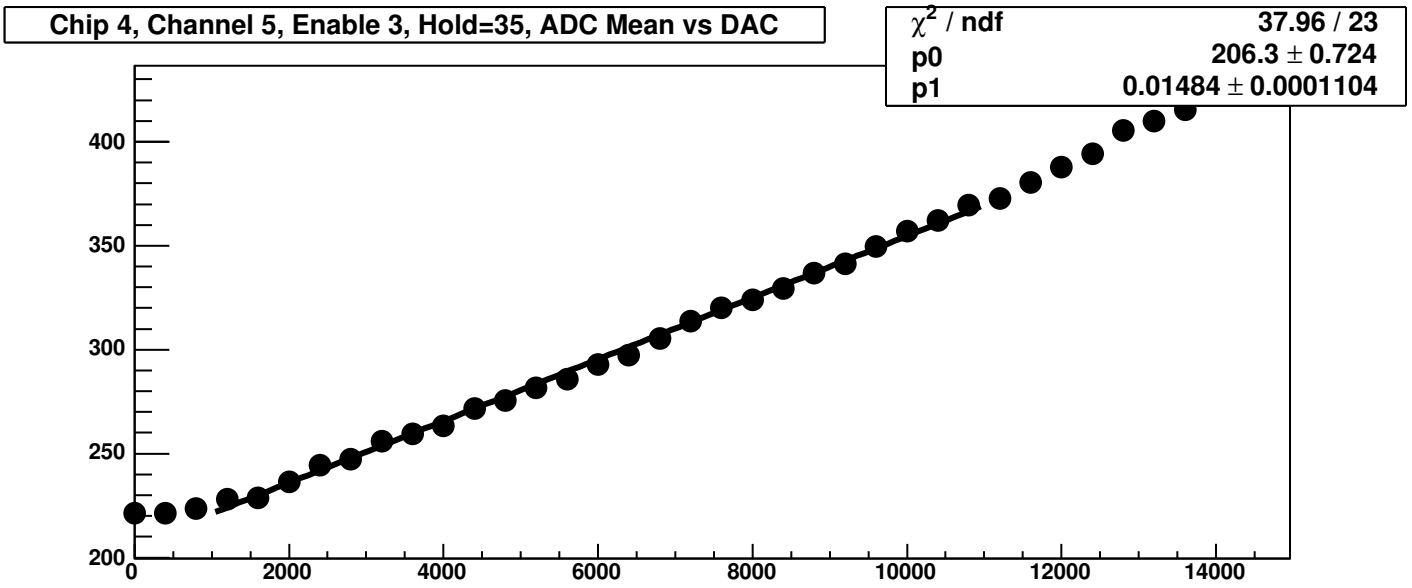

Chip 4, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

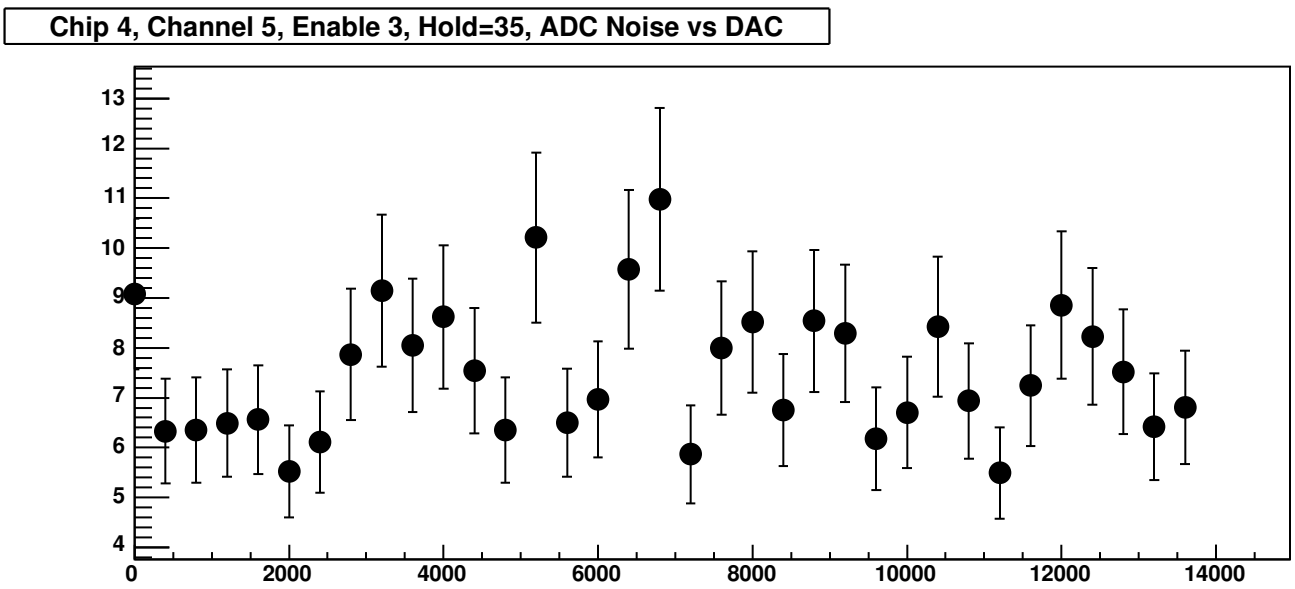

Chip 4, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

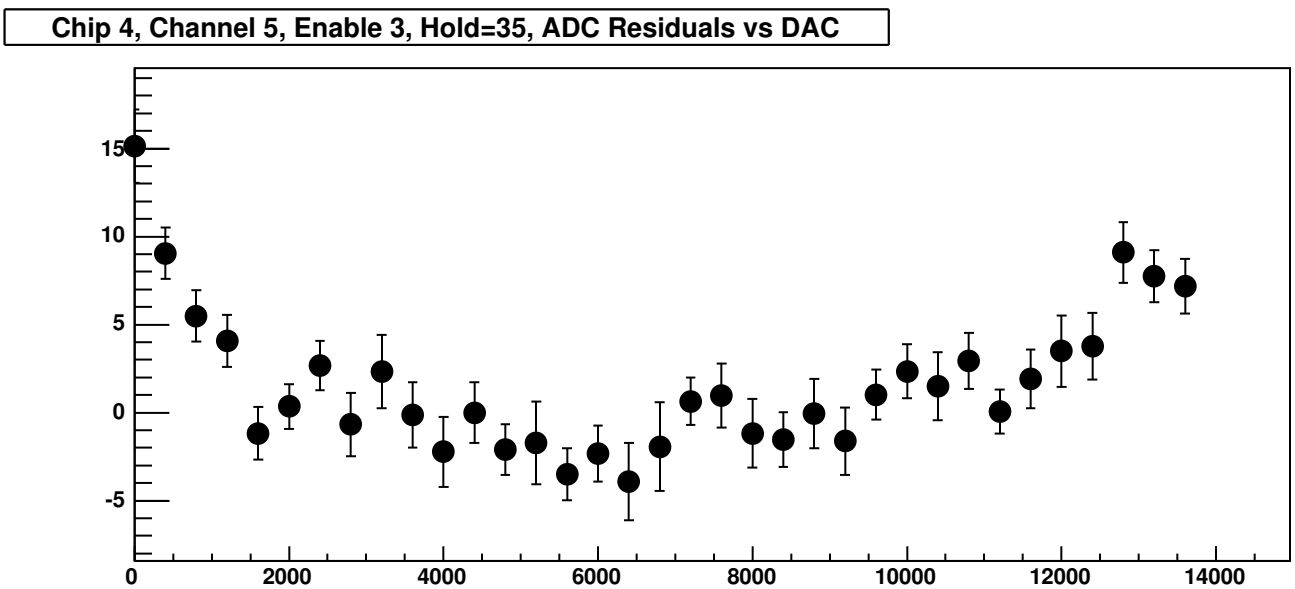

Chip 4, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 19.67 / 23
p0 209.3 ± 0.7702
p1 0.02285 ± 0.0001242
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

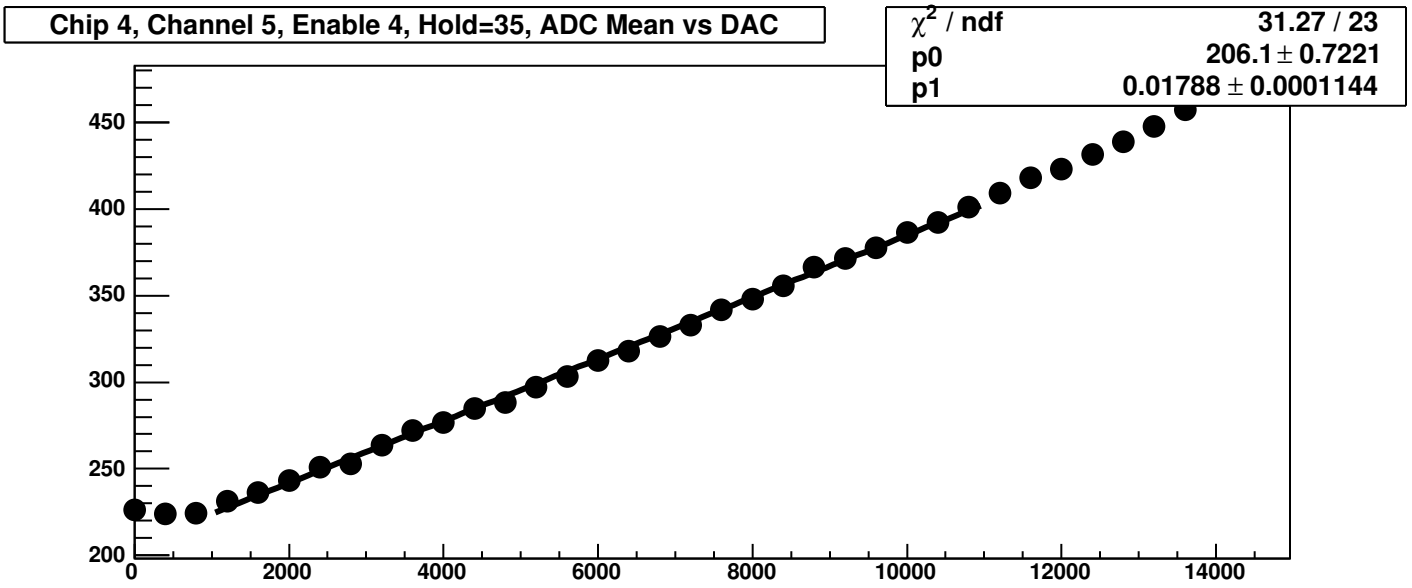

Chip 4, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
24
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

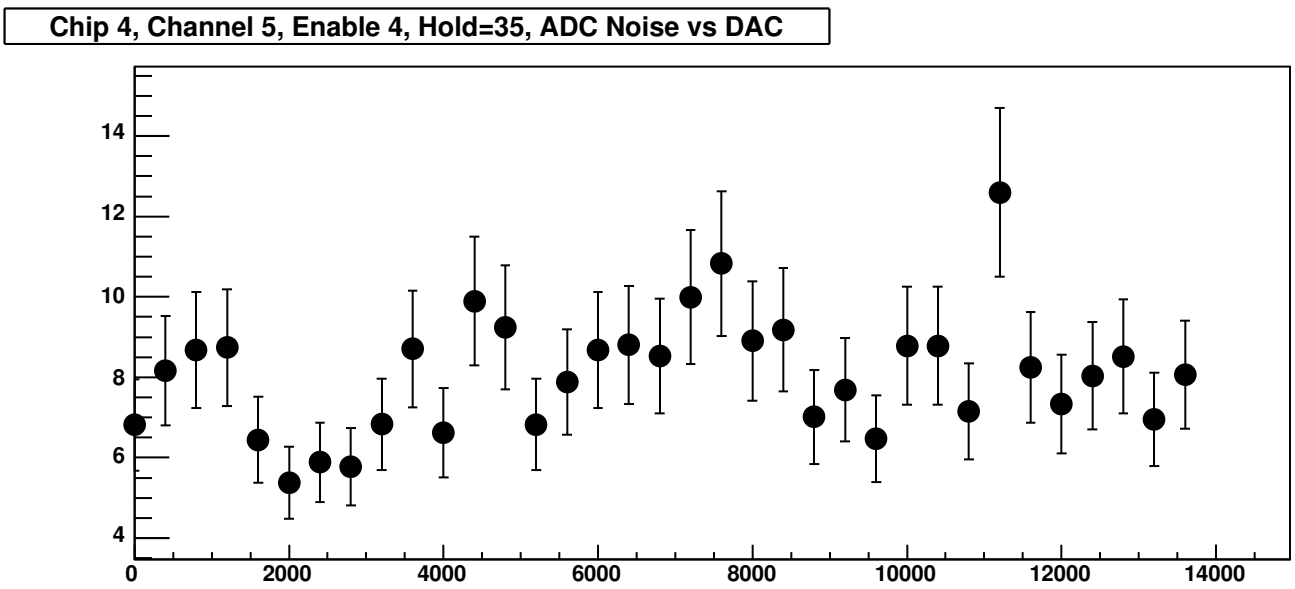

Chip 4, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

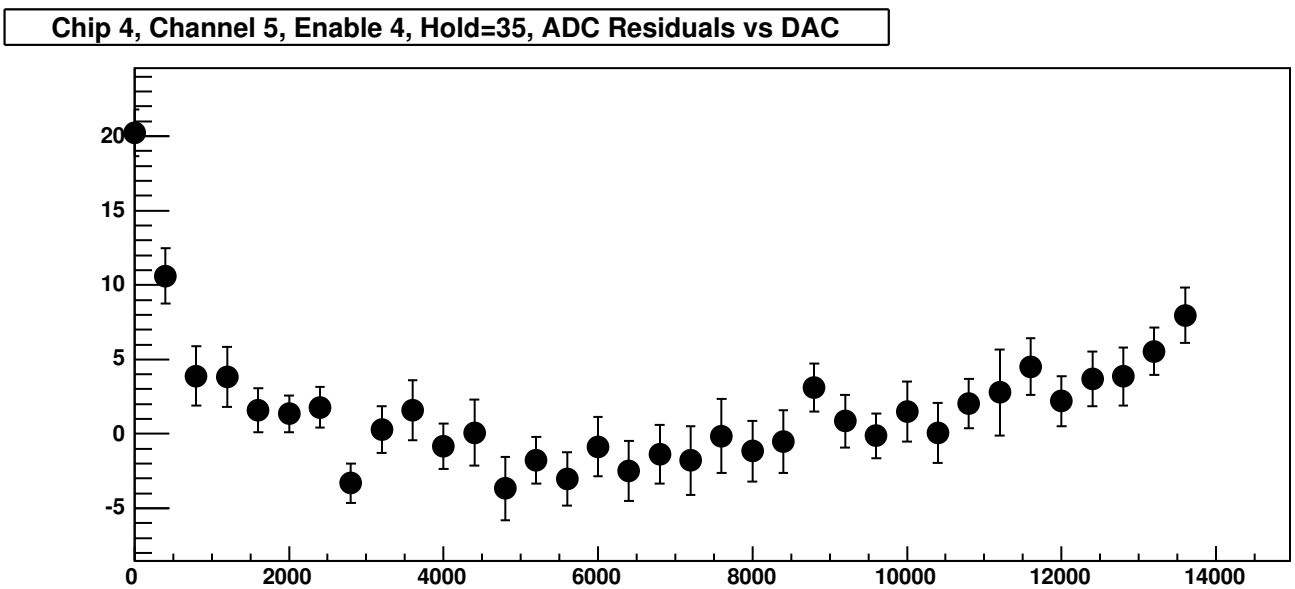

Chip 4, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 37.96 / 23
p0 206.3 ± 0.724
p1 0.01484 ± 0.0001104
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

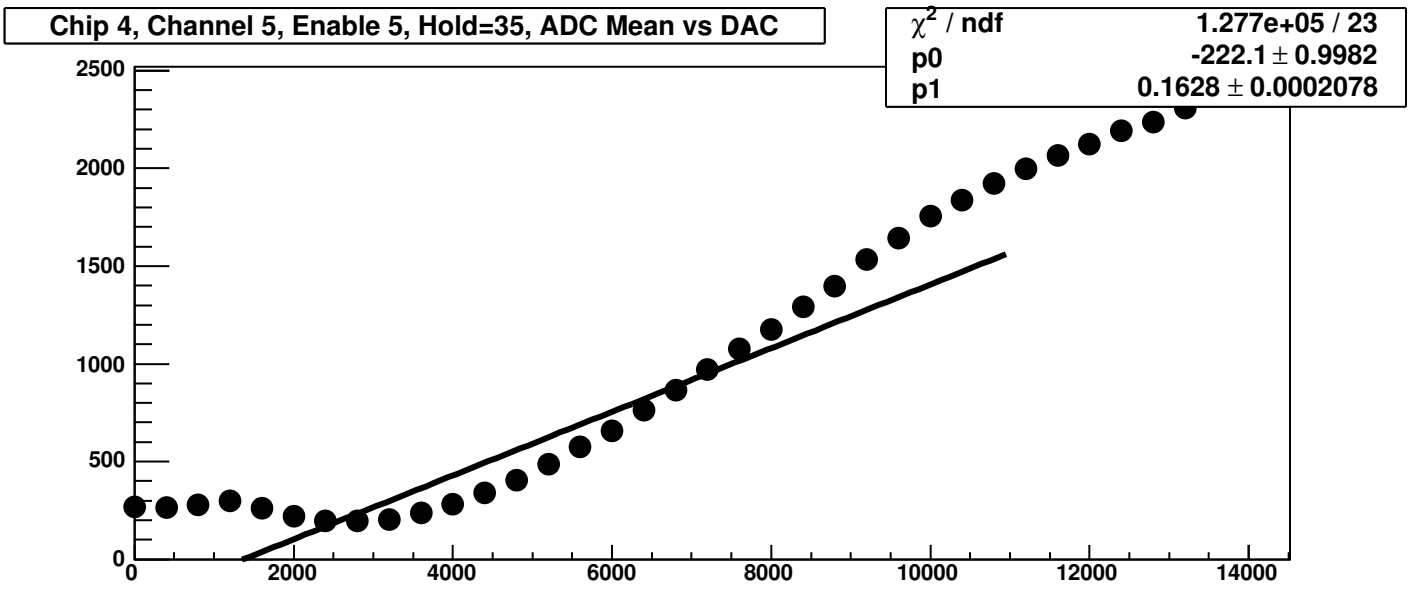

Chip 4, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

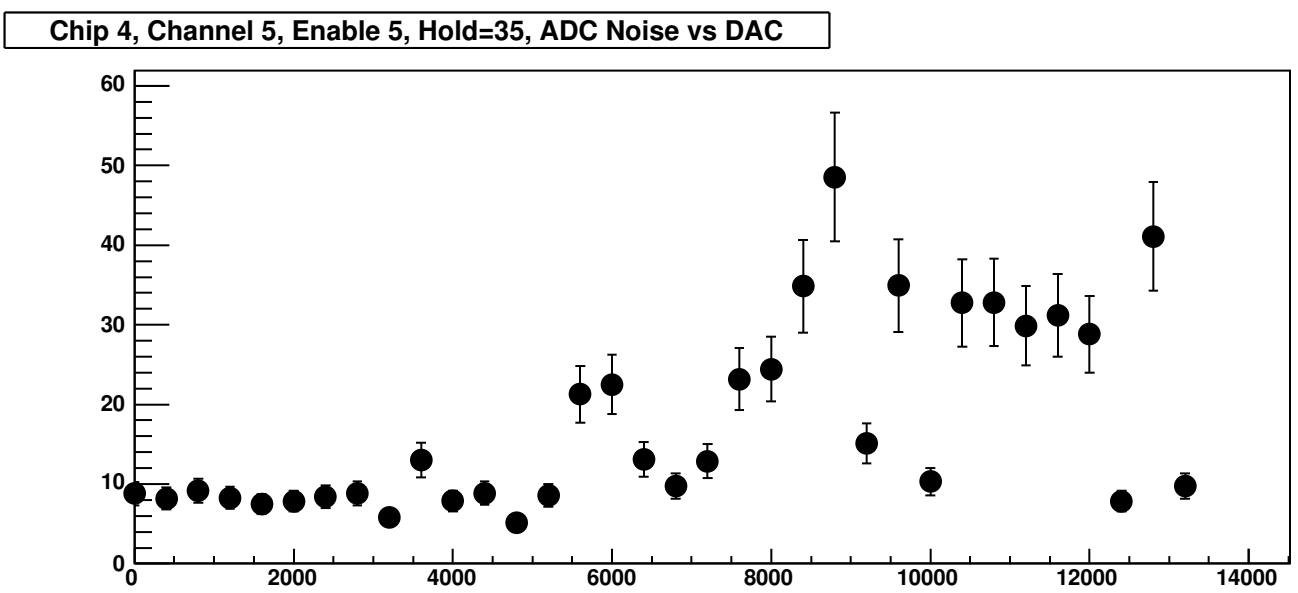

Chip 4, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

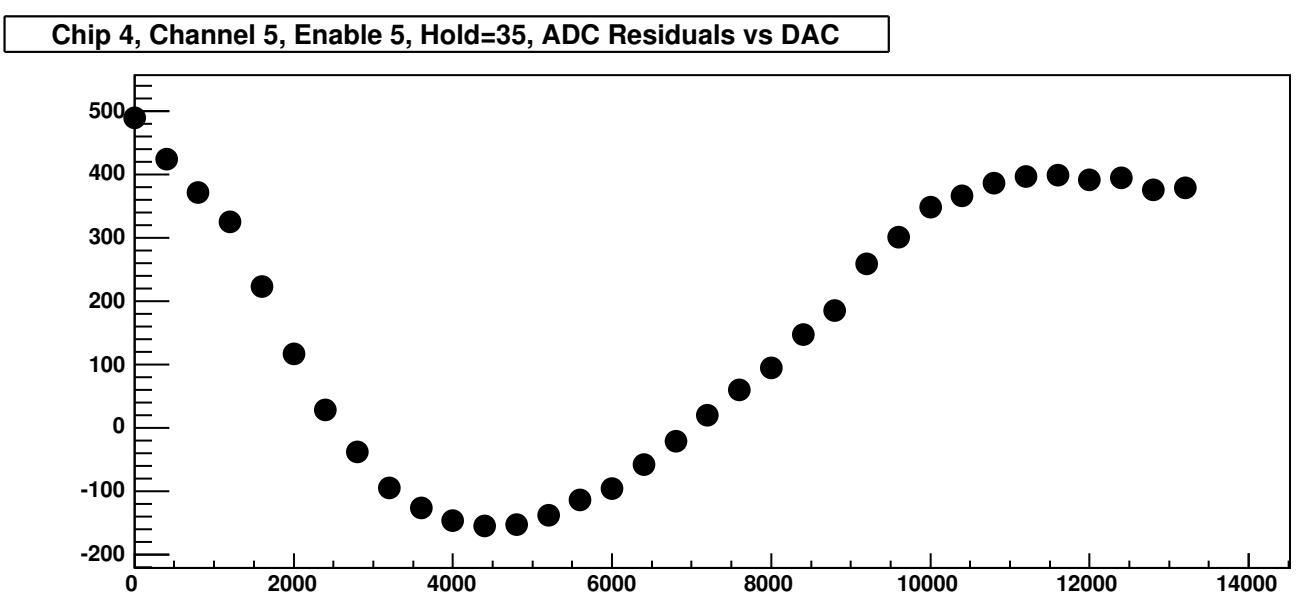

Chip 4, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 31.27 / 23
p0 206.1 ± 0.7221
p1 0.01788 ± 0.0001144
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

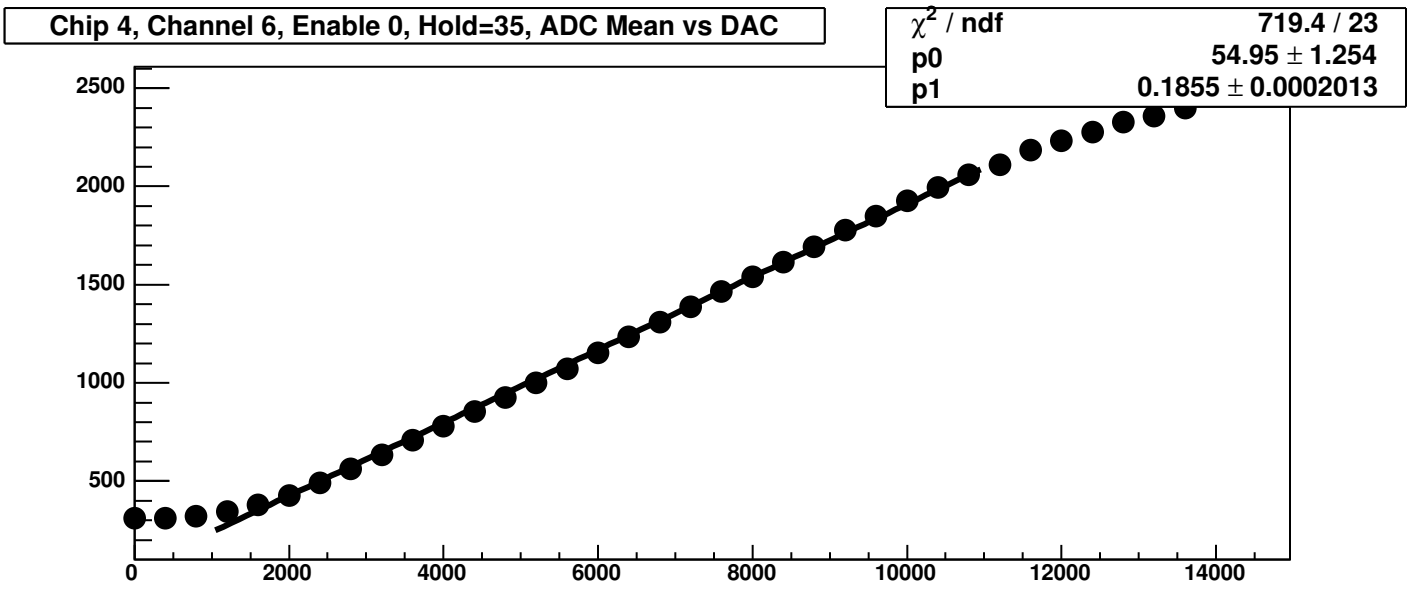

Chip 4, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

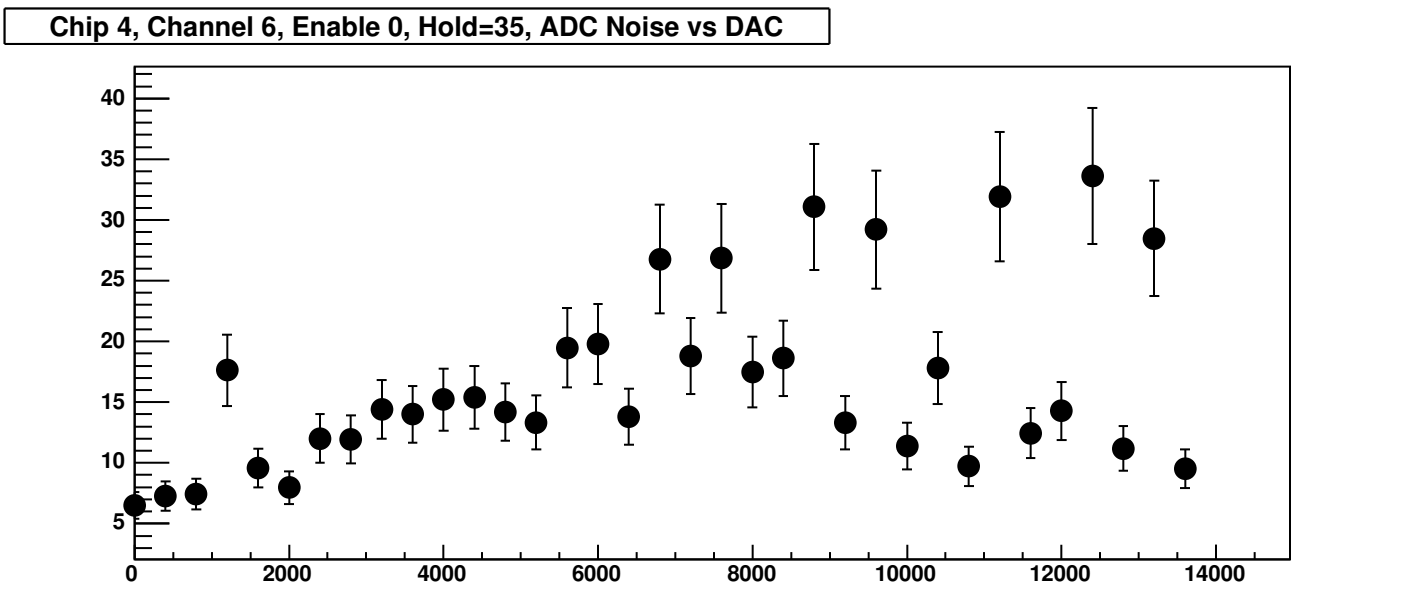

Chip 4, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

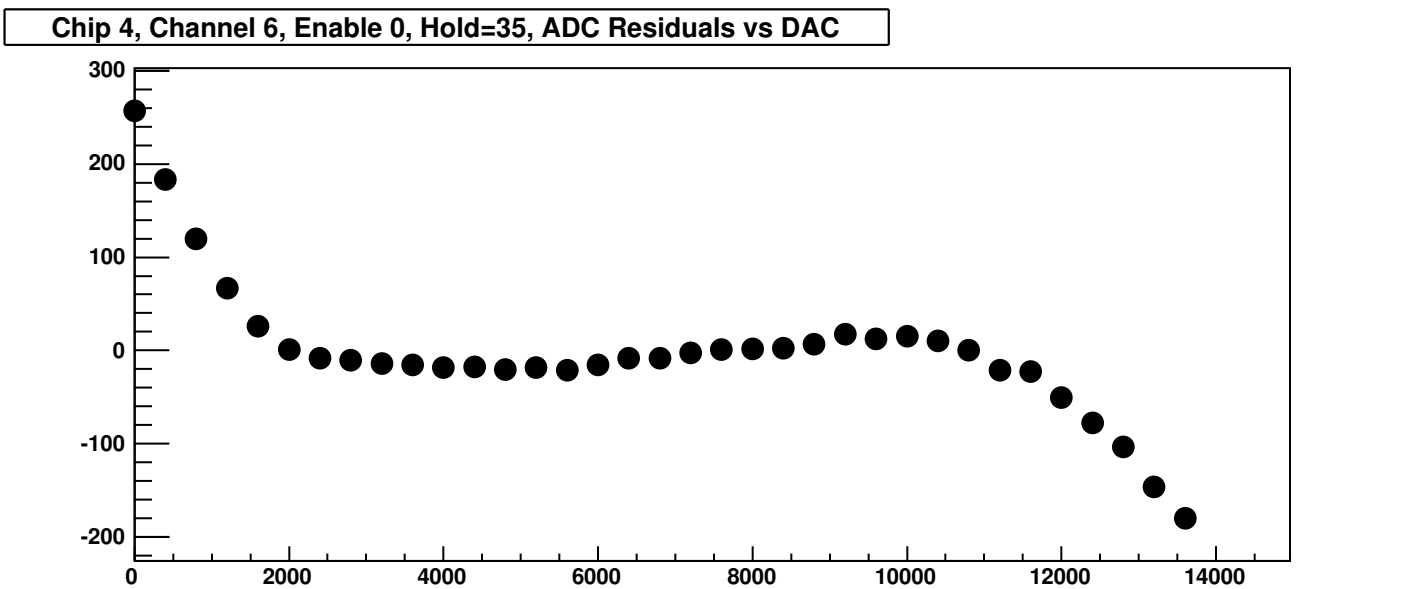

Chip 4, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.277e+05 / 23
p0 -222.1 ± 0.9982
p1 0.1628 ± 0.0002078
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

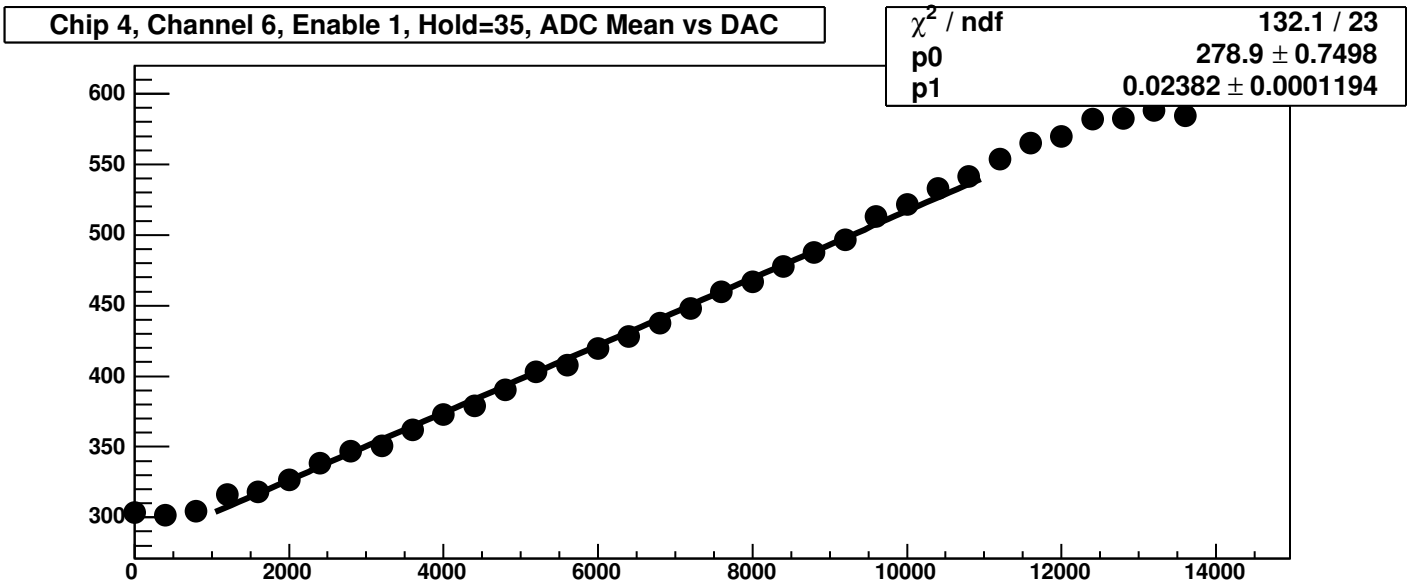

Chip 4, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

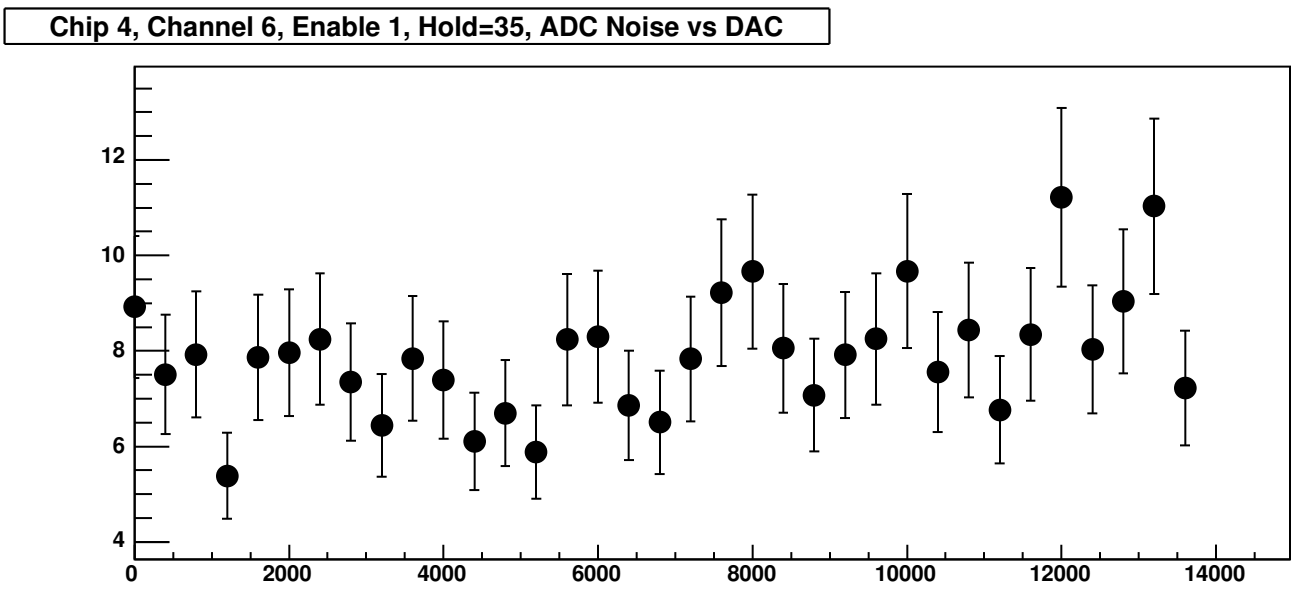

Chip 4, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

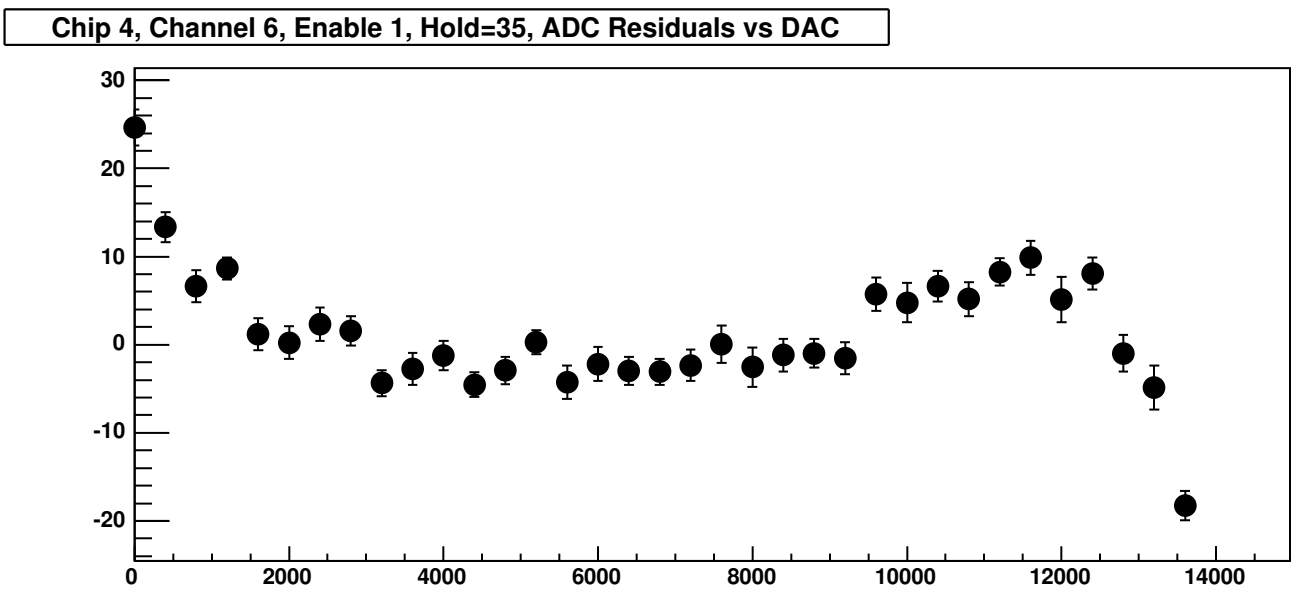

Chip 4, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 719.4 / 23
p0 54.95 ± 1.254
p1 0.1855 ± 0.0002013
2500
2000
1500
1000
500
0
2000
4000
6000
8000
10000
12000
14000

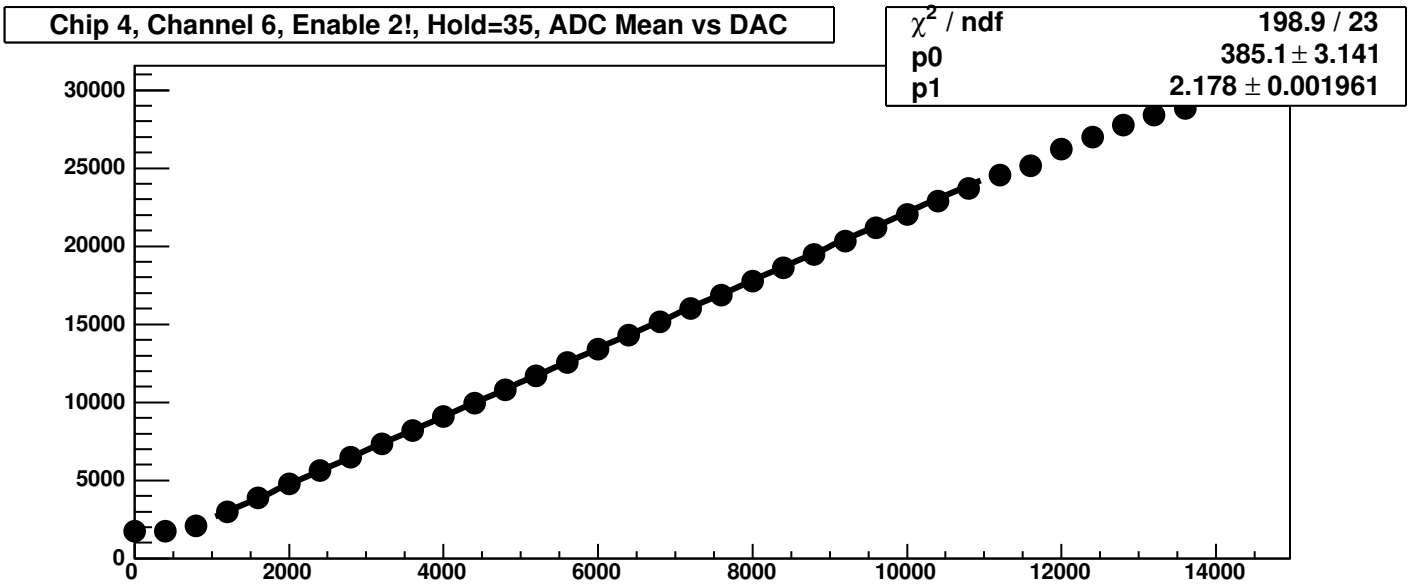

Chip 4, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

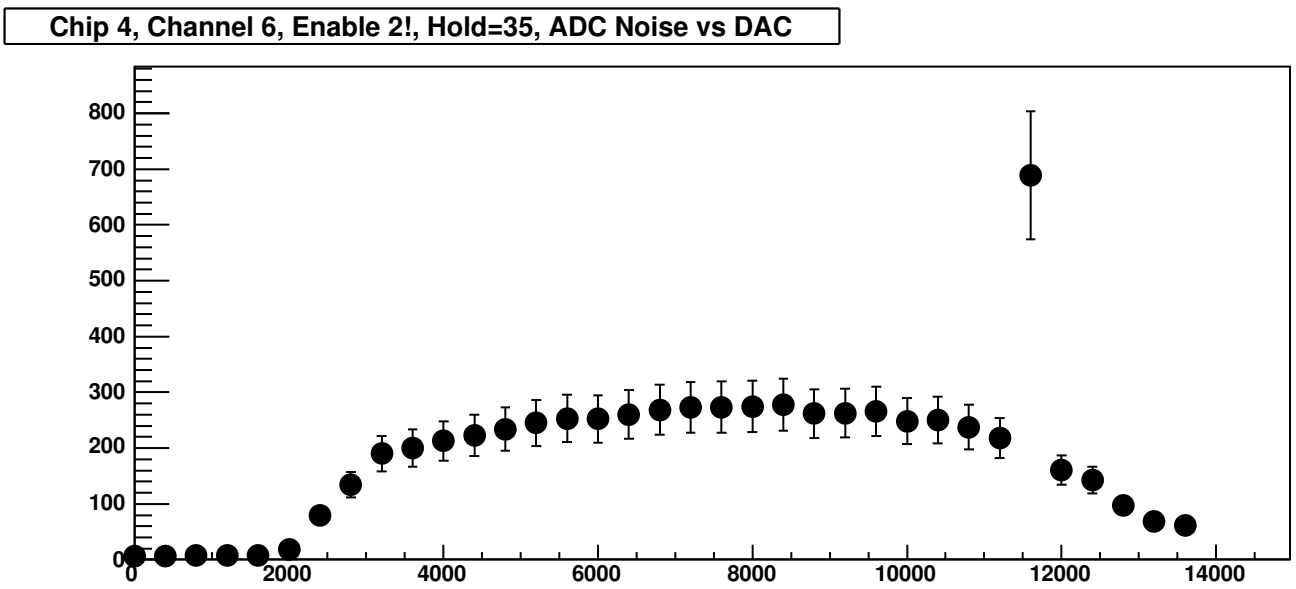

Chip 4, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

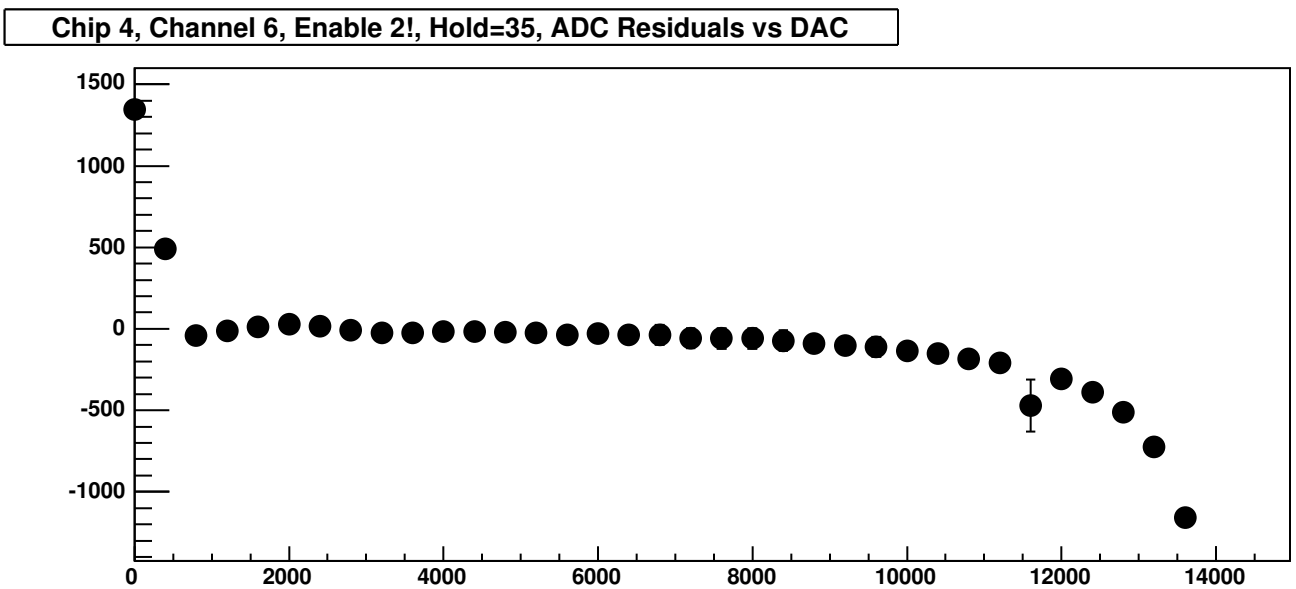

Chip 4, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 132.1 / 23
p0 278.9 ± 0.7498
p1 0.02382 ± 0.0001194
600
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

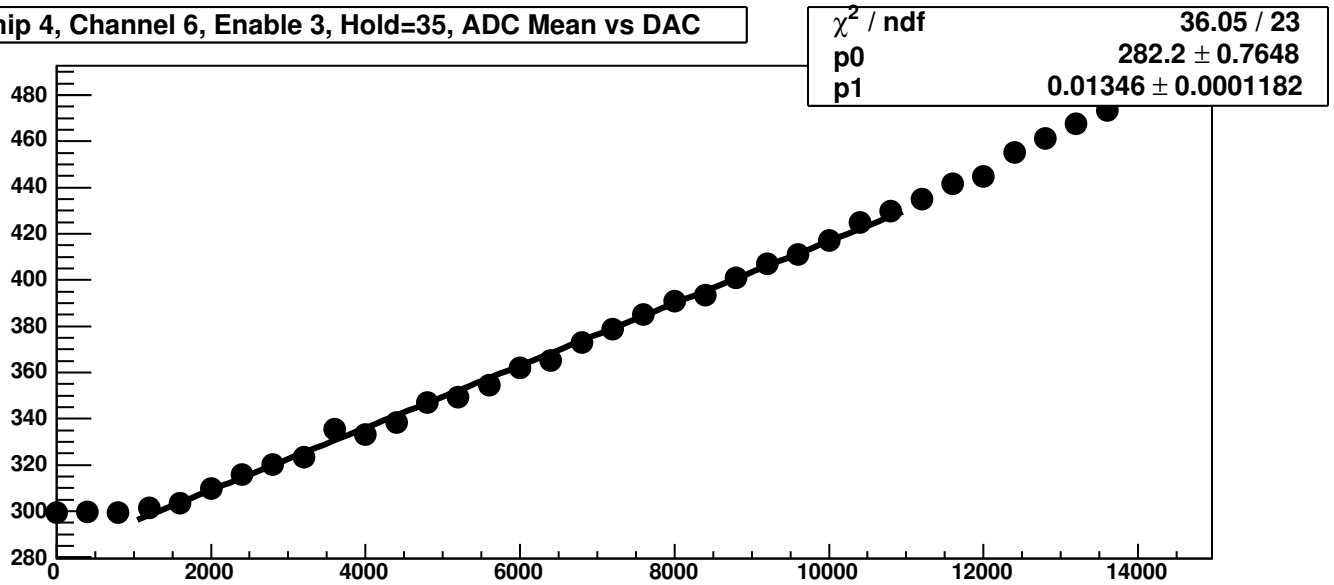

Chip 4, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 198.9 / 23
p0 385.1 ± 3.141
p1 2.178 ± 0.001961
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

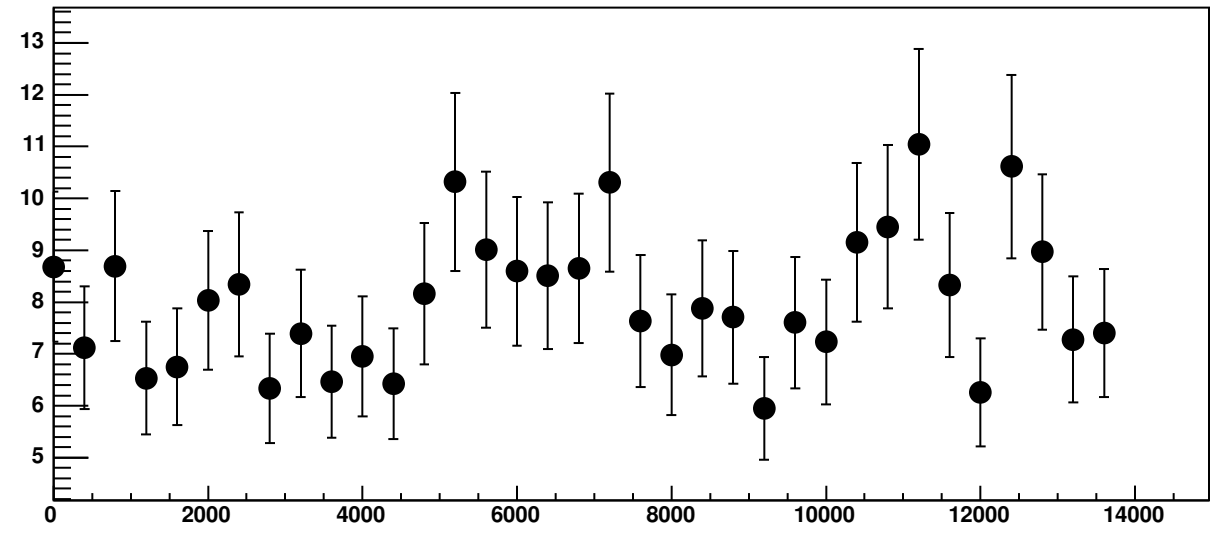

Chip 4, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
800
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

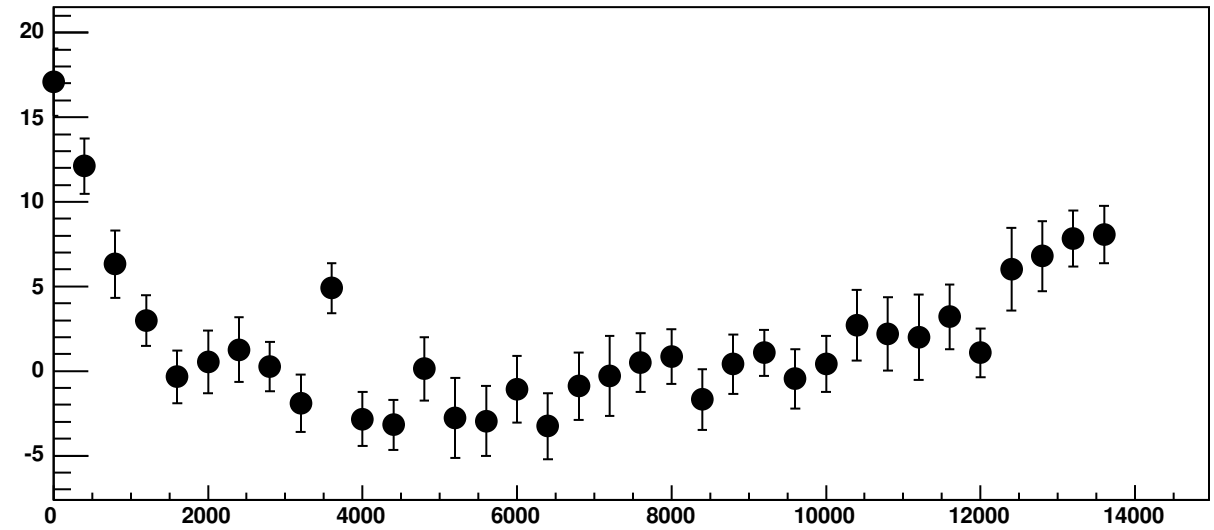
Chip 4, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

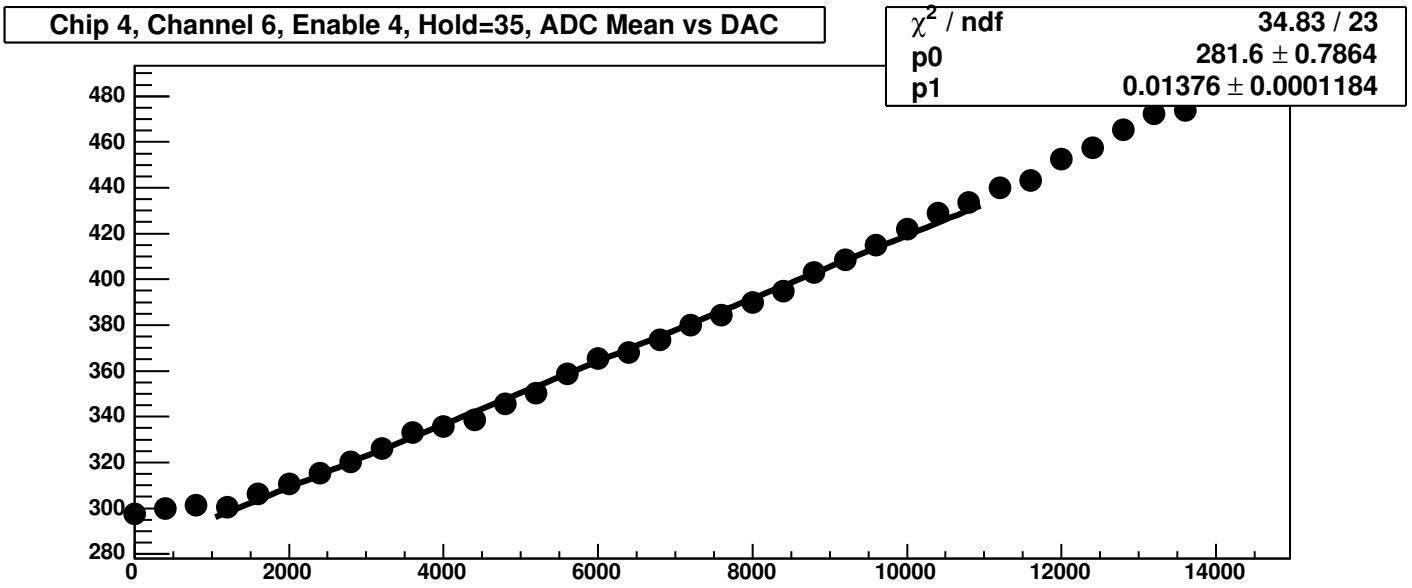


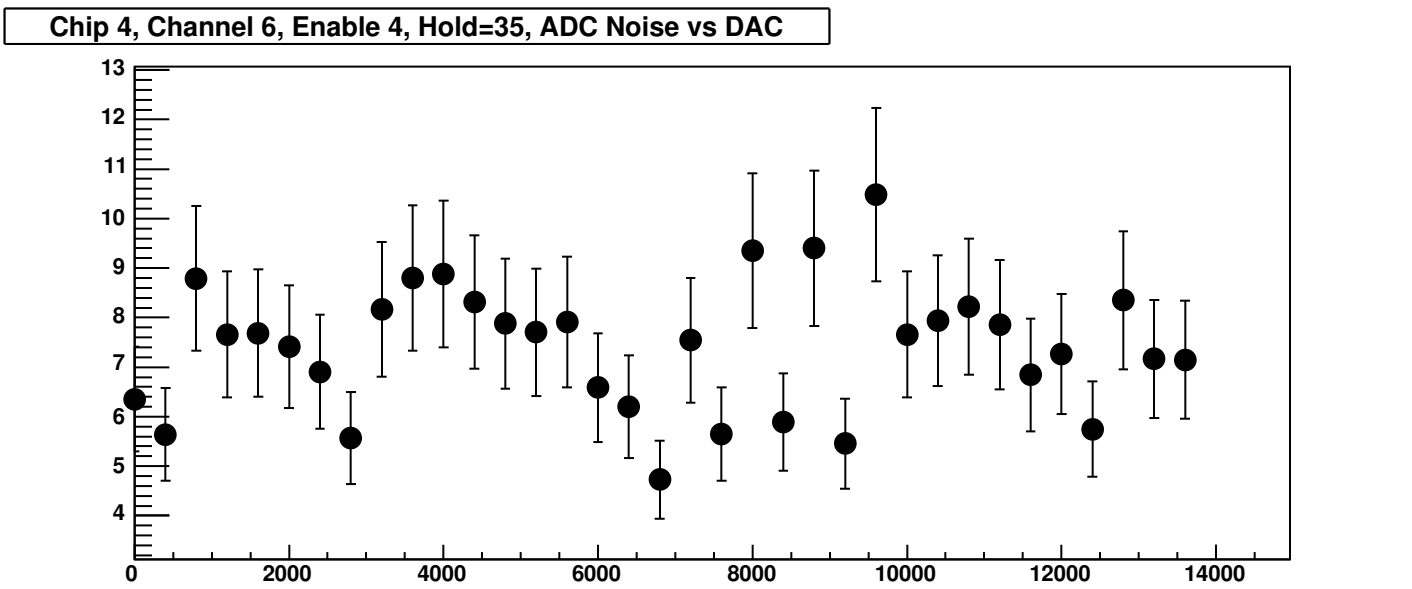
Chip 4, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC

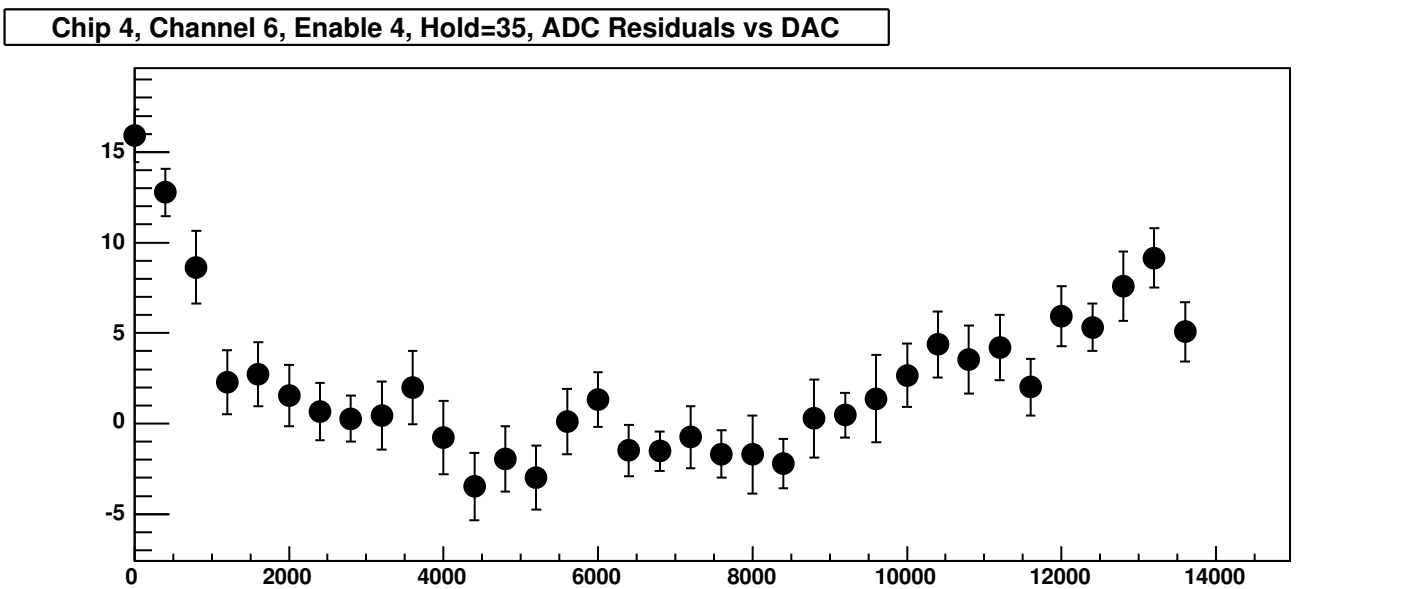


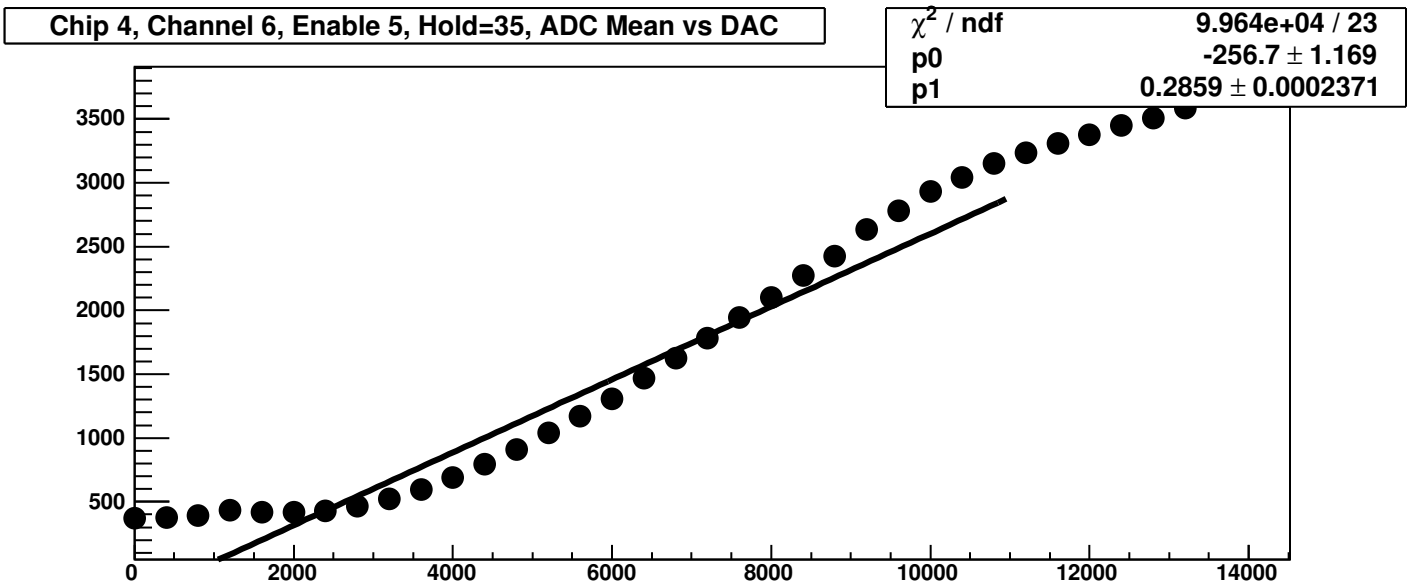
Chip 4, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

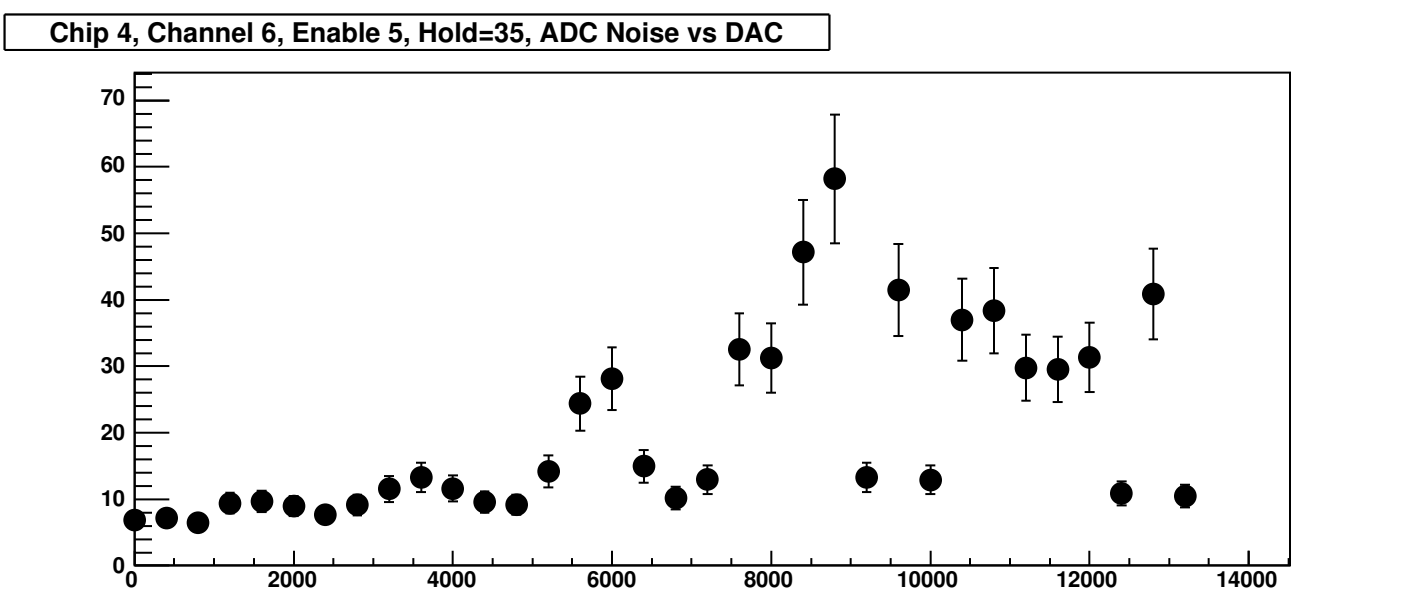


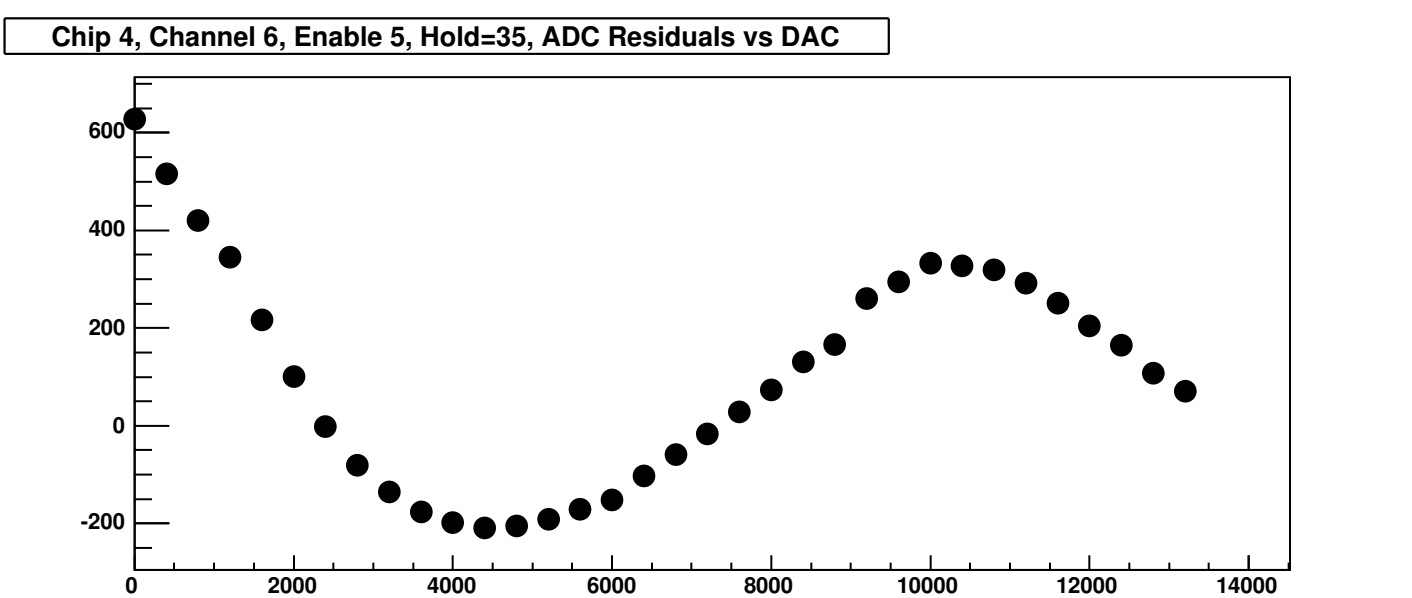

Chip 4, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 34.83 / 23
p0 281.6 ± 0.7864
p1 0.01376 ± 0.0001184
480
460
440
420
400
380
360
340
320
300
280
0
2000
4000
6000
8000
10000
12000
14000

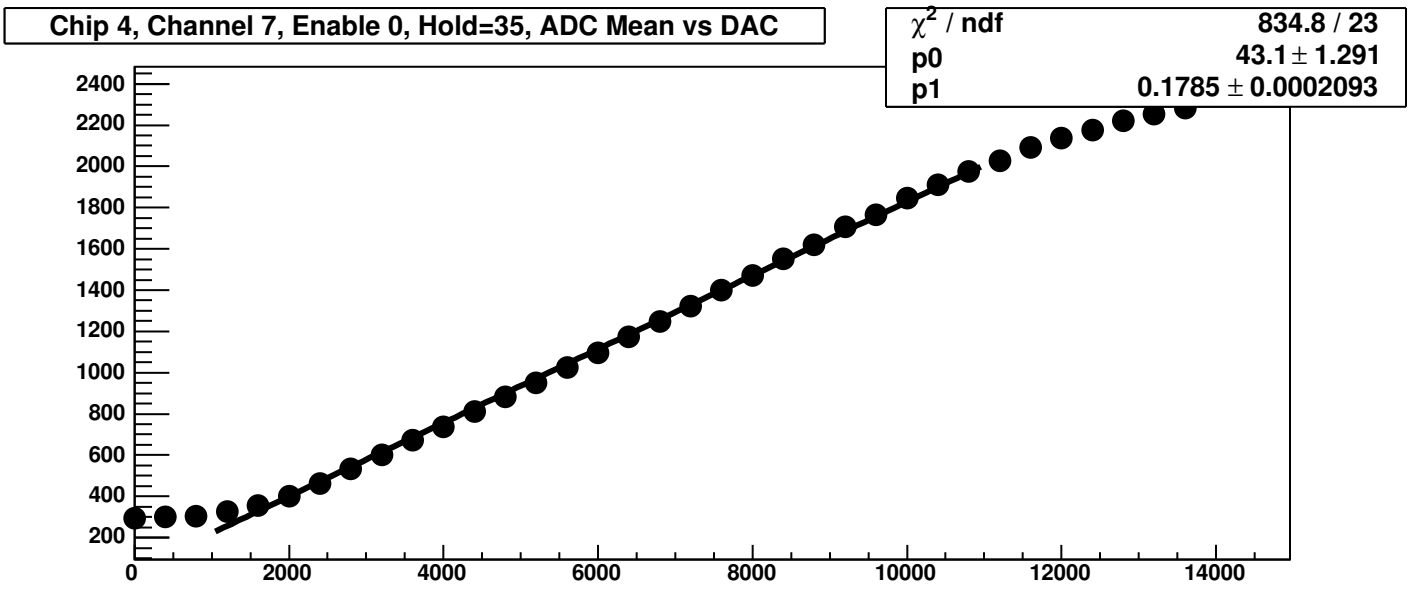

Chip 4, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

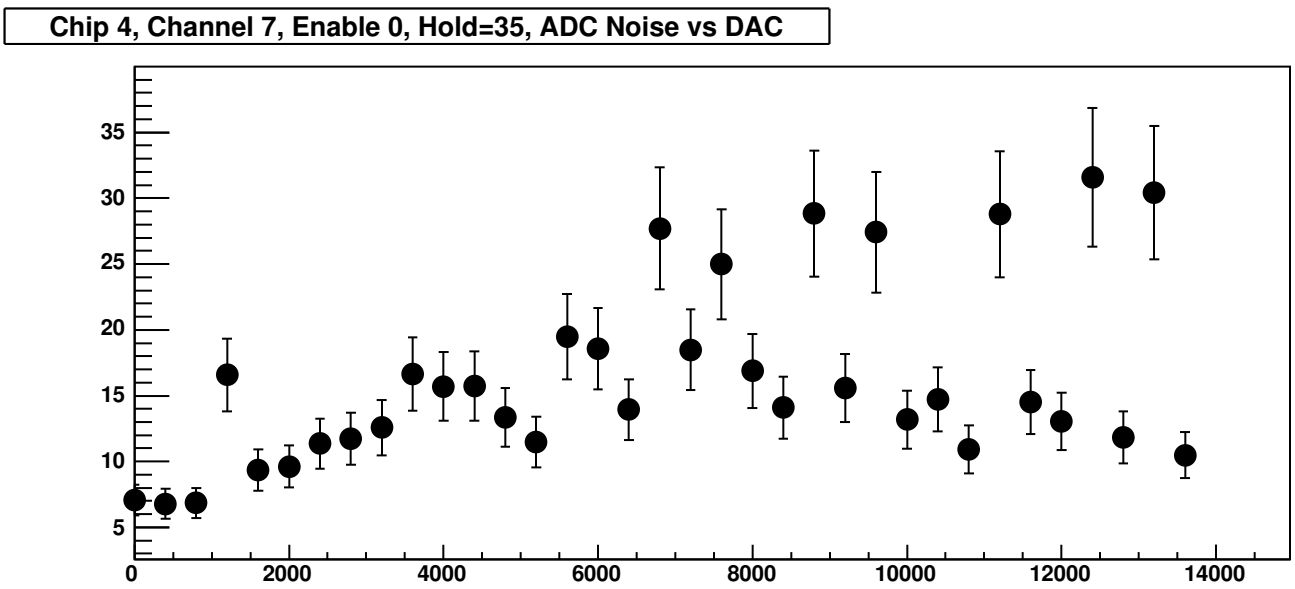

Chip 4, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

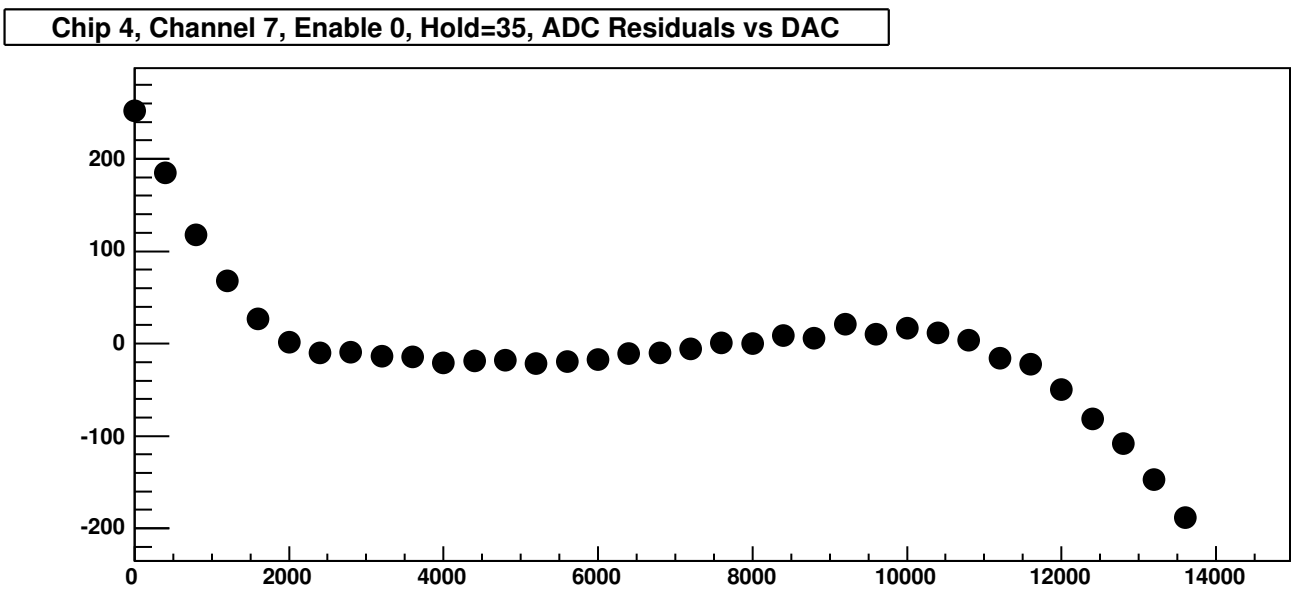

Chip 4, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 9.964e+04 / 23
p0 -256.7 ± 1.169
p1 0.2859 ± 0.0002371
3500
3000
2500
2000
1500
1000
500
0
2000
4000
6000
8000
10000
12000
14000

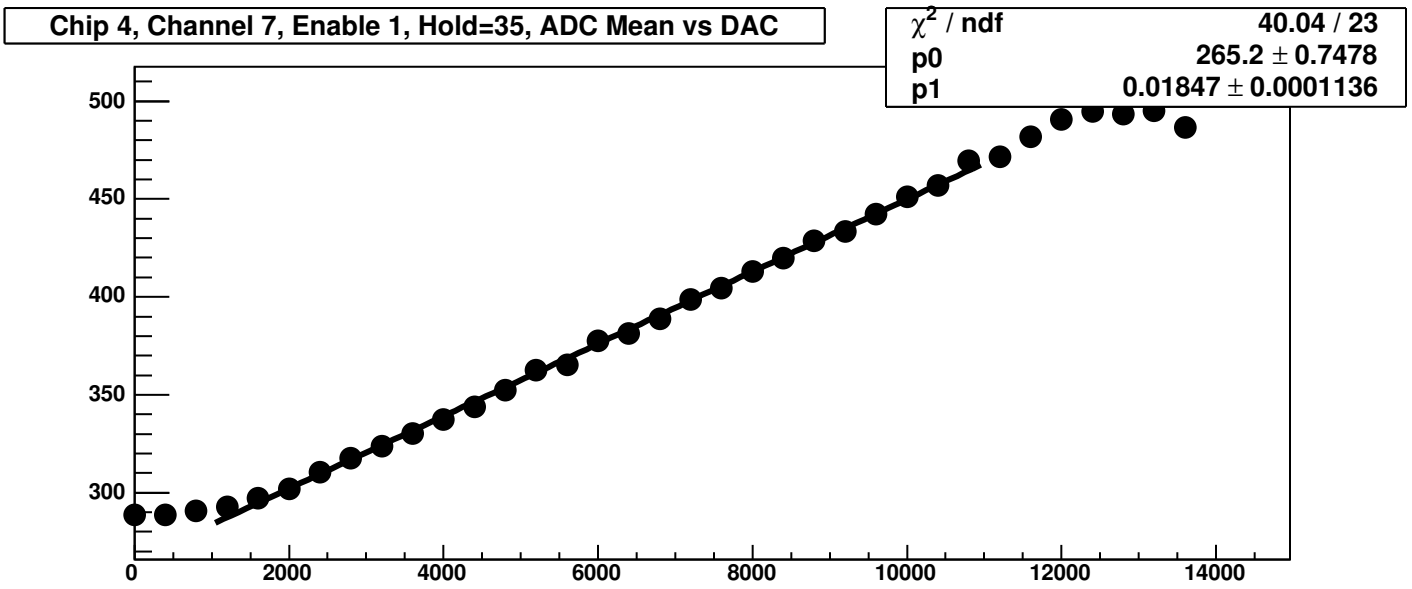

Chip 4, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

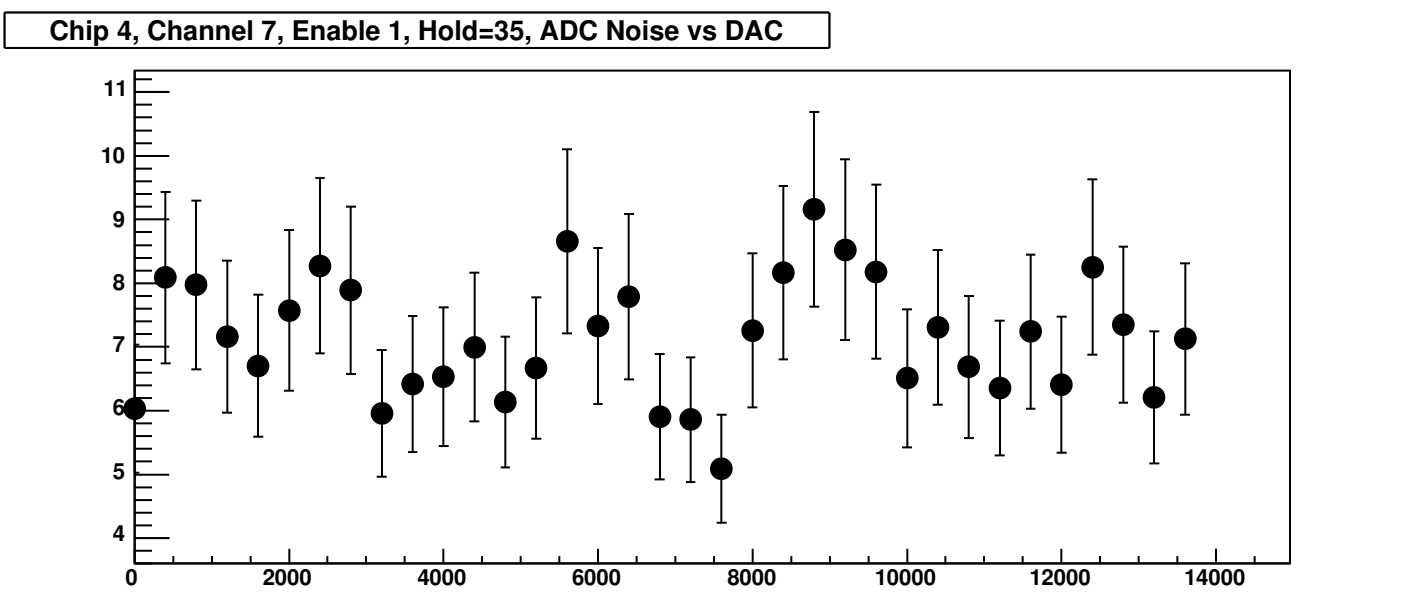

Chip 4, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

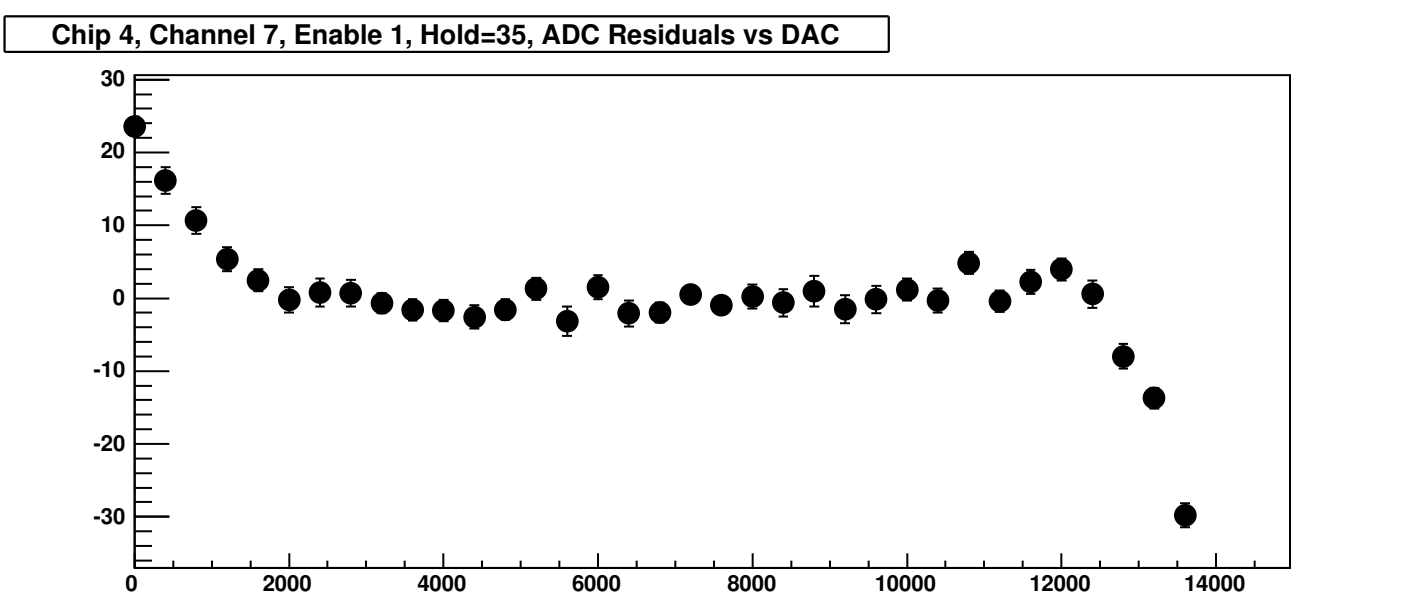

Chip 4, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 834.8 / 23
p0 43.1 ± 1.291
p1 0.1785 ± 0.0002093
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

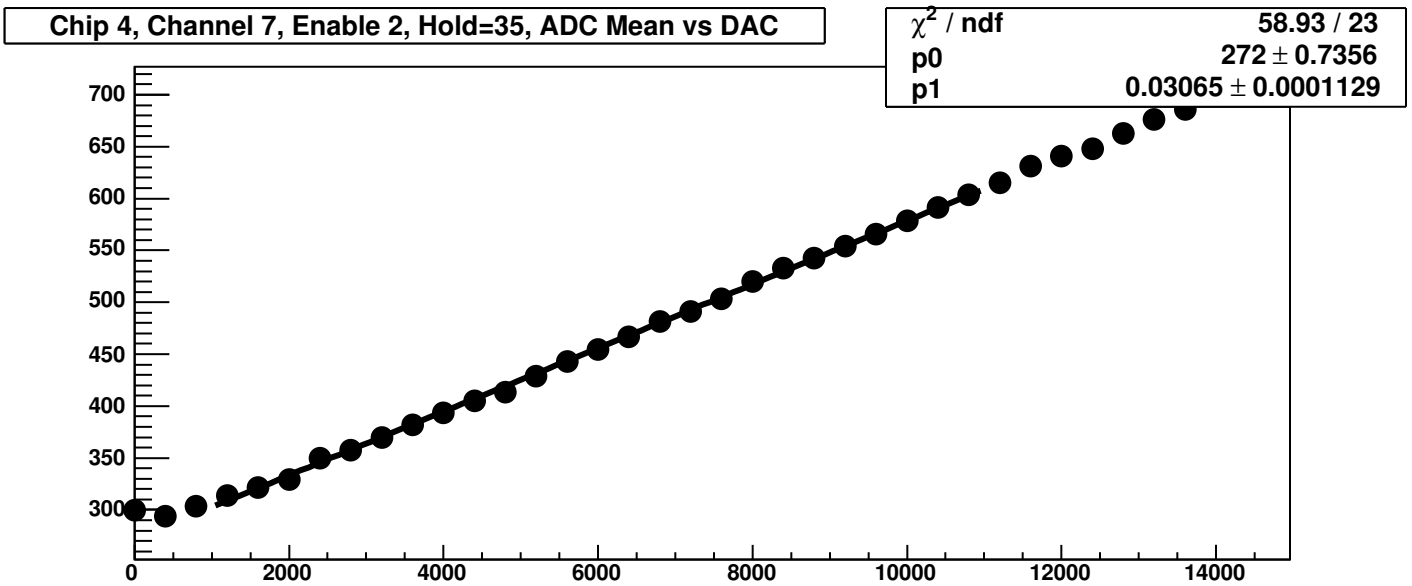

Chip 4, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

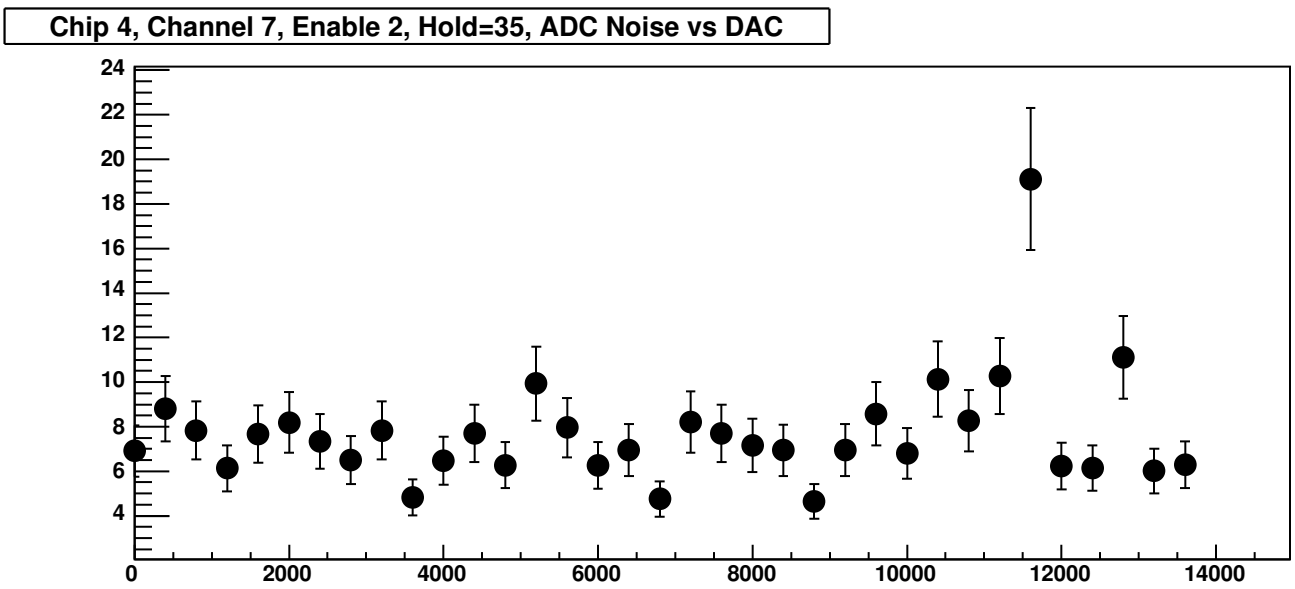

Chip 4, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

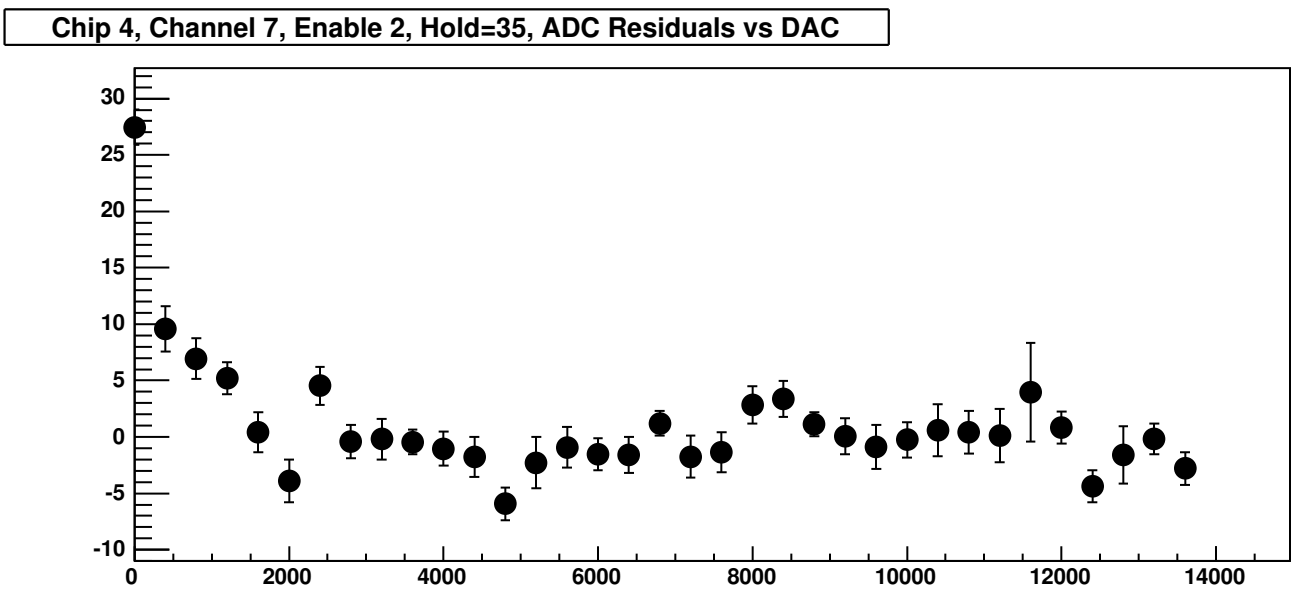

Chip 4, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 40.04 / 23
p0 265.2 ± 0.7478
p1 0.01847 ± 0.0001136
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

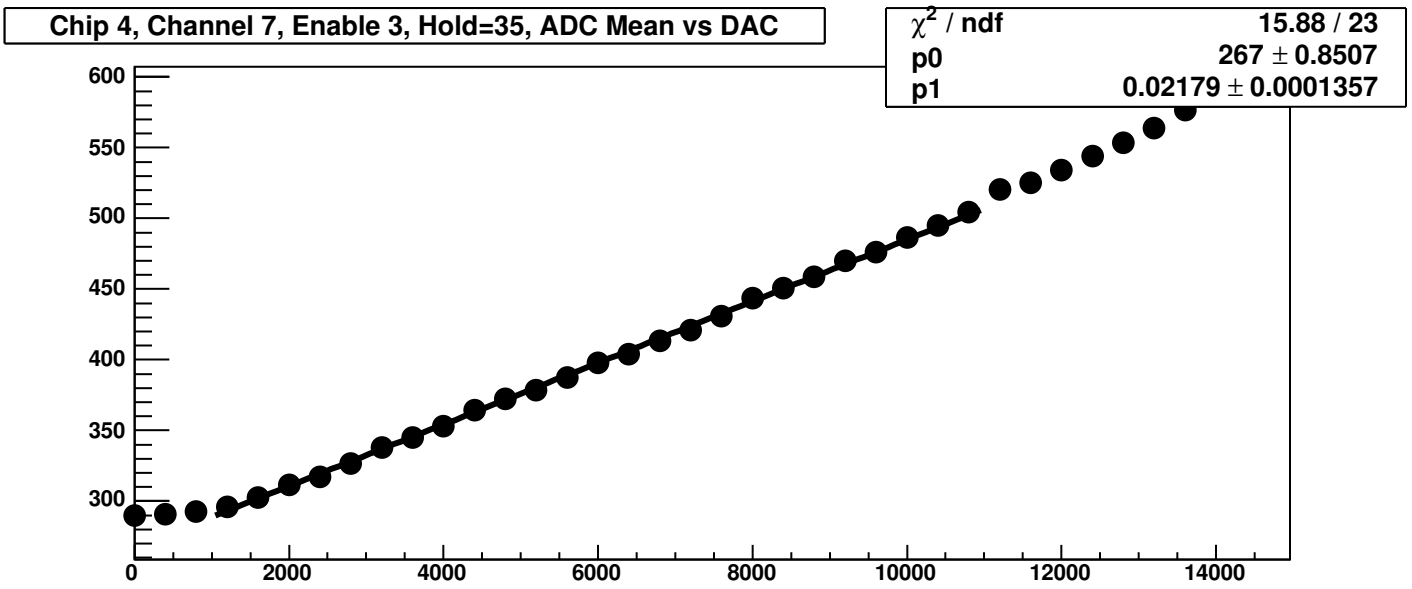

Chip 4, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

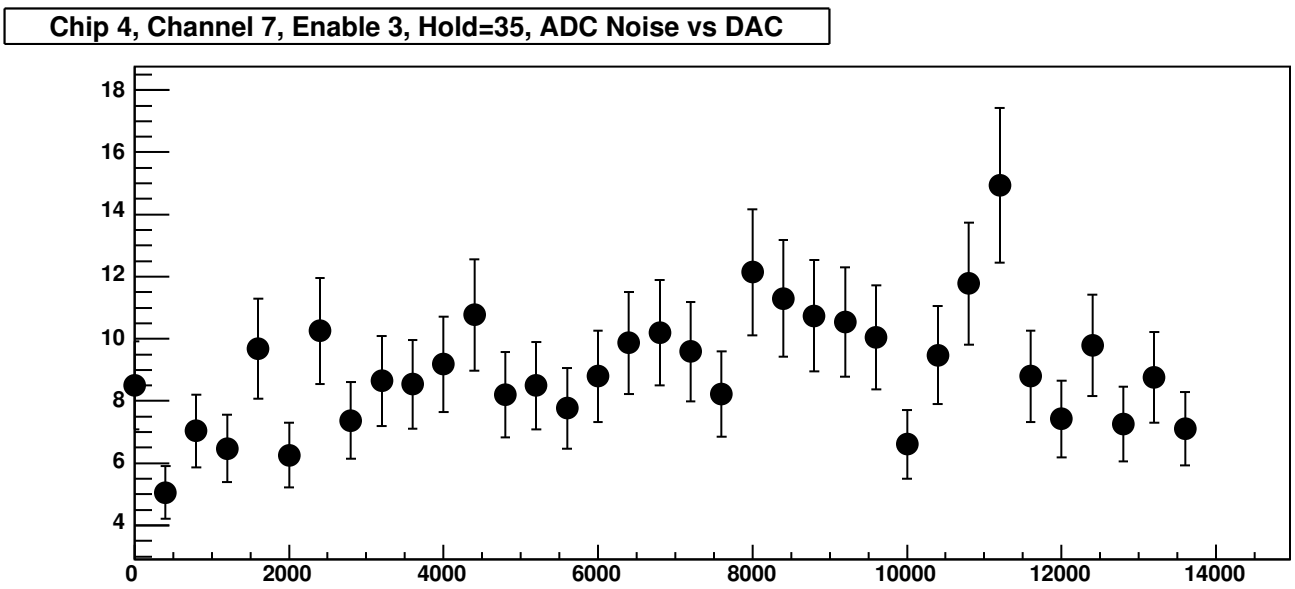

Chip 4, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

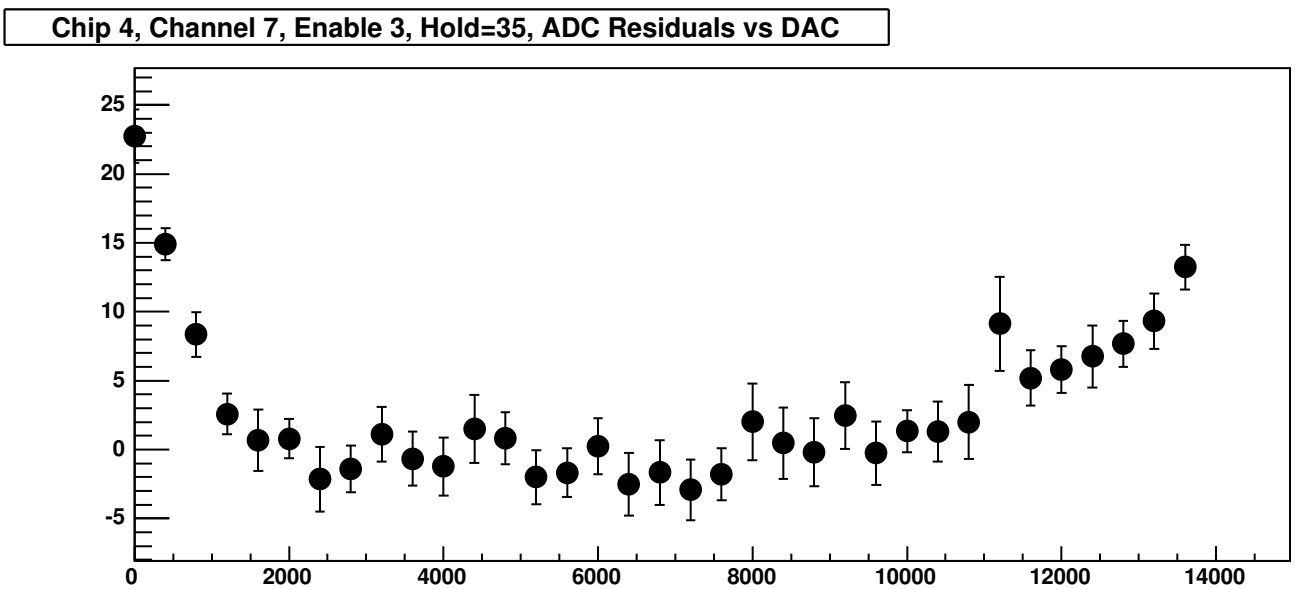

Chip 4, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 58.93 / 23
p0 272 ± 0.7356
p1 0.03065 ± 0.0001129
700
650
600
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

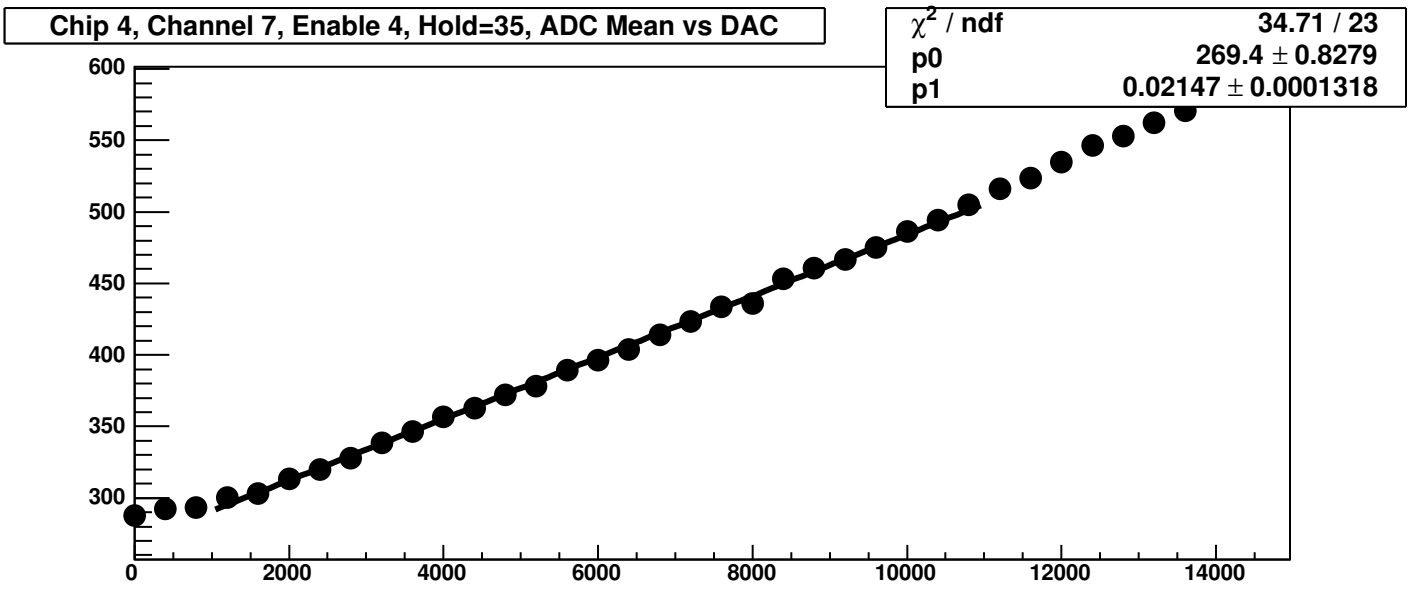

Chip 4, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
24
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

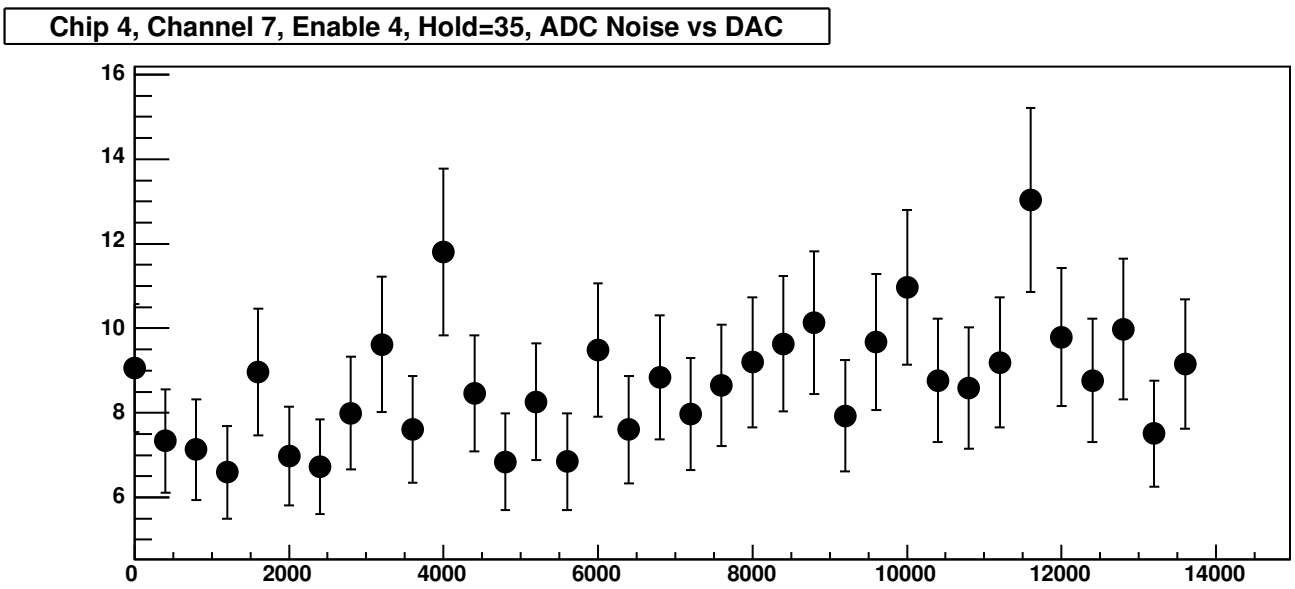

Chip 4, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

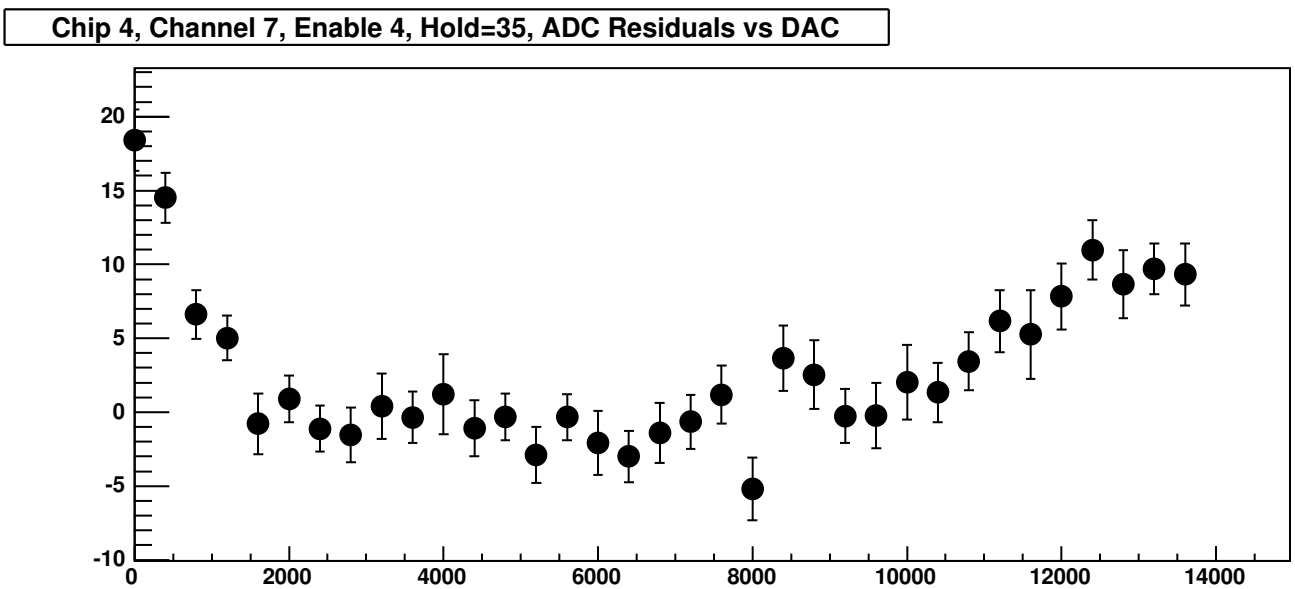

Chip 4, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 15.88 / 23
p0 267 ± 0.8507
p1 0.02179 ± 0.0001357
600
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

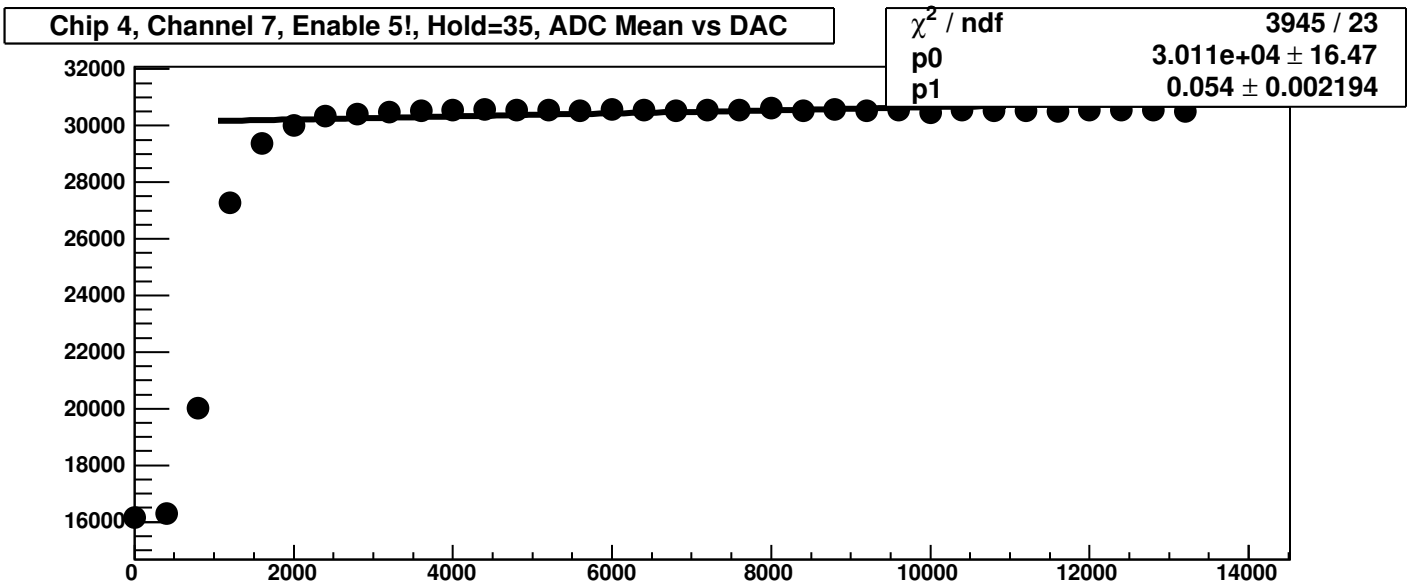

Chip 4, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

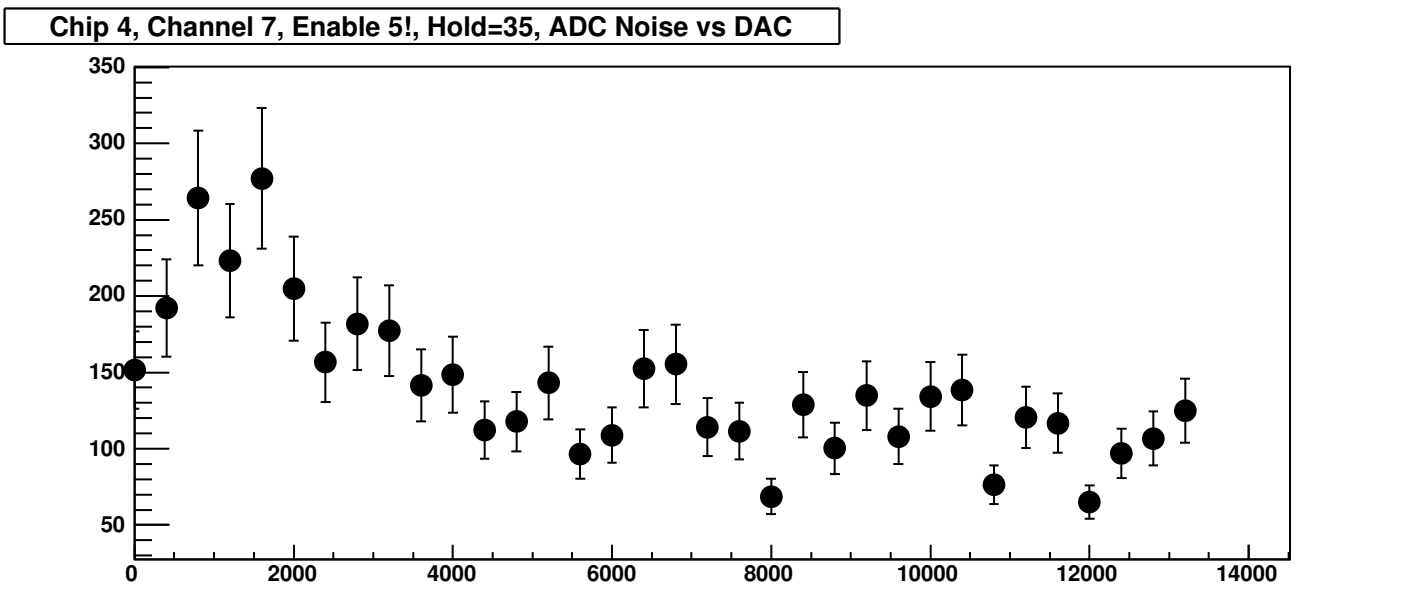

Chip 4, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

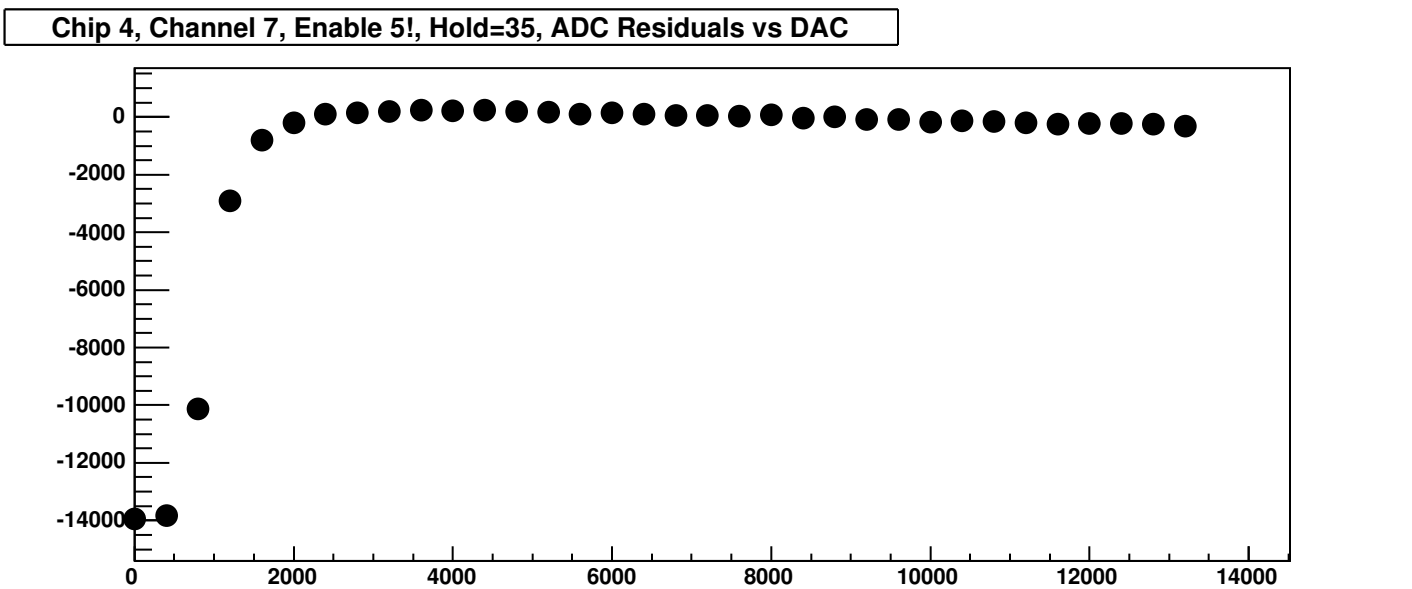

Chip 4, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 34.71 / 23
p0 269.4 ± 0.8279
p1 0.02147 ± 0.0001318
600
550
500
450
400
350
300
0
2000
4000
6000
8000
10000
12000
14000

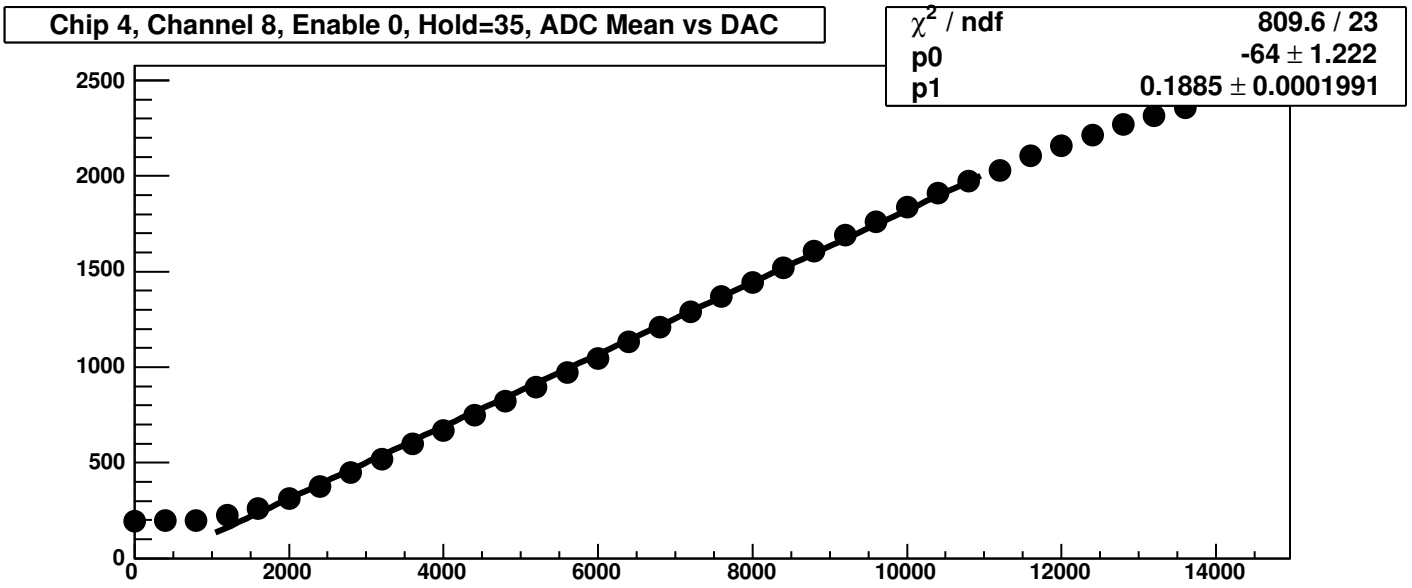

Chip 4, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

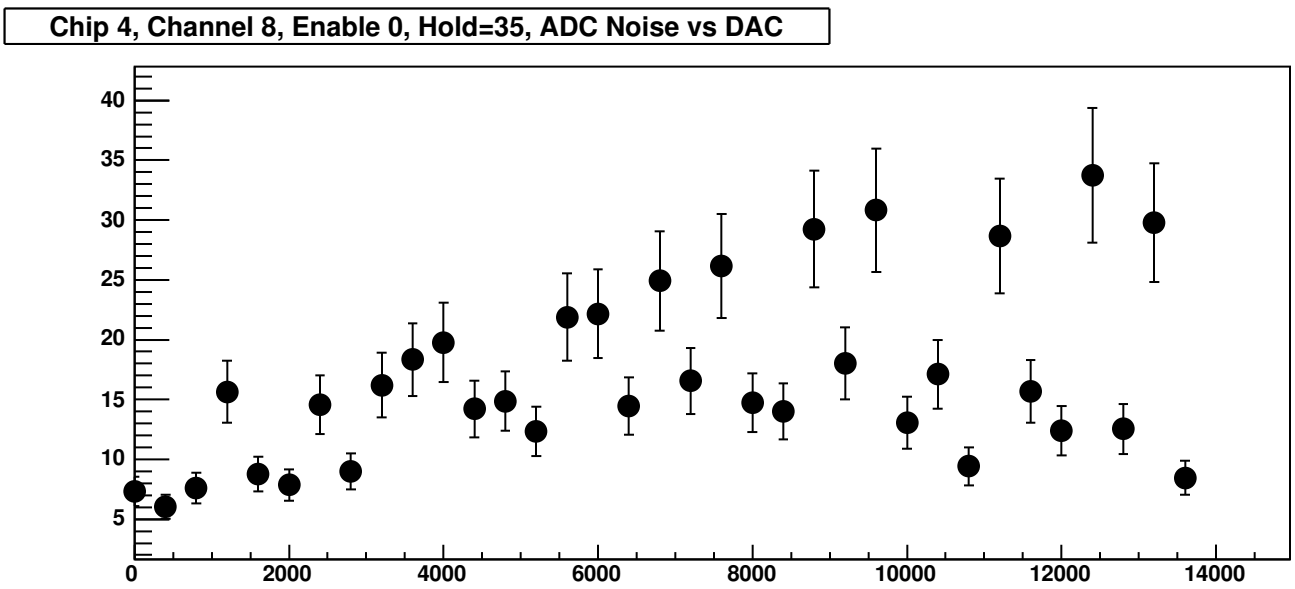

Chip 4, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

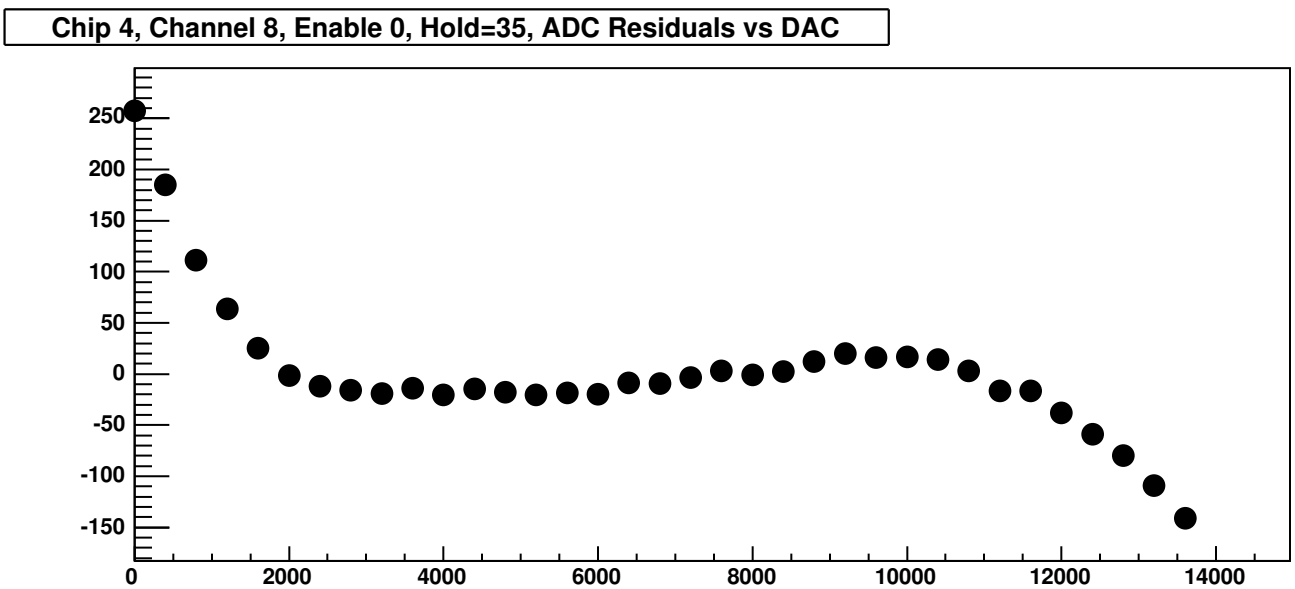

Chip 4, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3945 / 23
p0 3.011e+04 ± 16.47
p1 0.054 ± 0.002194
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

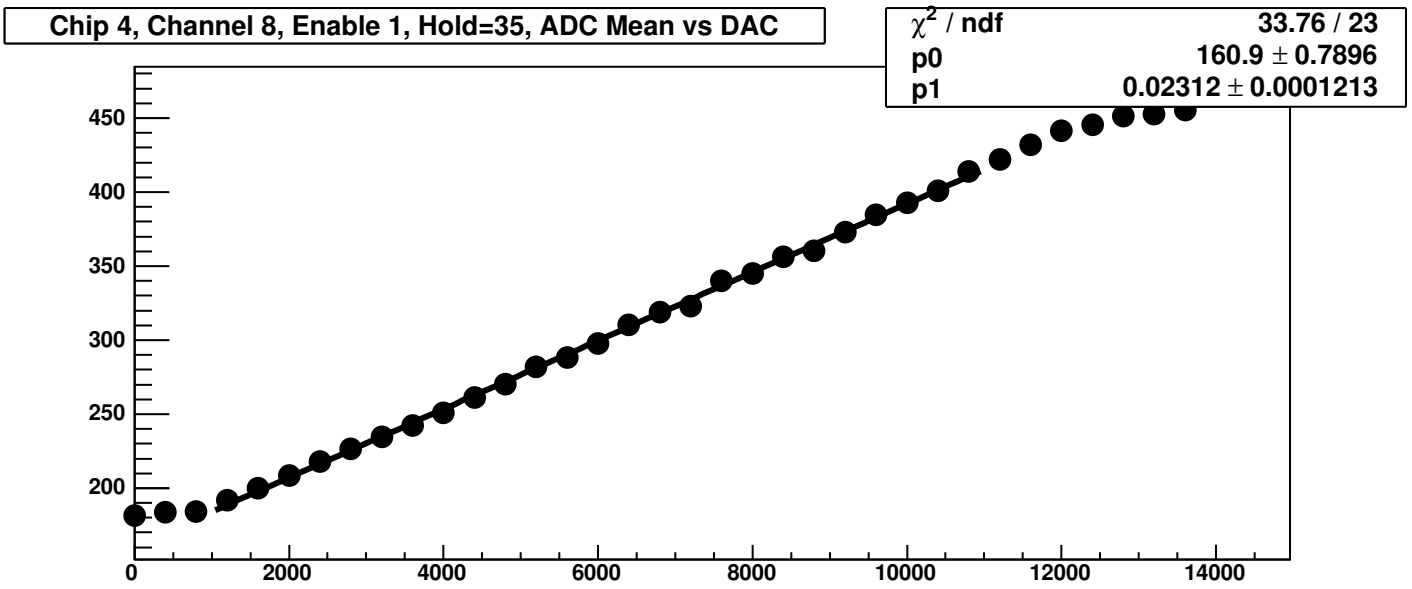

Chip 4, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

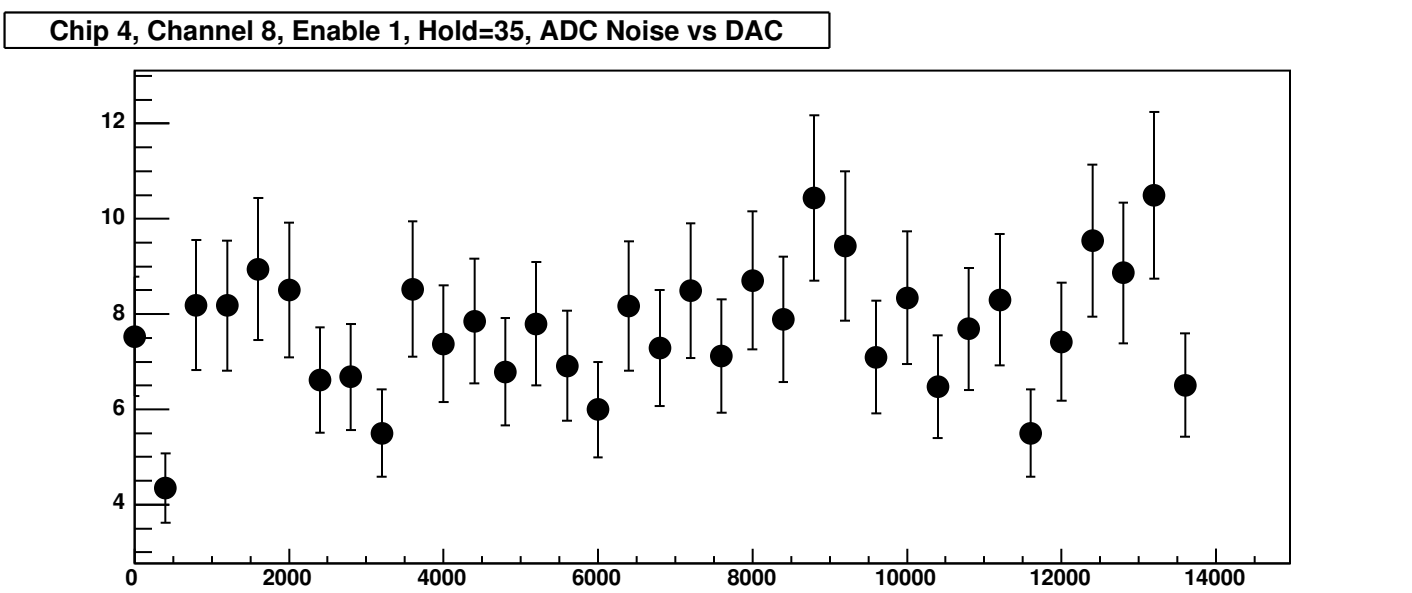

Chip 4, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

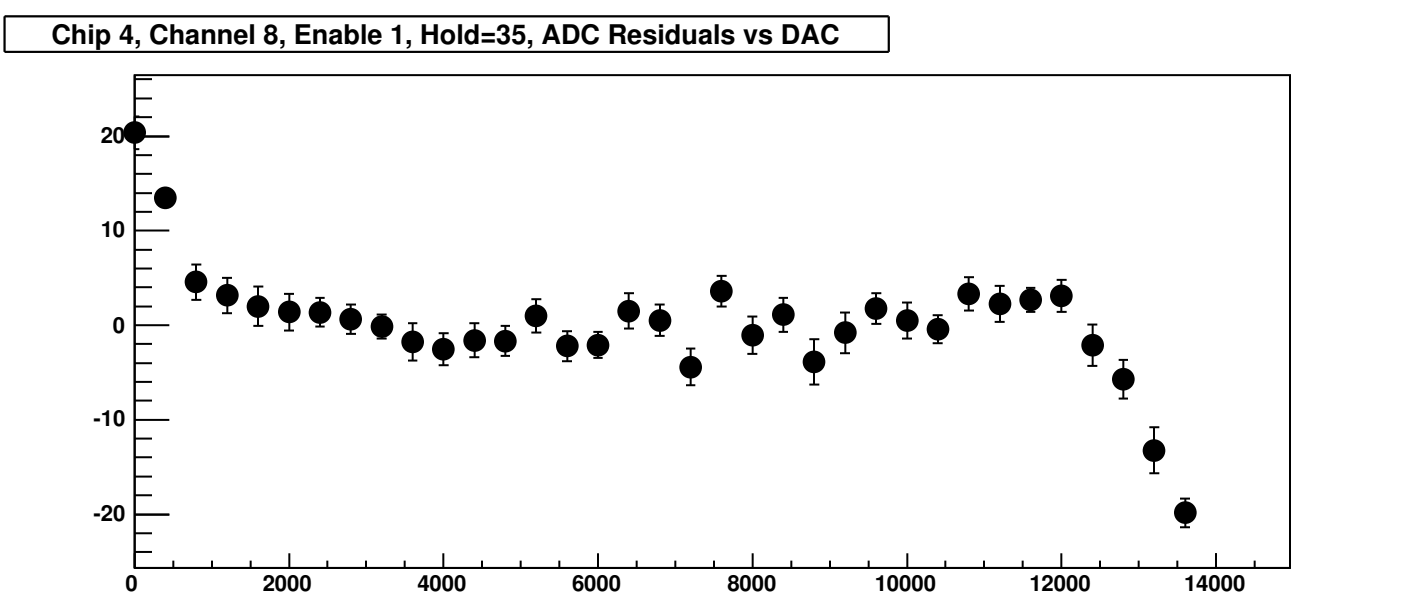

Chip 4, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 809.6 / 23
p0 -64 ± 1.222
p1 0.1885 ± 0.0001991
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

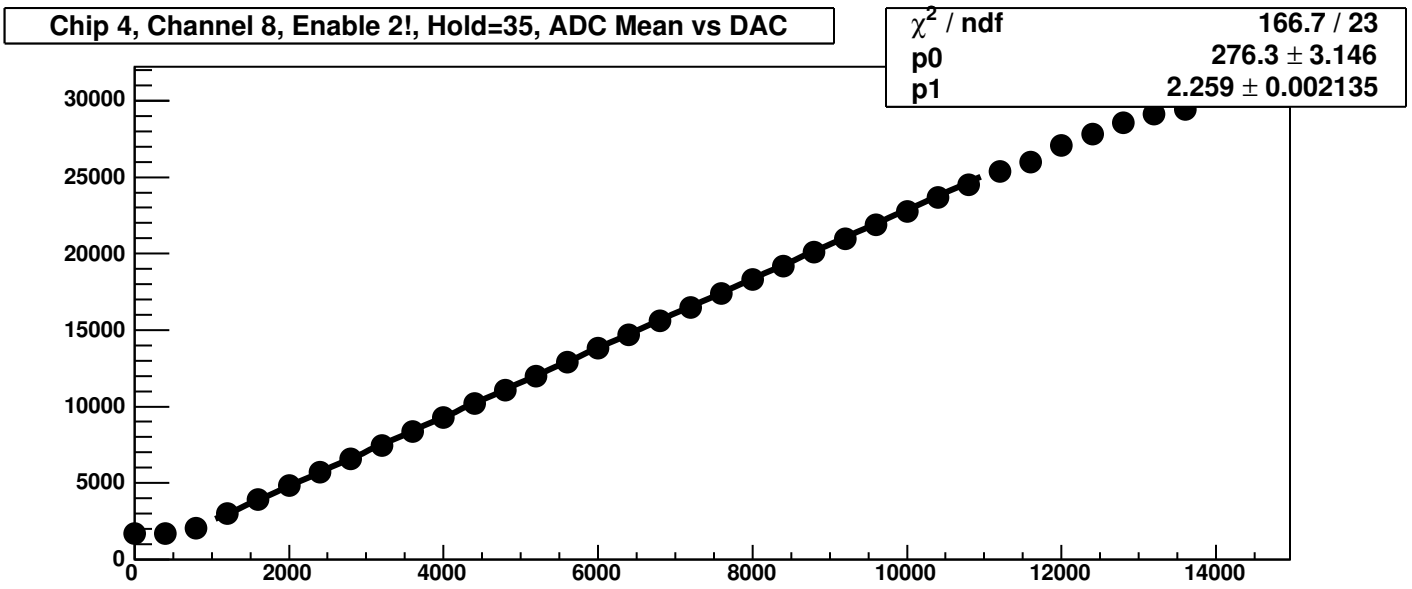

Chip 4, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

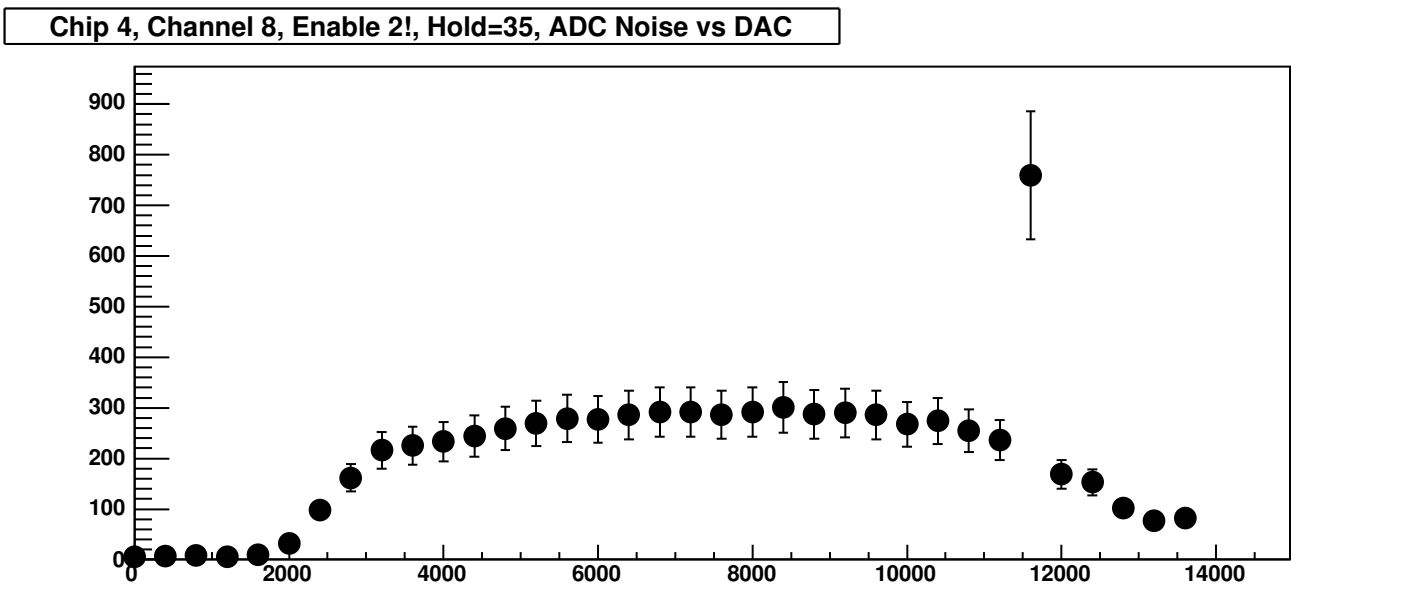

Chip 4, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

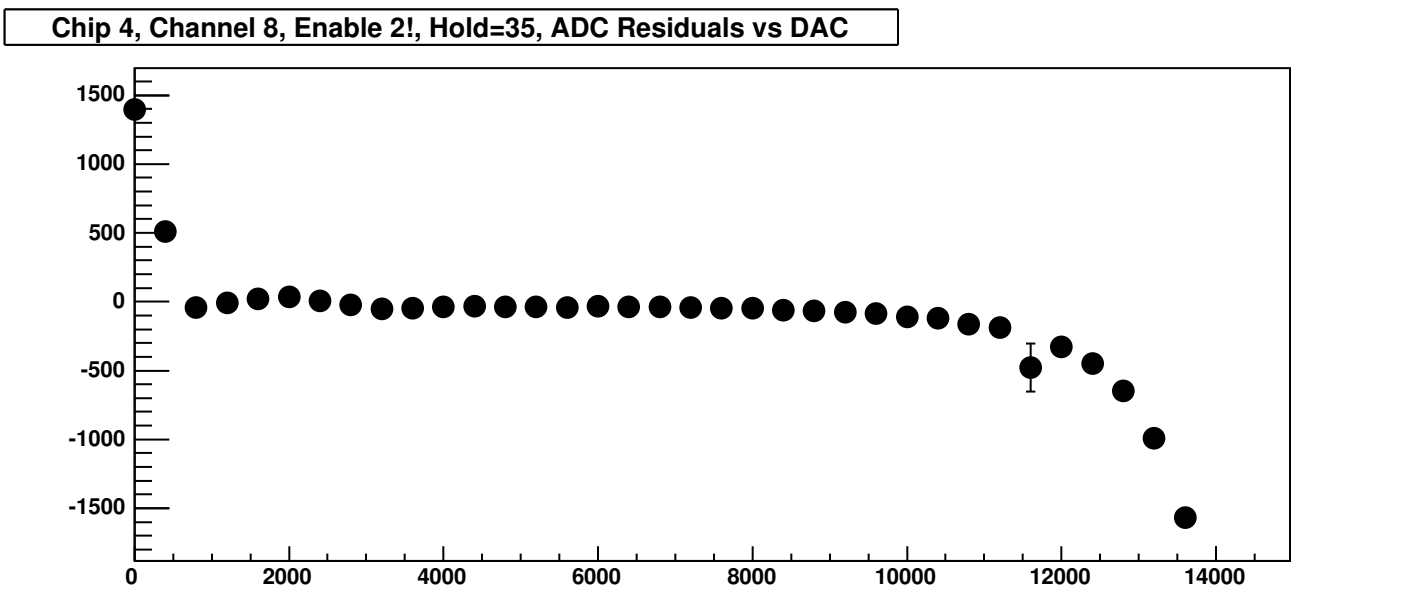

Chip 4, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.76 / 23
p0 160.9 ± 0.7896
p1 0.02312 ± 0.0001213
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

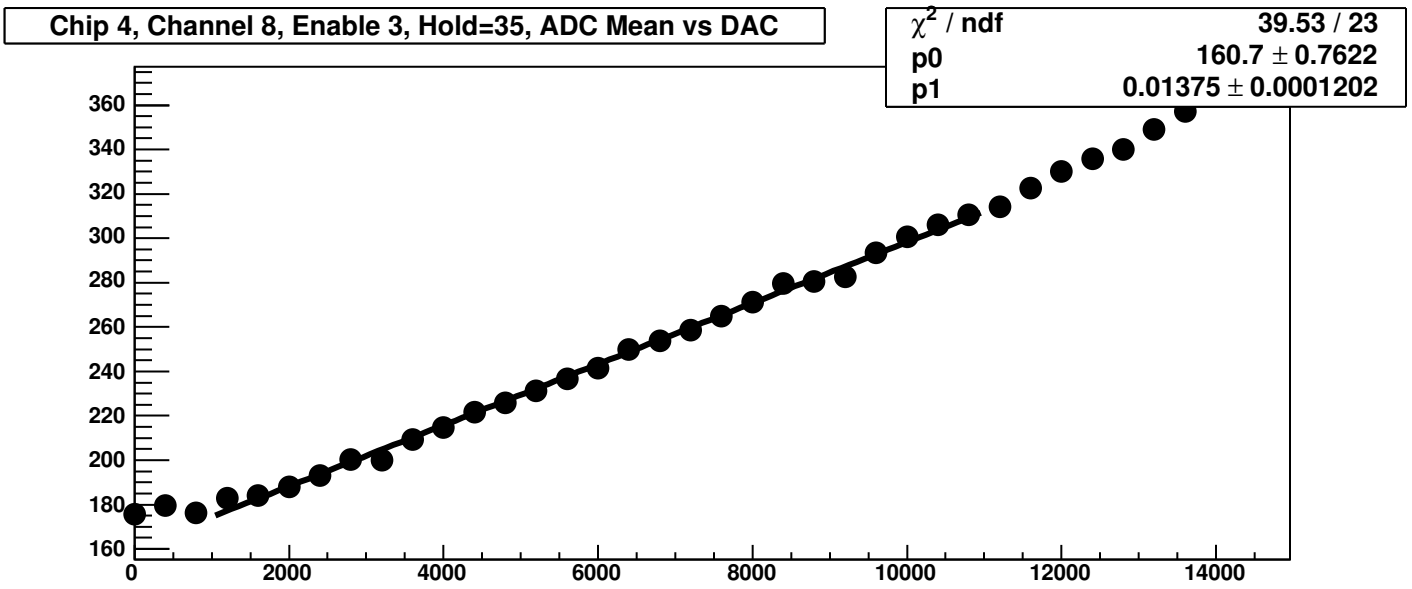

Chip 4, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

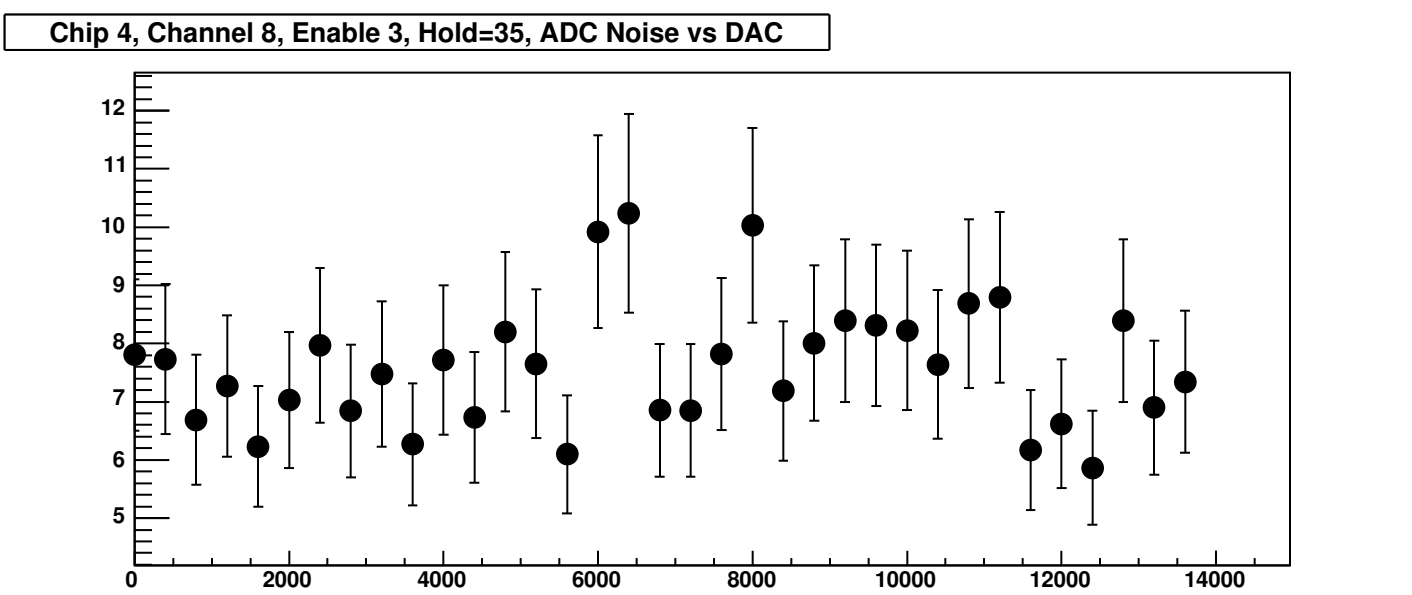

Chip 4, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

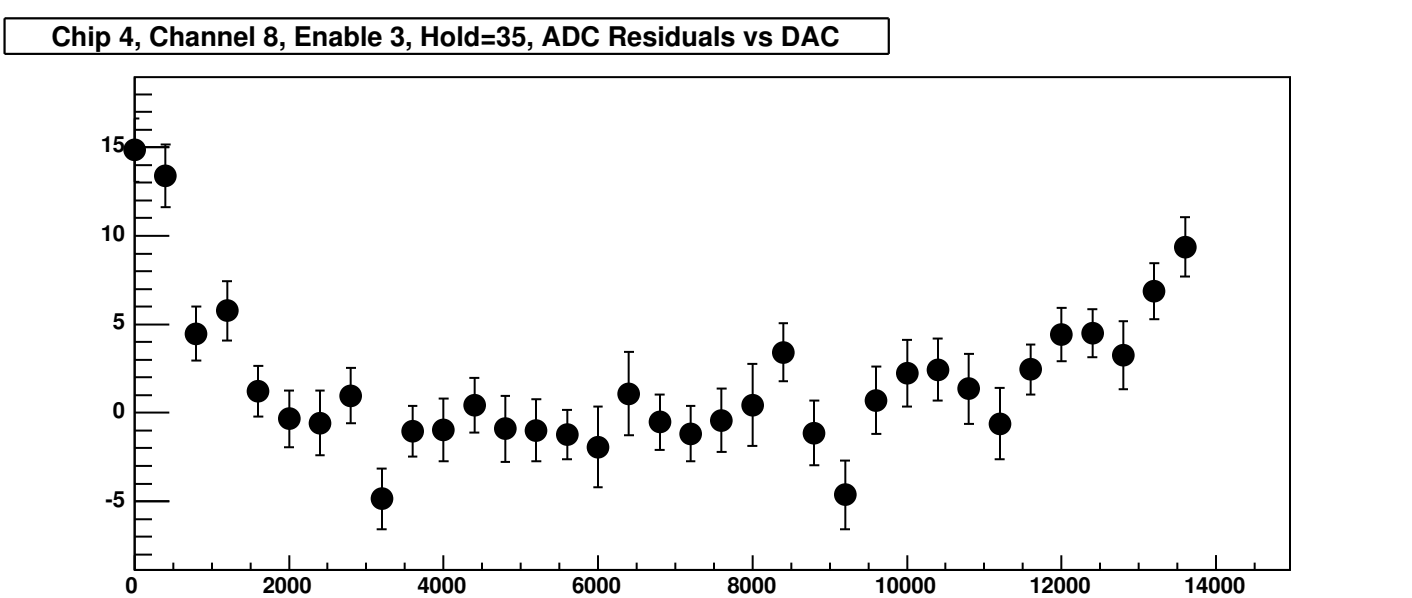

Chip 4, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 166.7 / 23
p0 276.3 ± 3.146
p1 2.259 ± 0.002135
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
900
800
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

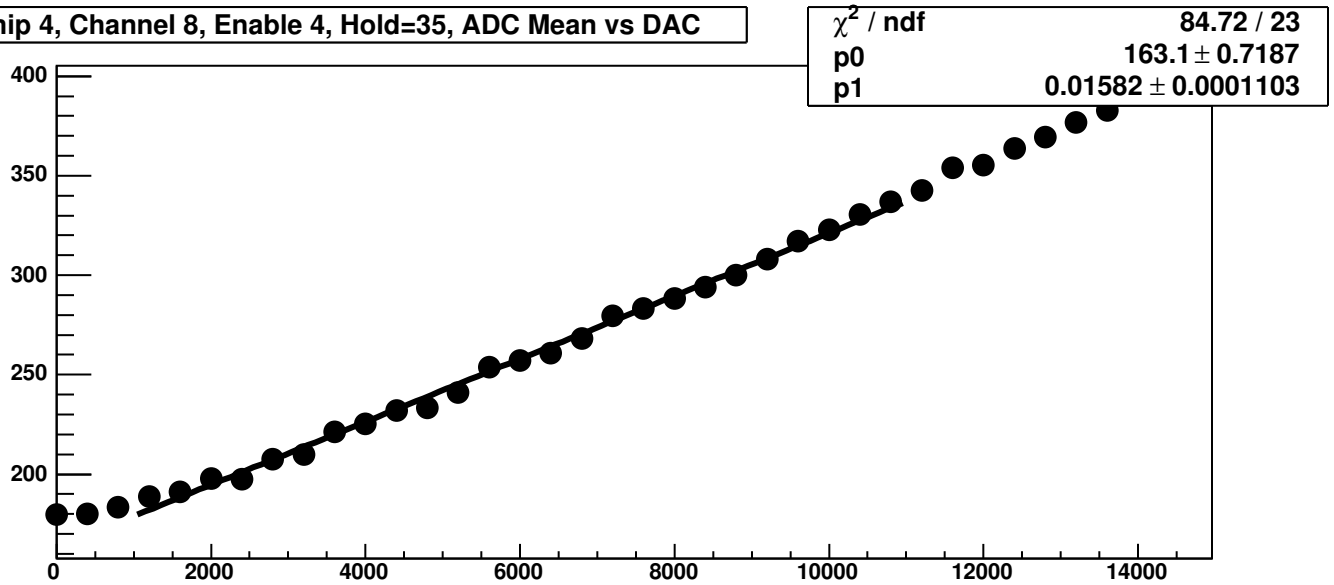

Chip 4, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 39.53 / 23
p0 160.7 ± 0.7622
p1 0.01375 ± 0.0001202
360
340
320
300
280
260
240
220
200
180
160
0
2000
4000
6000
8000
10000
12000
14000

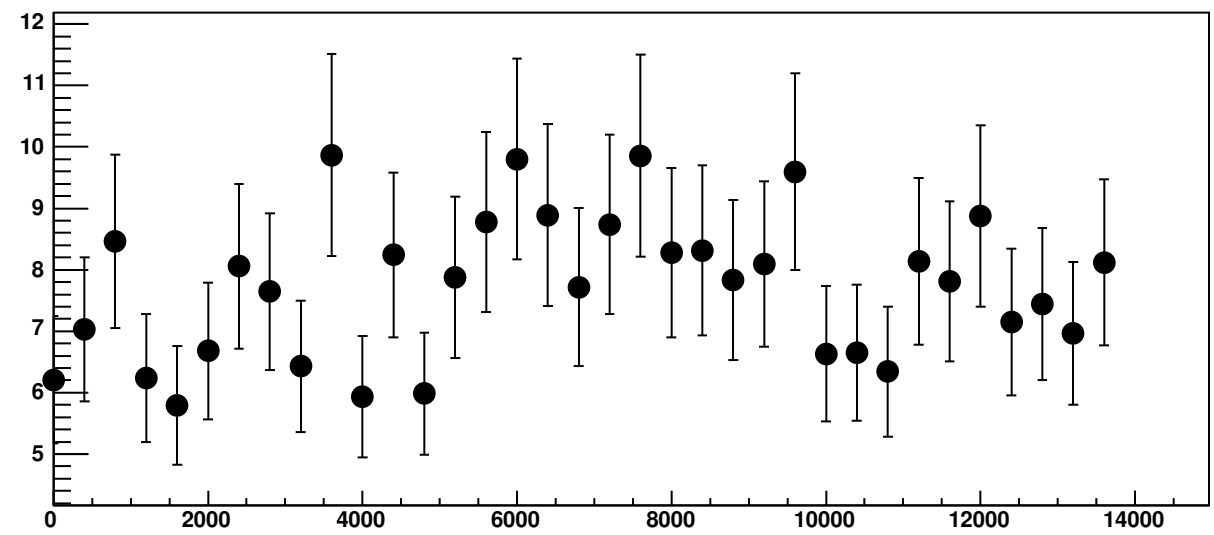

Chip 4, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

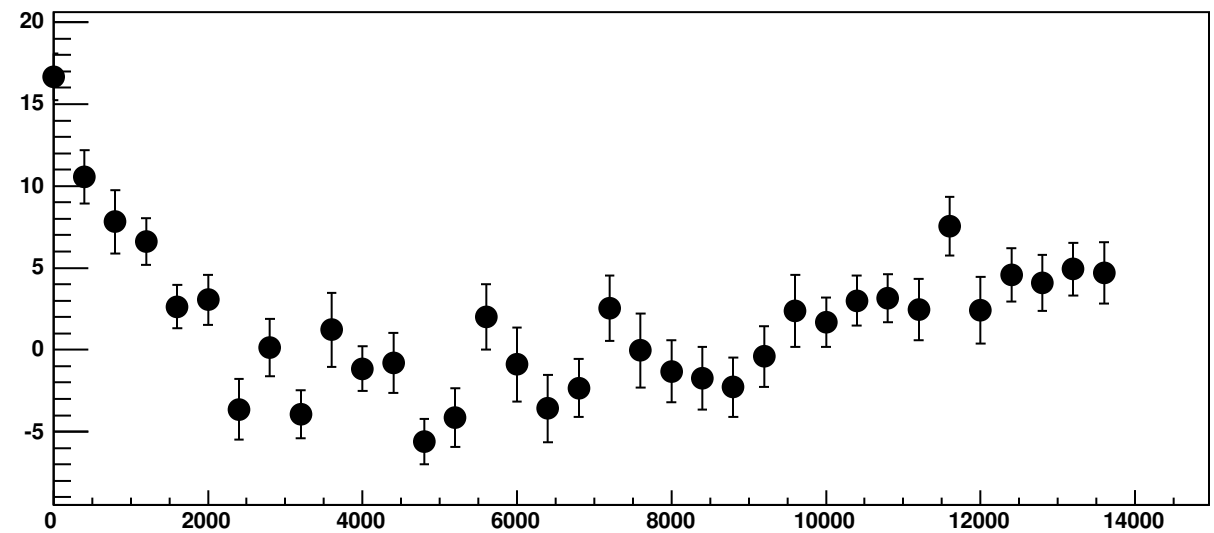
Chip 4, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

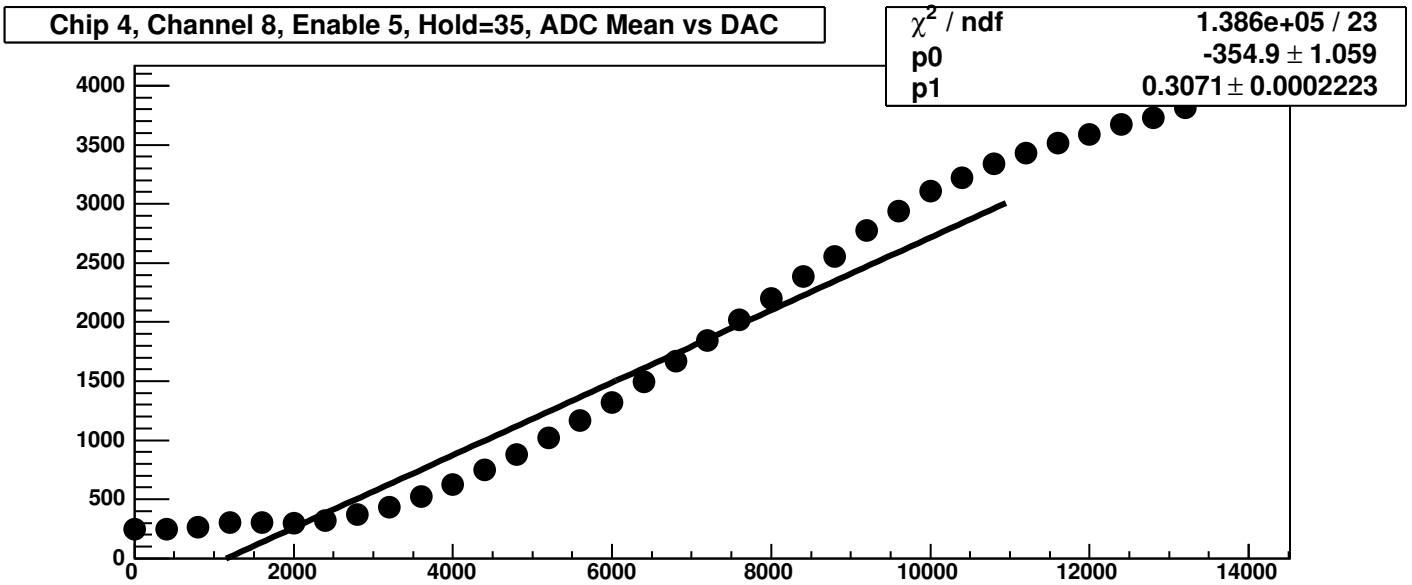


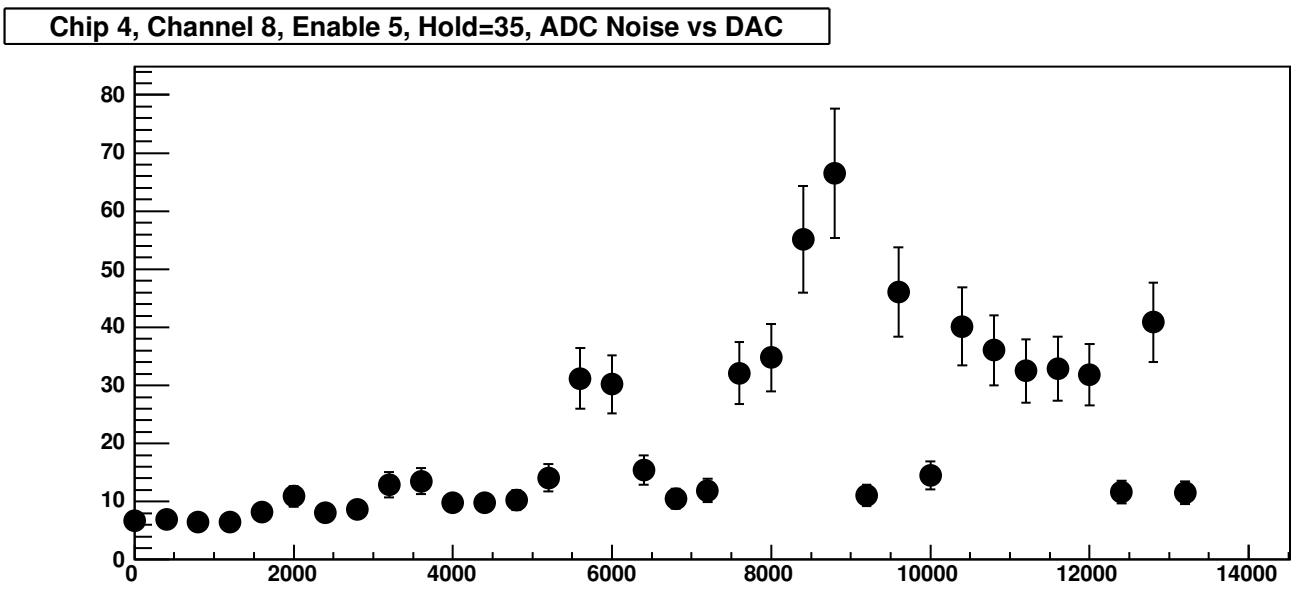
Chip 4, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC

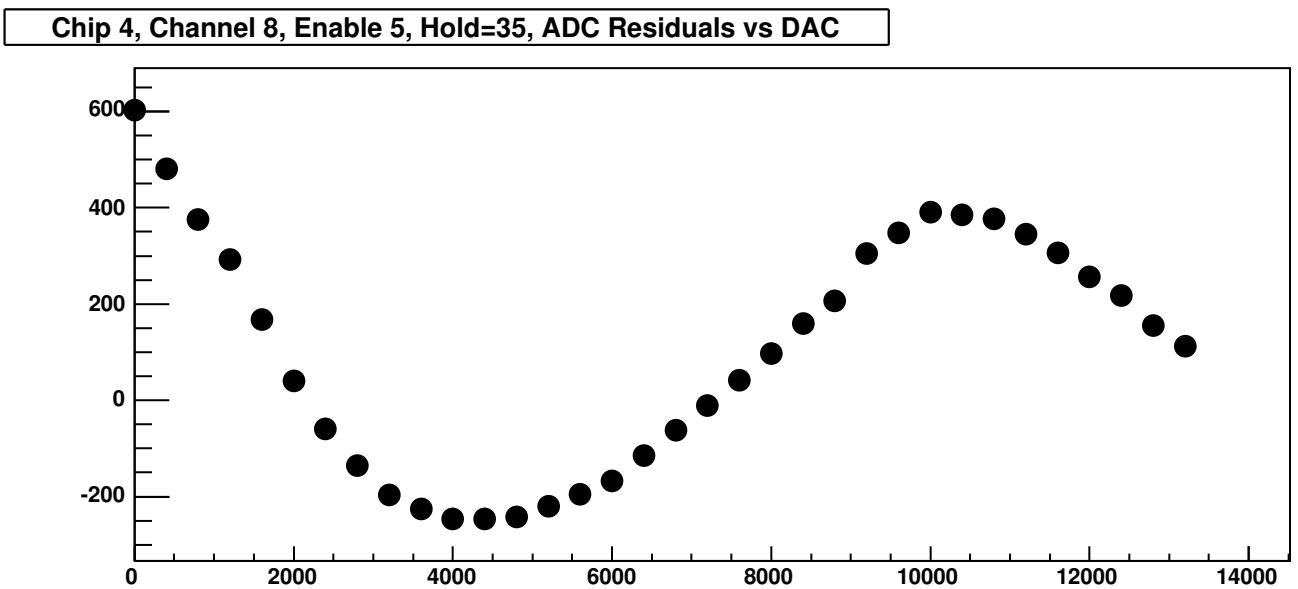


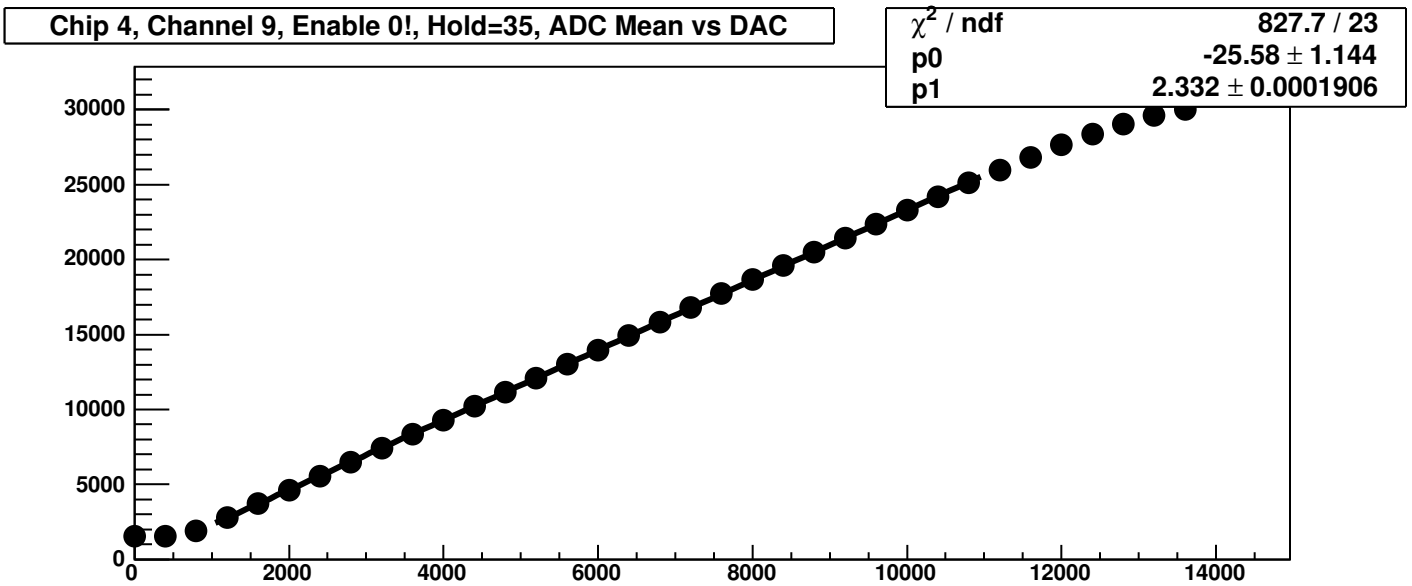
Chip 4, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

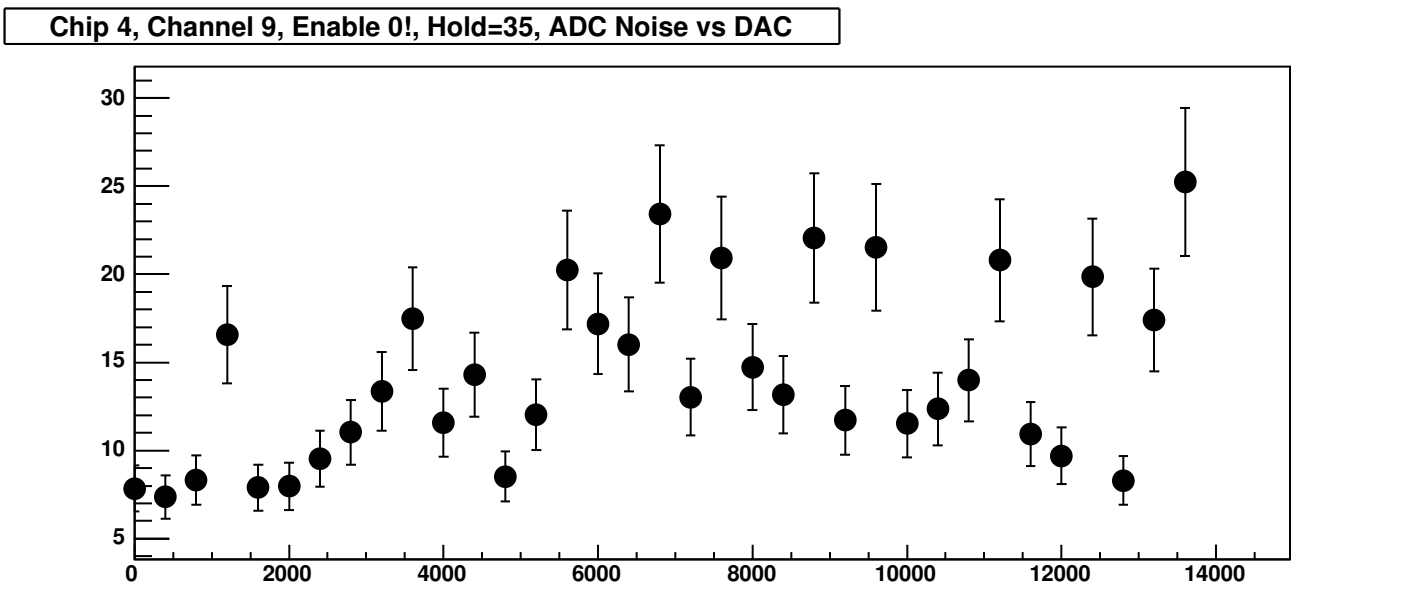


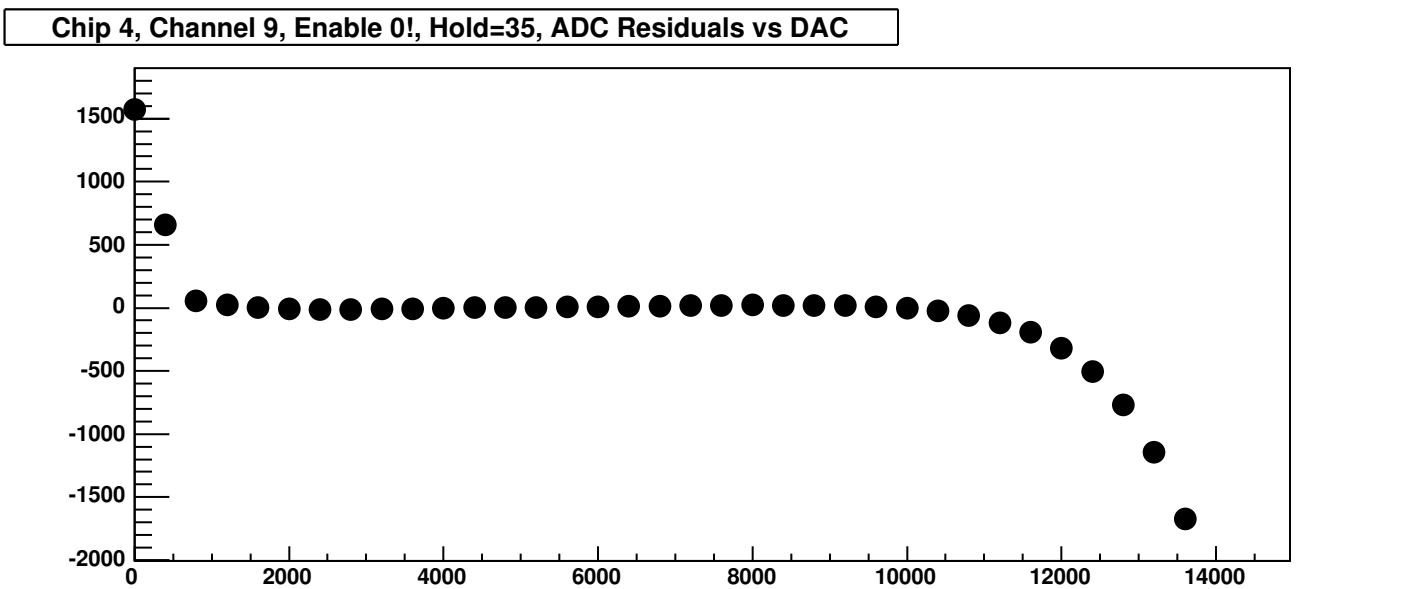

Chip 4, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.386e+05 / 23
p0 -354.9 ± 1.059
p1 0.3071 ± 0.0002223
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

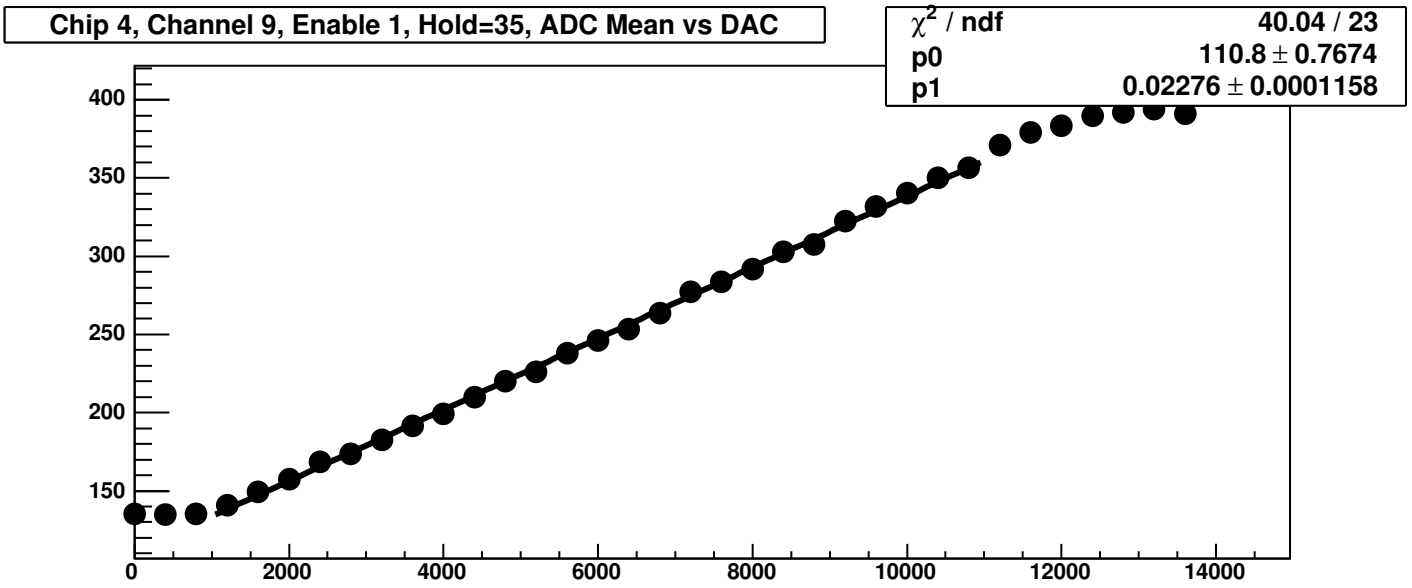

Chip 4, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

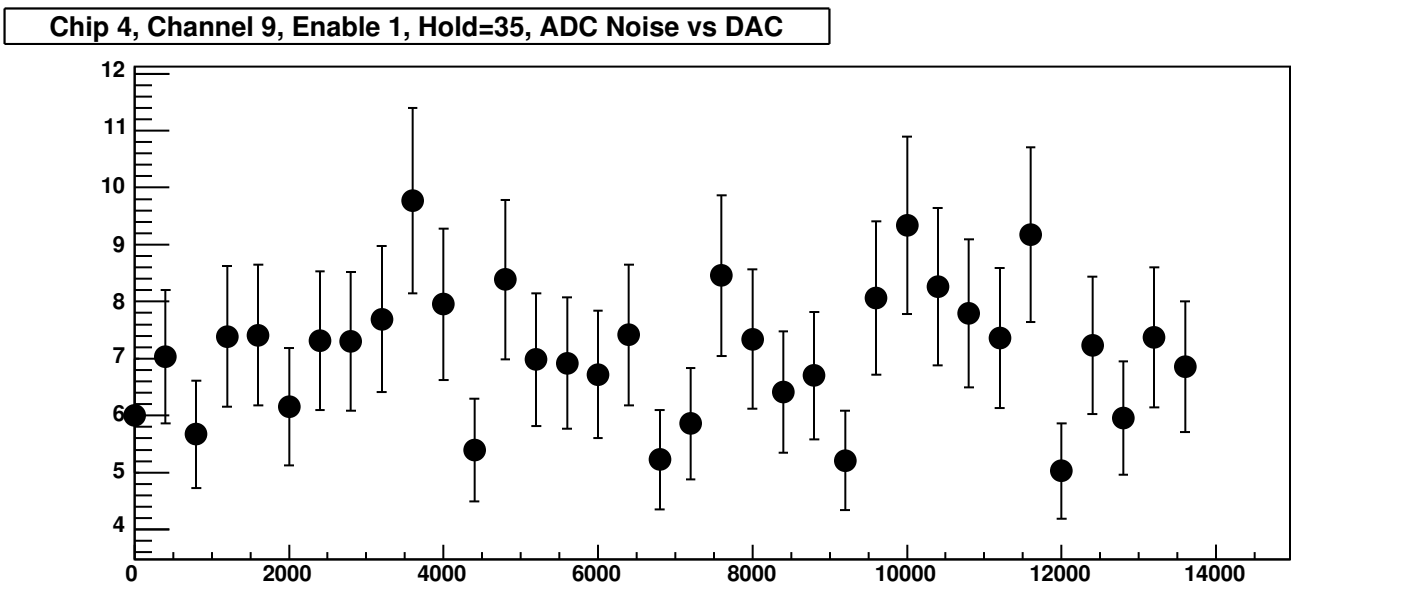

Chip 4, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

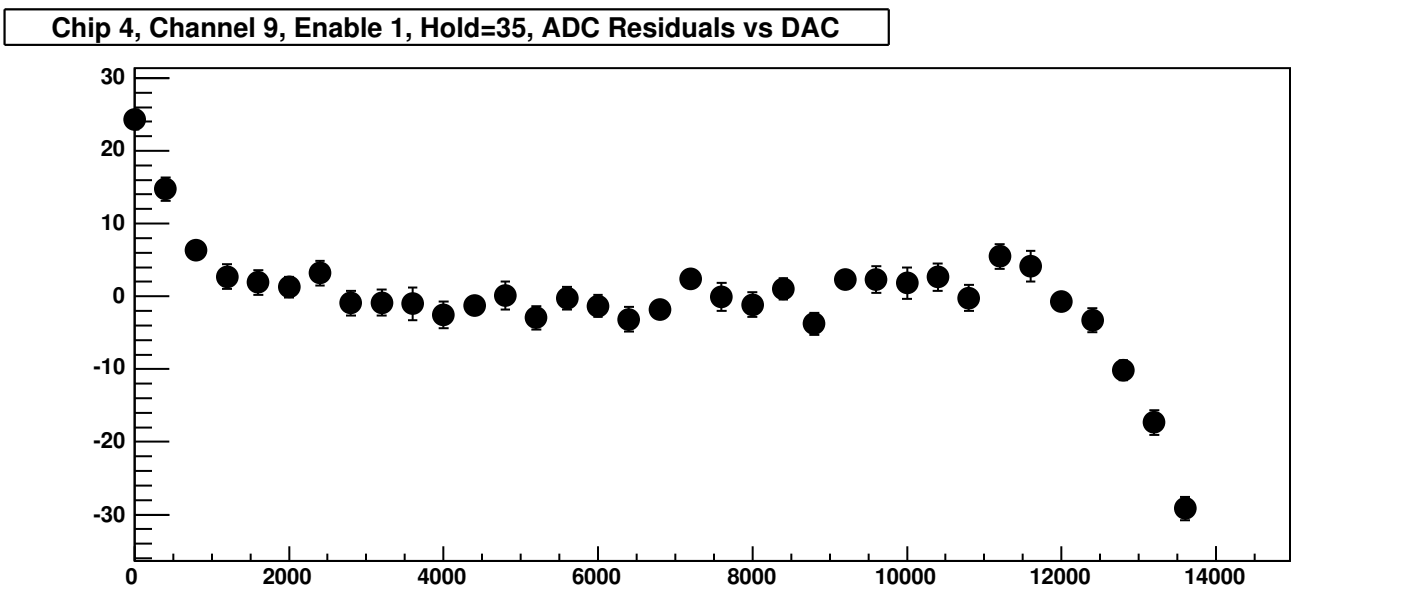

Chip 4, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 827.7 / 23
p0 -25.58 ± 1.144
p1 2.332 ± 0.0001906
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

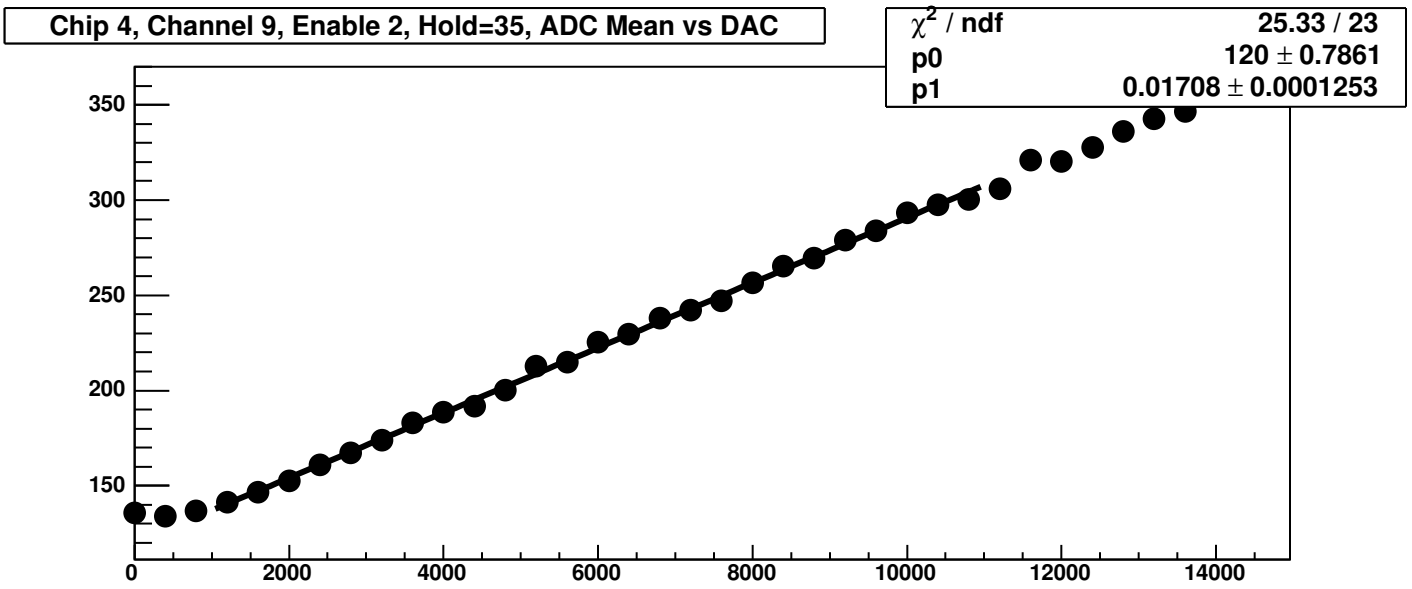

Chip 4, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

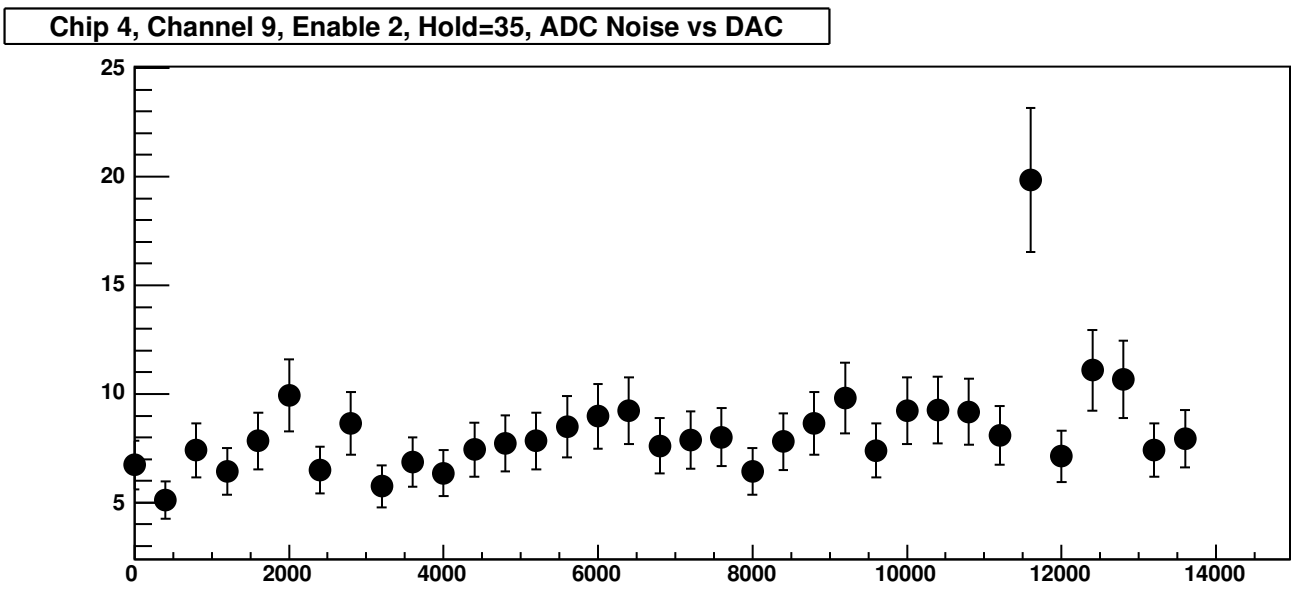

Chip 4, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

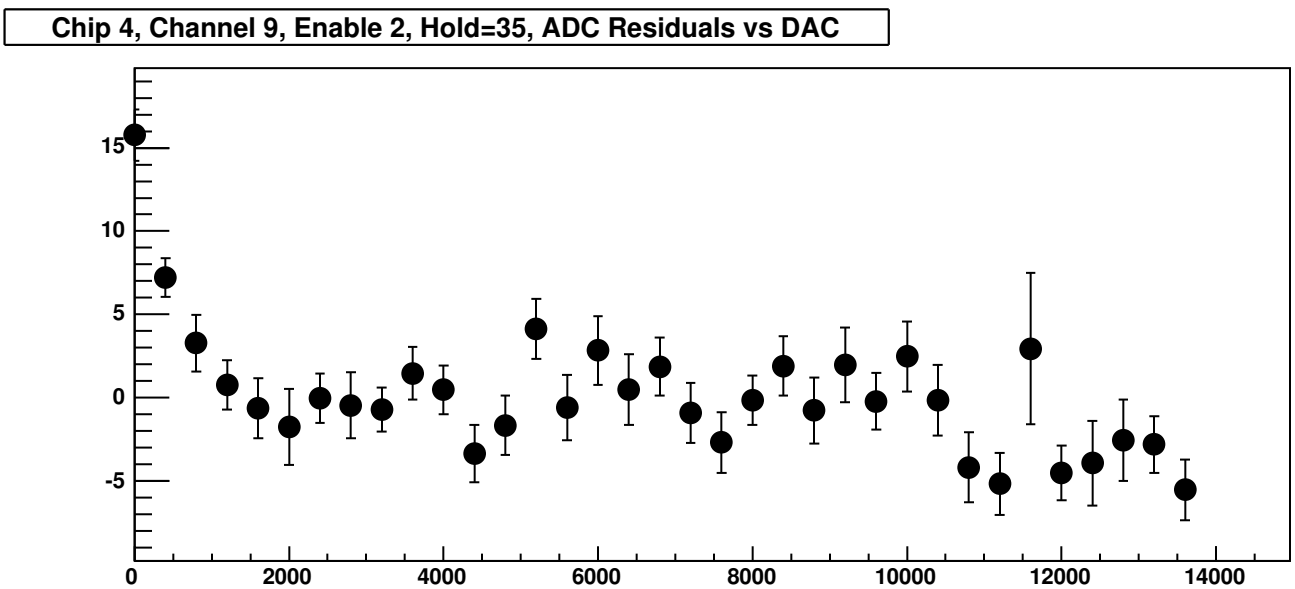

Chip 4, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 40.04 / 23
p0 110.8 ± 0.7674
p1 0.02276 ± 0.0001158
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

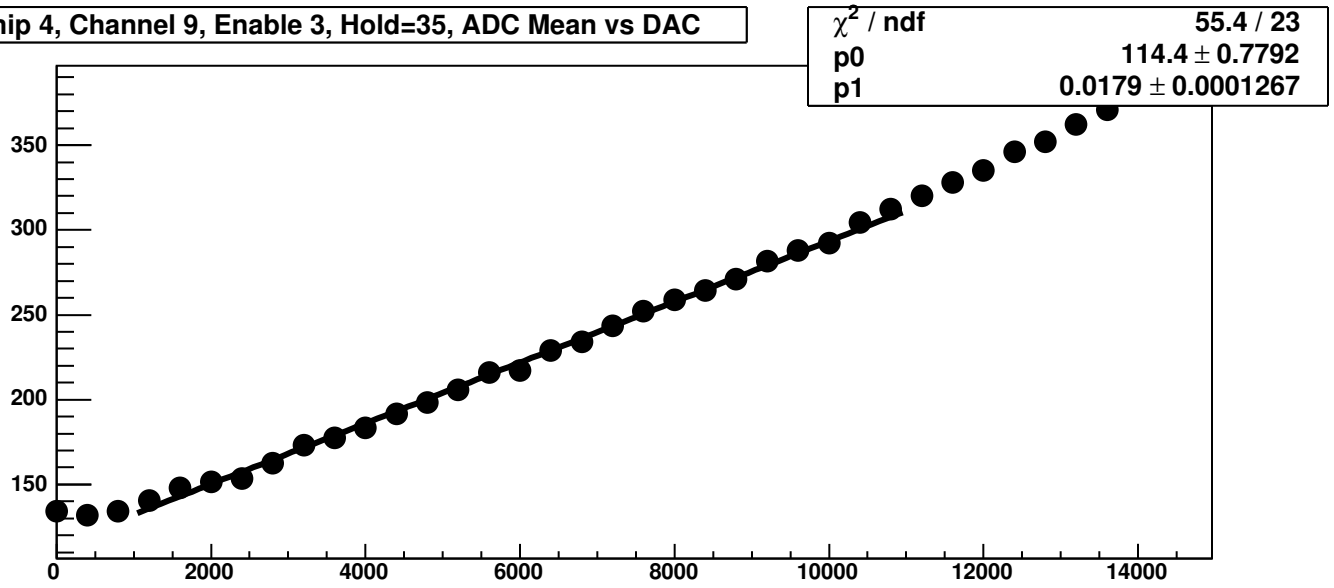

Chip 4, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 25.33 / 23
p0 120 ± 0.7861
p1 0.01708 ± 0.0001253
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

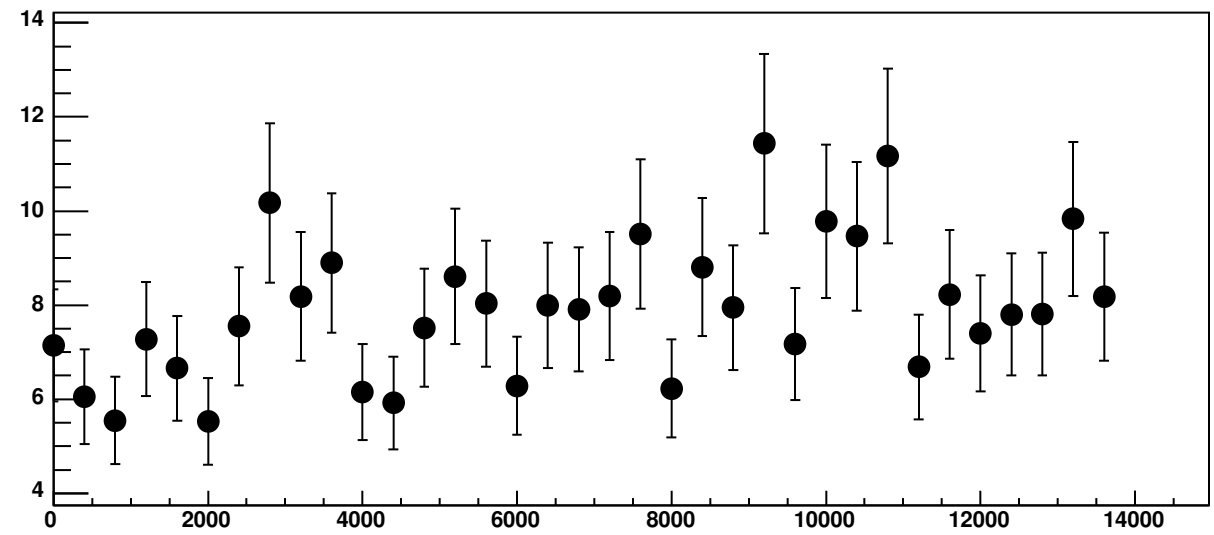

Chip 4, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

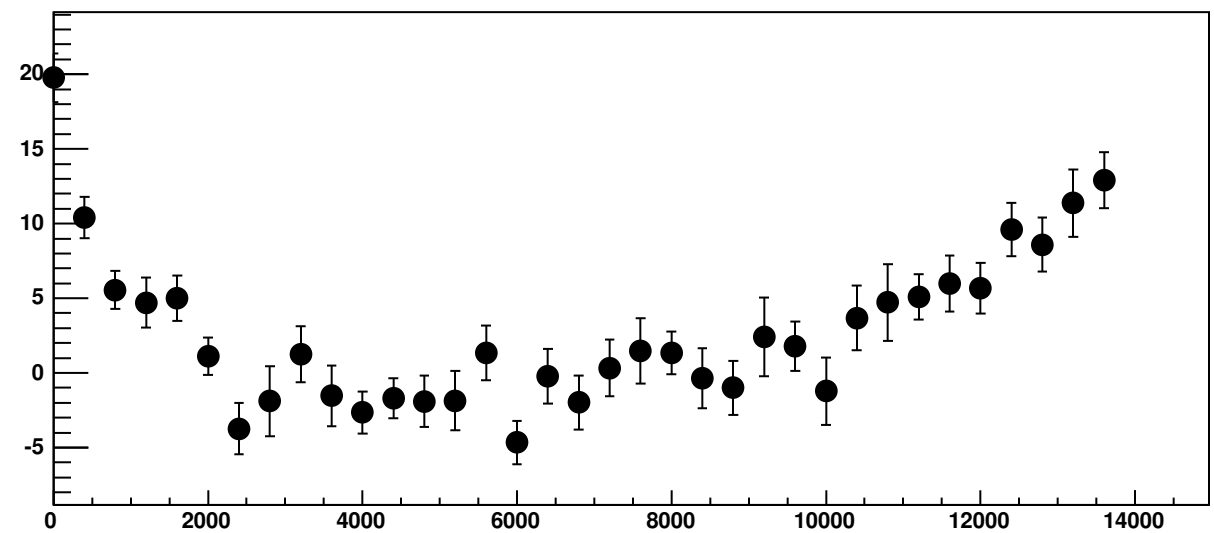
Chip 4, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

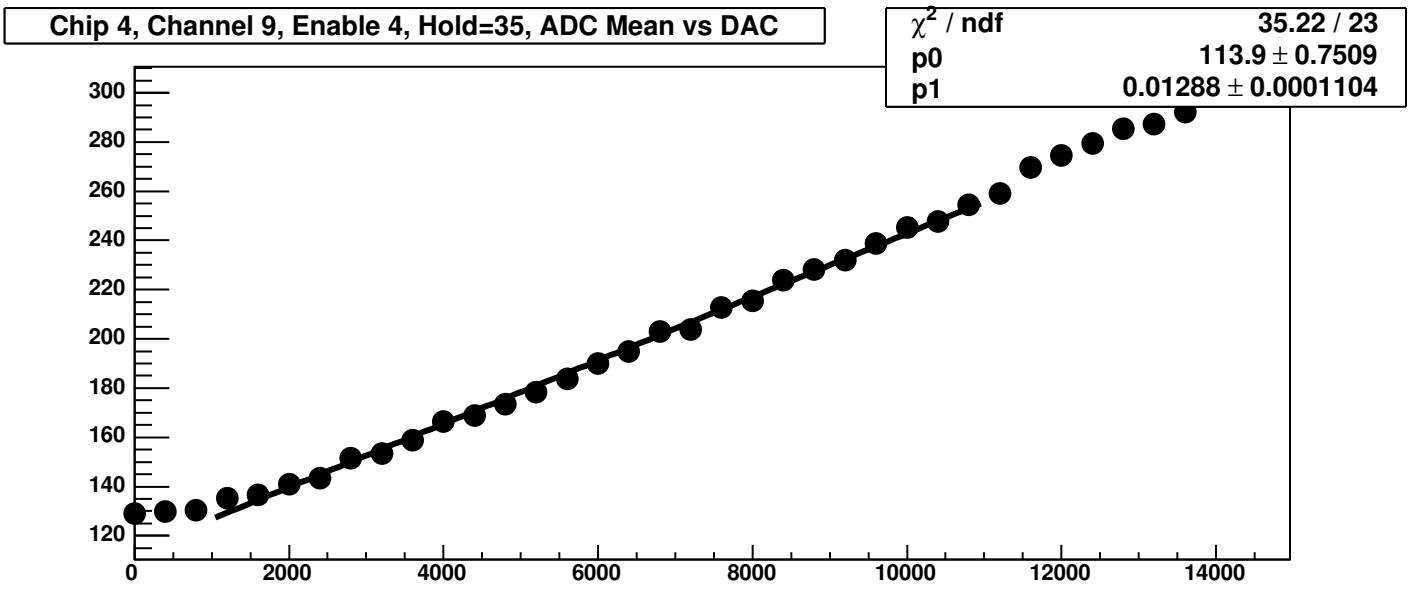


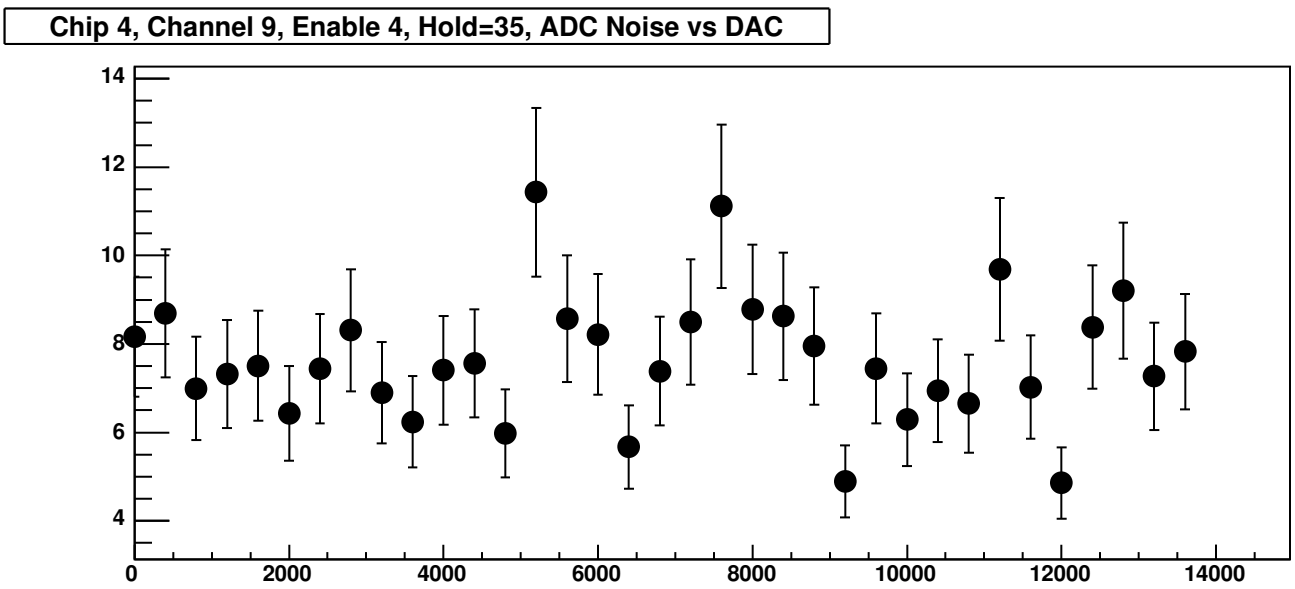
Chip 4, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC

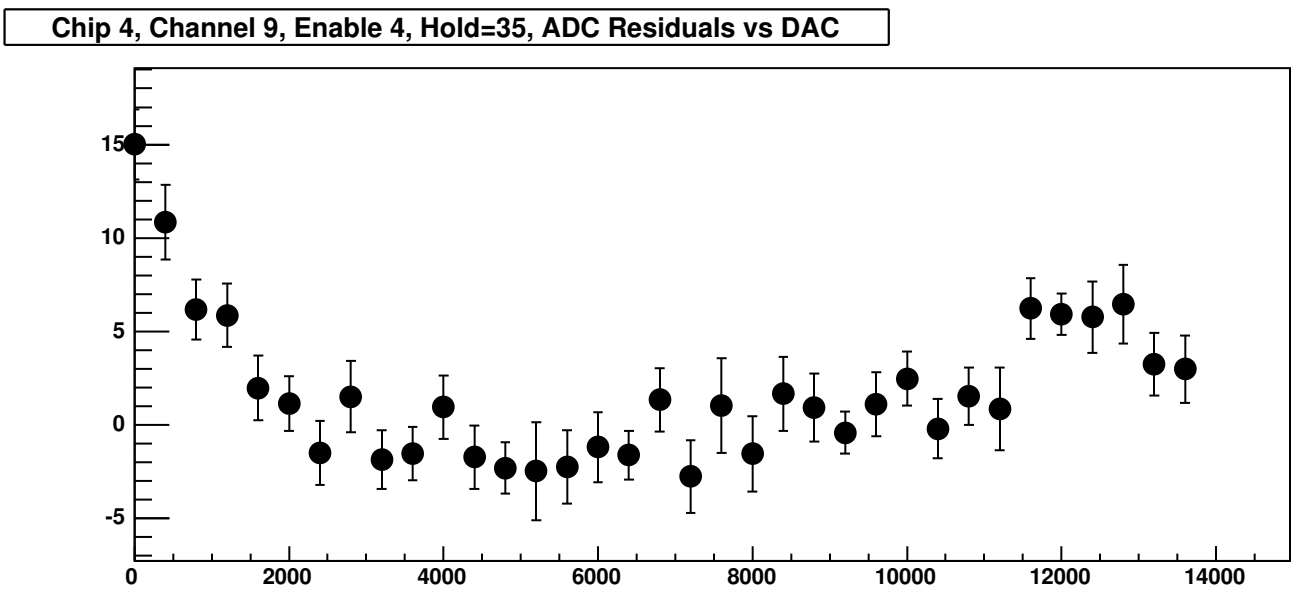


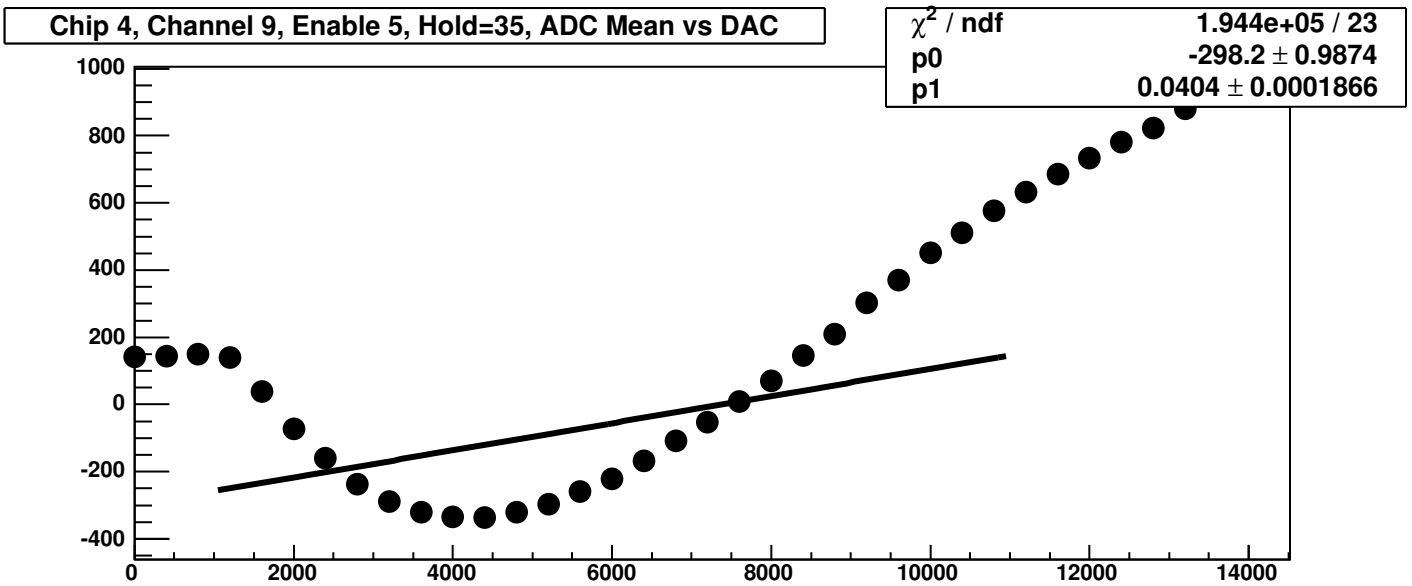
Chip 4, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

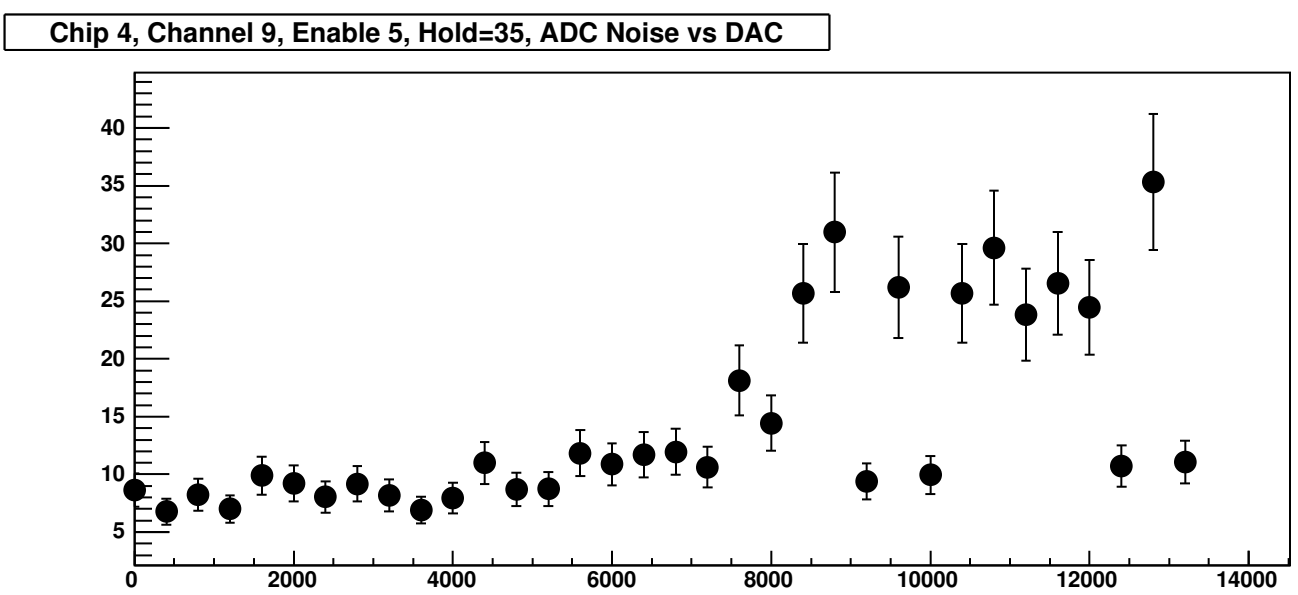


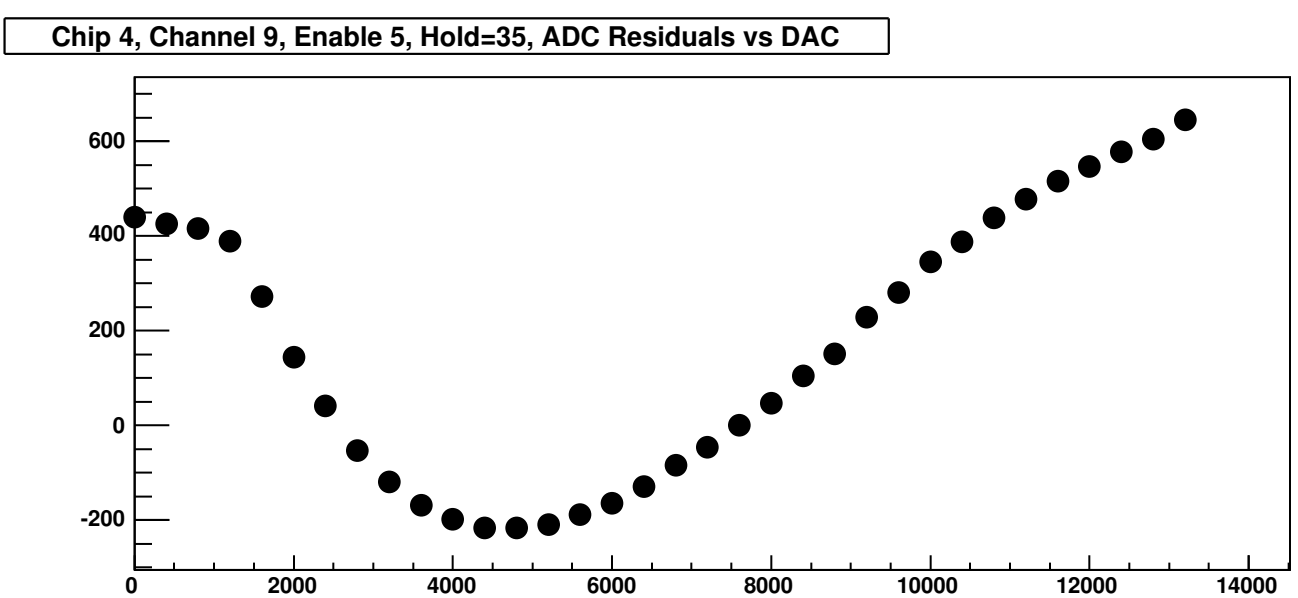

Chip 4, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 35.22 / 23
p0 113.9 ± 0.7509
p1 0.01288 ± 0.0001104
300
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

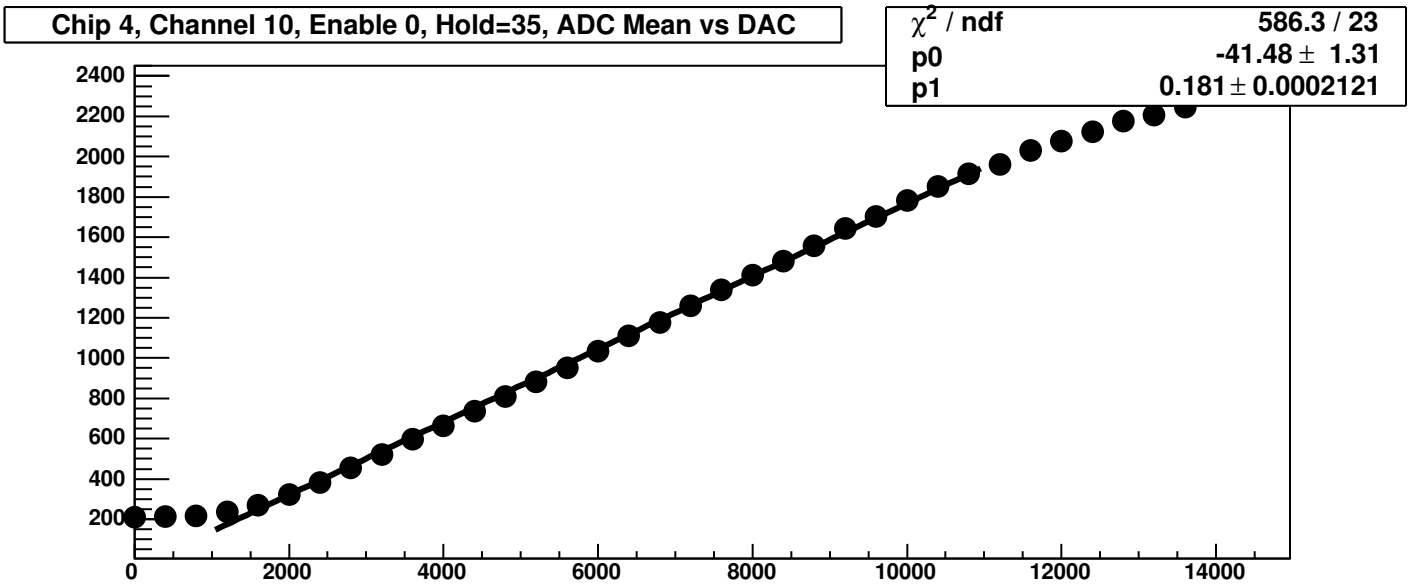

Chip 4, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

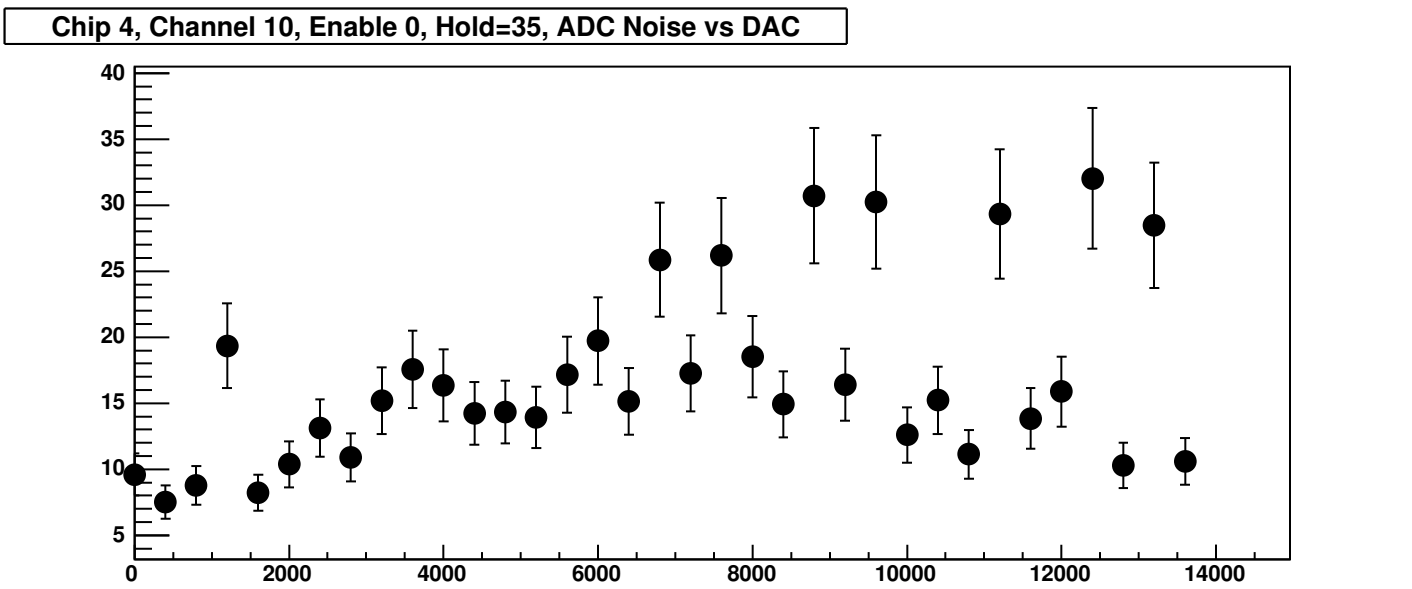

Chip 4, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

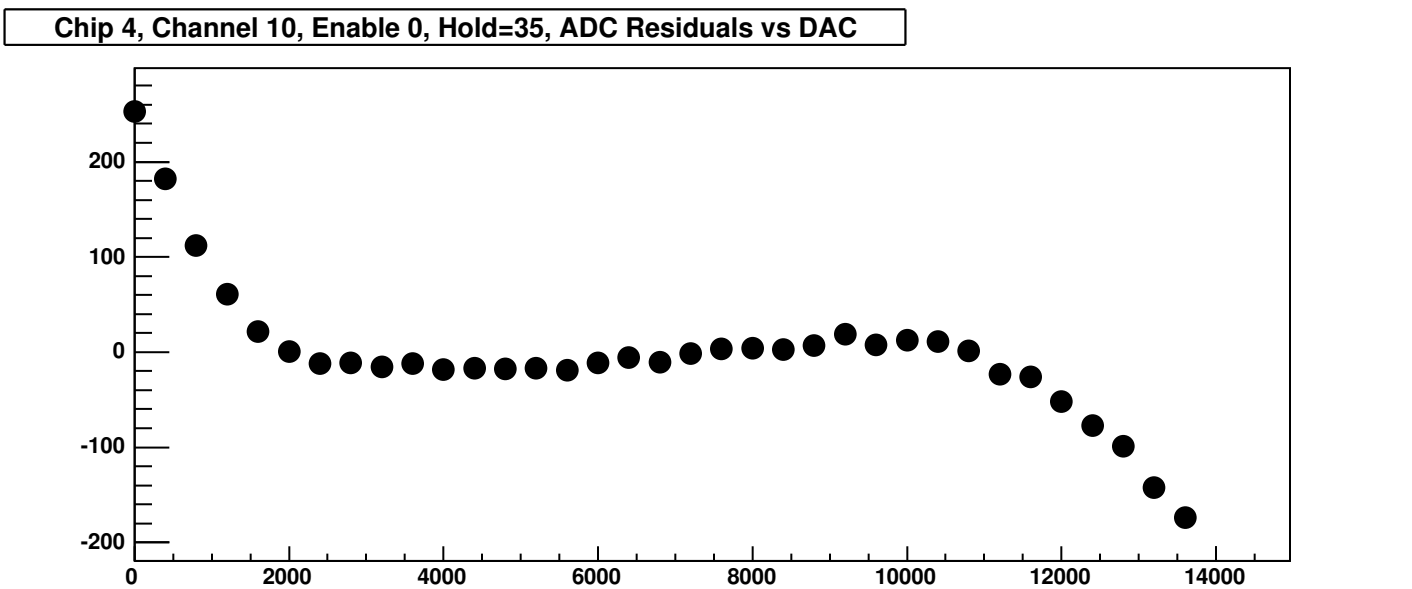

Chip 4, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.944e+05 / 23
p0 -298.2 ± 0.9874
p1 0.0404 ± 0.0001866
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

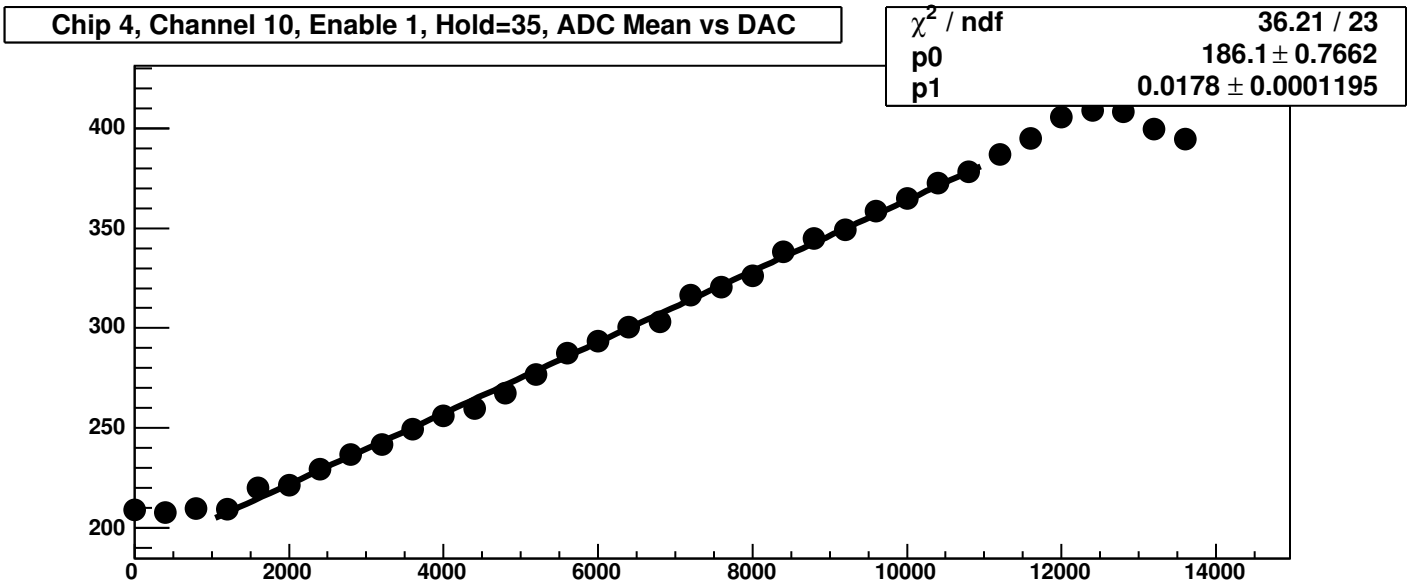

Chip 4, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

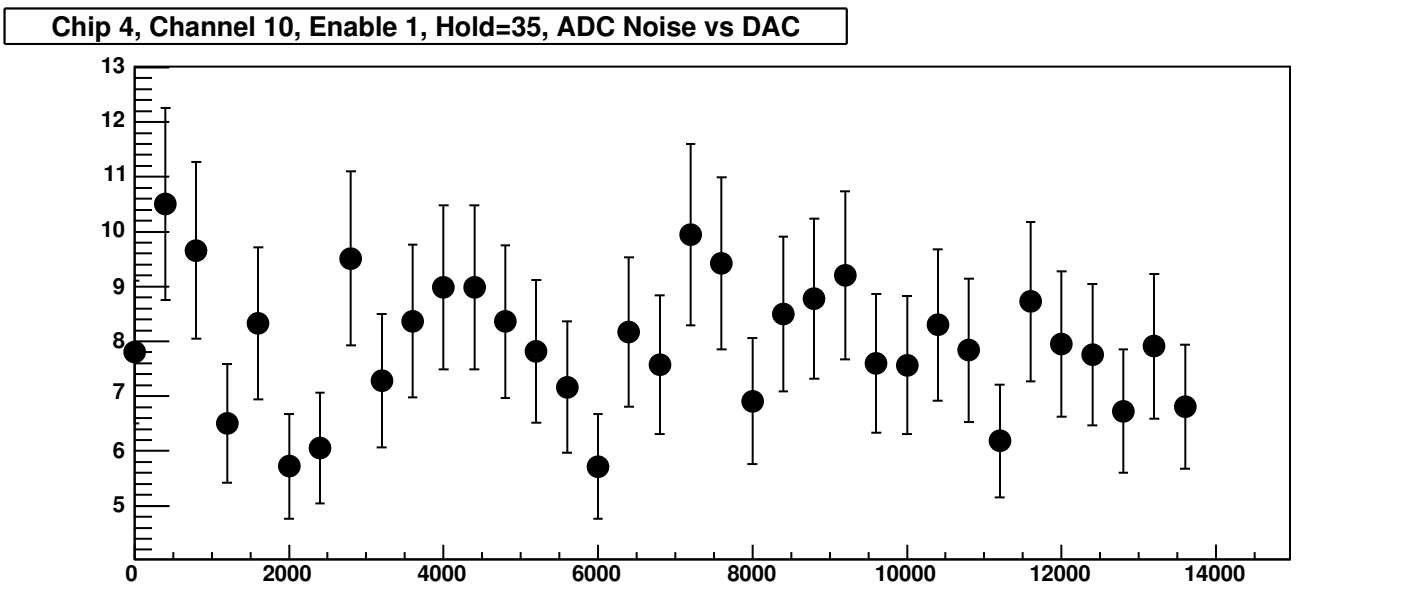

Chip 4, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

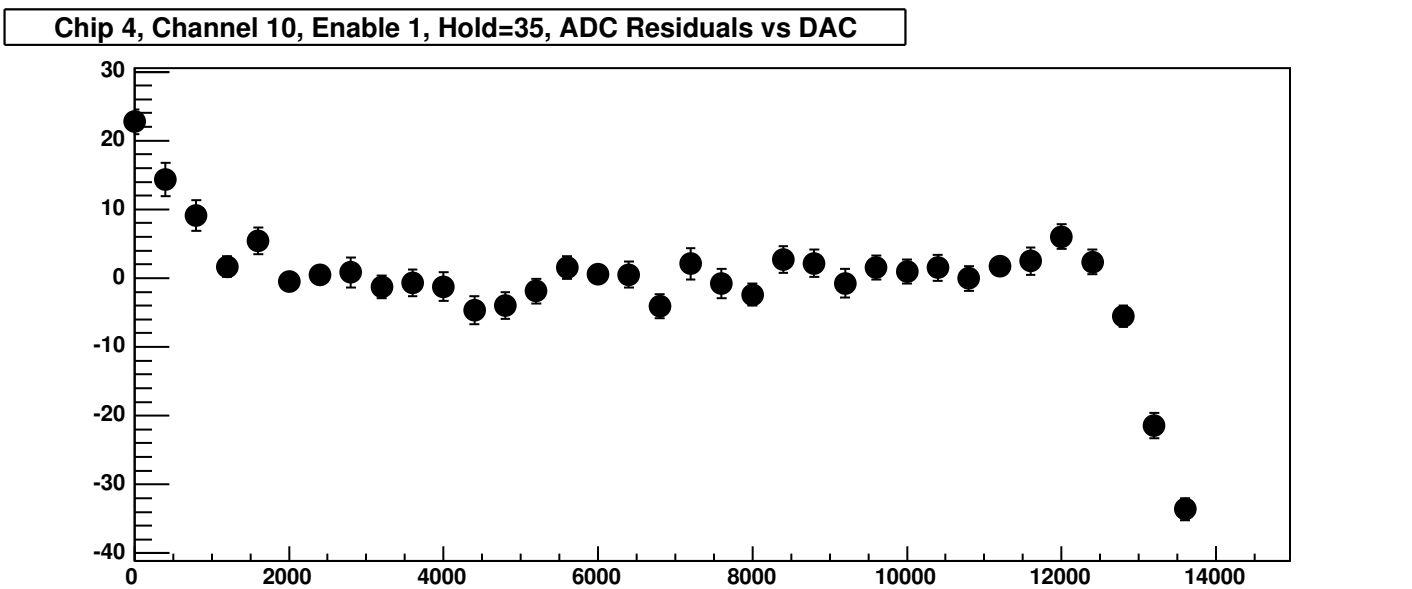

Chip 4, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 586.3 / 23
p0 -41.48 ± 1.31
p1 0.181 ± 0.0002121
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

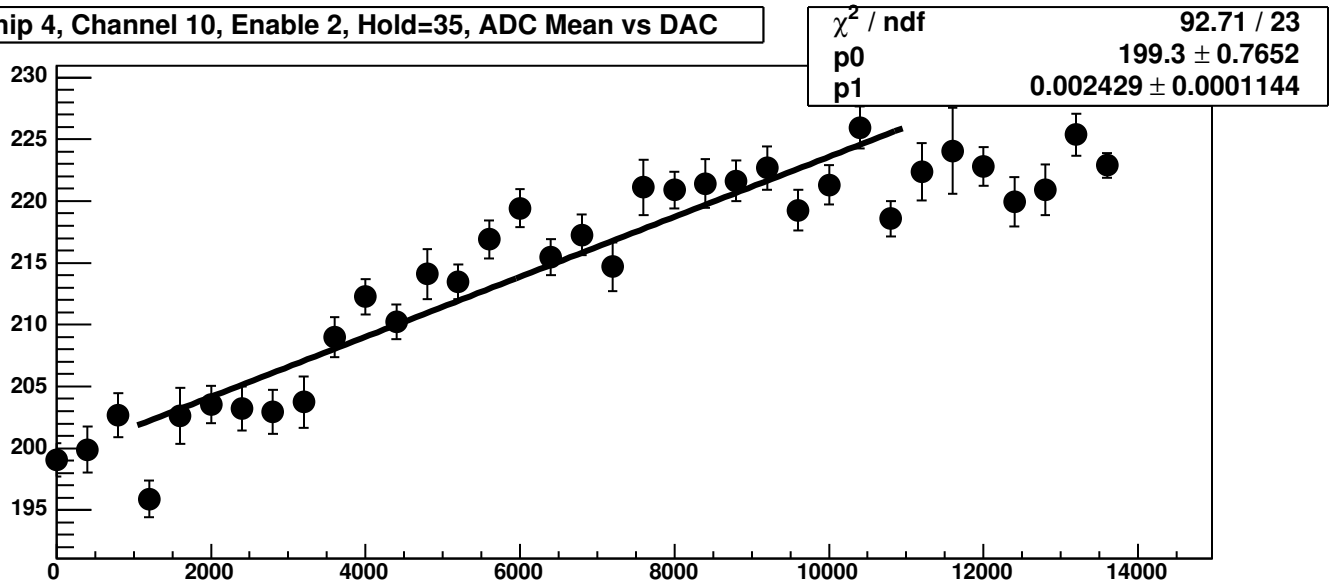

Chip 4, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 36.21 / 23
p0 186.1 ± 0.7662
p1 0.0178 ± 0.0001195
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

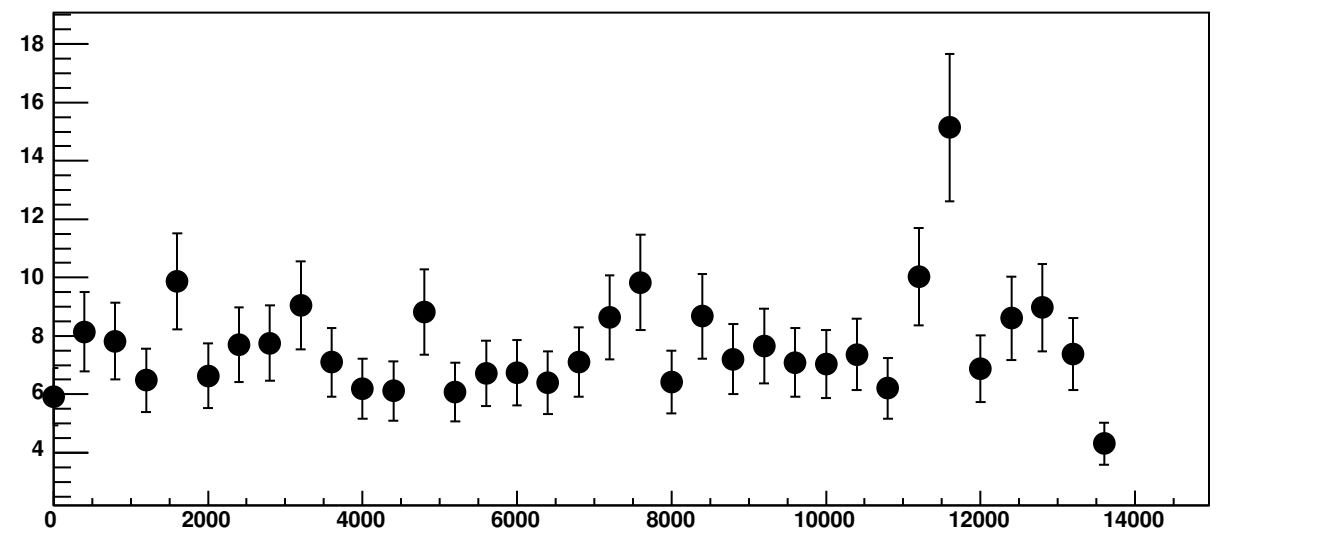

Chip 4, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

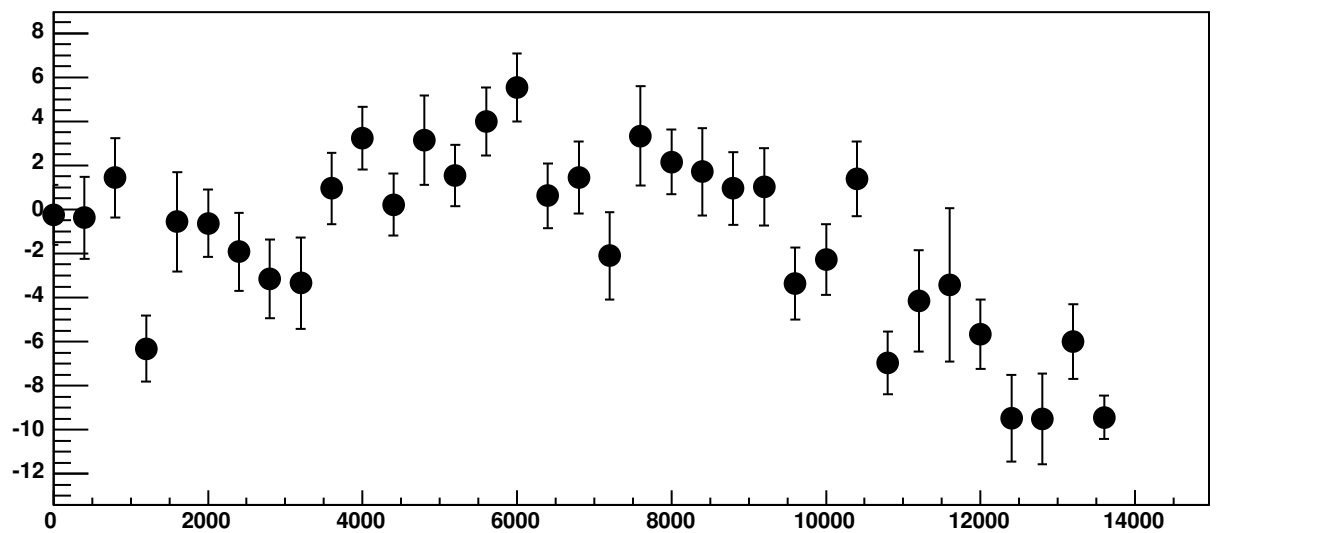
Chip 4, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

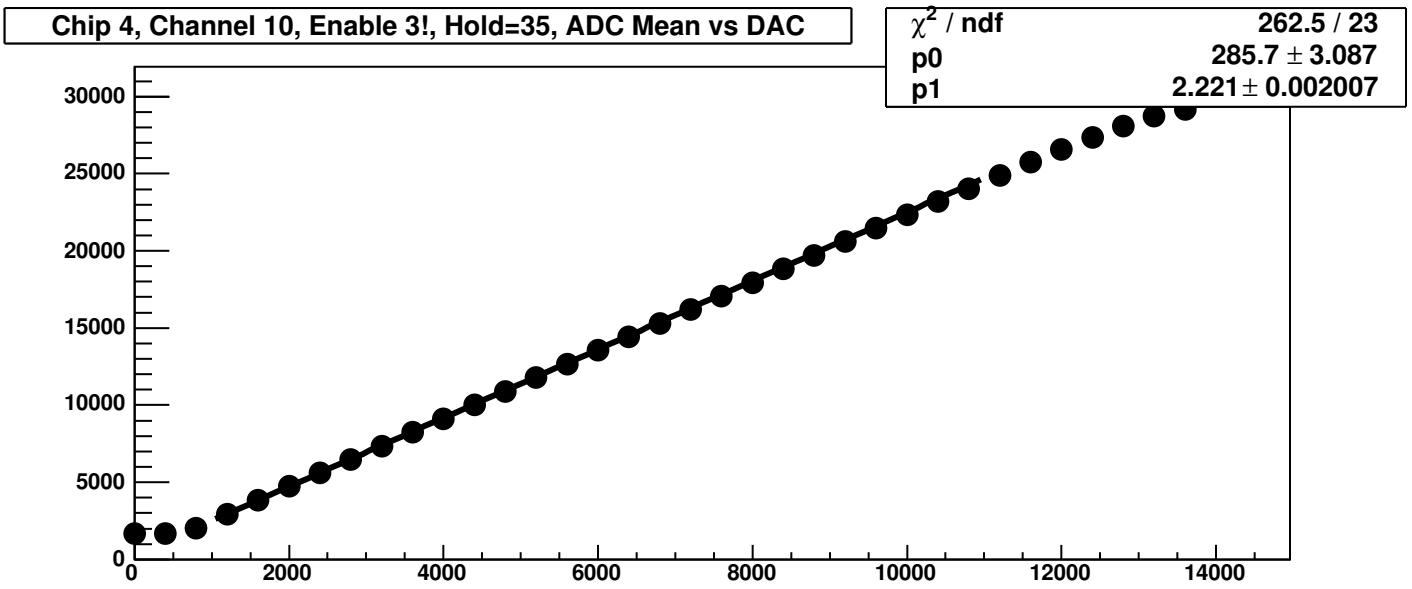


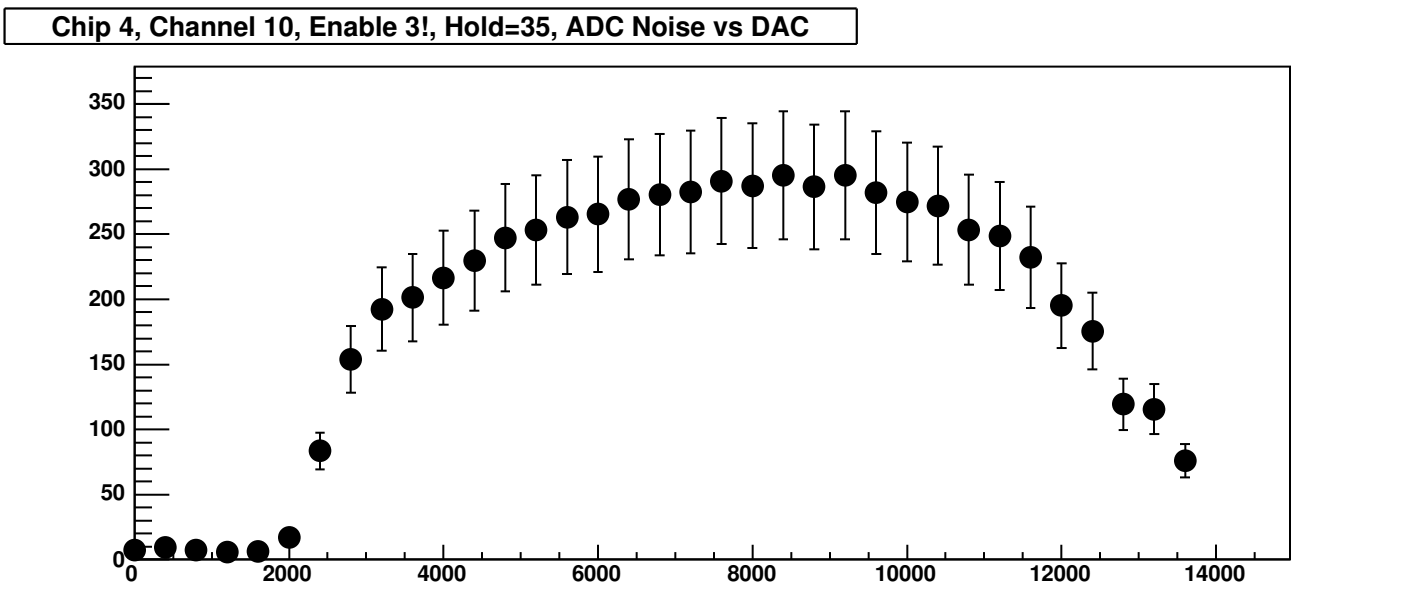
Chip 4, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

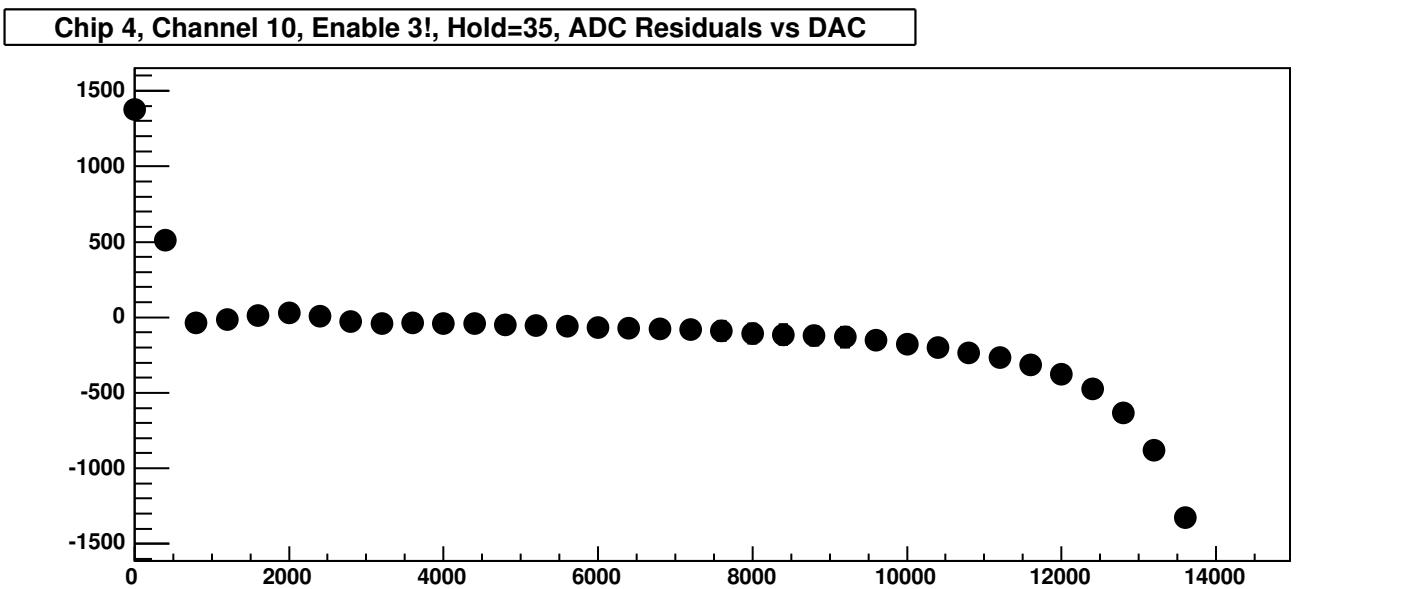


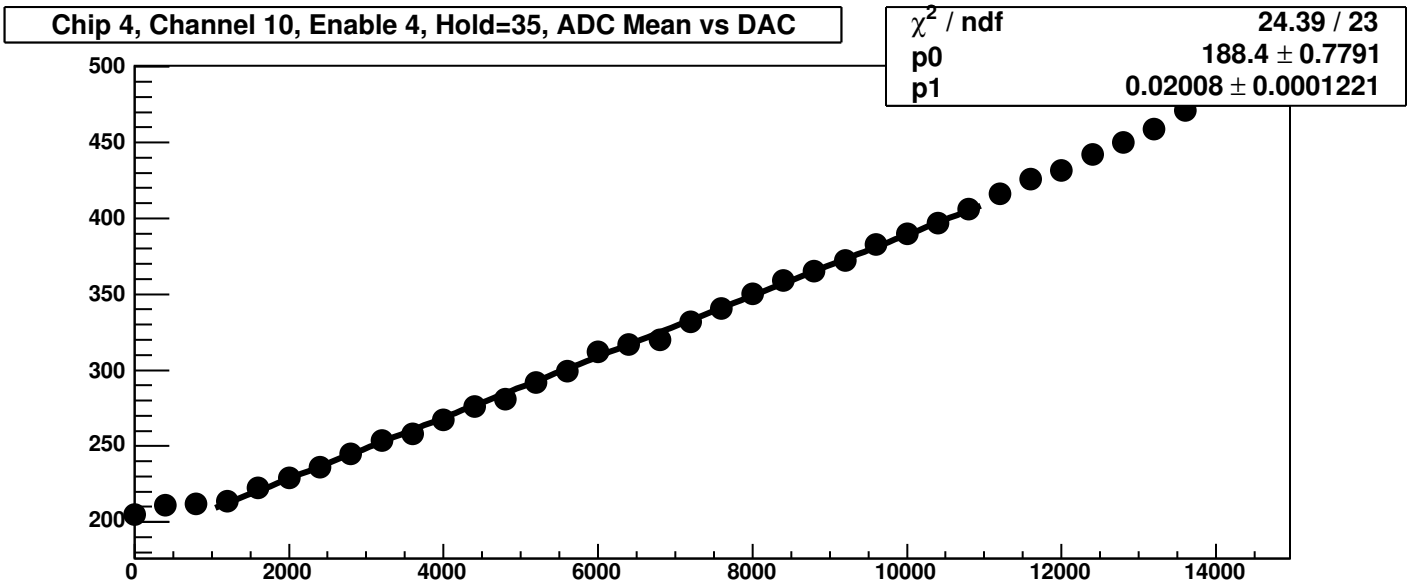
Chip 4, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

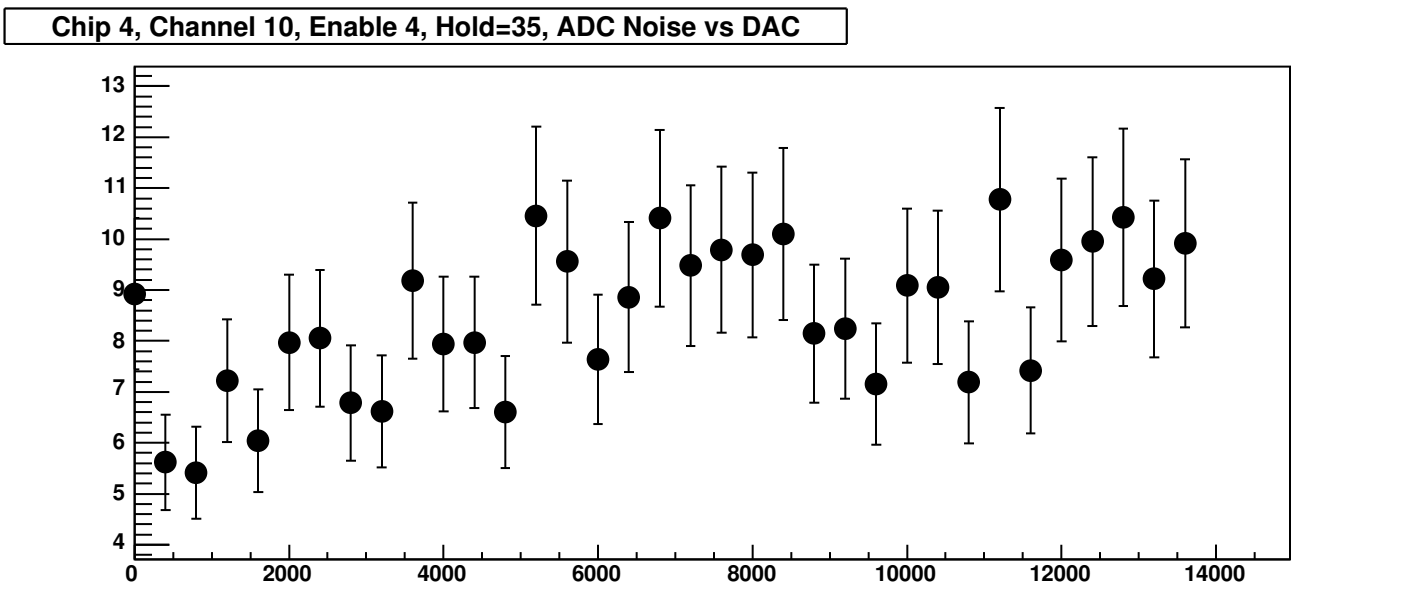


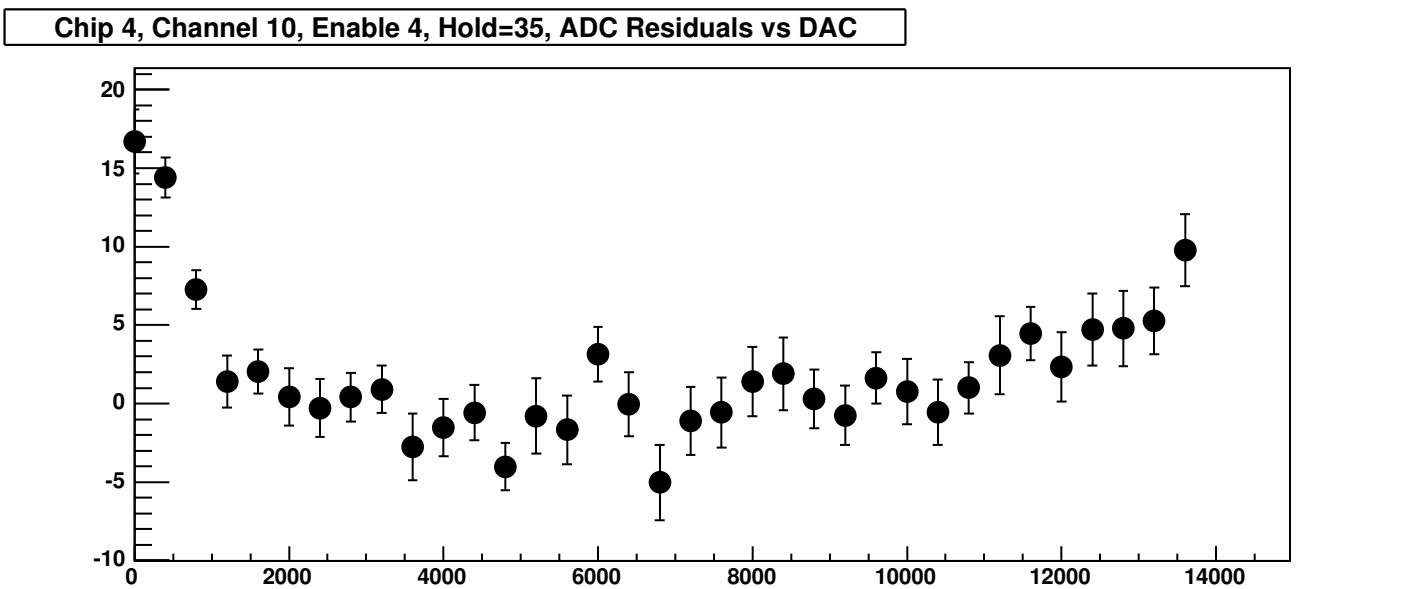

Chip 4, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 262.5 / 23
p0 285.7 ± 3.087
p1 2.221 ± 0.002007
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

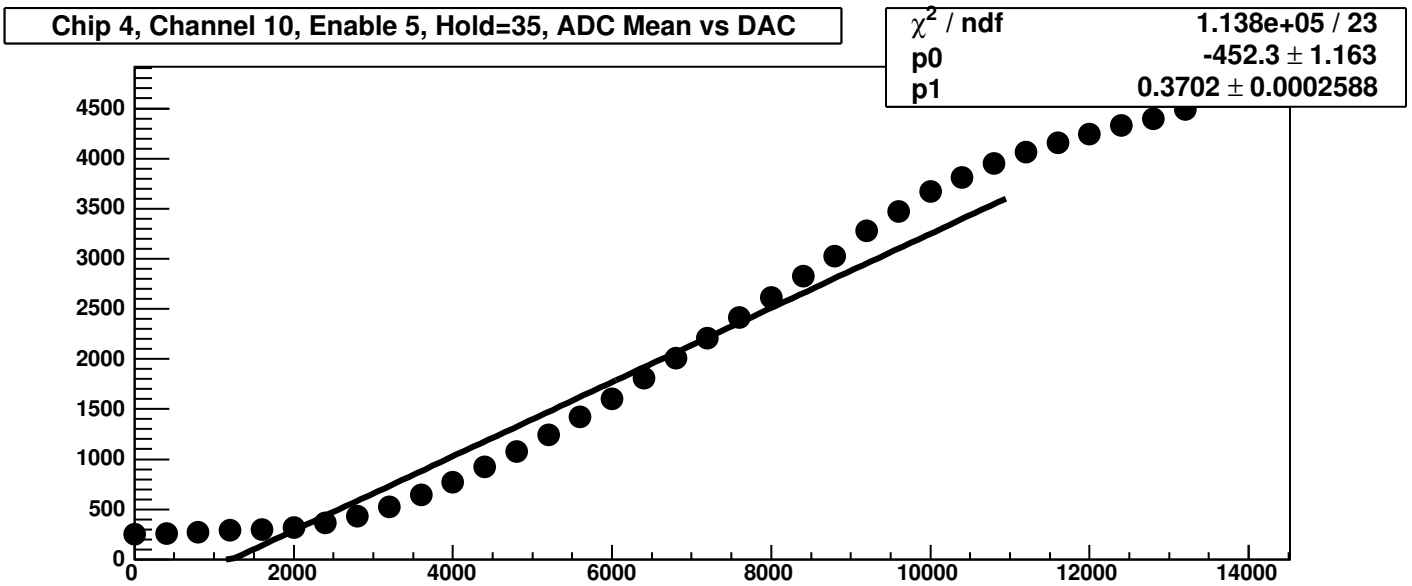

Chip 4, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

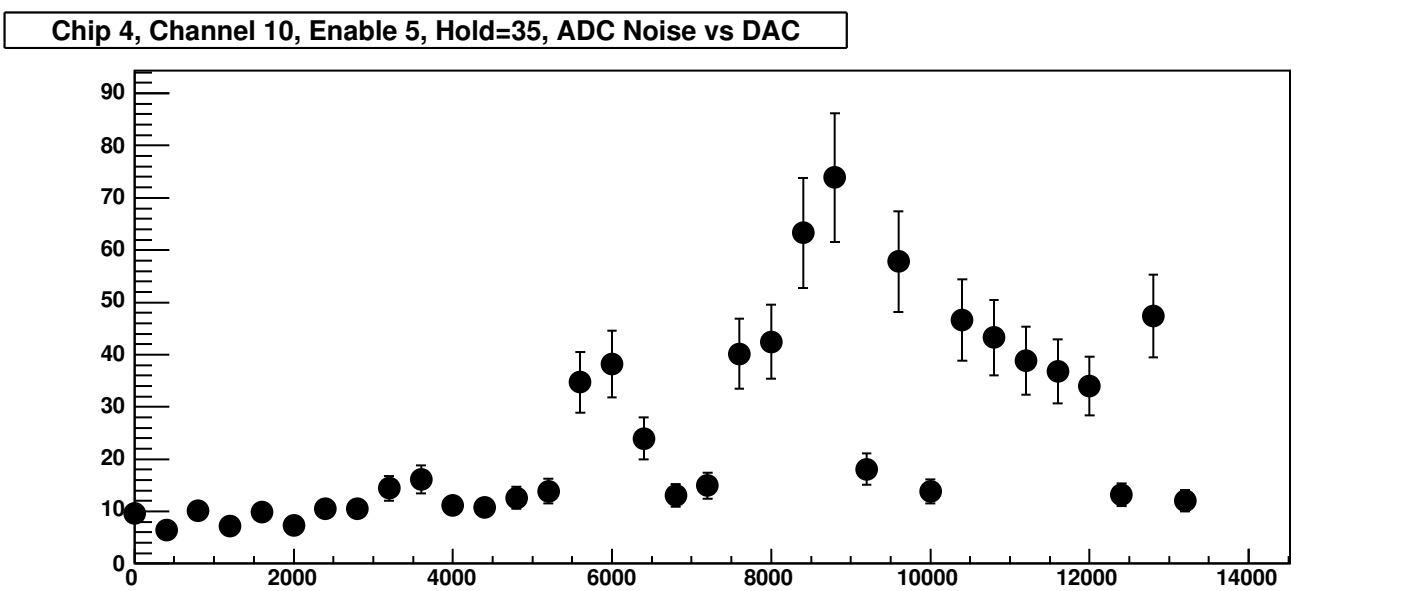

Chip 4, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

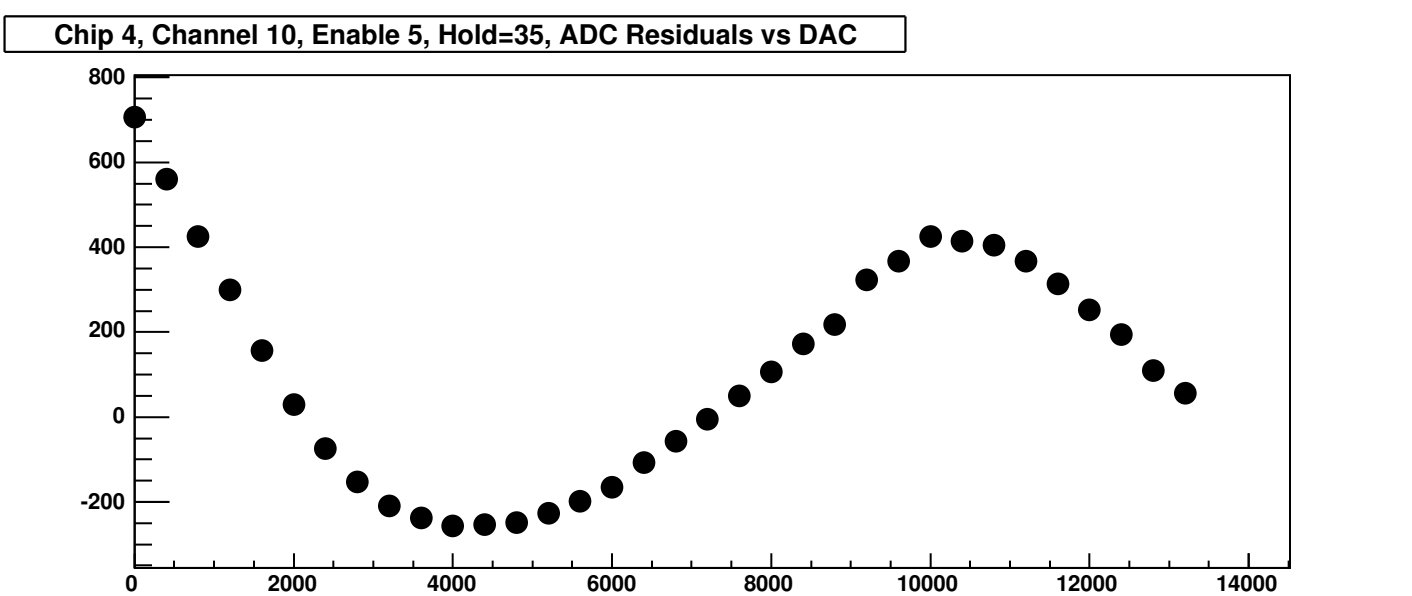

Chip 4, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.39 / 23
p0 188.4 ± 0.7791
p1 0.02008 ± 0.0001221
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

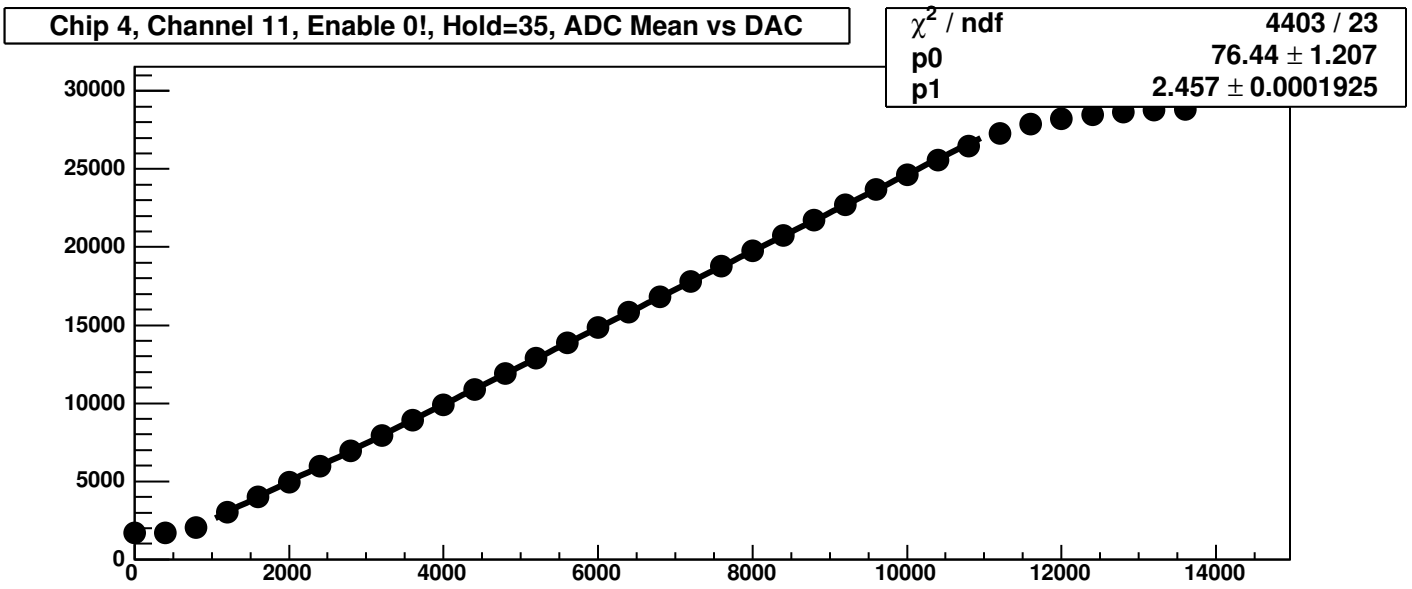

Chip 4, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

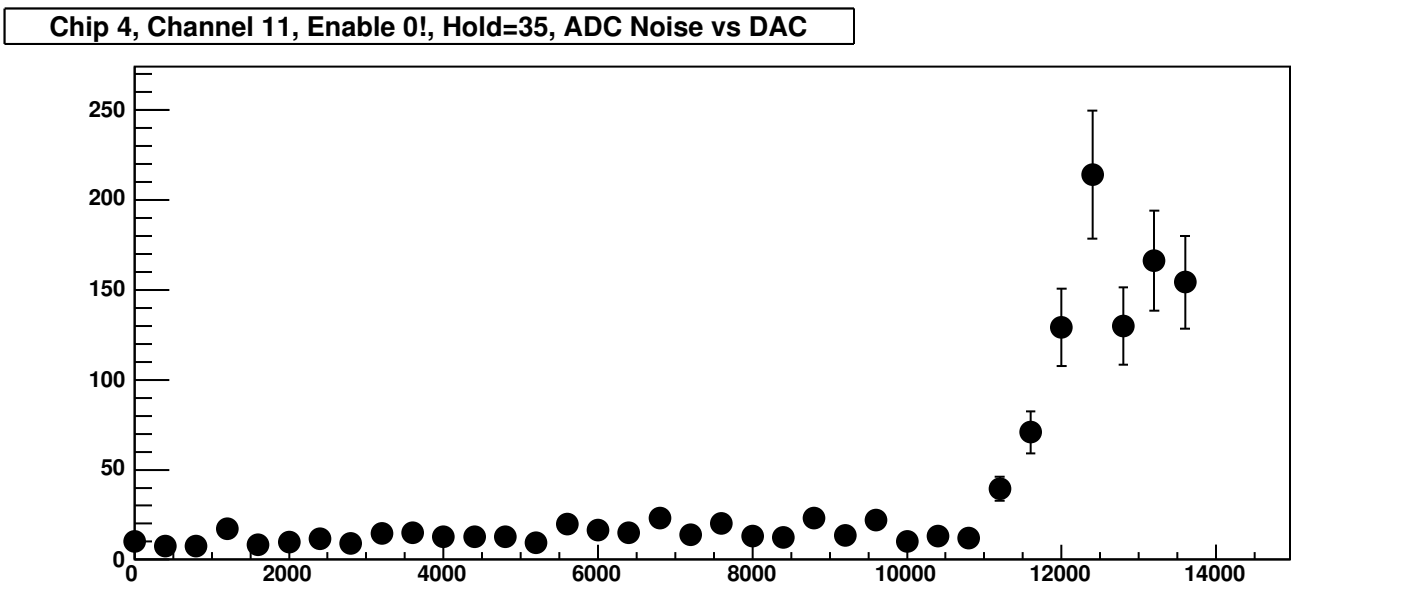

Chip 4, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

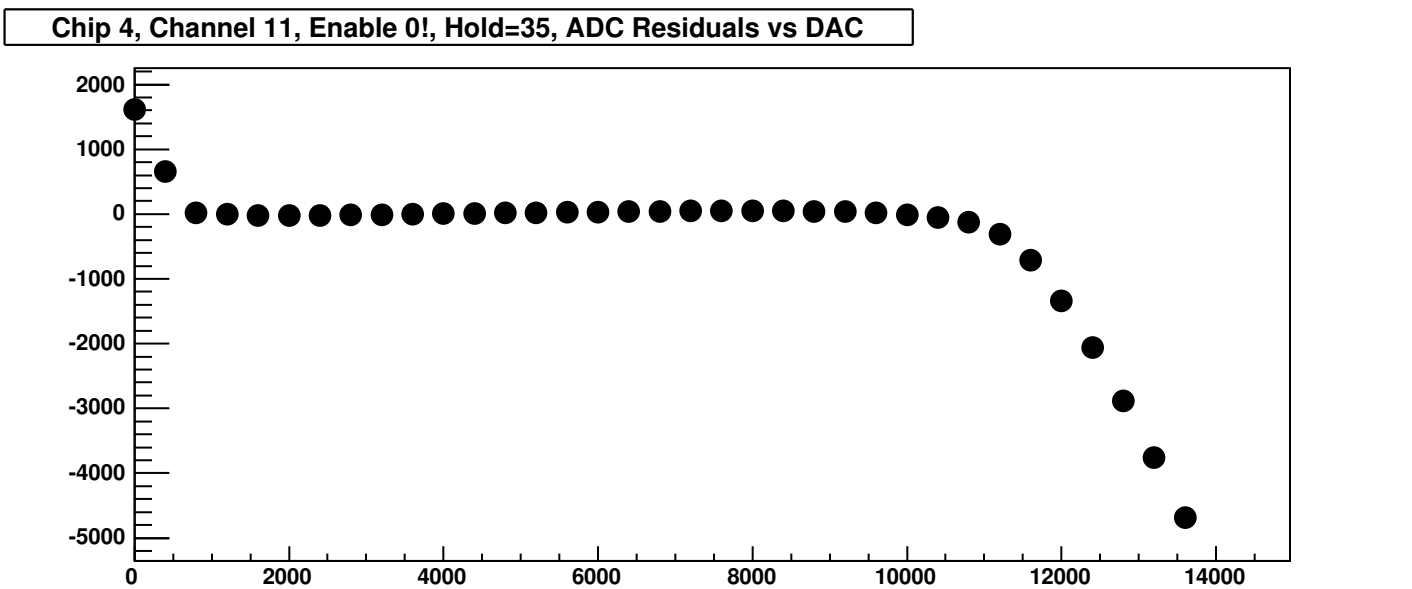

Chip 4, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.138e+05 / 23
p0 -452.3 ± 1.163
p1 0.3702 ± 0.0002588
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

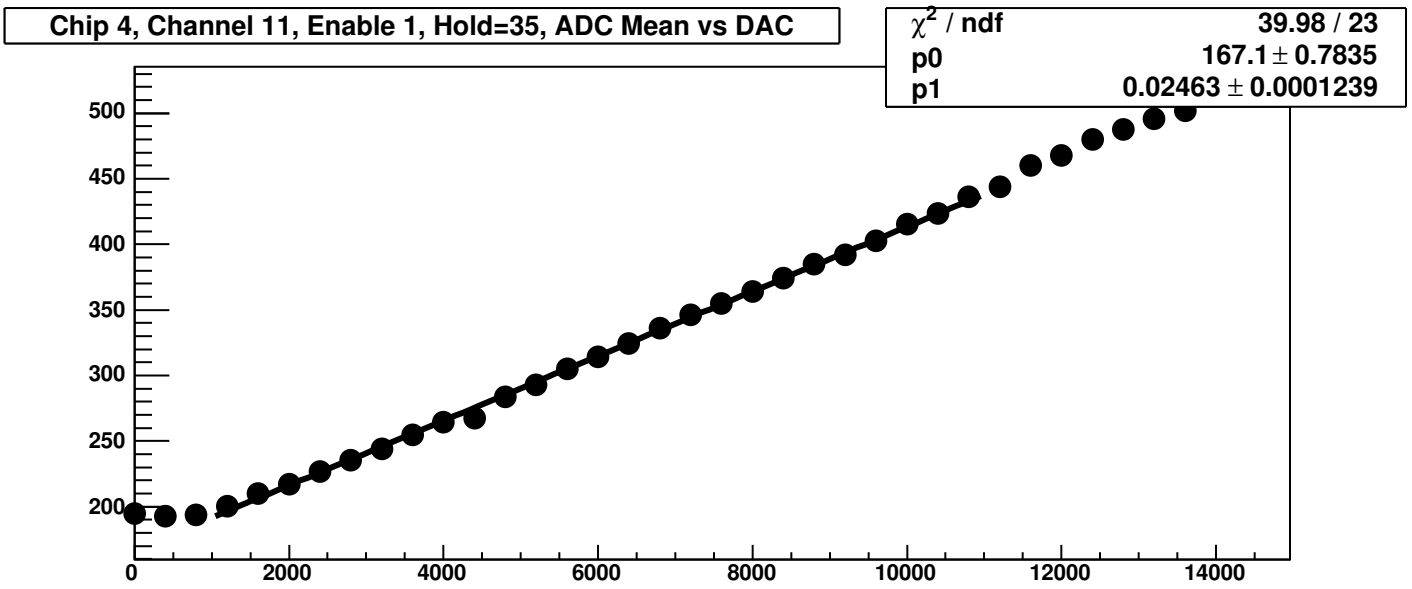

Chip 4, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

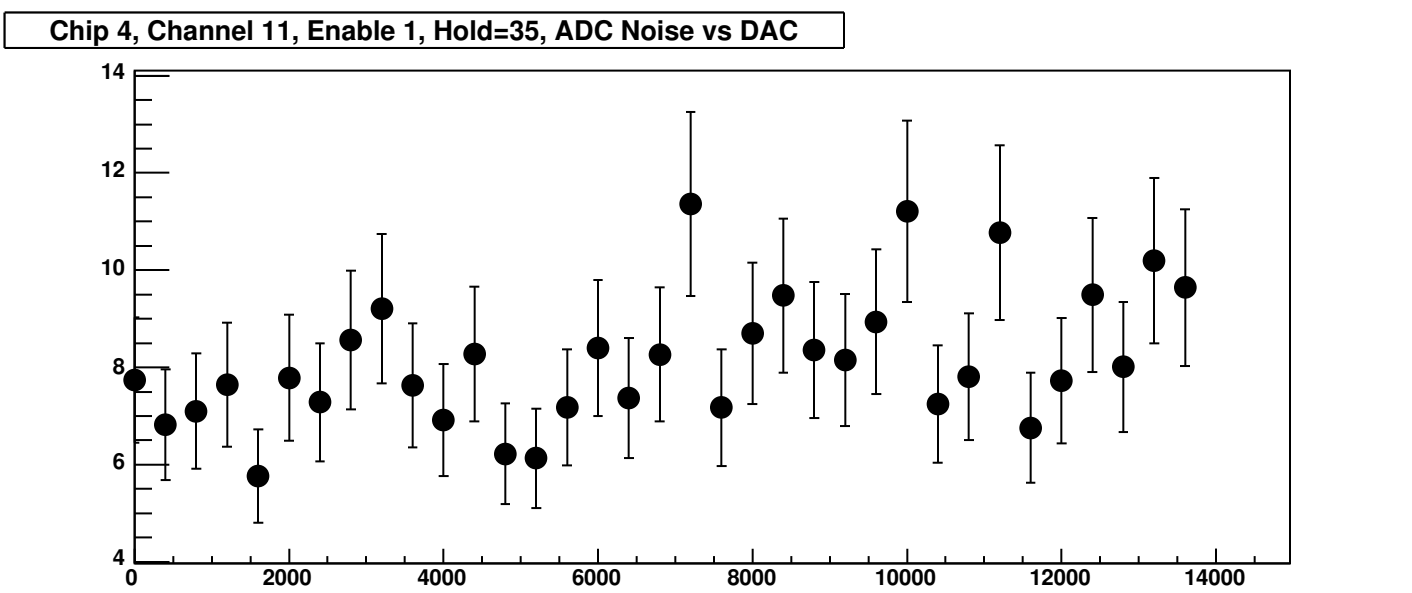

Chip 4, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

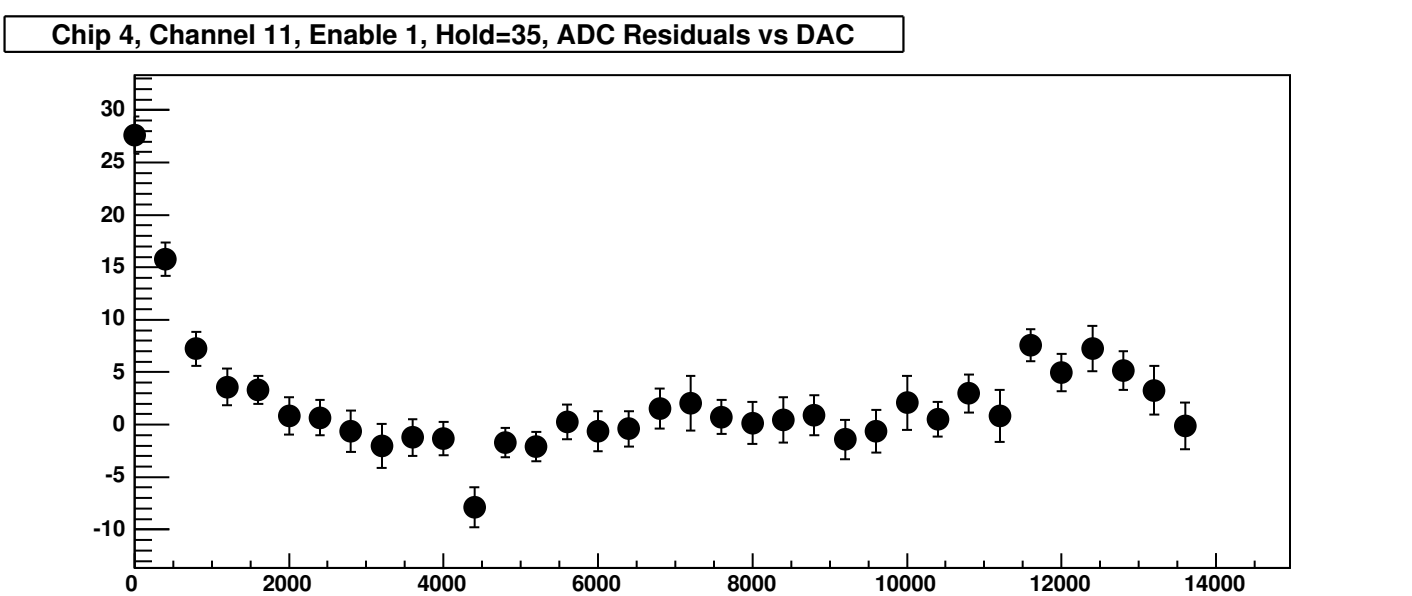

Chip 4, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 4403 / 23
p0 76.44 ± 1.207
p1 2.457 ± 0.0001925
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

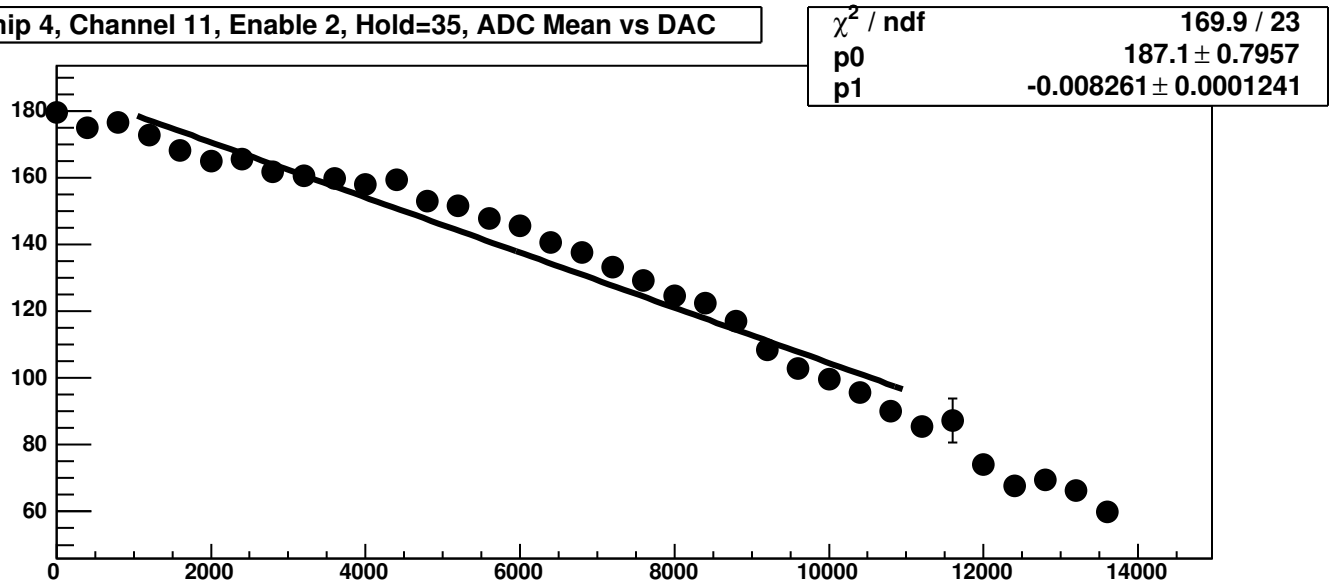

Chip 4, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 39.98 / 23
p0 167.1 ± 0.7835
p1 0.02463 ± 0.0001239
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

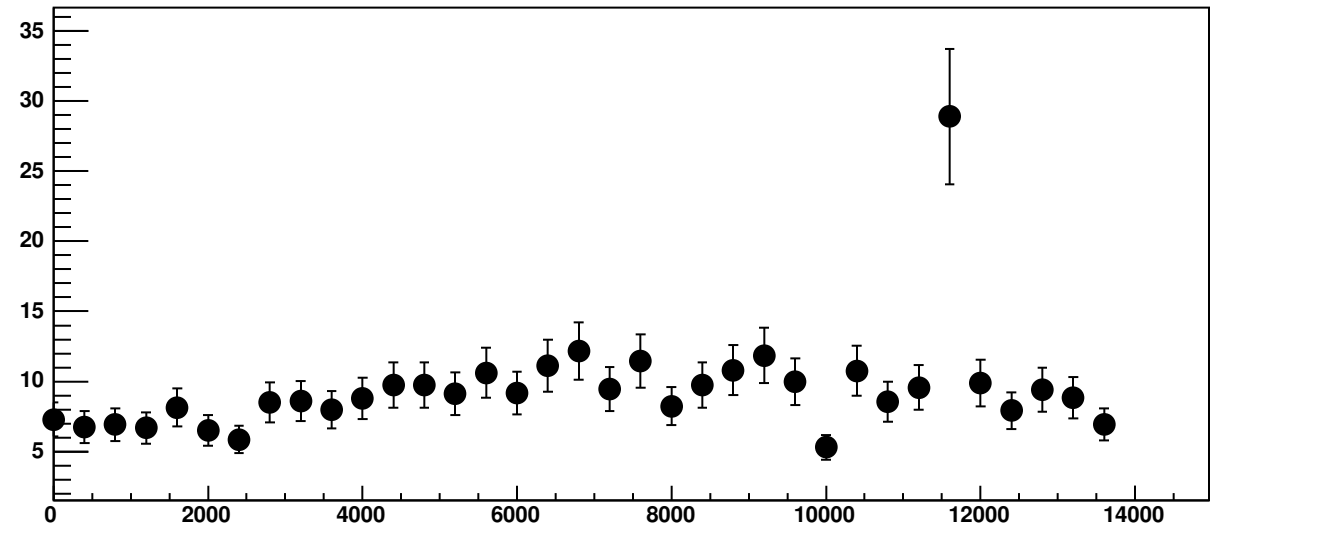

Chip 4, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

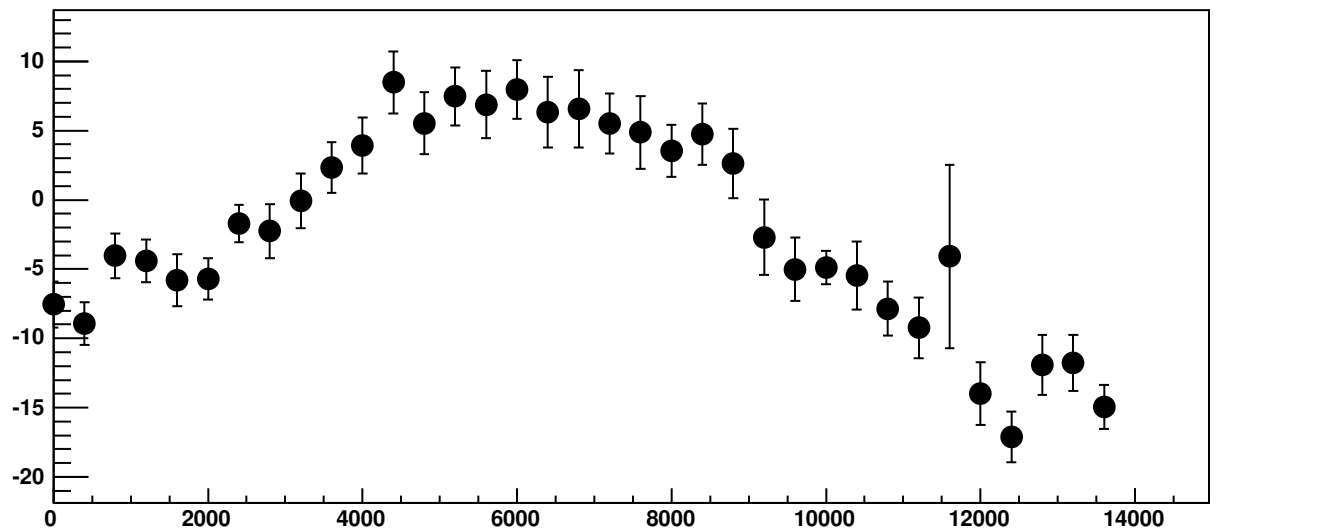
Chip 4, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

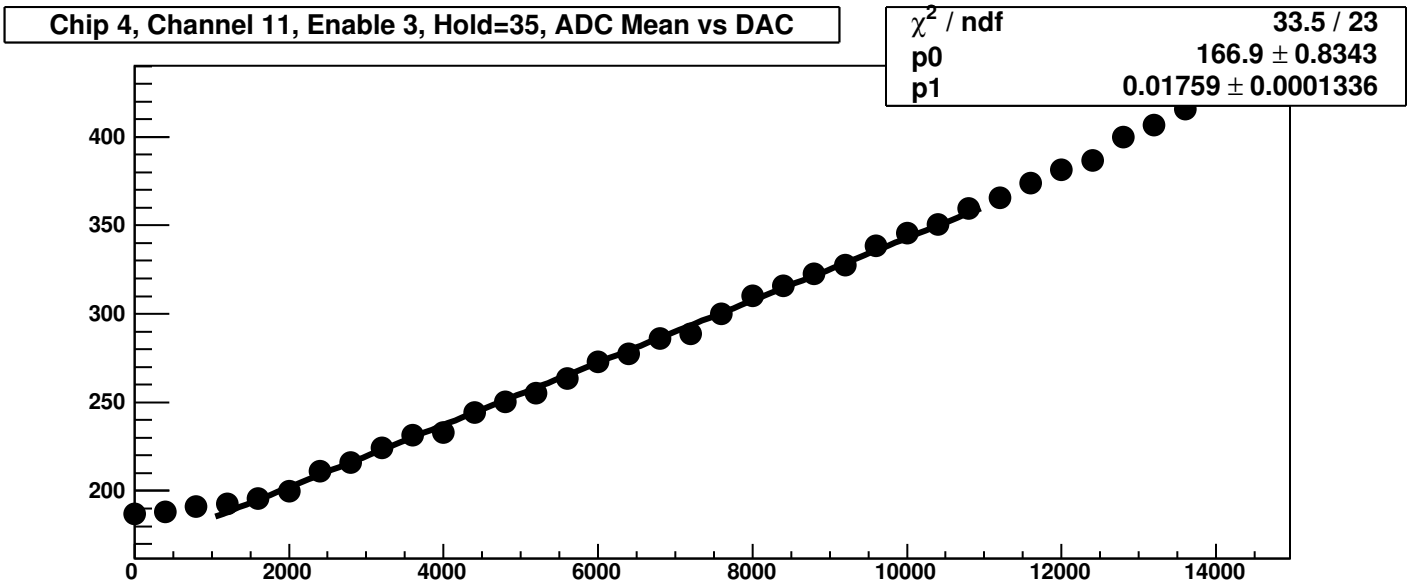


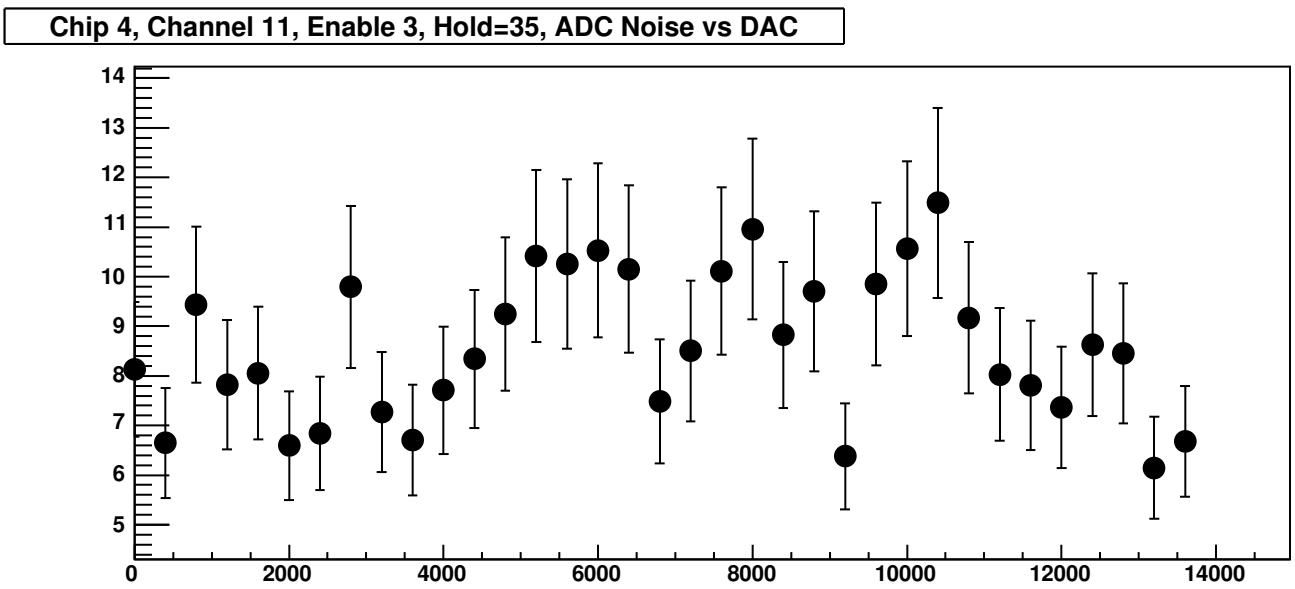
Chip 4, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

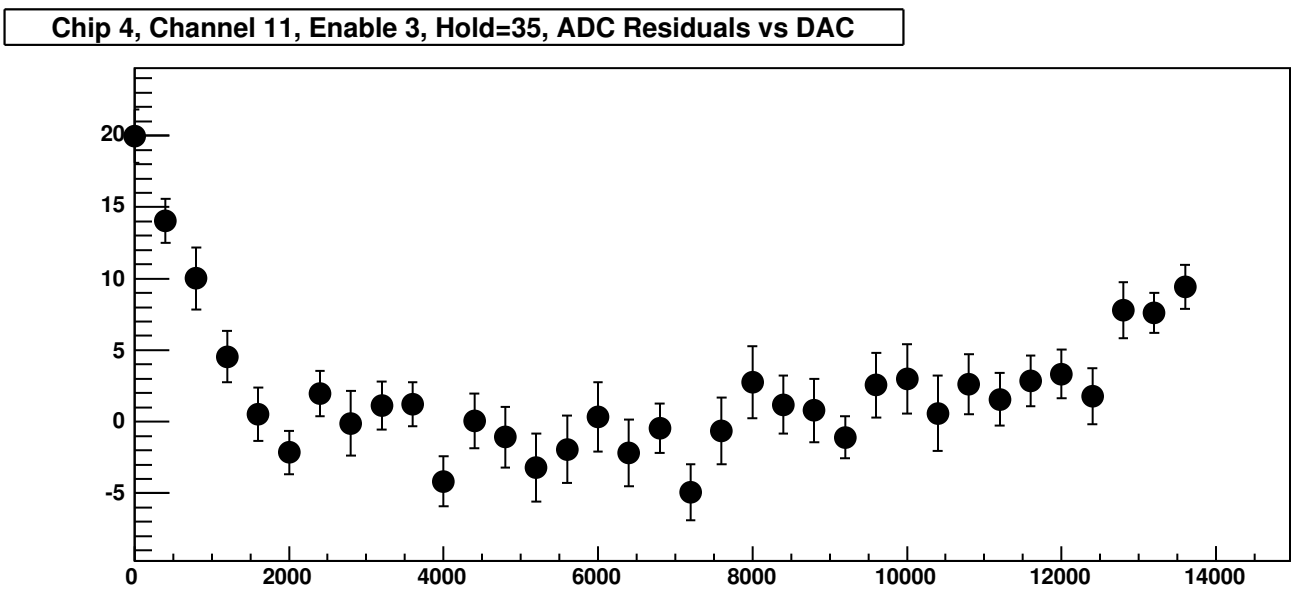


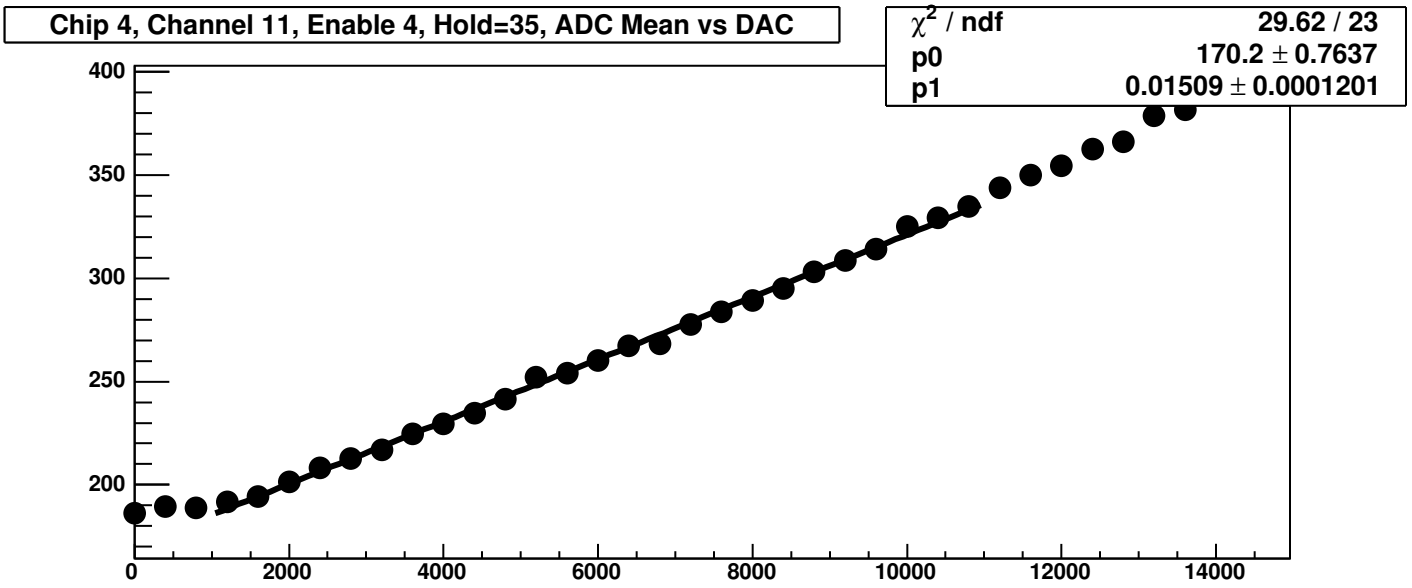
Chip 4, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

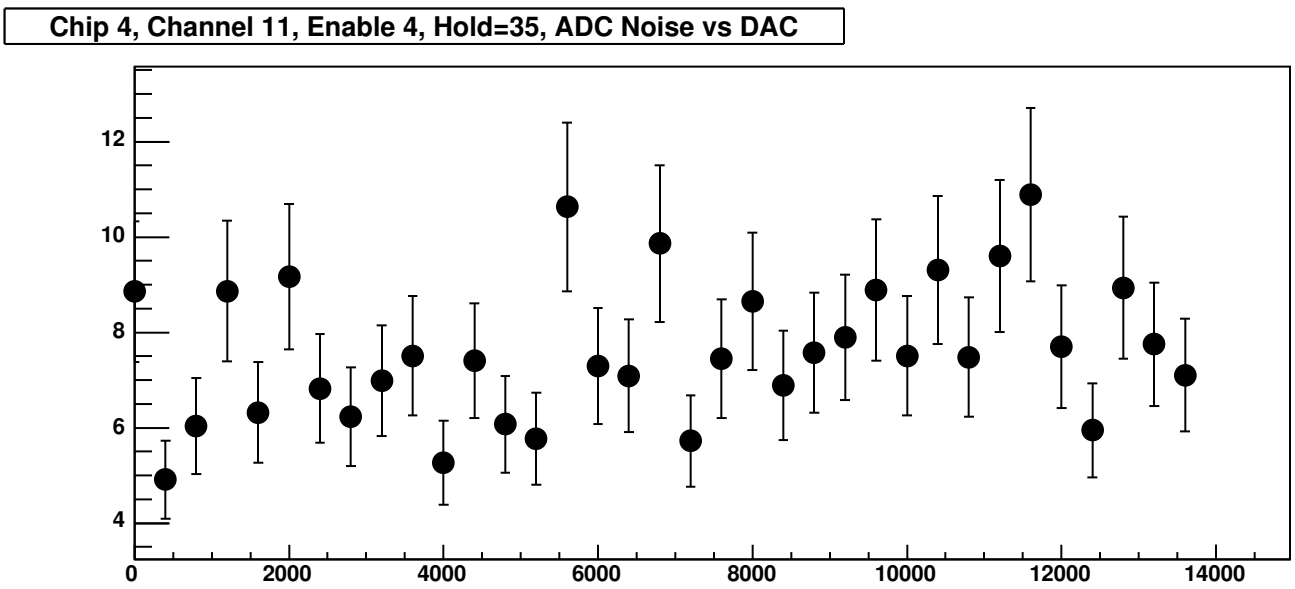


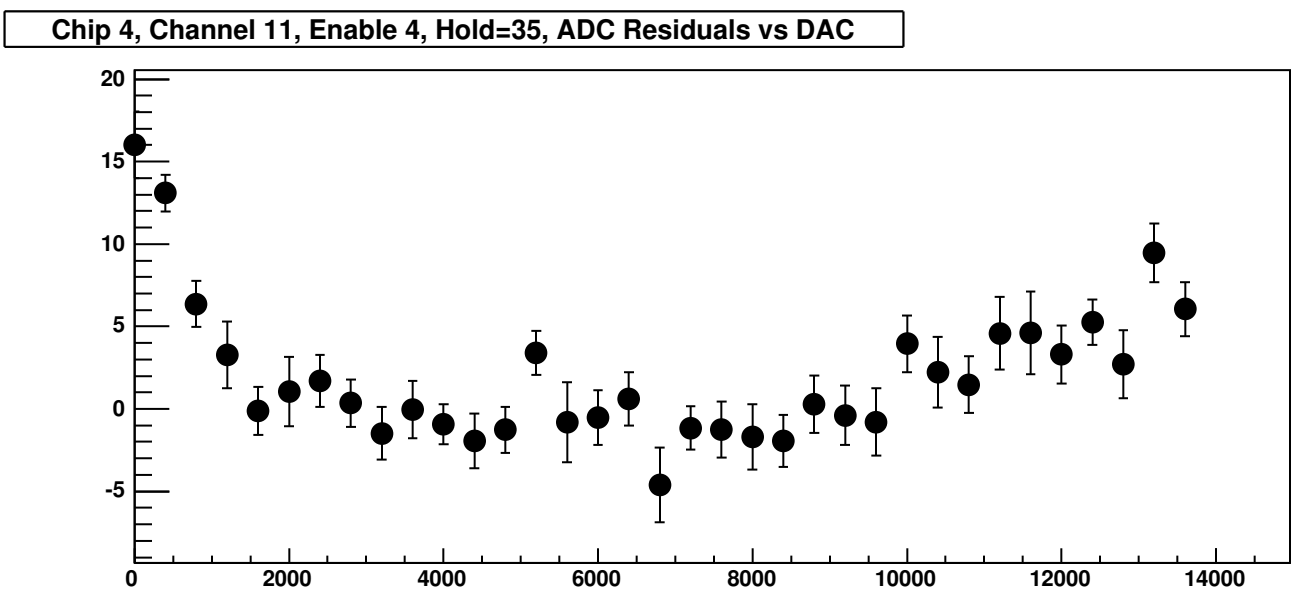

Chip 4, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 33.5 / 23
p0 166.9 ± 0.8343
p1 0.01759 ± 0.0001336
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC
14
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

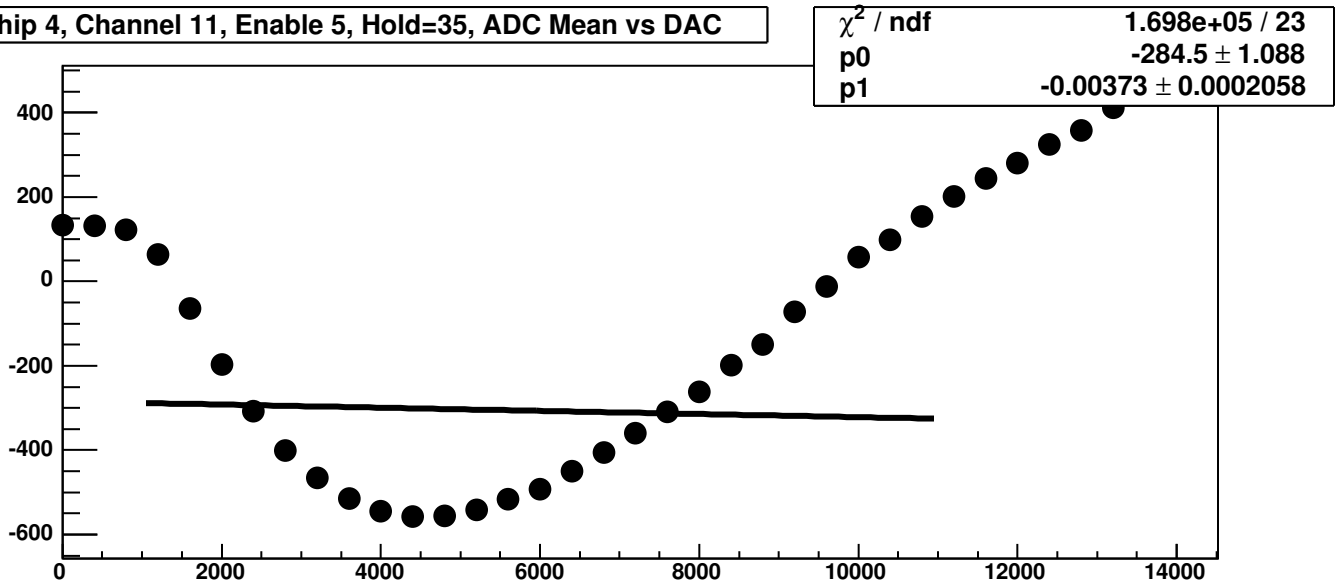

Chip 4, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.62 / 23
p0 170.2 ± 0.7637
p1 0.01509 ± 0.0001201
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

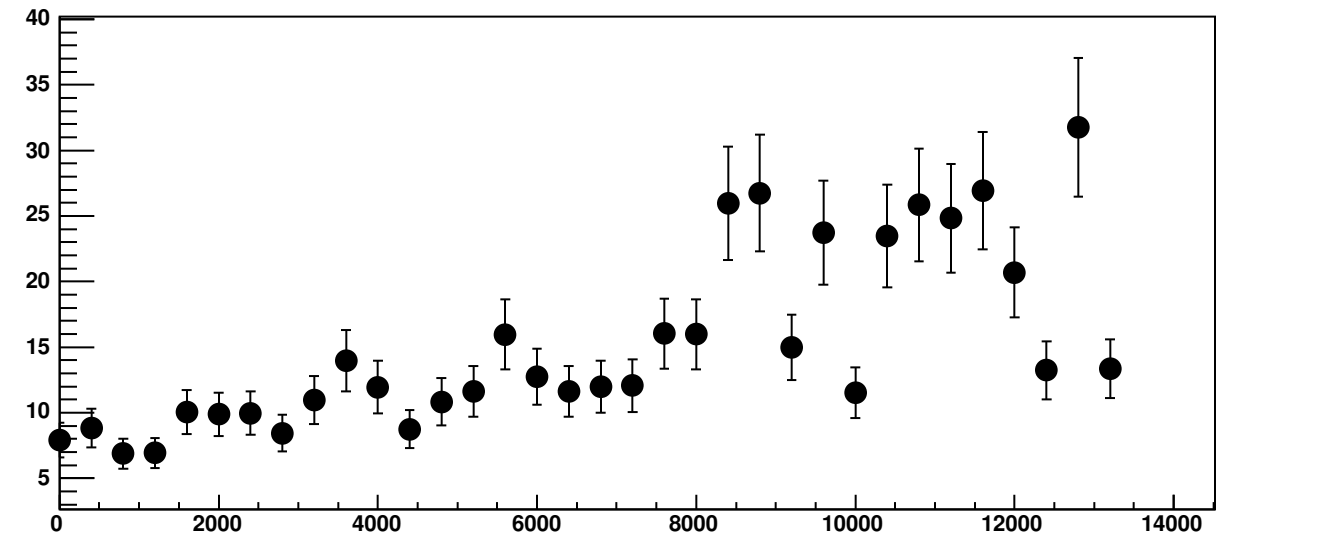

Chip 4, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

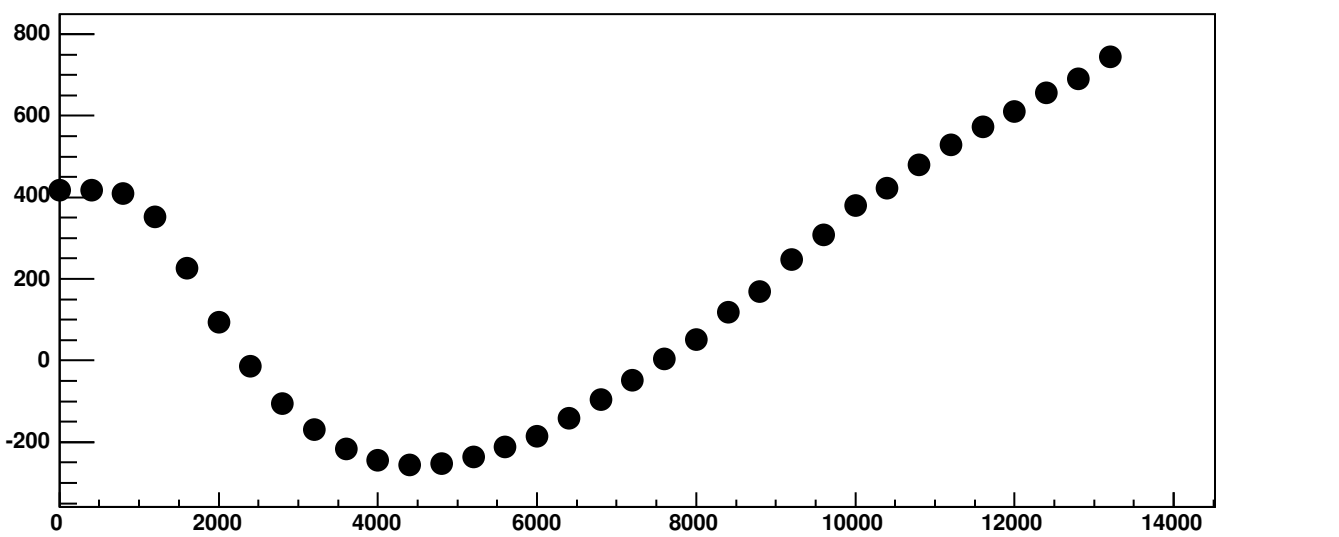
Chip 4, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

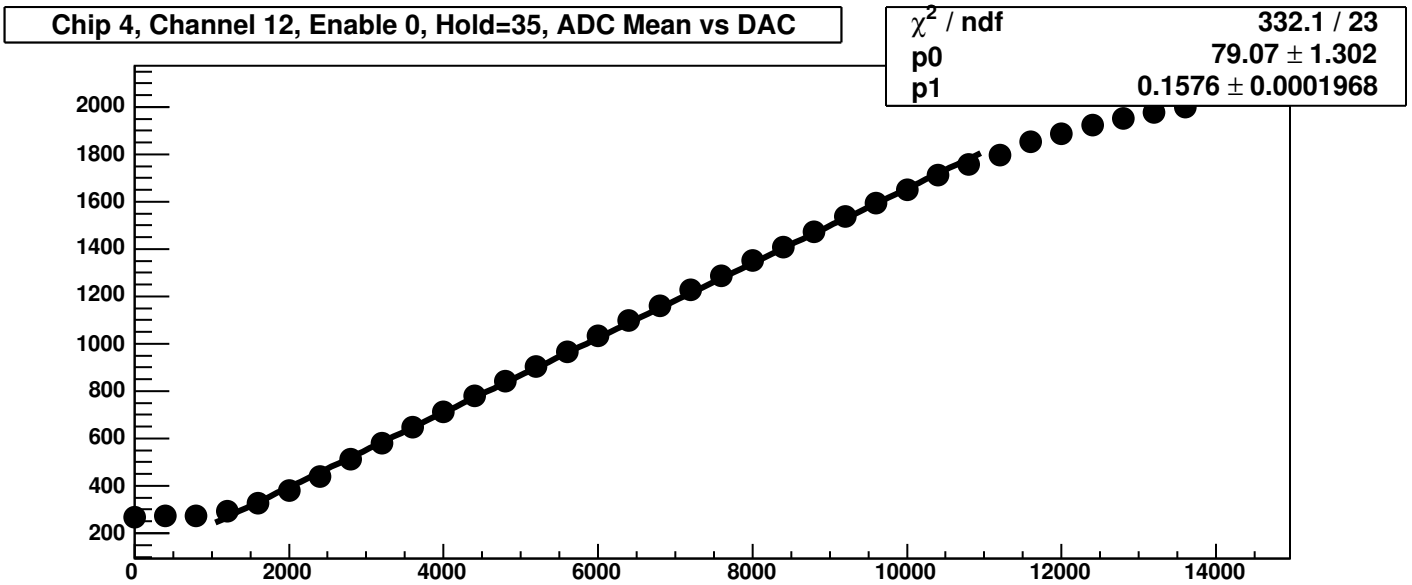


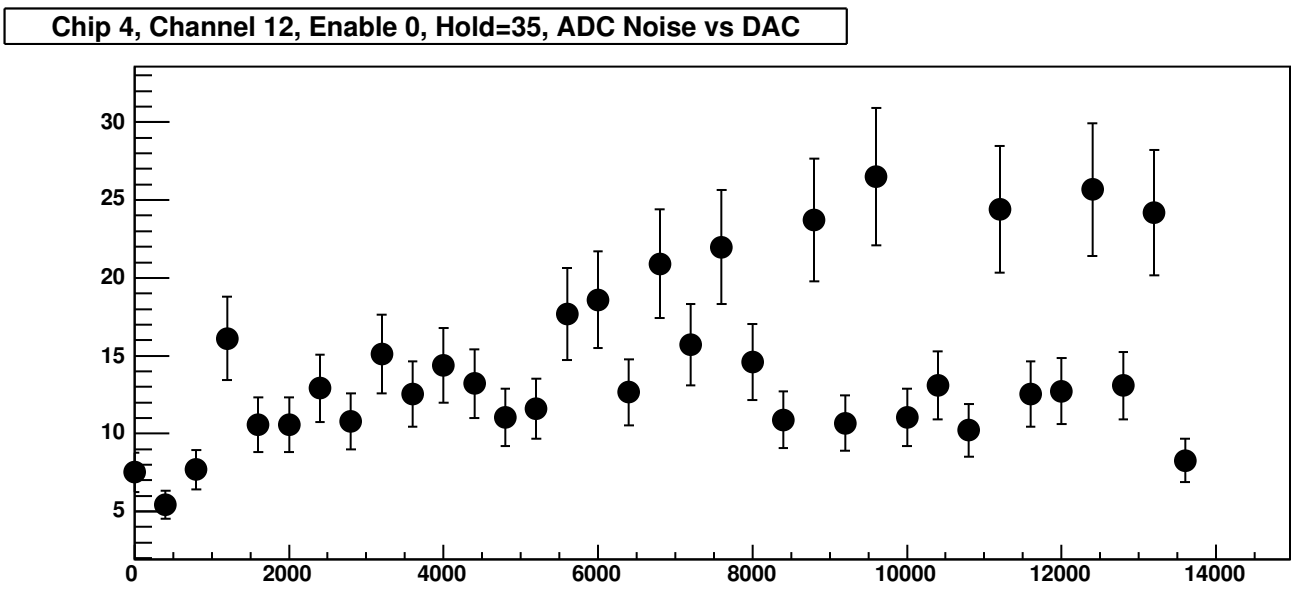
Chip 4, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

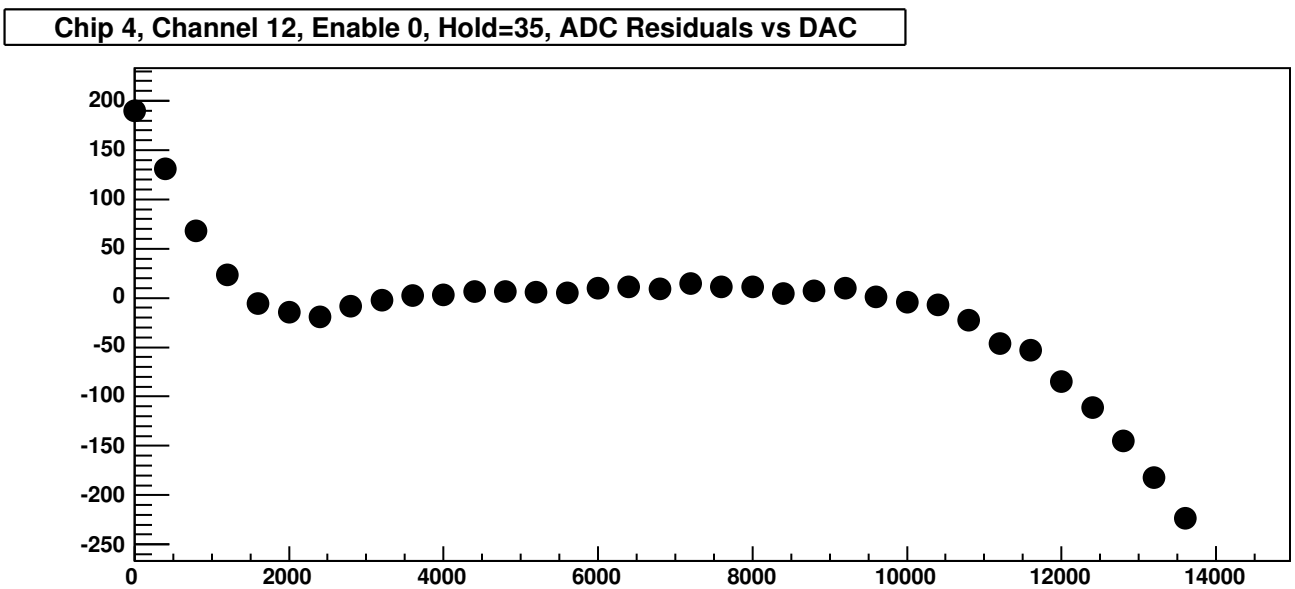


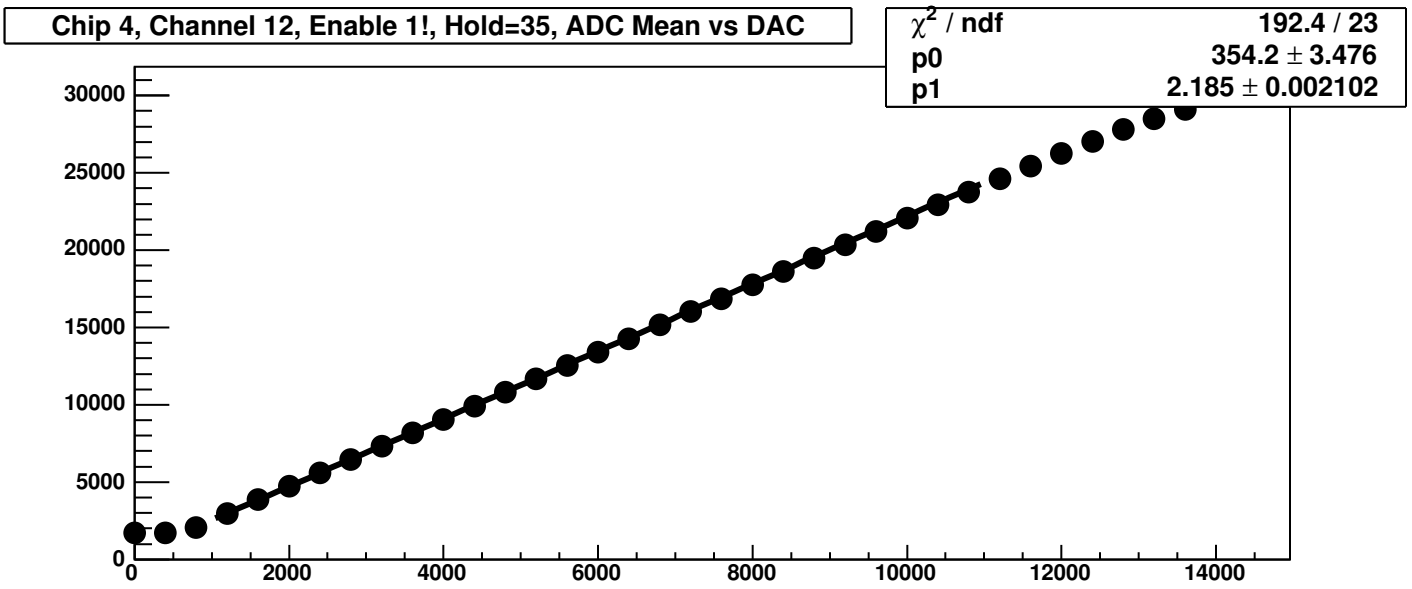
Chip 4, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

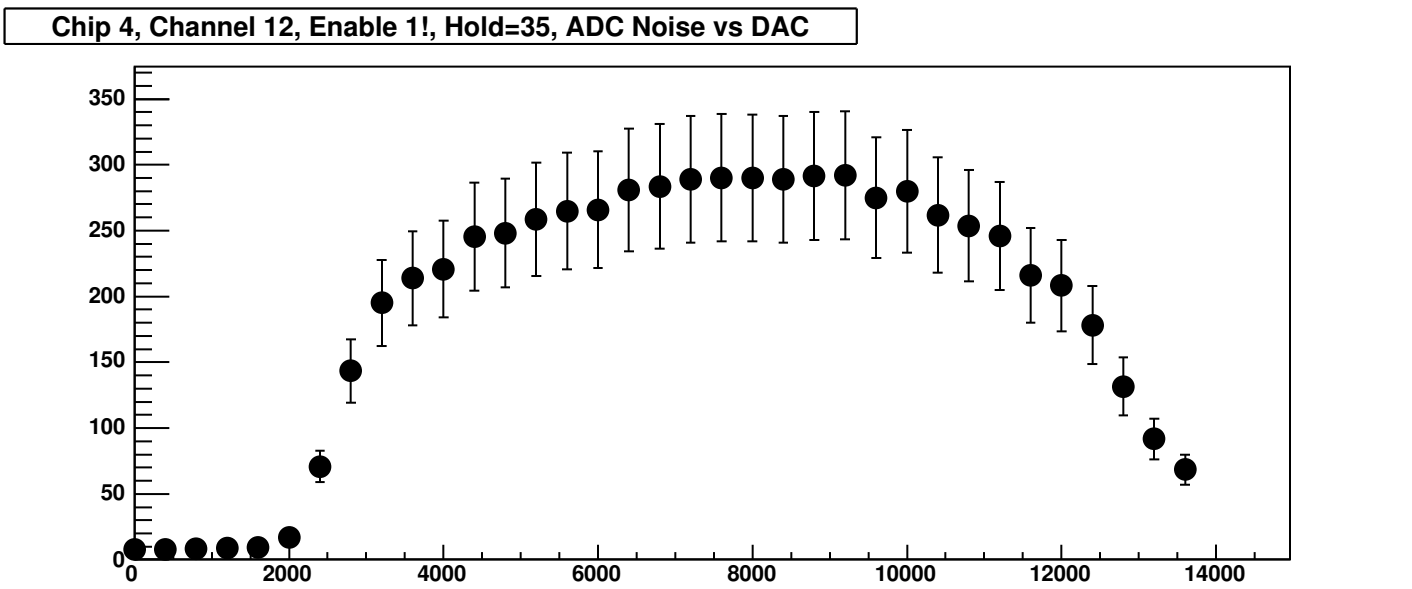


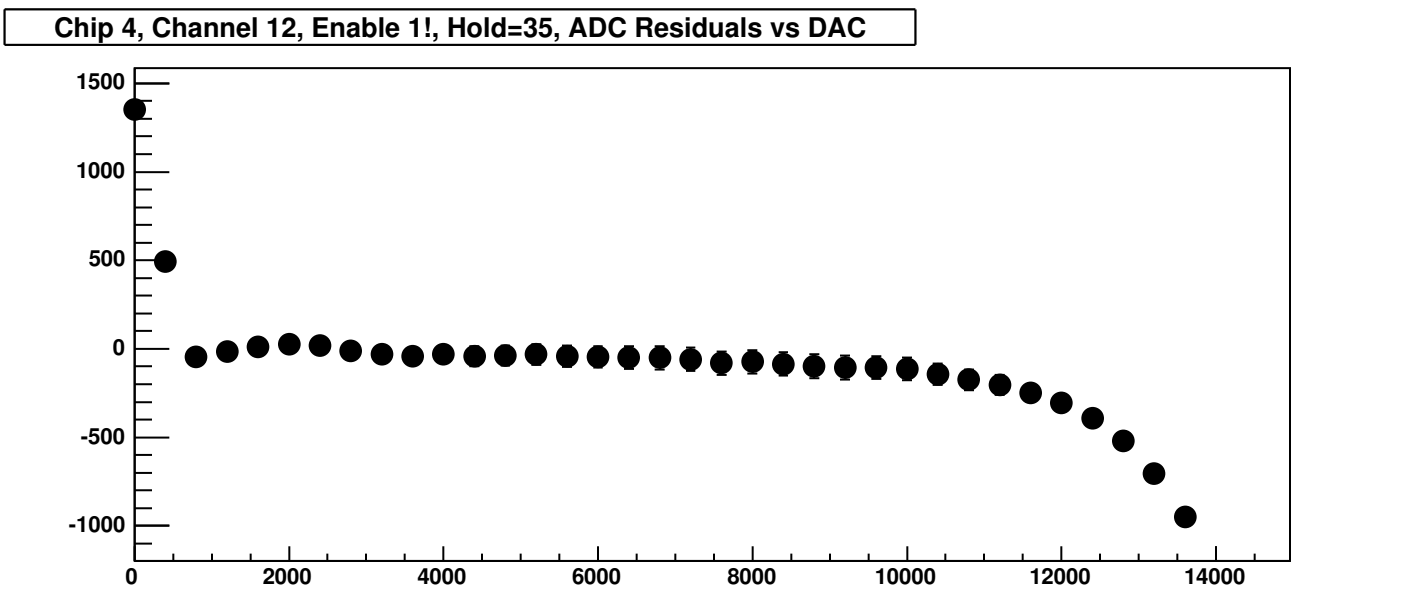

Chip 4, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 332.1 / 23
p0 79.07 ± 1.302
p1 0.1576 ± 0.0001968
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

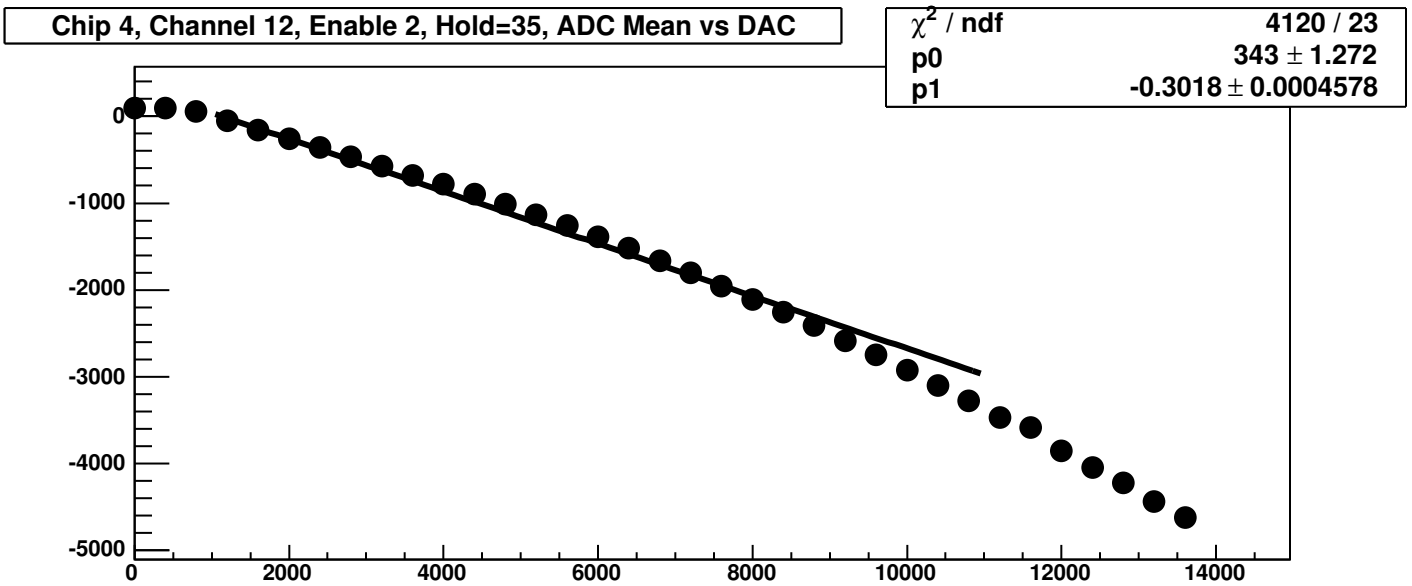

Chip 4, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

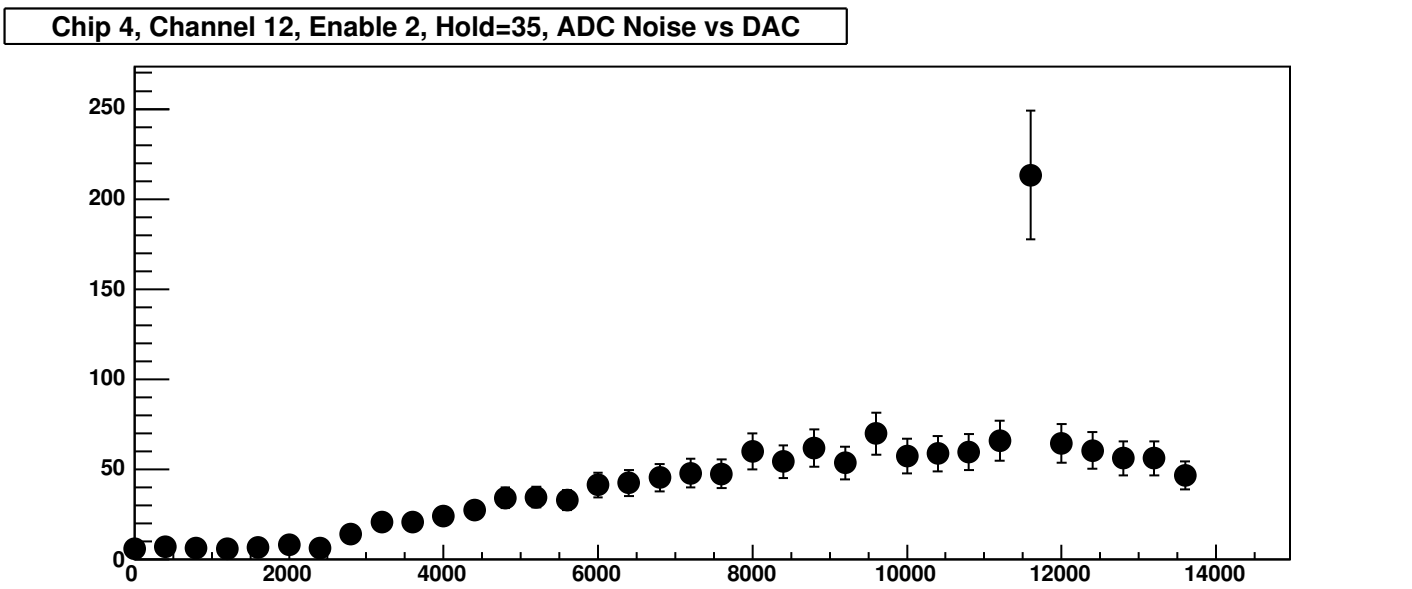

Chip 4, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

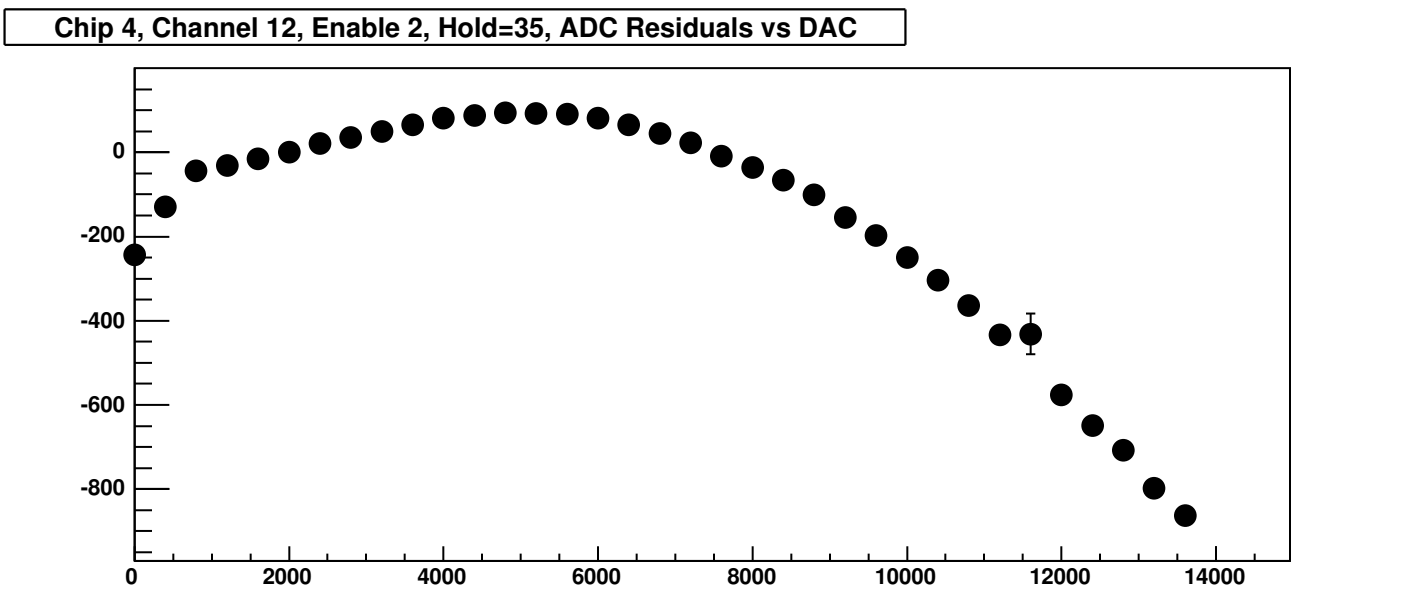

Chip 4, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 192.4 / 23
p0 354.2 ± 3.476
p1 2.185 ± 0.002102
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

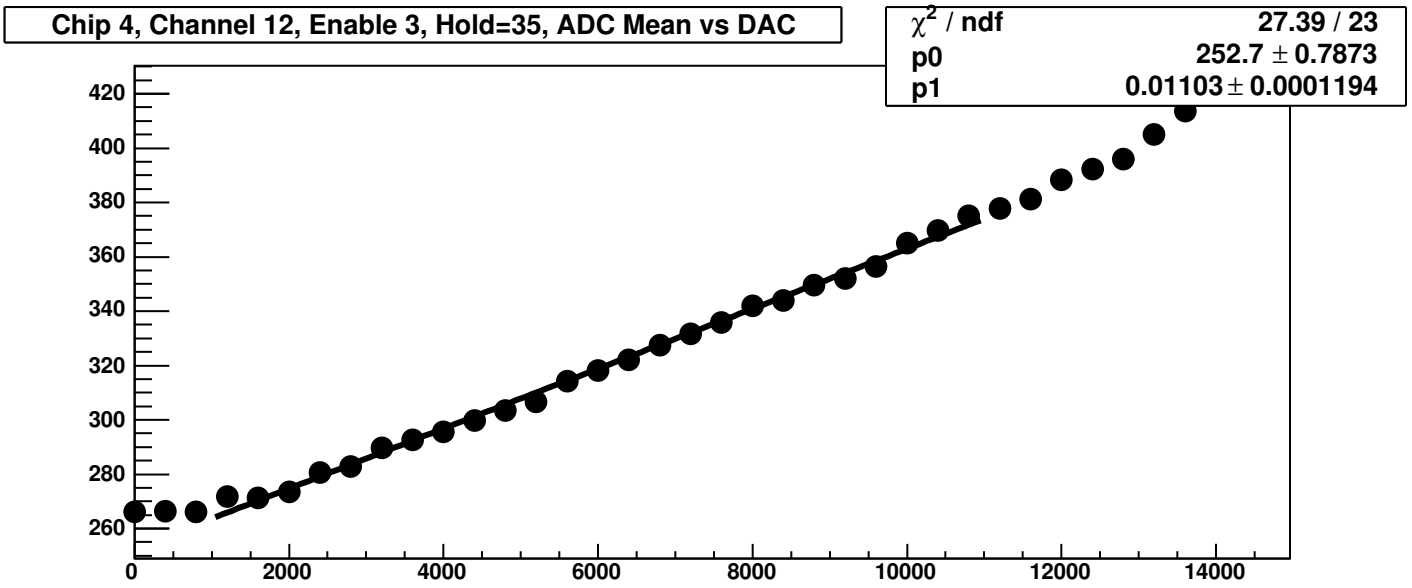

Chip 4, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

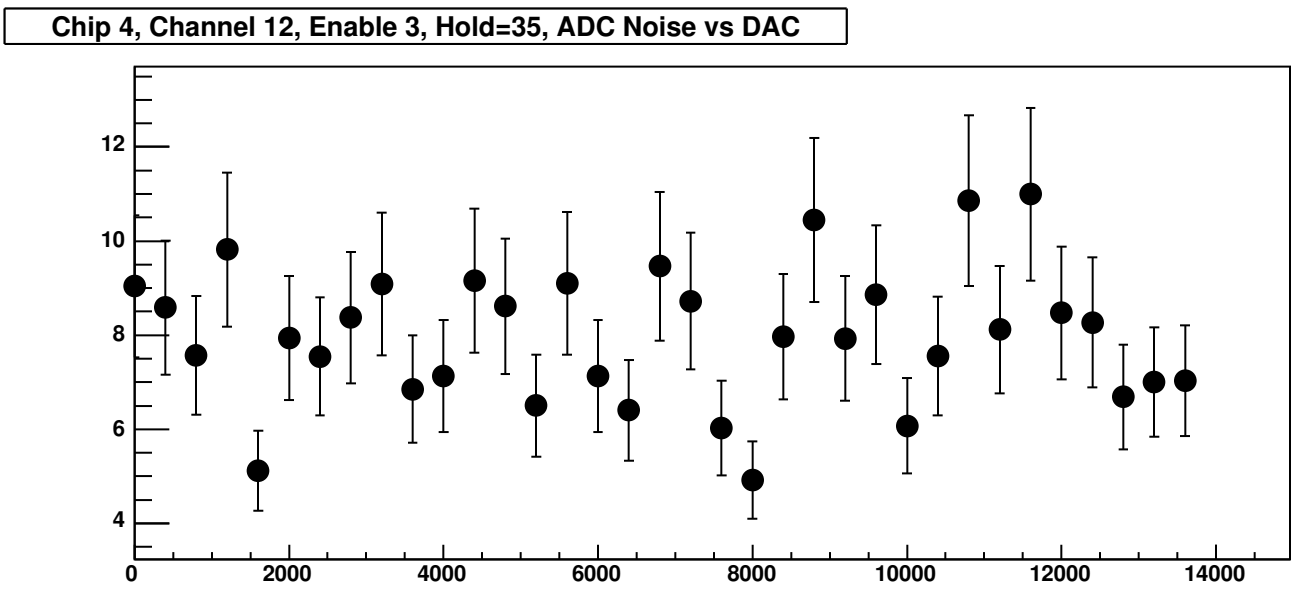

Chip 4, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

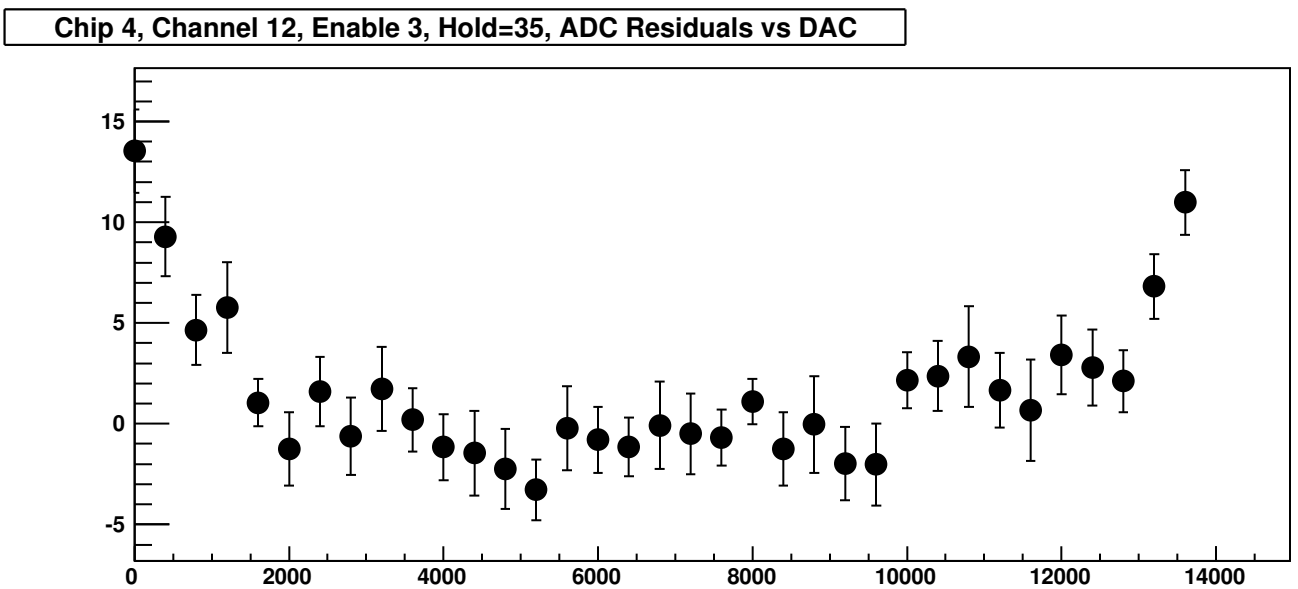

Chip 4, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 4120 / 23
p0 343 ± 1.272
p1 -0.3018 ± 0.0004578
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

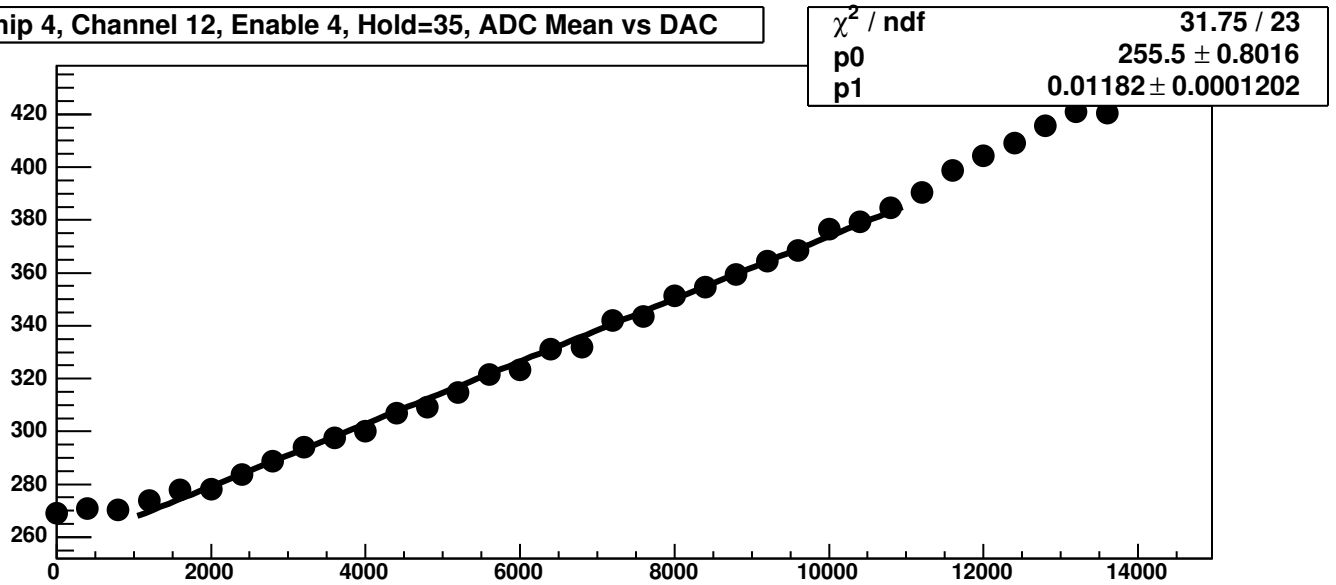

Chip 4, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 27.39 / 23
p0 252.7 ± 0.7873
p1 0.01103 ± 0.0001194
420
400
380
360
340
320
300
280
260
0
2000
4000
6000
8000
10000
12000
14000

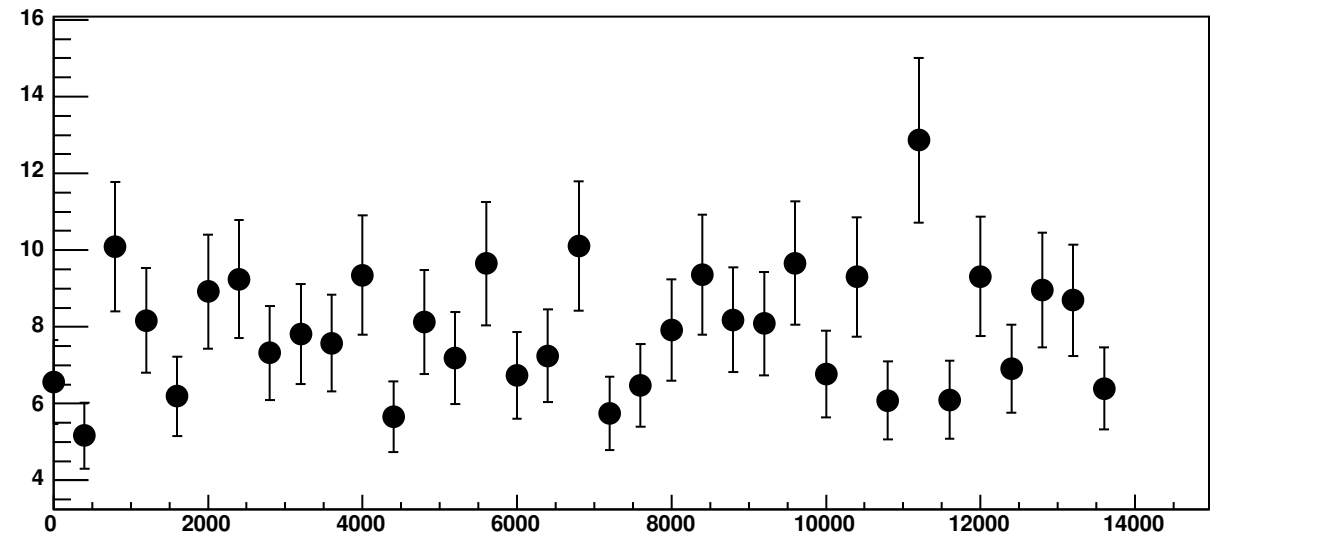

Chip 4, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

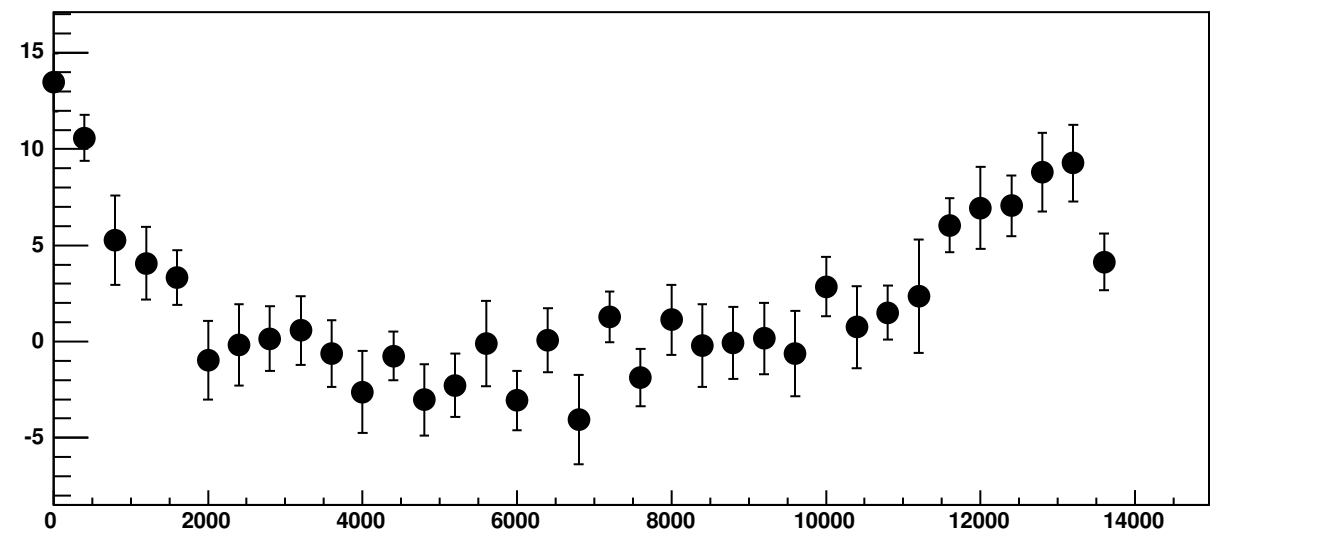
Chip 4, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC

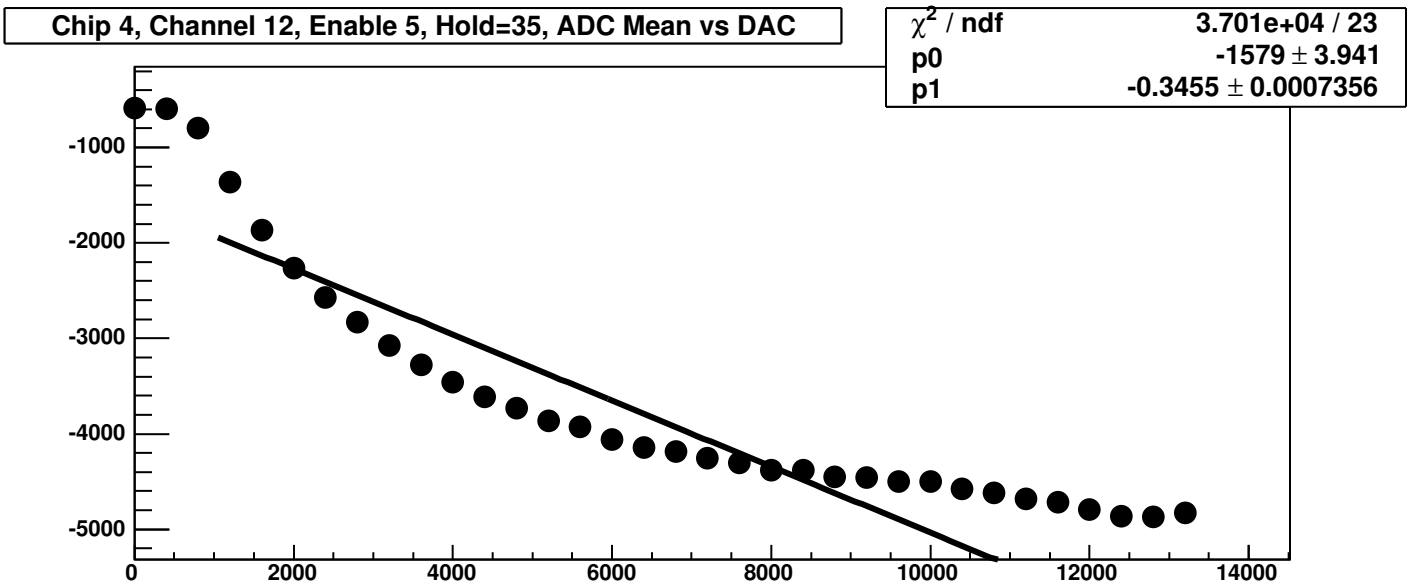


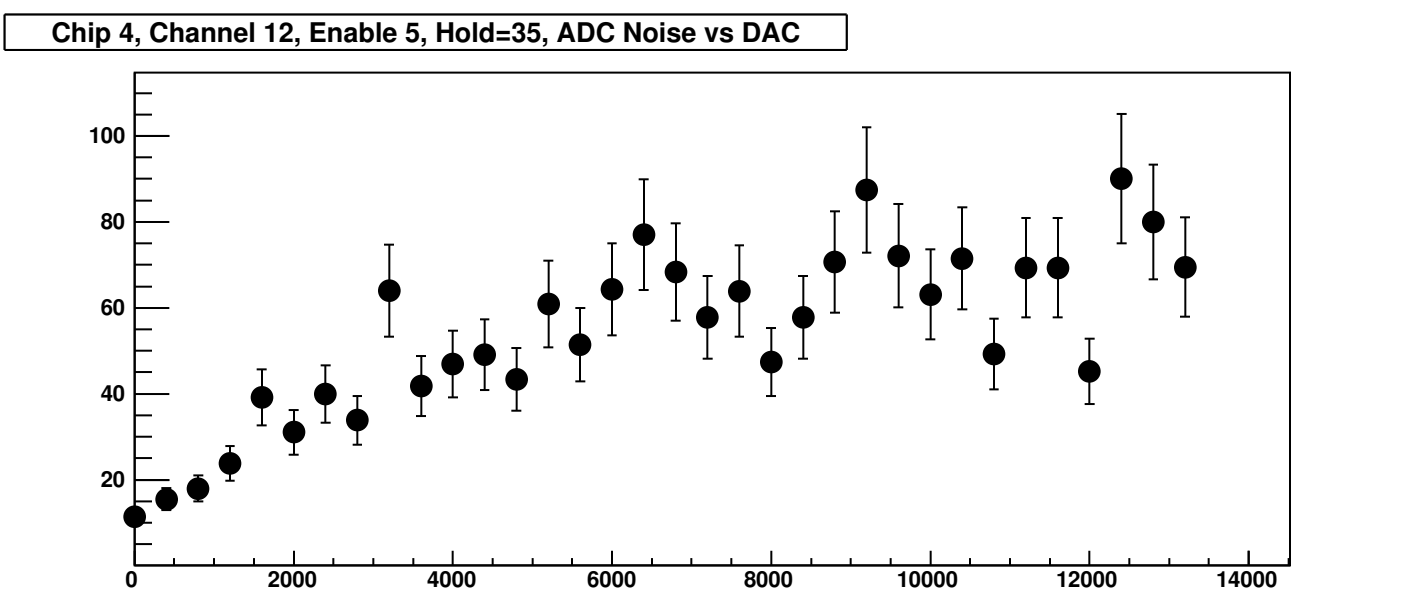
Chip 4, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC

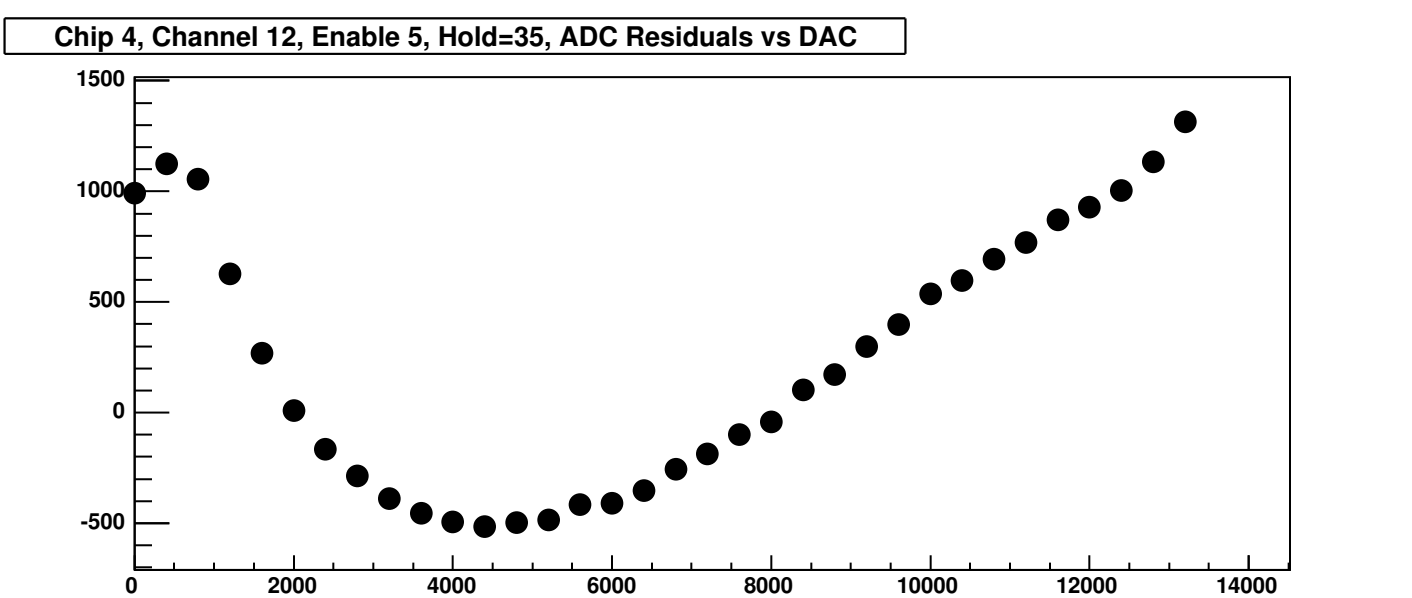


Chip 4, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC

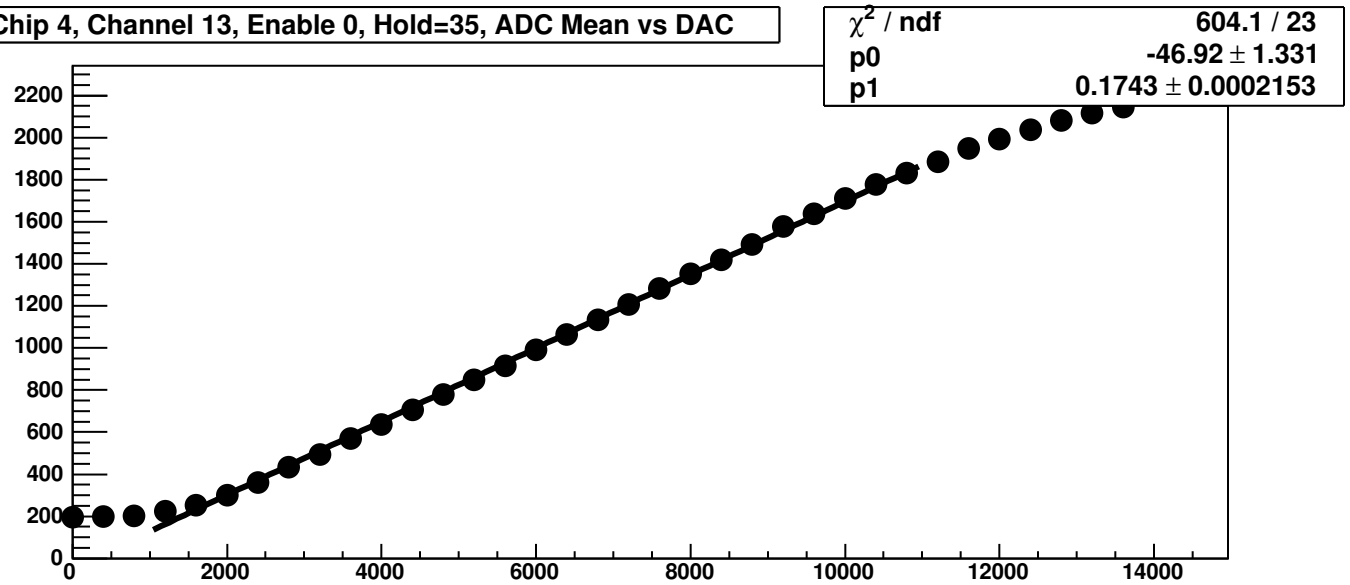



Chip 4, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.701e+04 / 23
p0 -1579 ± 3.941
p1 -0.3455 ± 0.0007356
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000

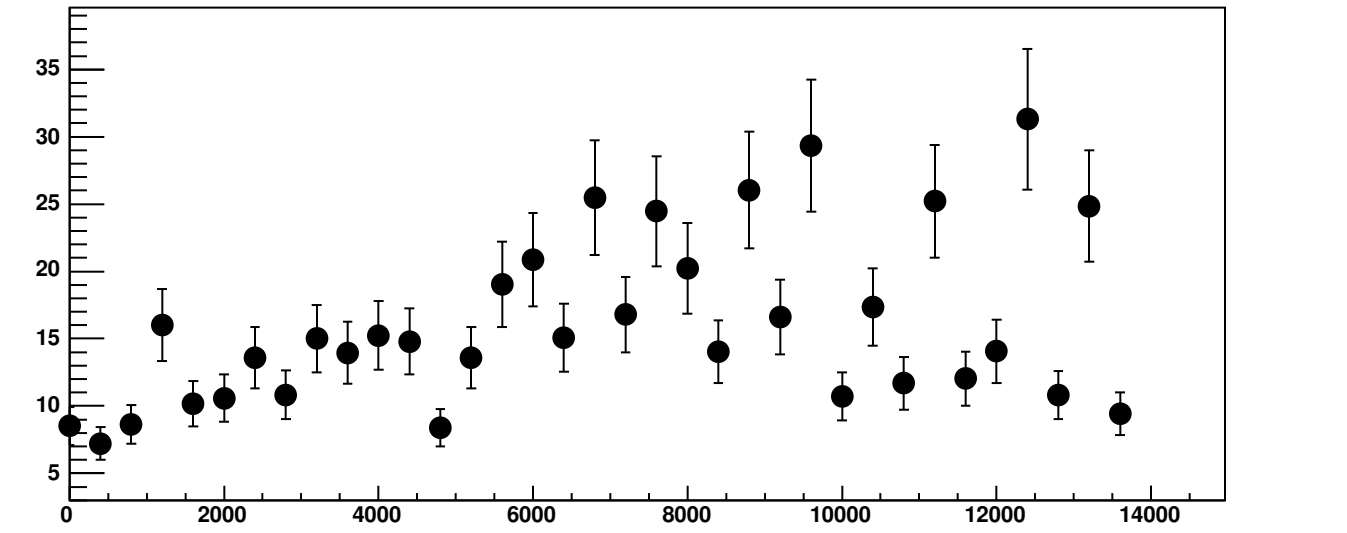

Chip 4, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

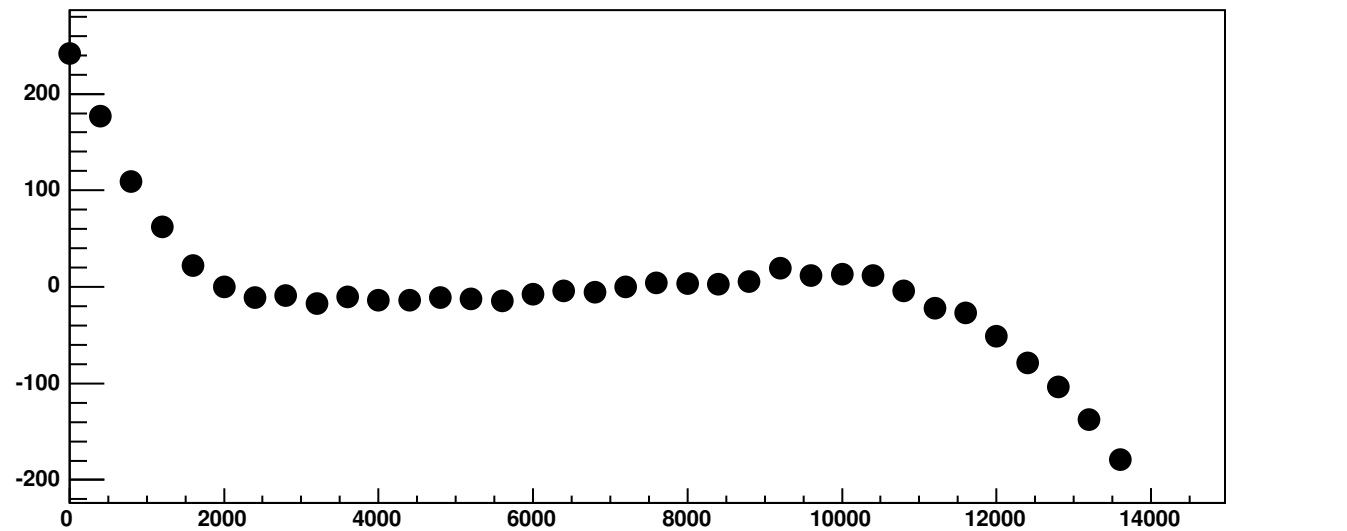
Chip 4, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

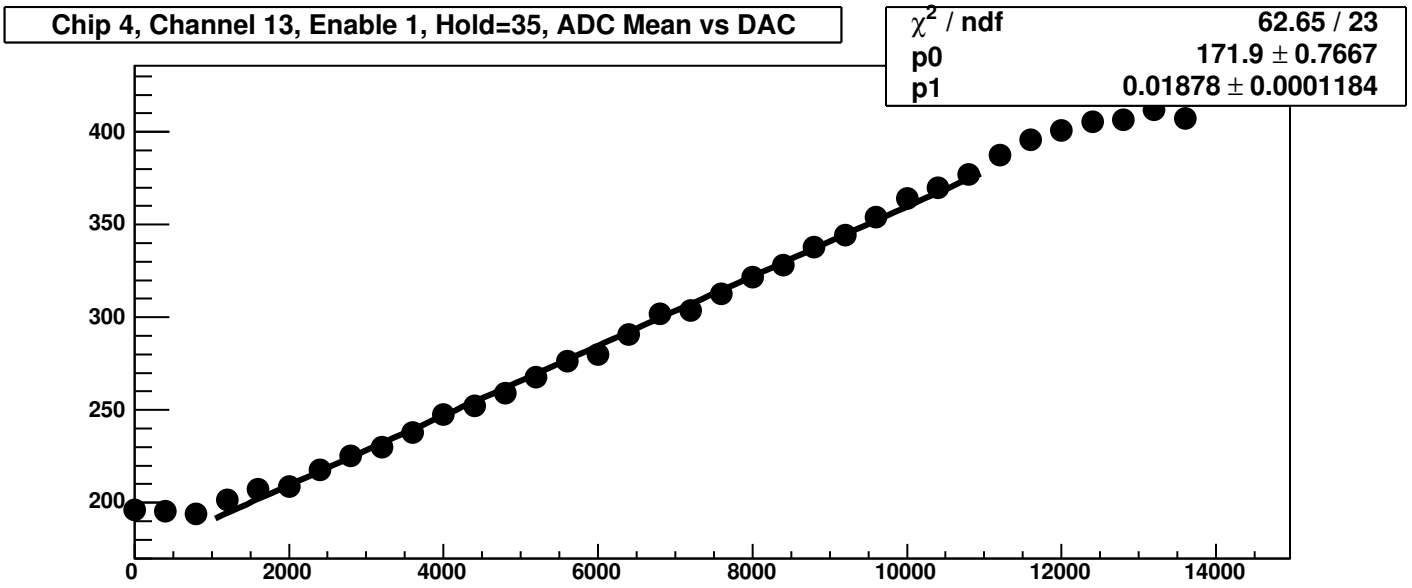


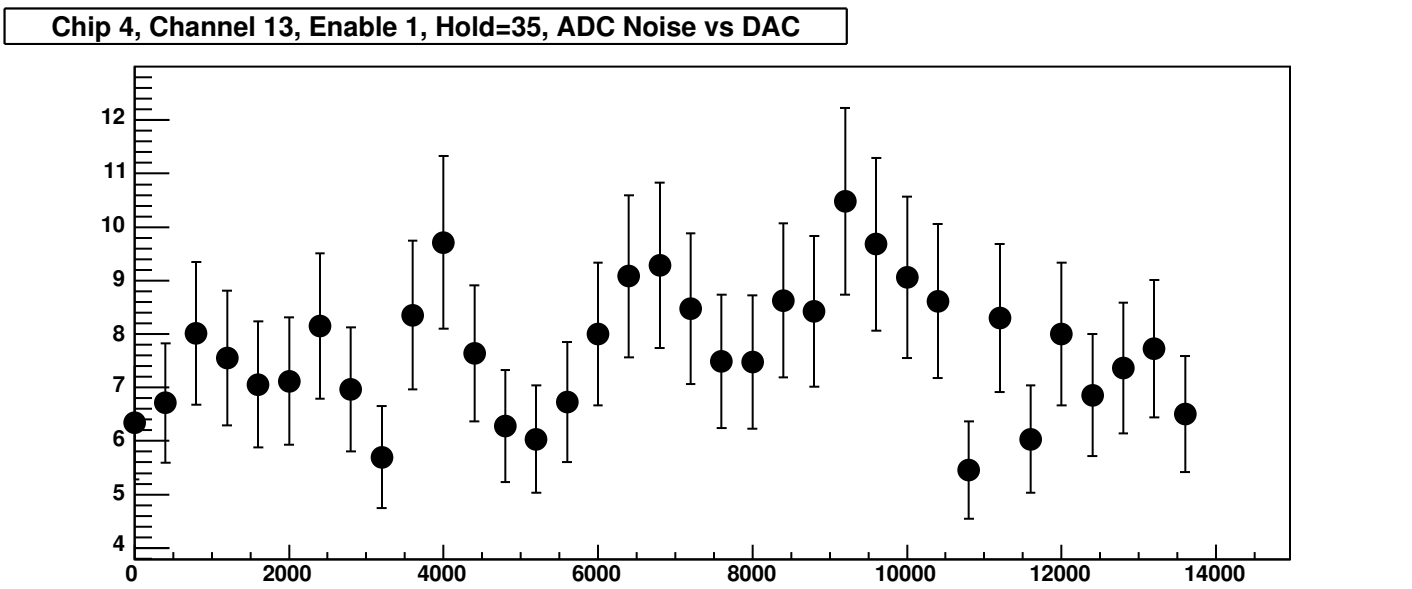
Chip 4, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

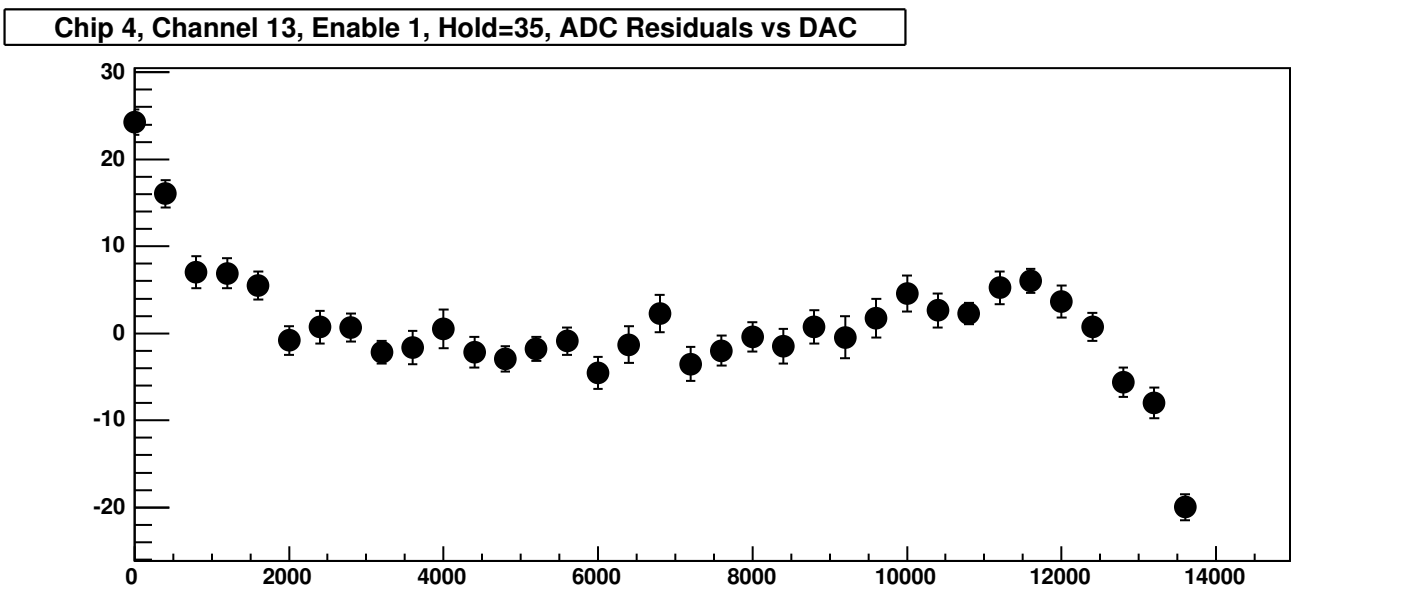


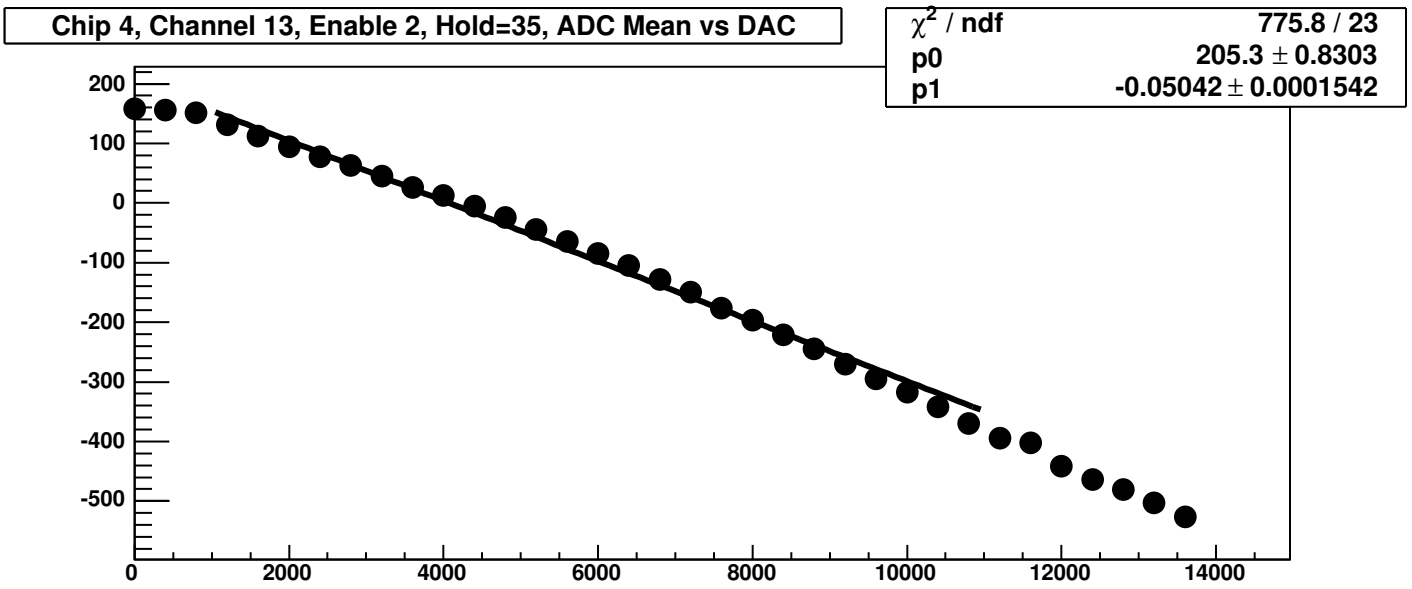
Chip 4, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

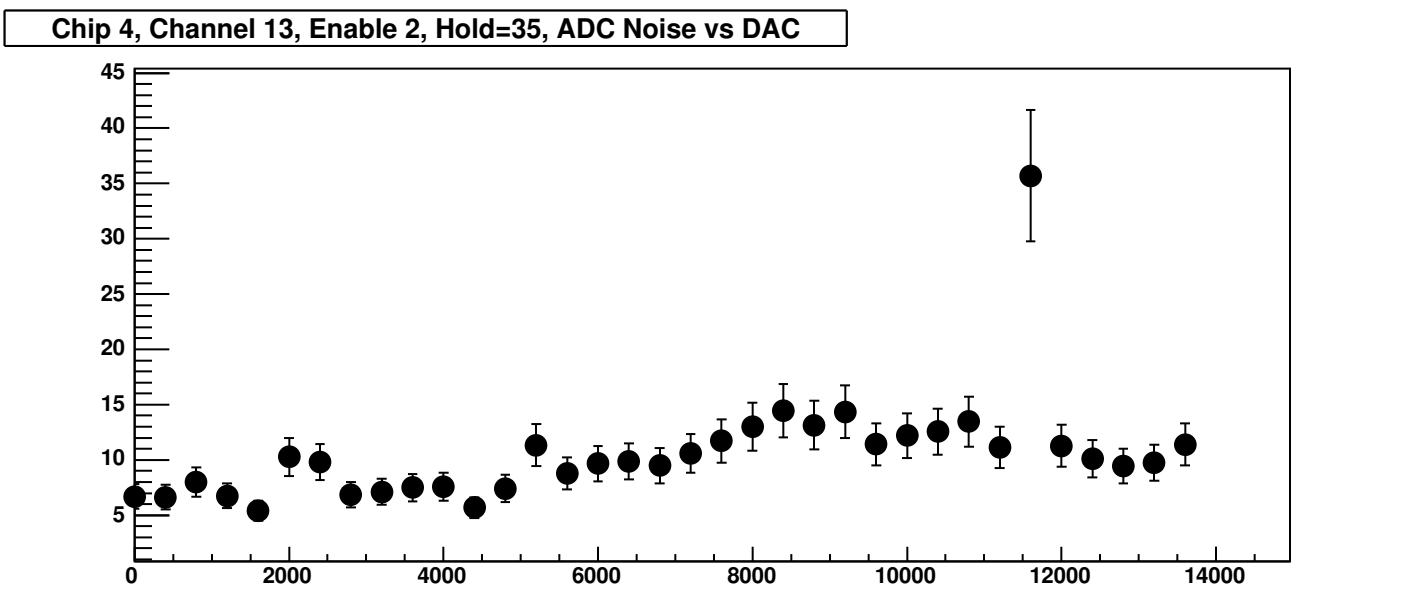


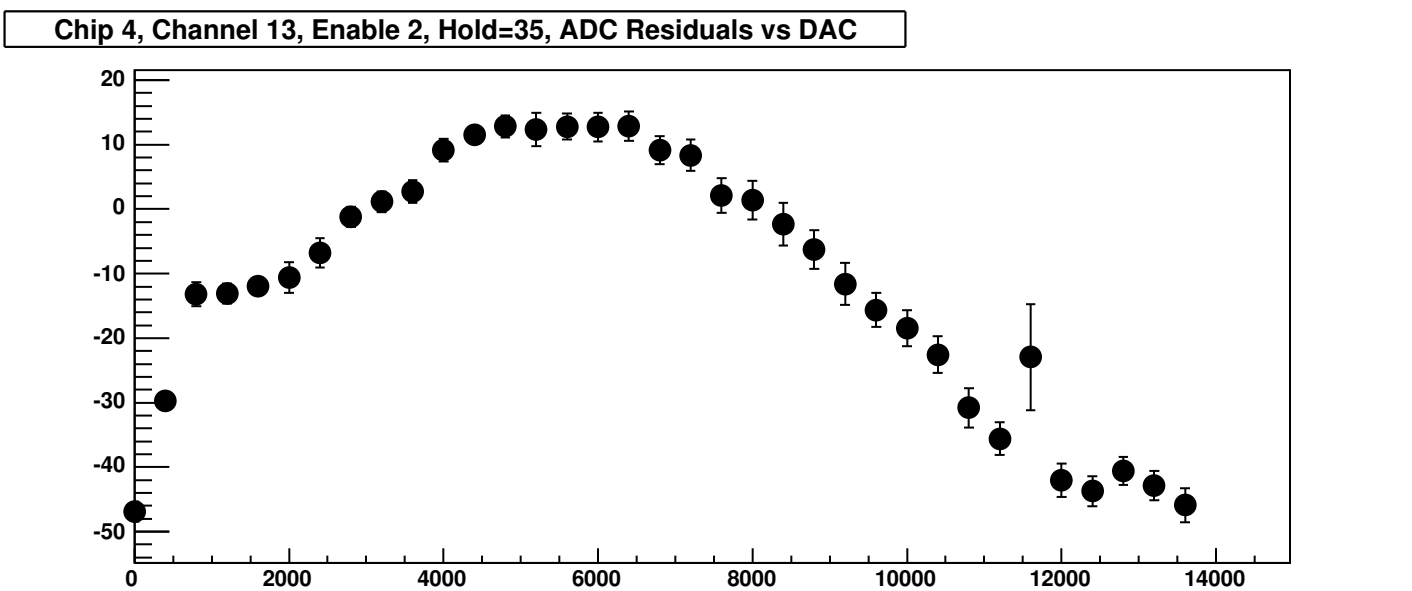

Chip 4, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 62.65 / 23
p0 171.9 ± 0.7667
p1 0.01878 ± 0.0001184
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

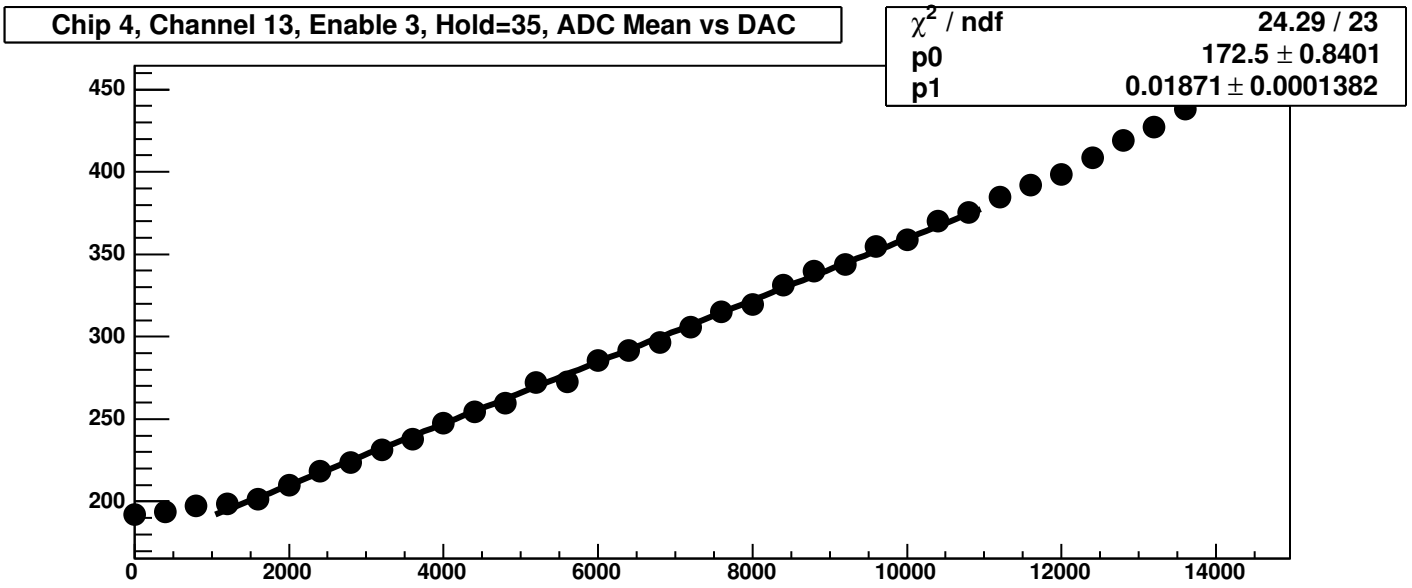

Chip 4, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

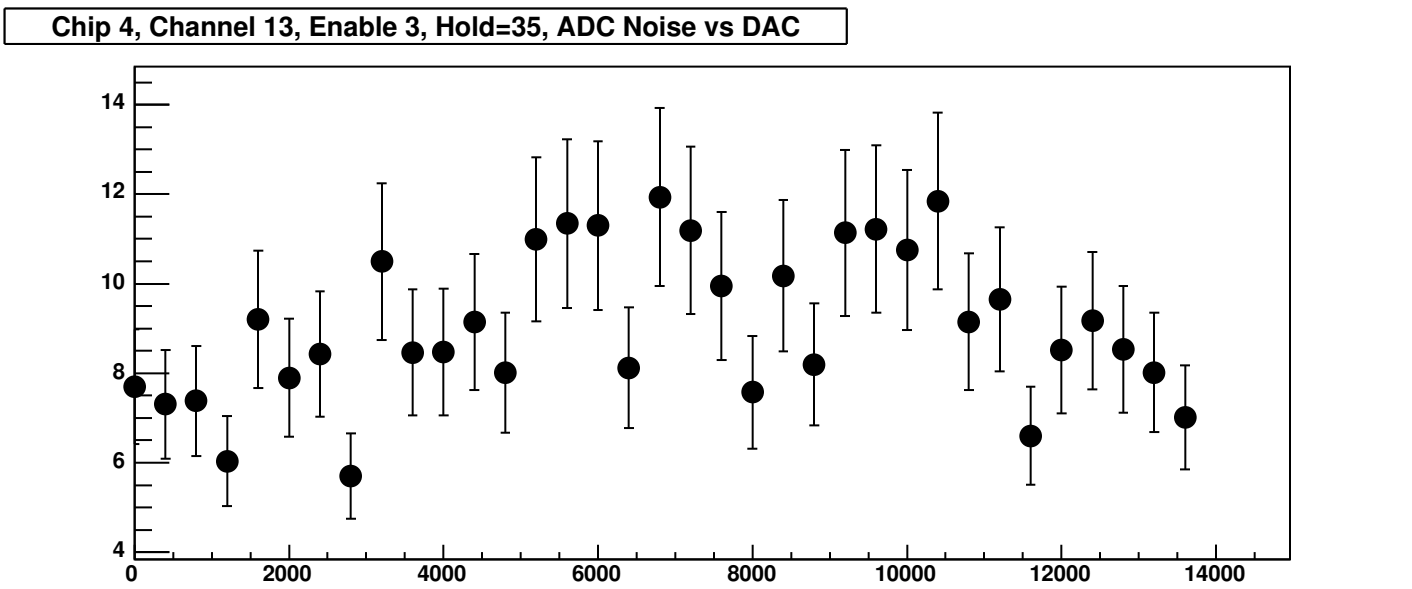

Chip 4, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

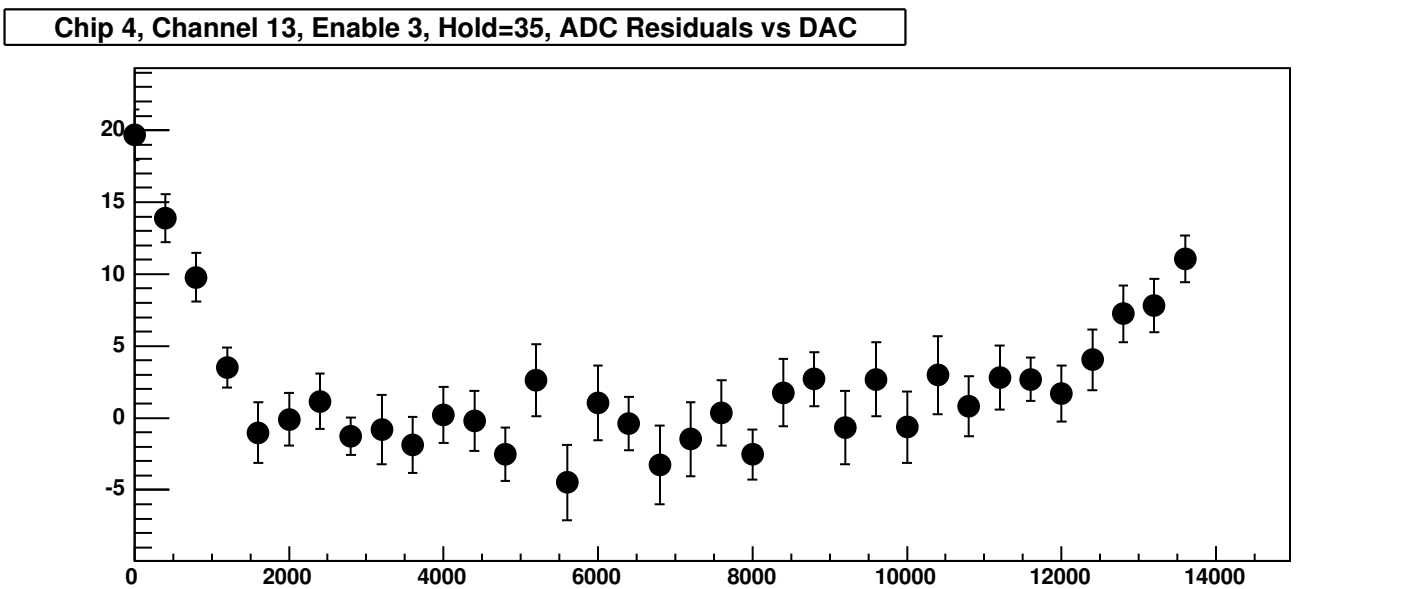

Chip 4, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 775.8 / 23
p0 205.3 ± 0.8303
p1 -0.05042 ± 0.0001542
200
100
0
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000

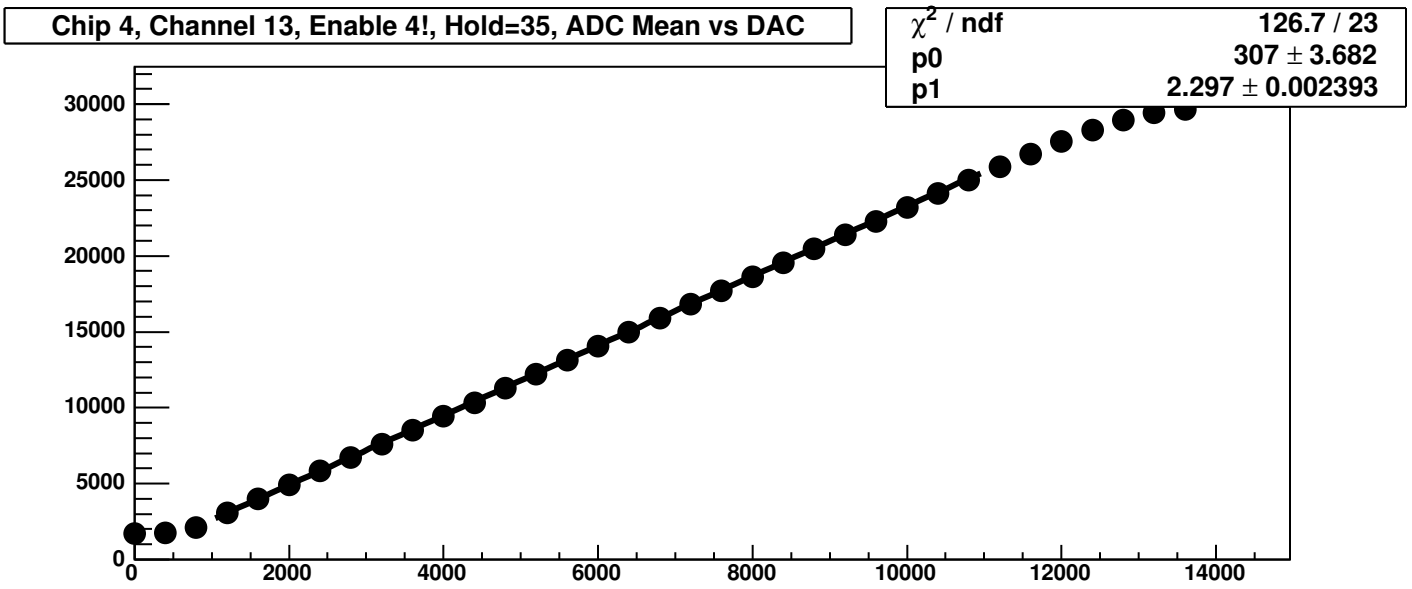

Chip 4, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

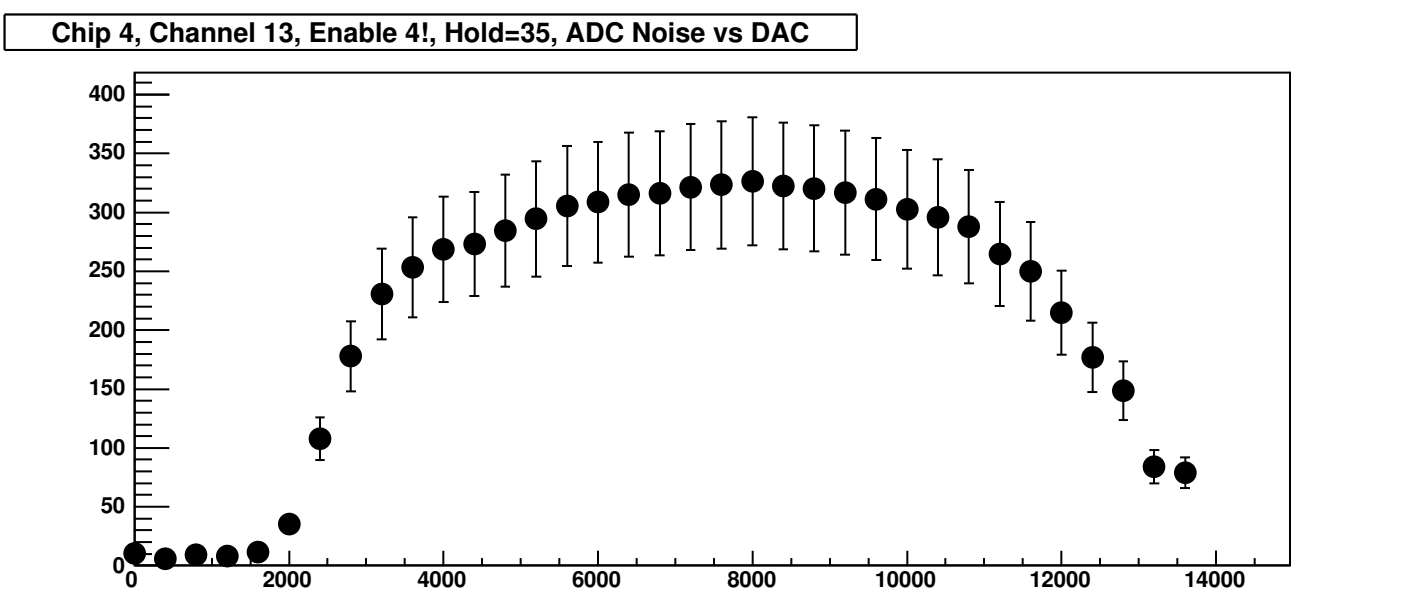

Chip 4, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

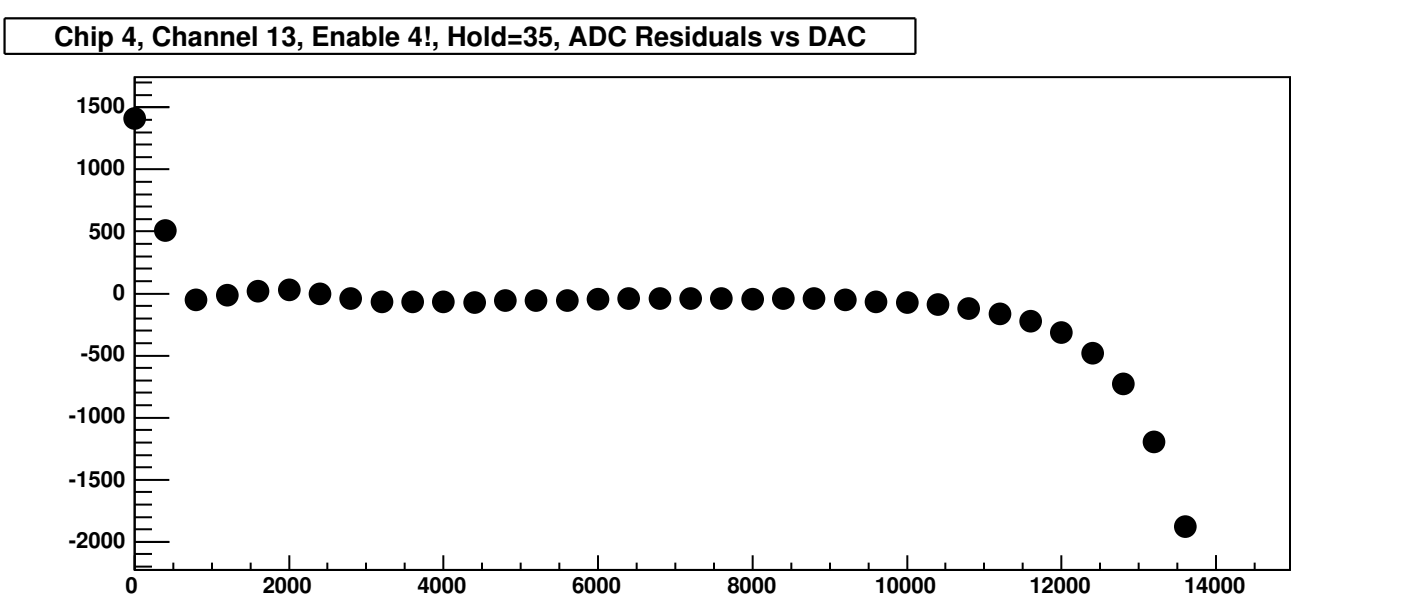

Chip 4, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.29 / 23
p0 172.5 ± 0.8401
p1 0.01871 ± 0.0001382
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

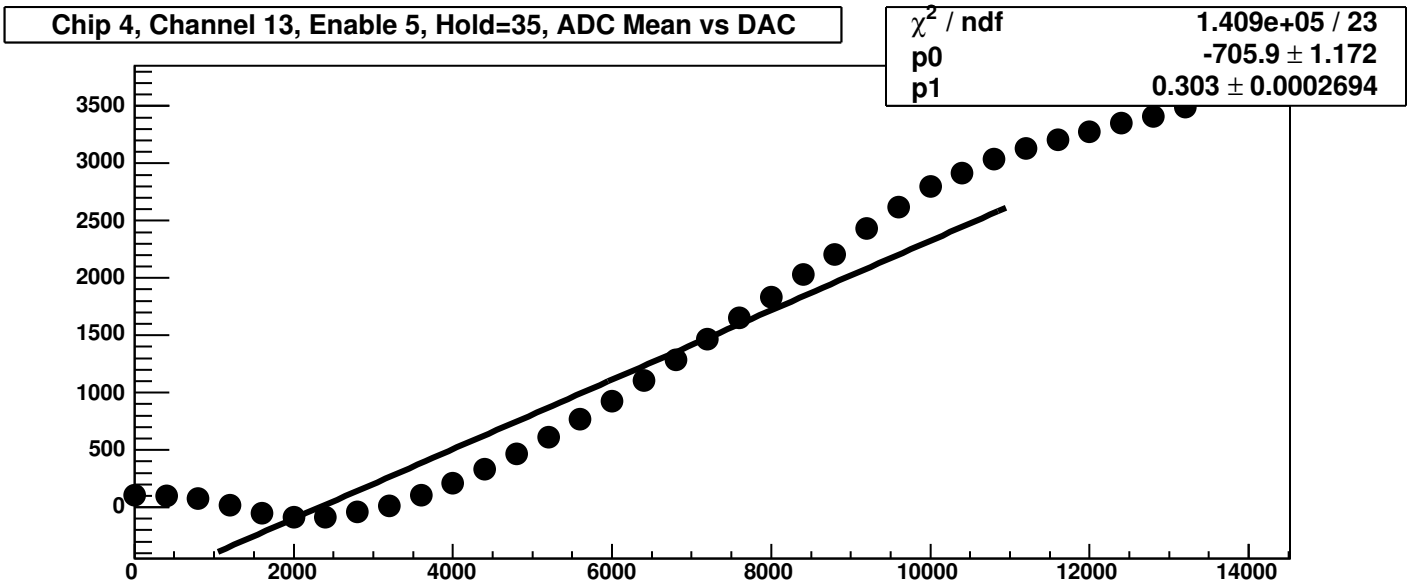

Chip 4, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

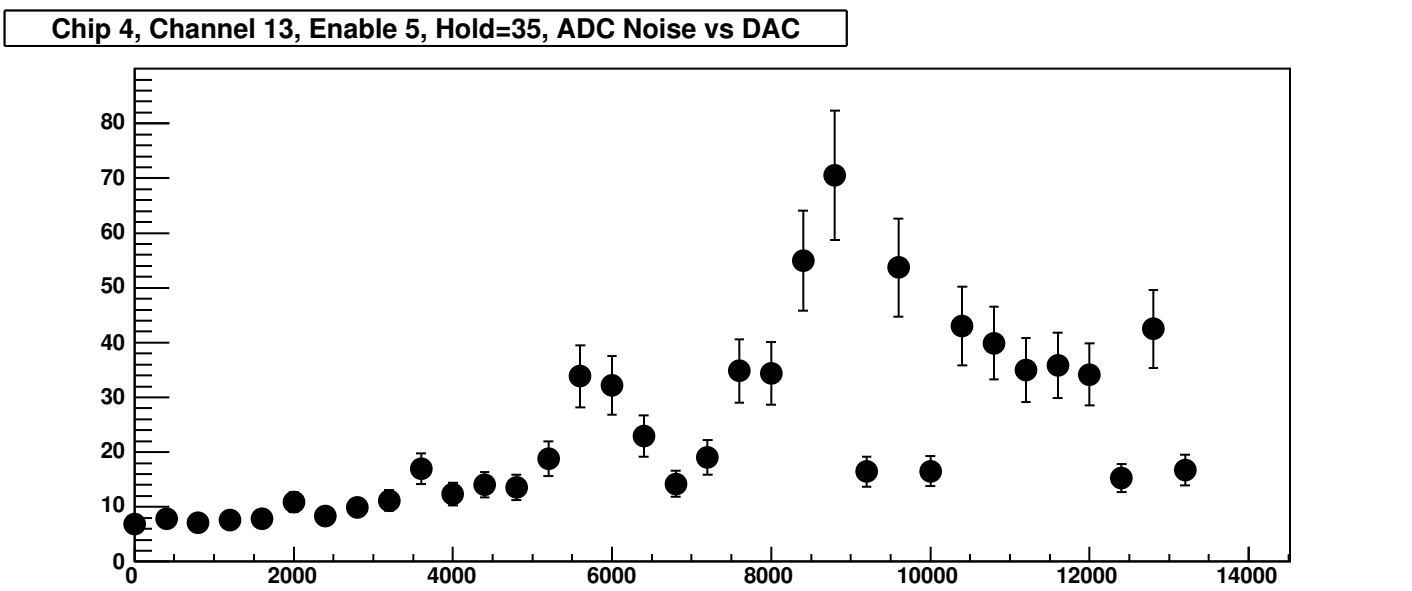

Chip 4, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

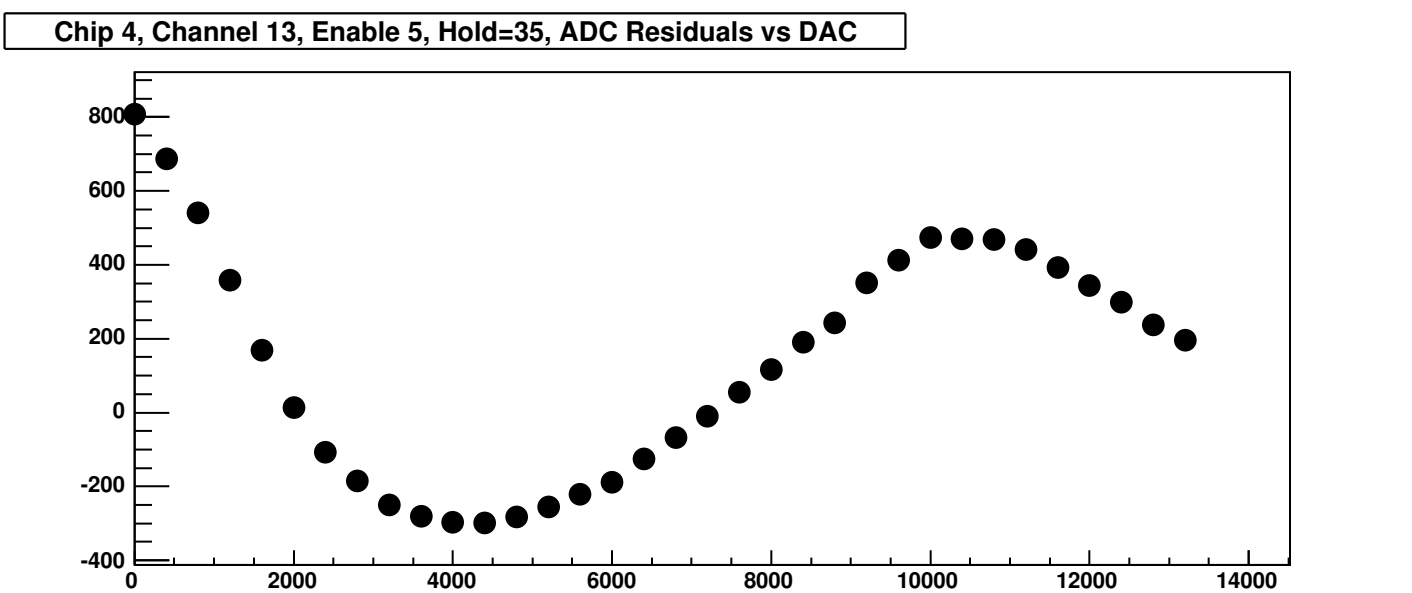

Chip 4, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 126.7 / 23
p0 307 ± 3.682
p1 2.297 ± 0.002393
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

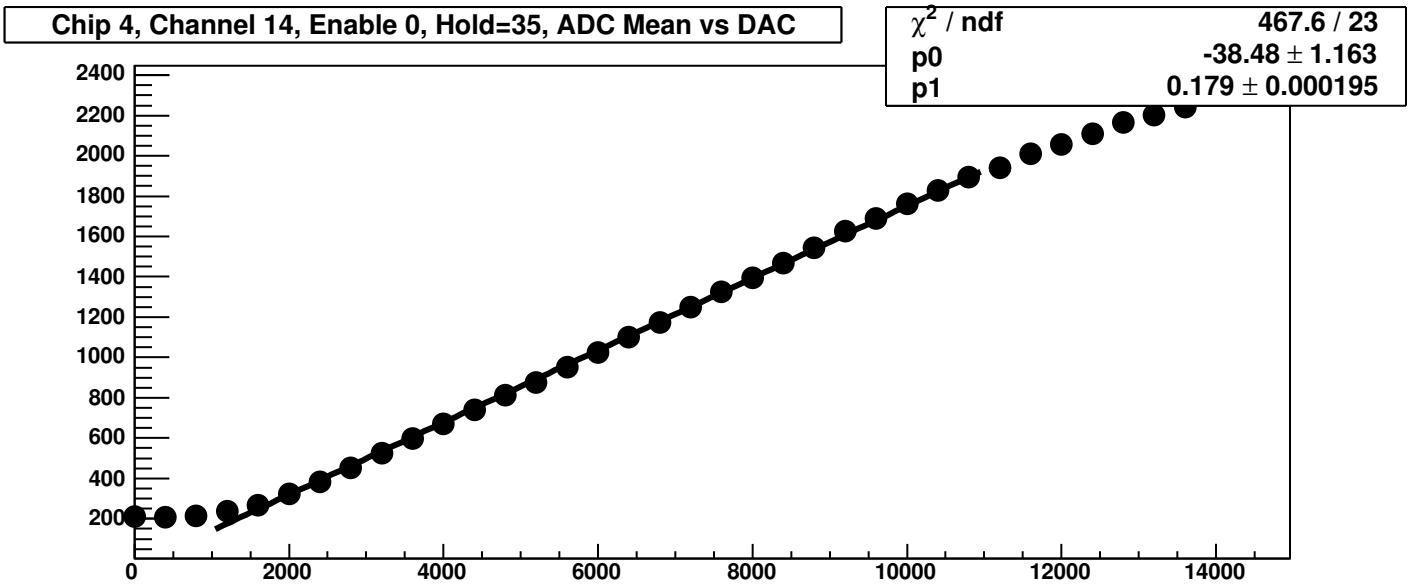

Chip 4, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

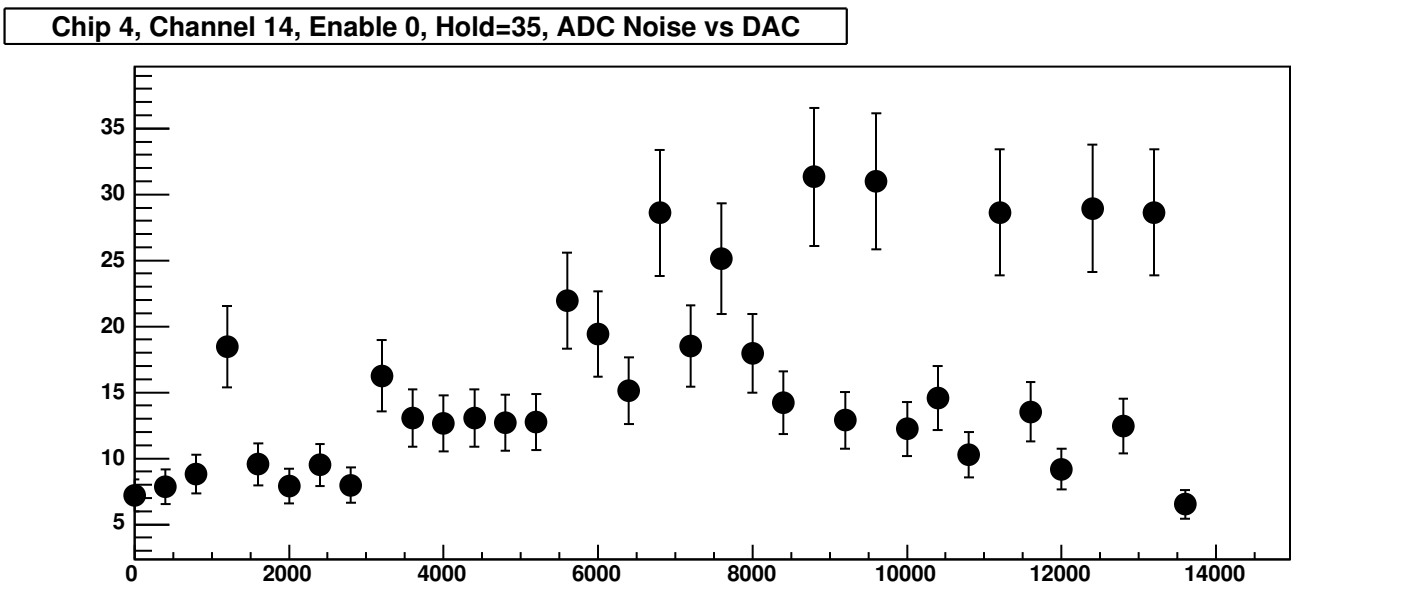

Chip 4, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

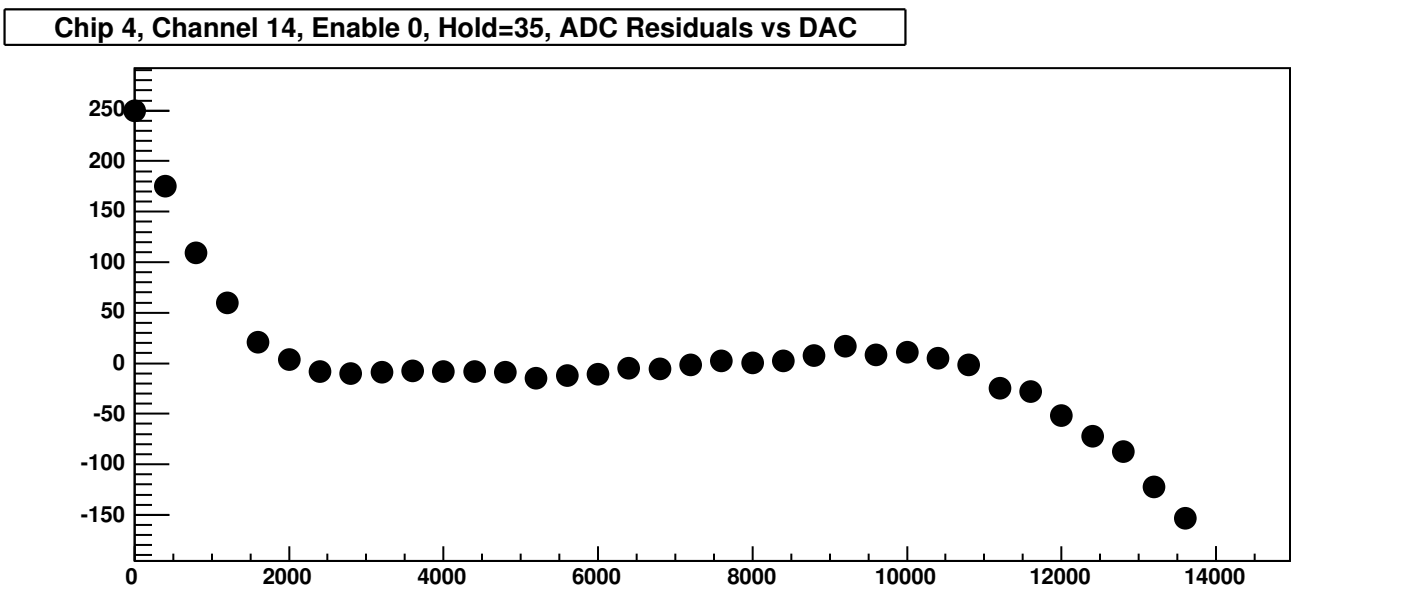

Chip 4, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.409e+05 / 23
p0 -705.9 ± 1.172
p1 0.303 ± 0.0002694
3500
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

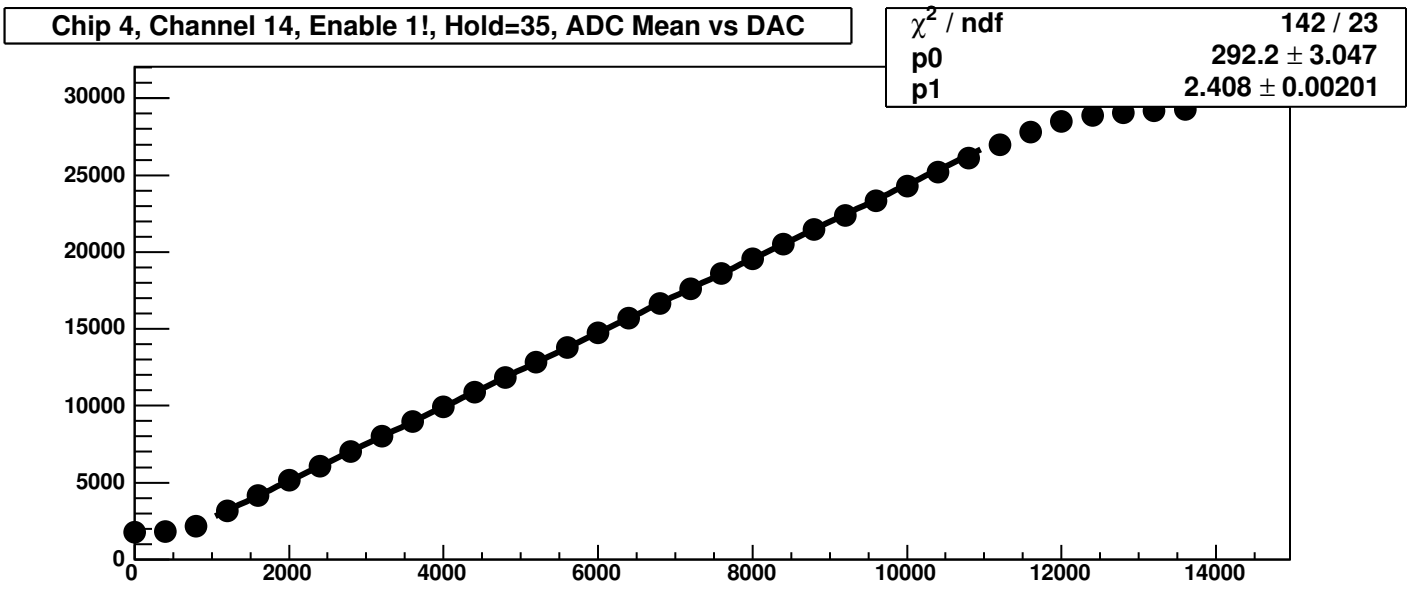

Chip 4, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

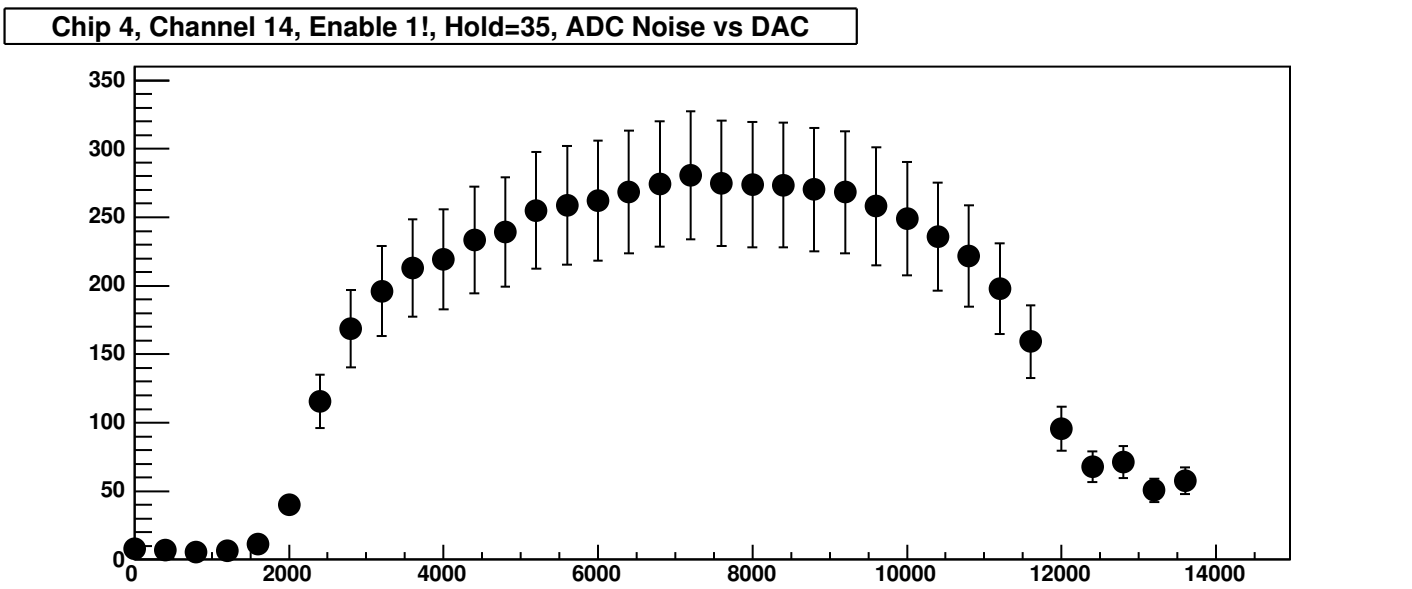

Chip 4, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

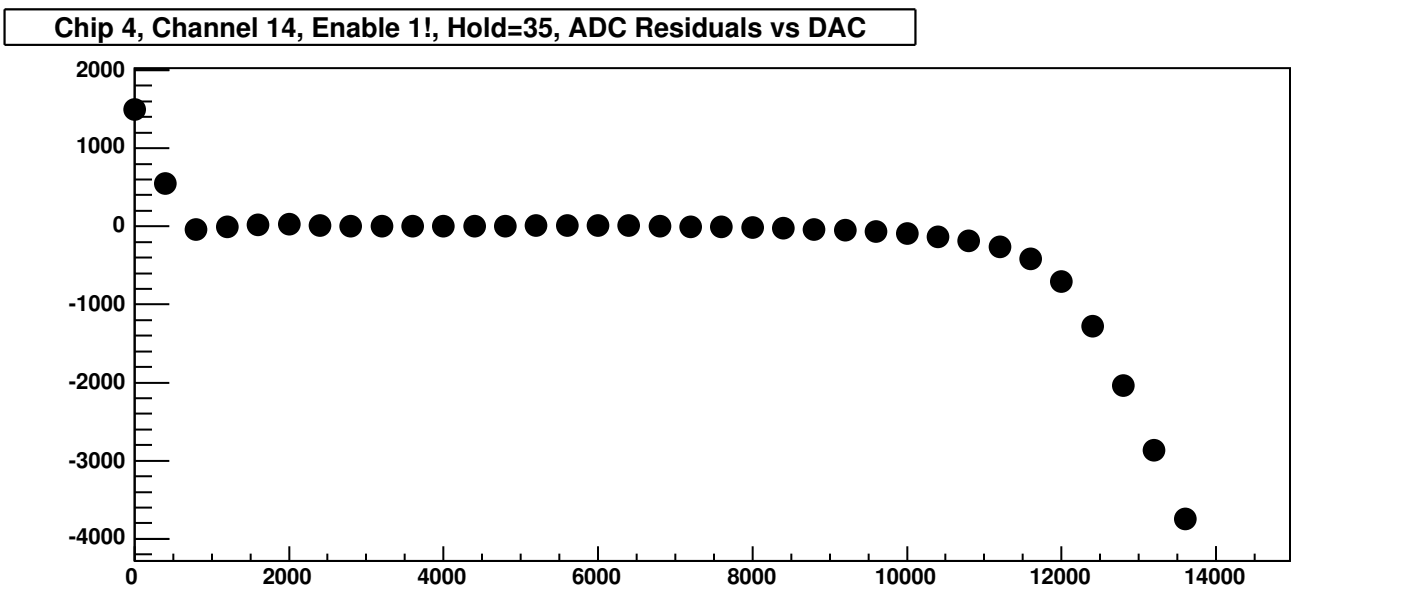

Chip 4, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 467.6 / 23
p0 -38.48 ± 1.163
p1 0.179 ± 0.000195
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

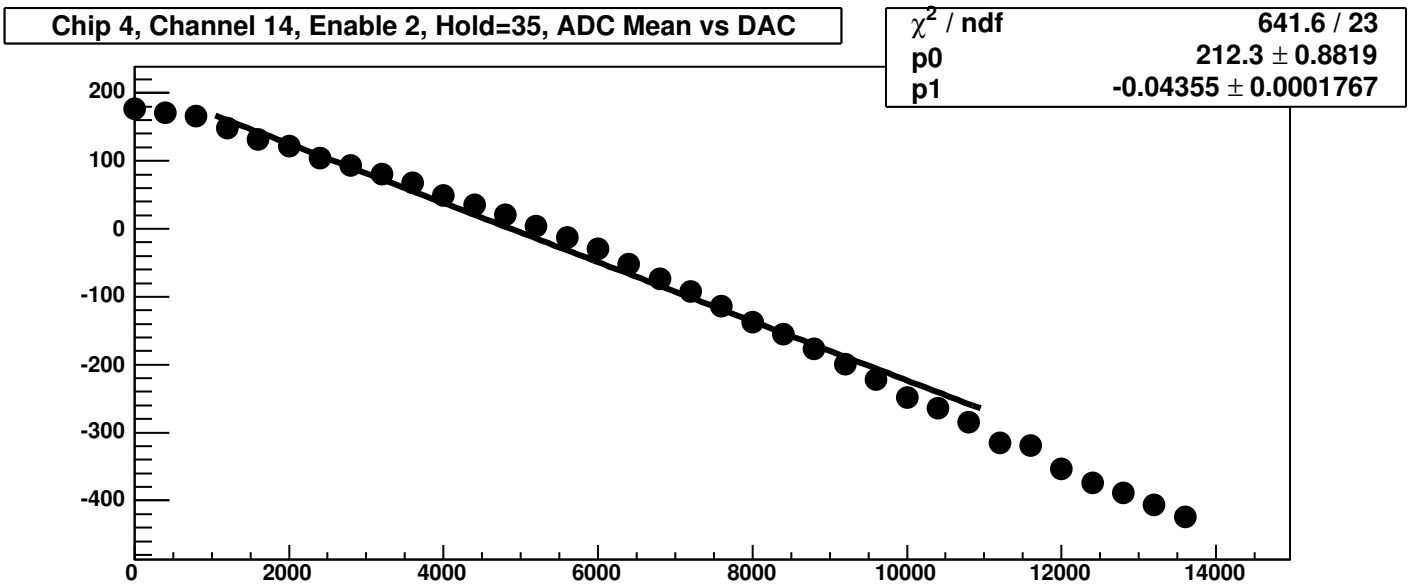

Chip 4, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

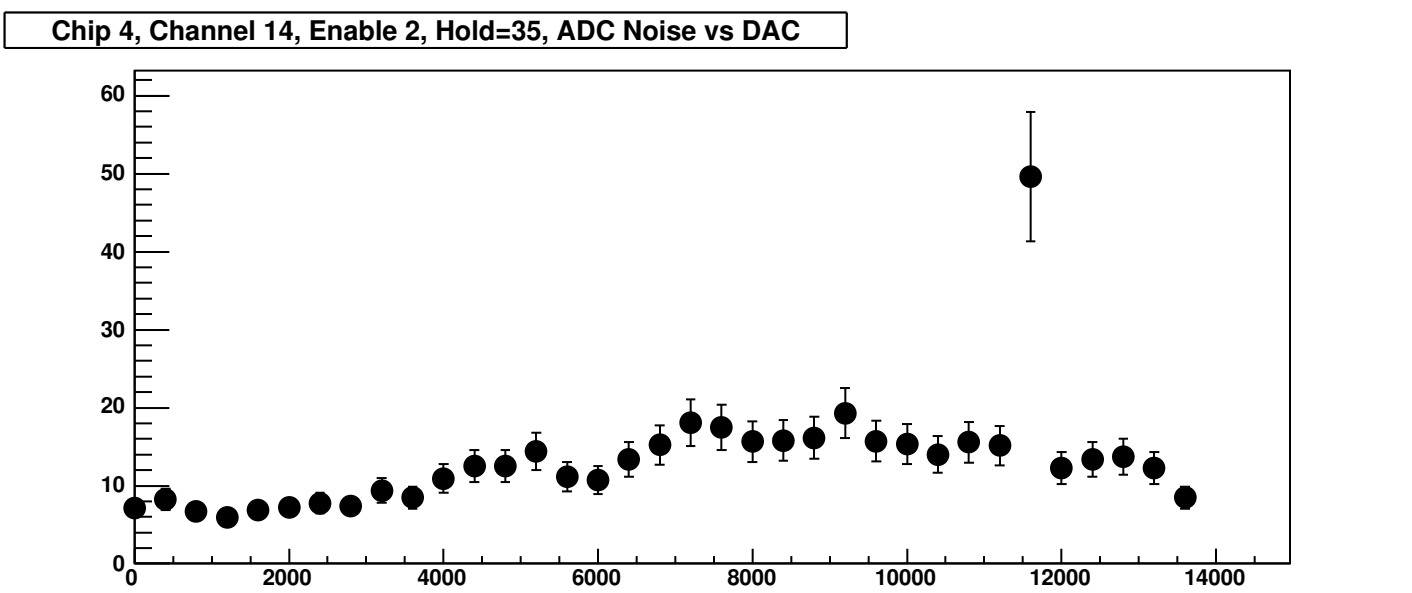

Chip 4, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

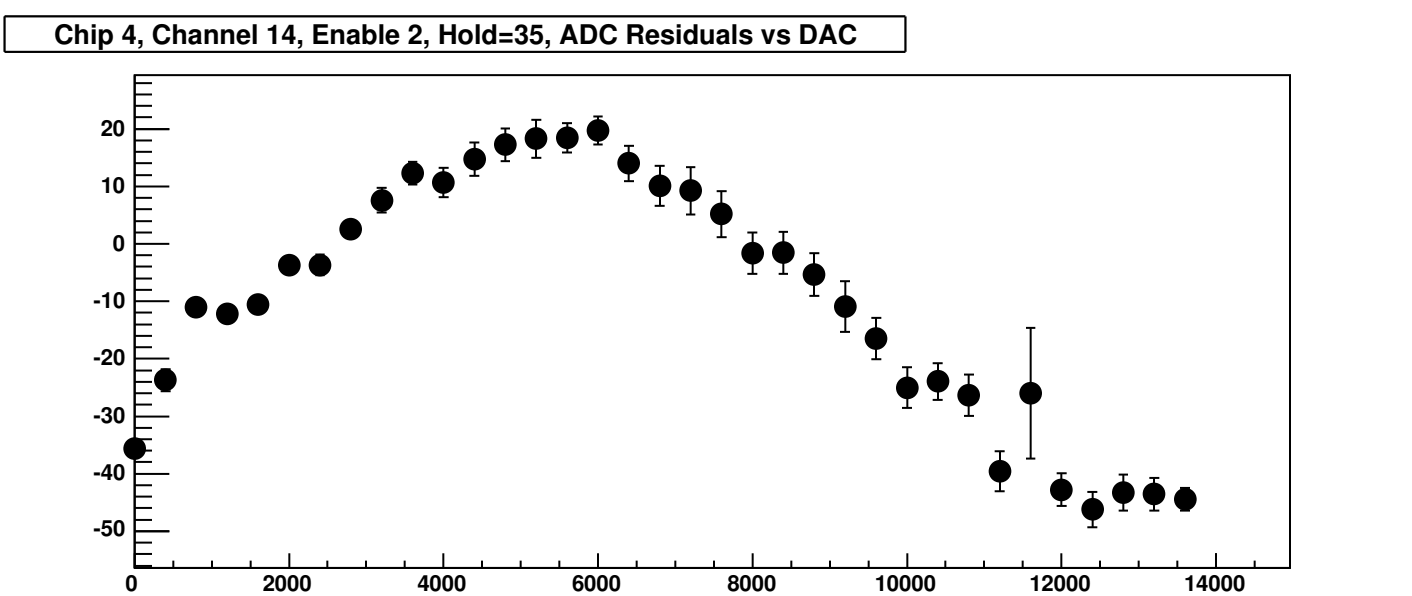

Chip 4, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 142 / 23
p0 292.2 ± 3.047
p1 2.408 ± 0.00201
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

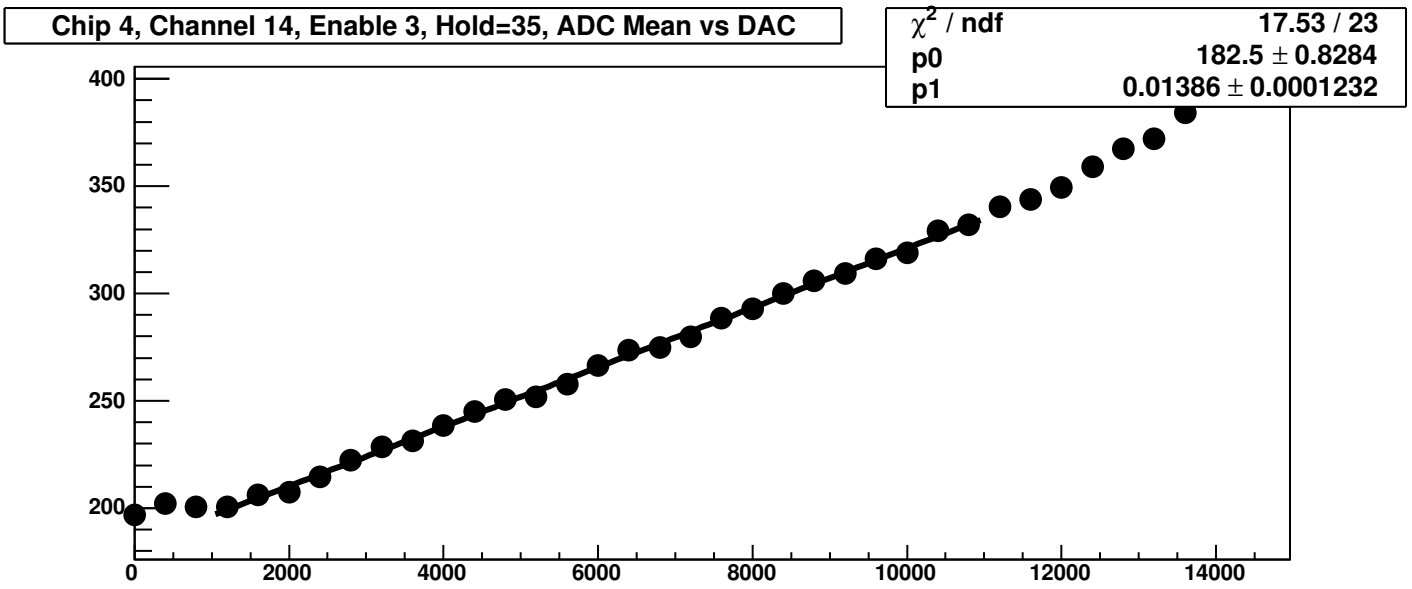

Chip 4, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

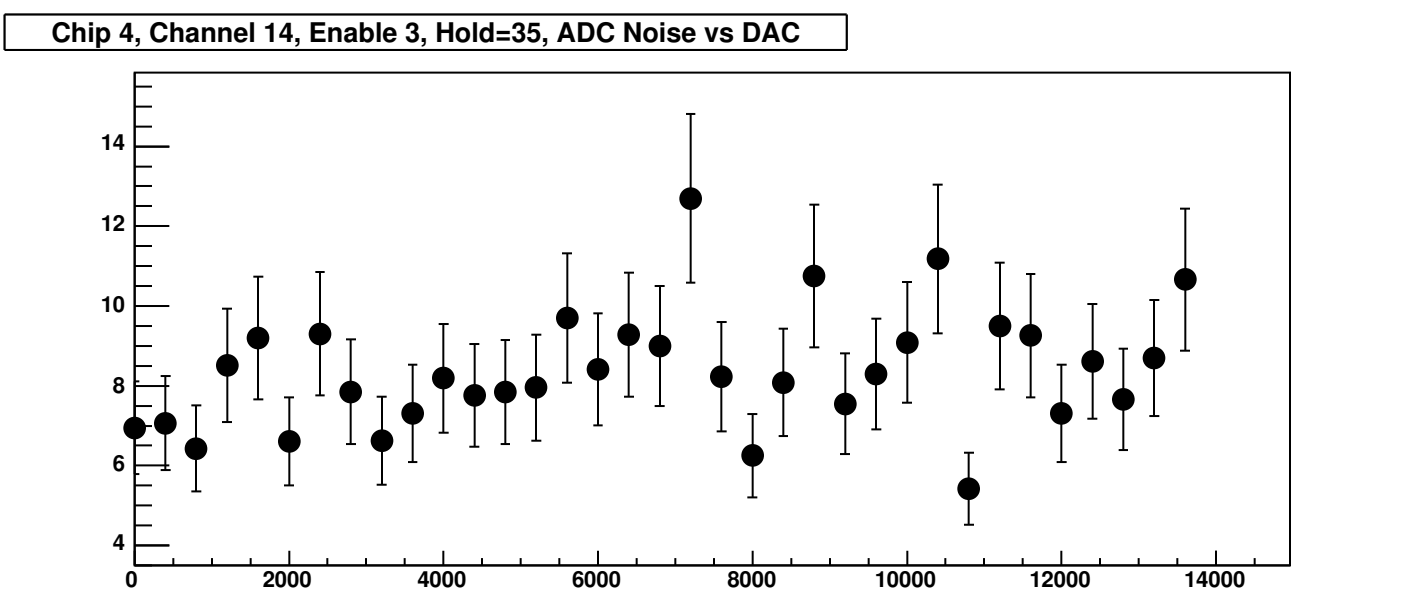

Chip 4, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

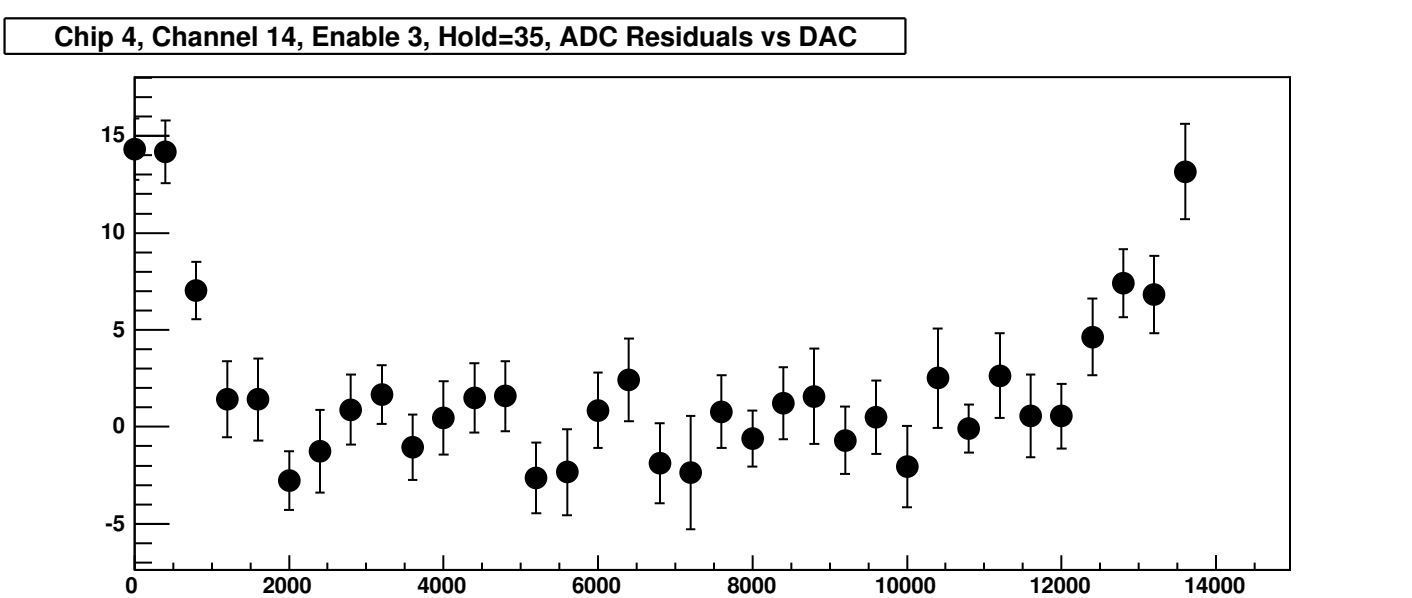

Chip 4, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 641.6 / 23
p0 212.3 ± 0.8819
p1 -0.04355 ± 0.0001767
200
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

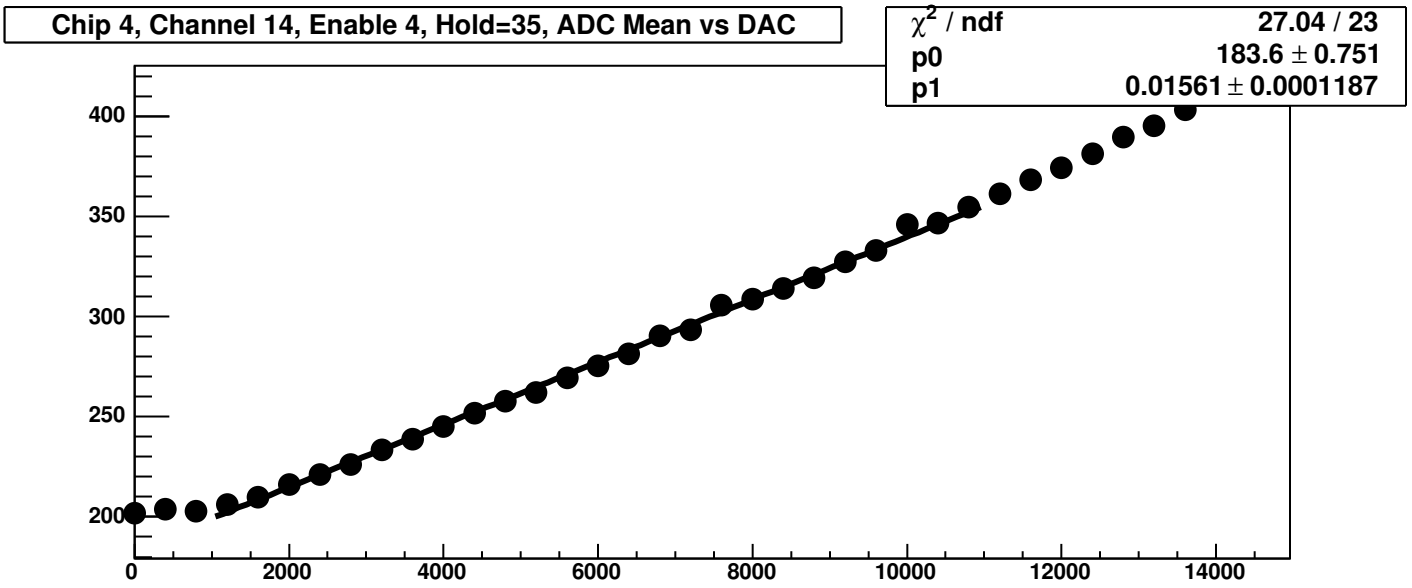

Chip 4, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

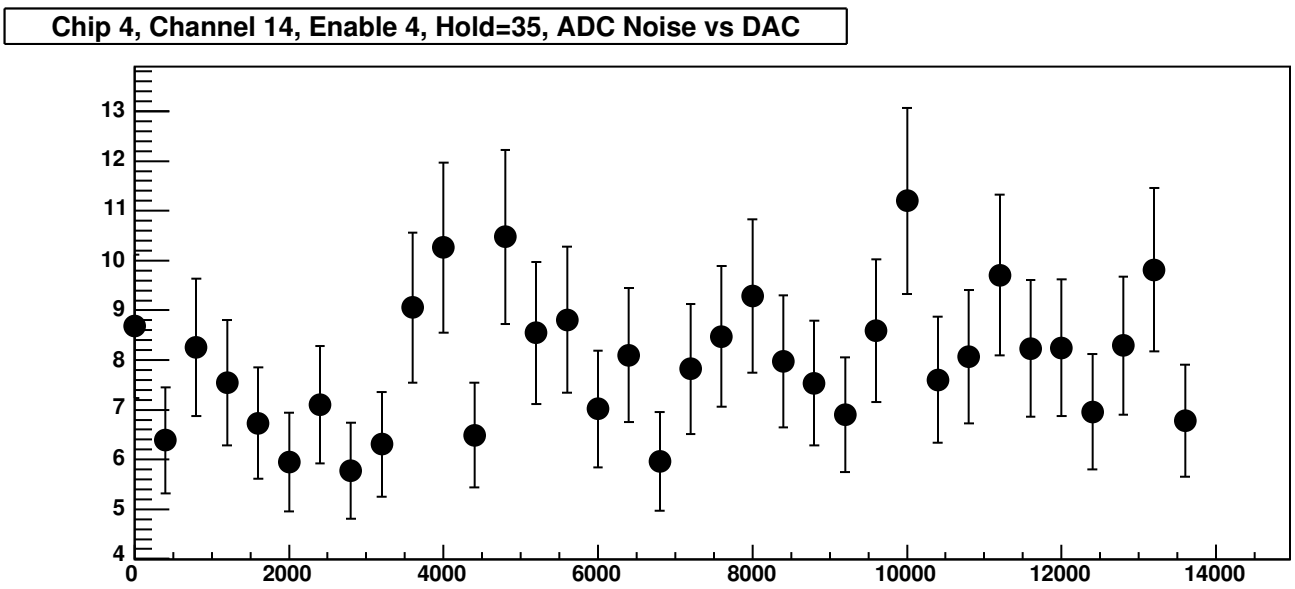

Chip 4, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

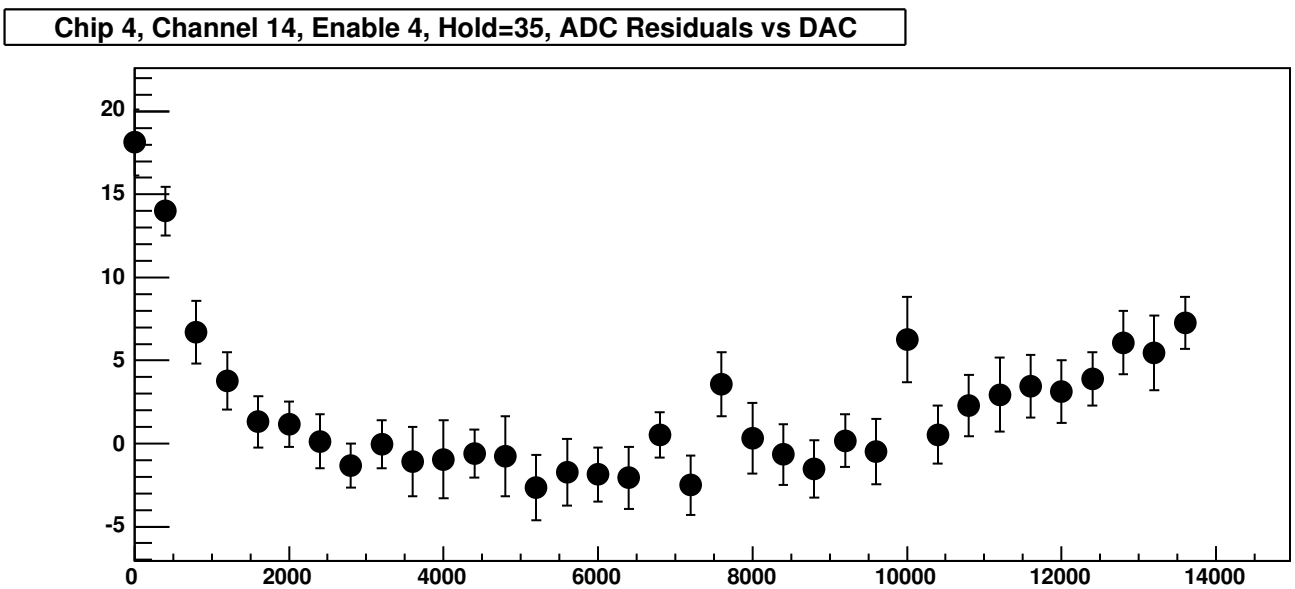

Chip 4, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 17.53 / 23
p0 182.5 ± 0.8284
p1 0.01386 ± 0.0001232
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

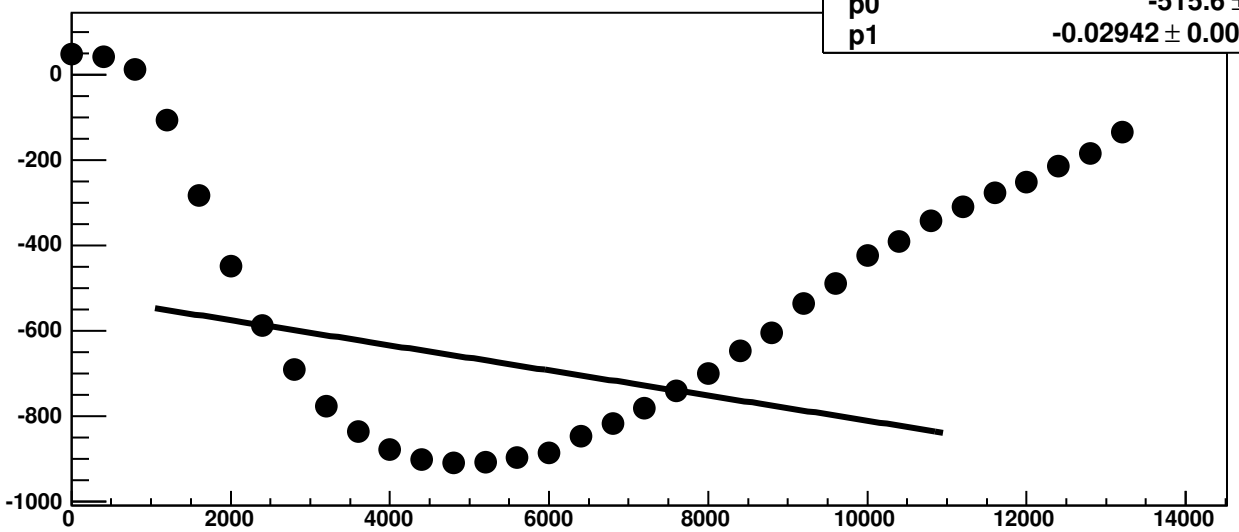

Chip 4, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.04 / 23
p0 183.6 ± 0.751
p1 0.01561 ± 0.0001187
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

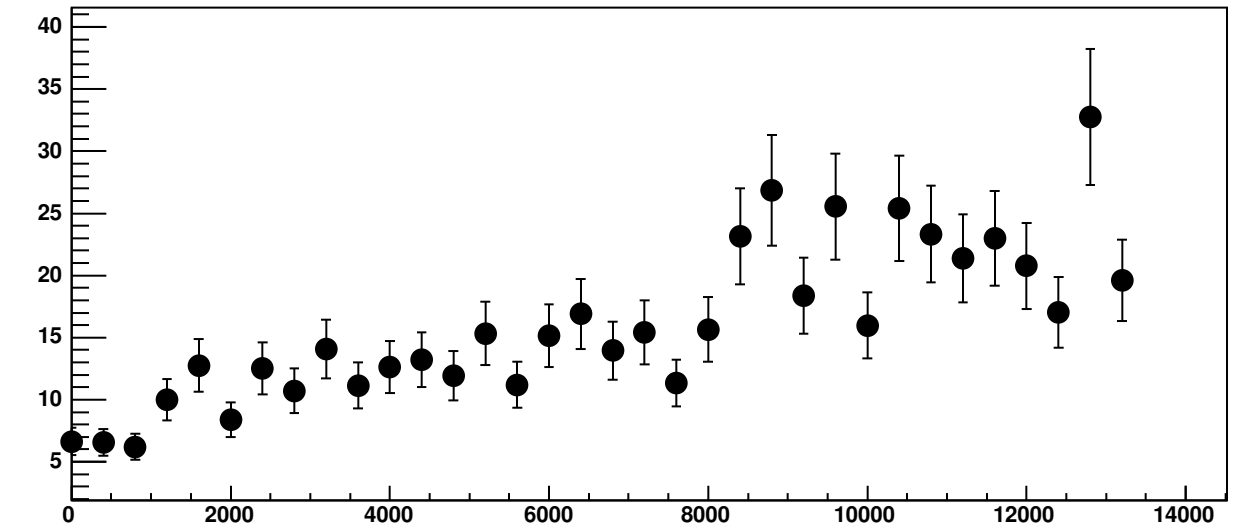

Chip 4, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

Chip 4, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

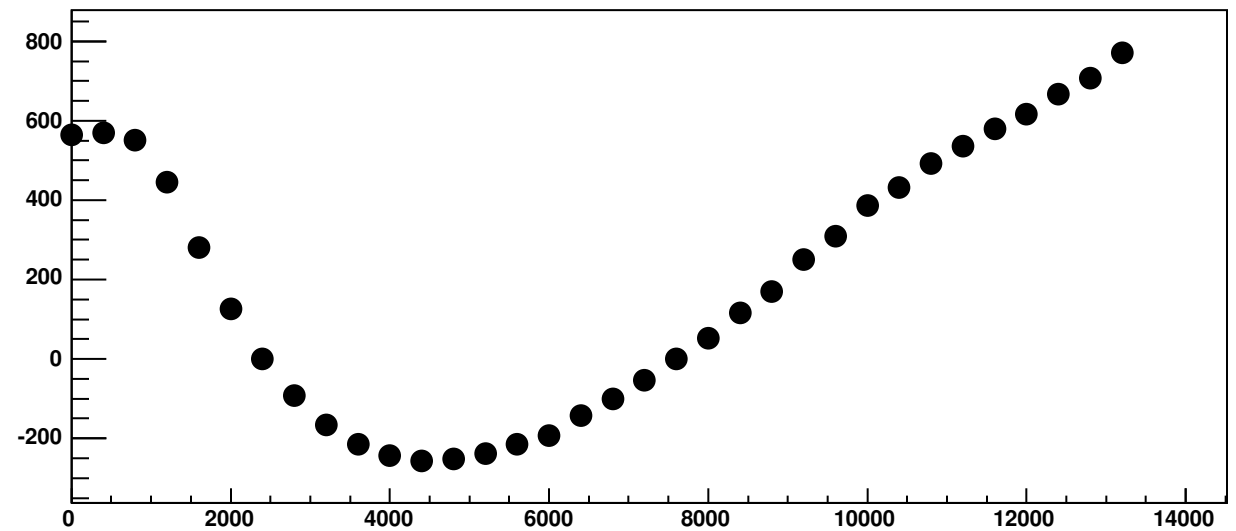
χ^2 / ndf	1.353e+05 / 23
p0	-515.6 ± 1.29
p1	-0.02942 ± 0.0002374

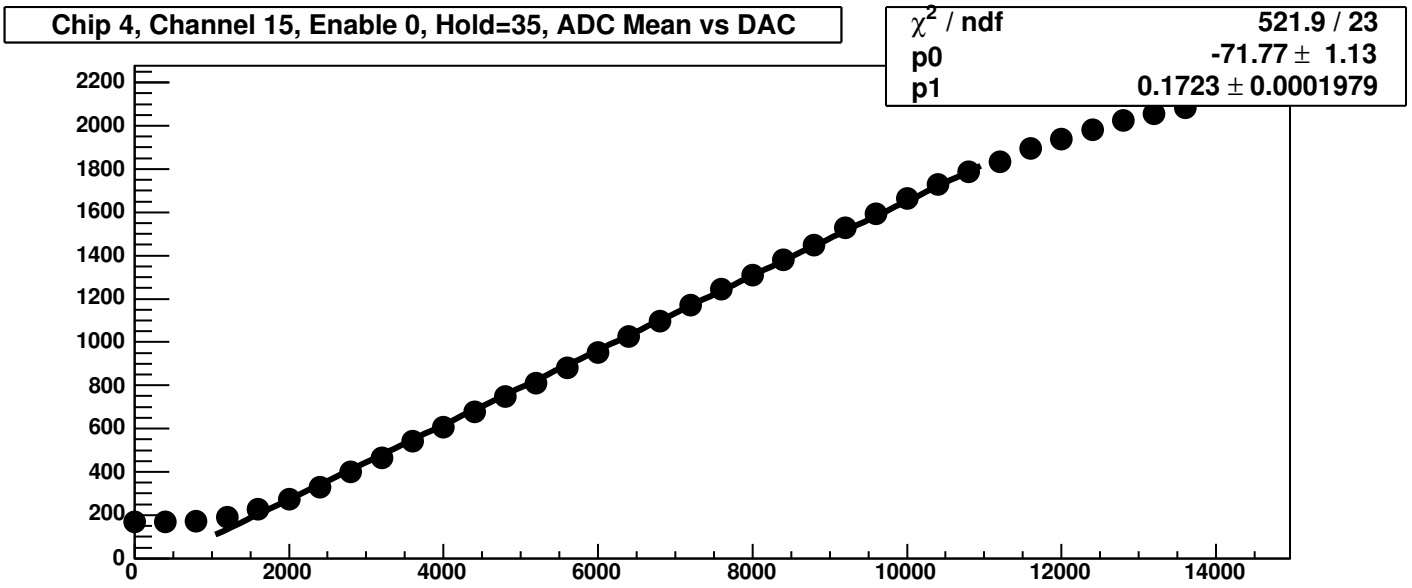


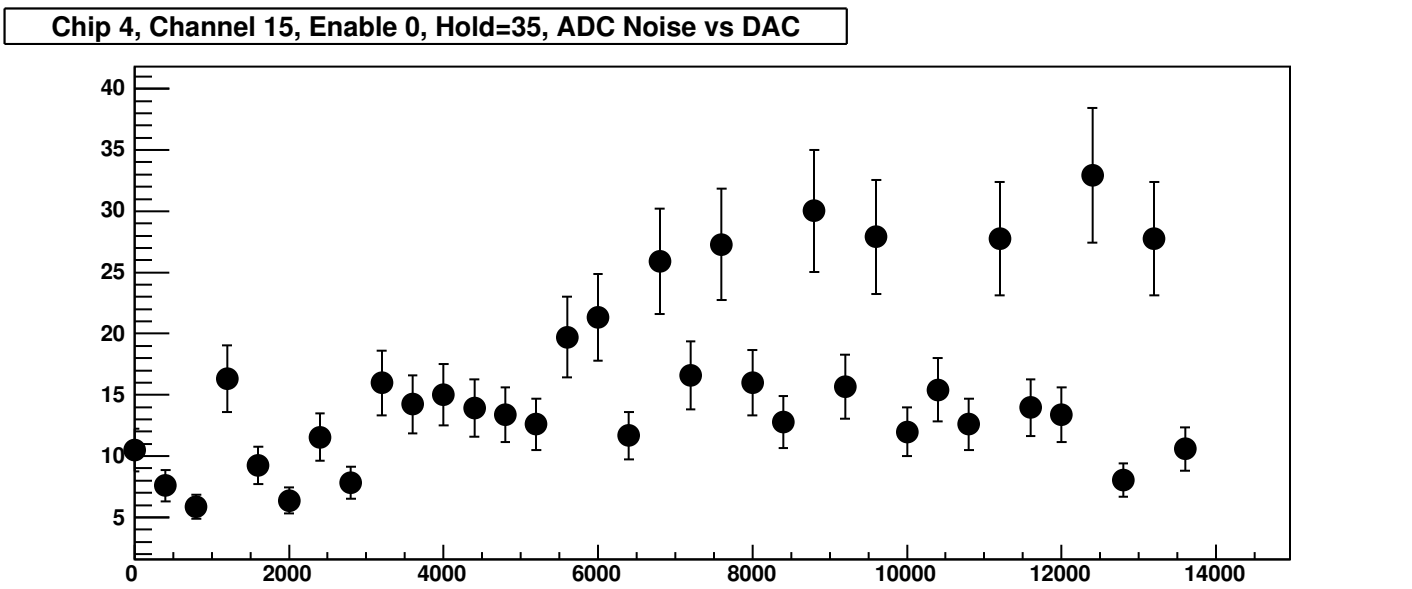
Chip 4, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC

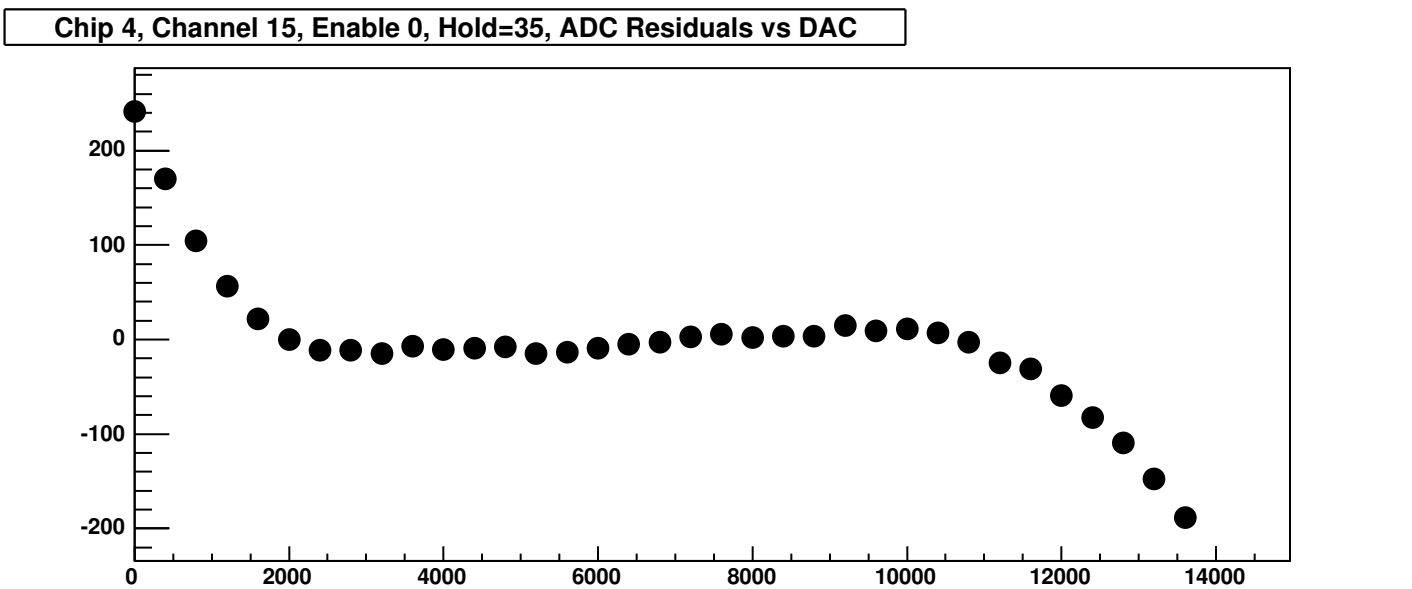


Chip 4, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



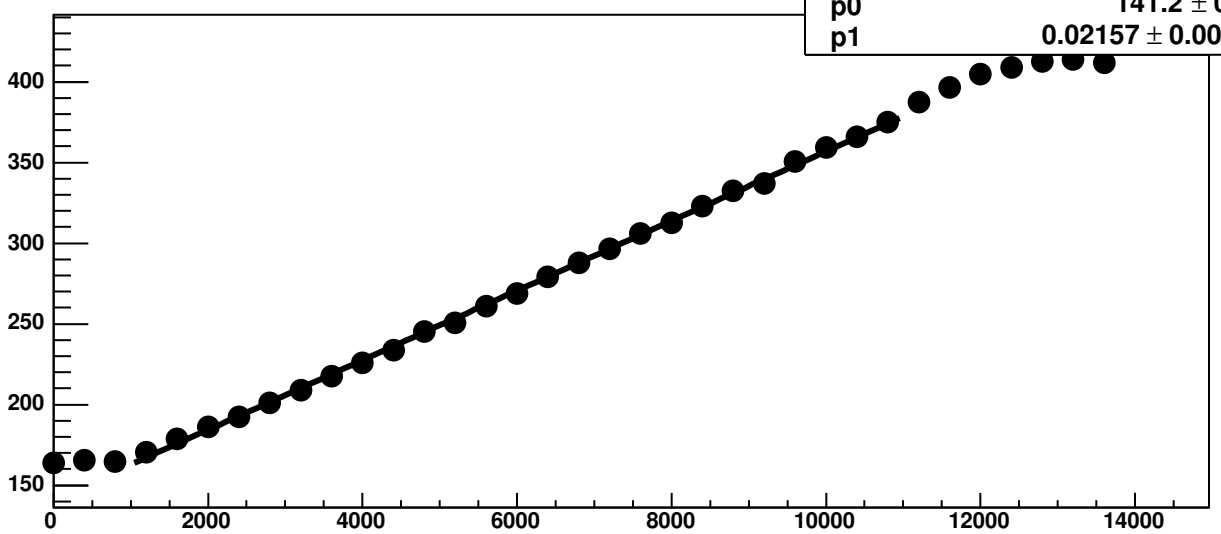

Chip 4, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 521.9 / 23
p0 -71.77 ± 1.13
p1 0.1723 ± 0.0001979
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

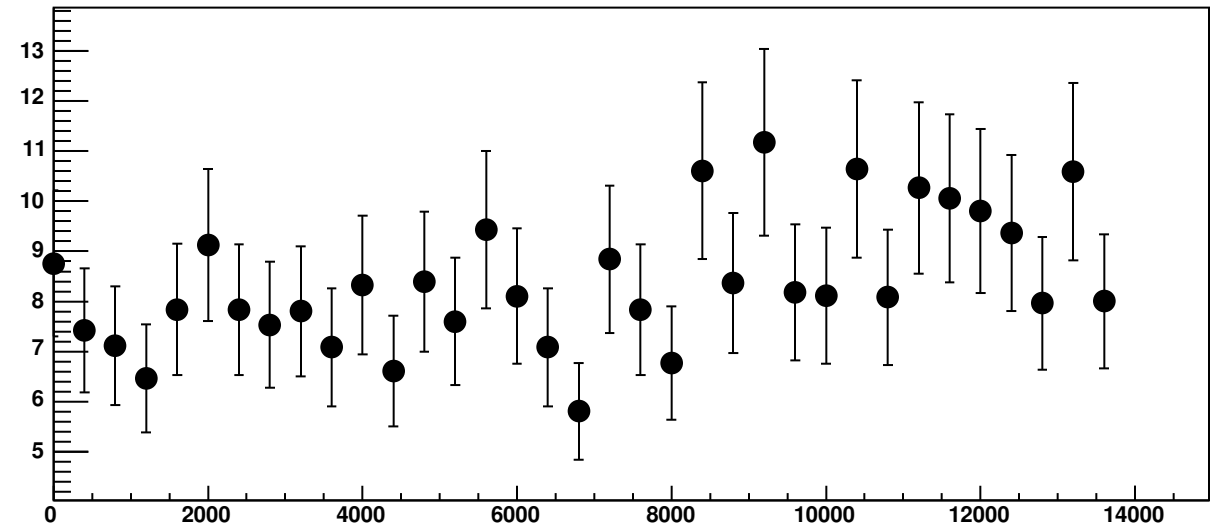

Chip 4, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

Chip 4, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

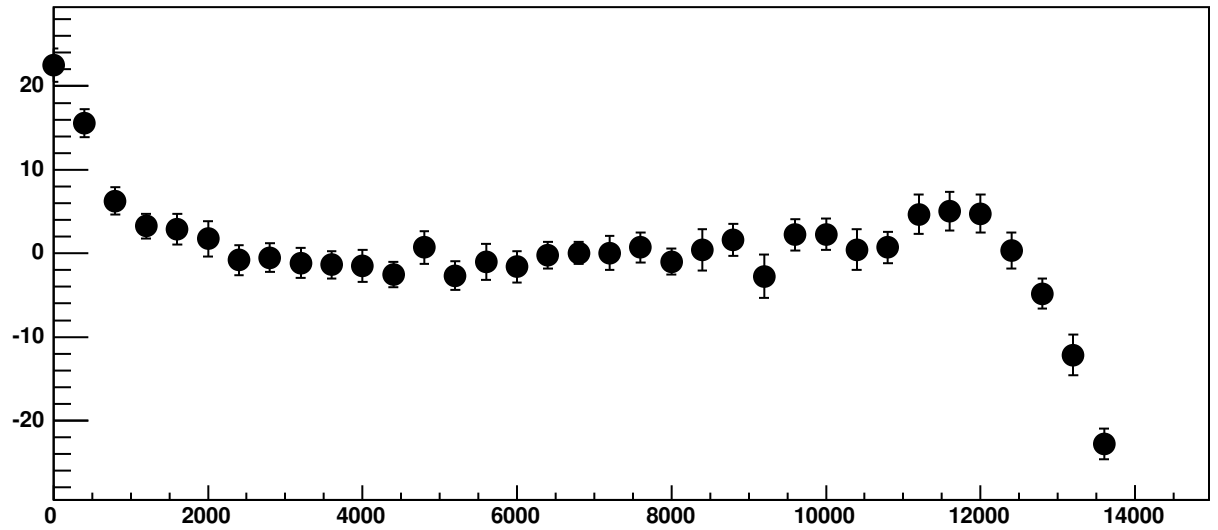
χ^2 / ndf	21.94 / 23
p0	141.2 ± 0.8189
p1	0.02157 ± 0.0001285

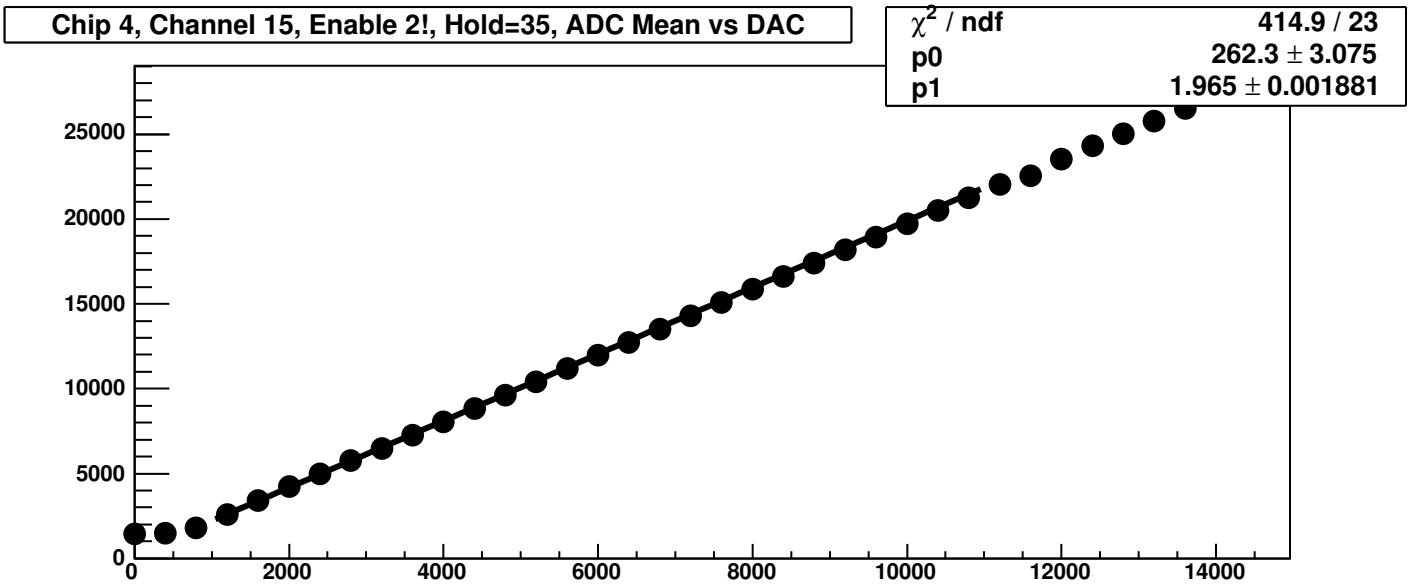


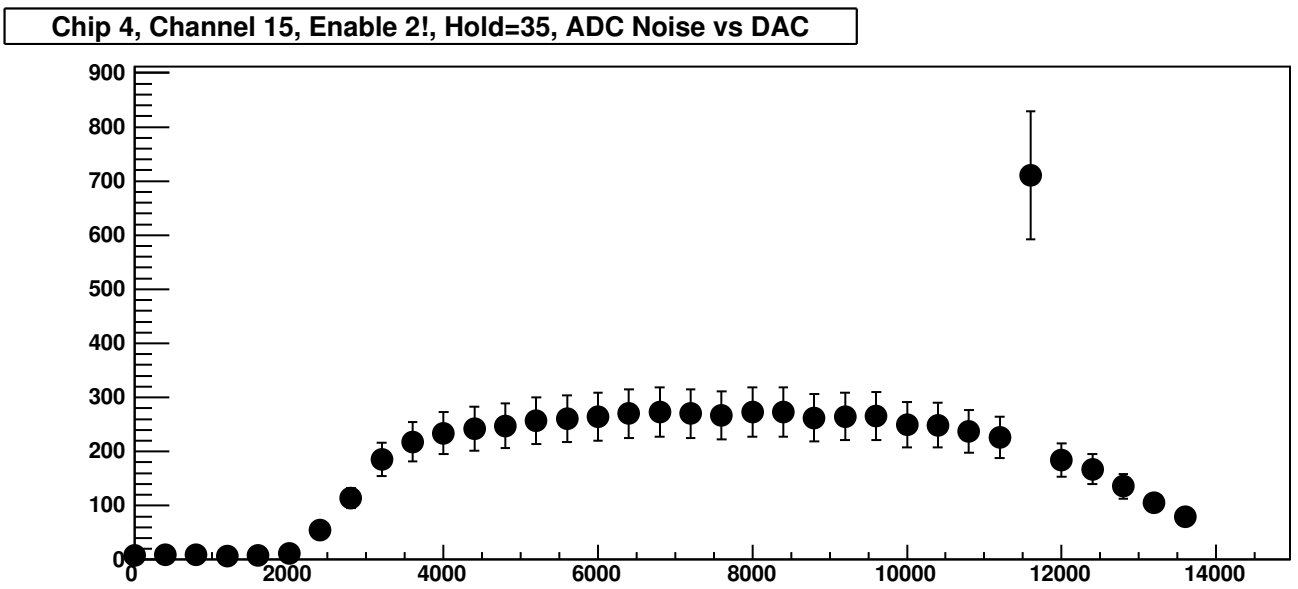
Chip 4, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

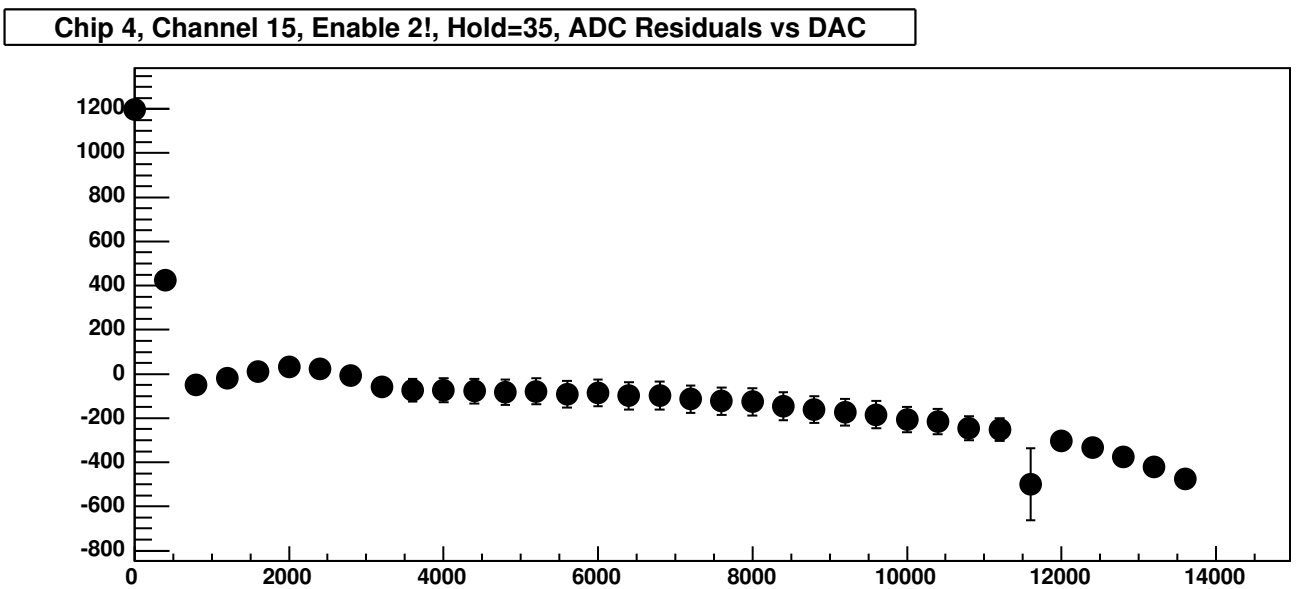


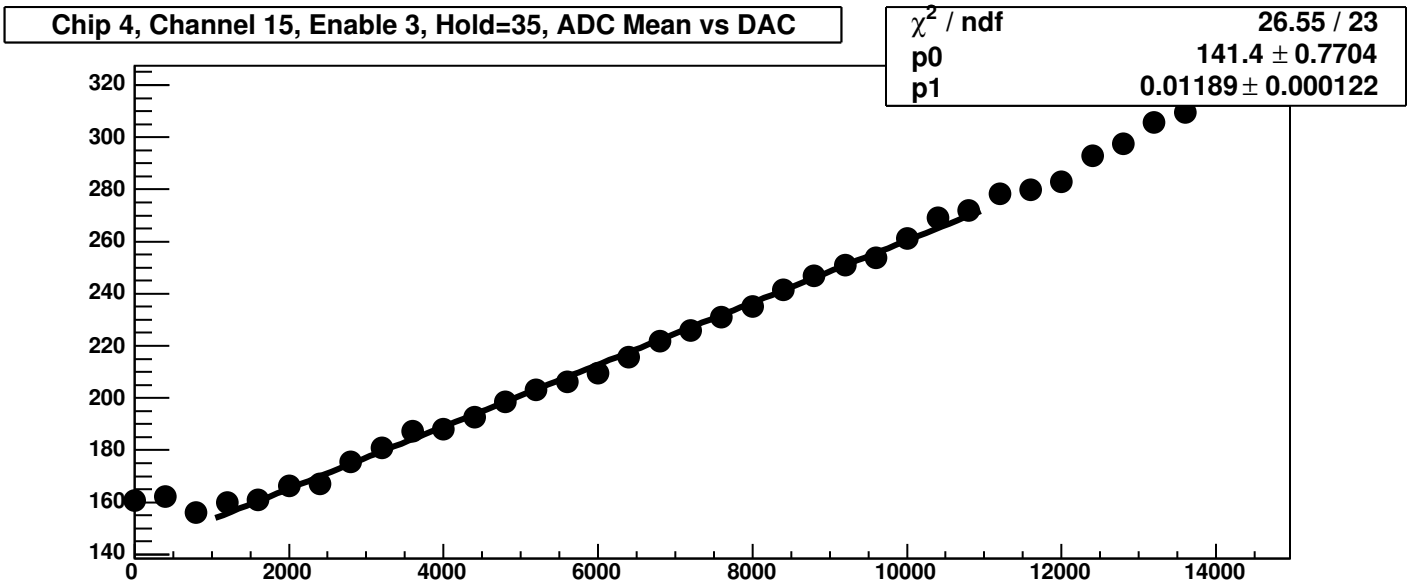
Chip 4, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

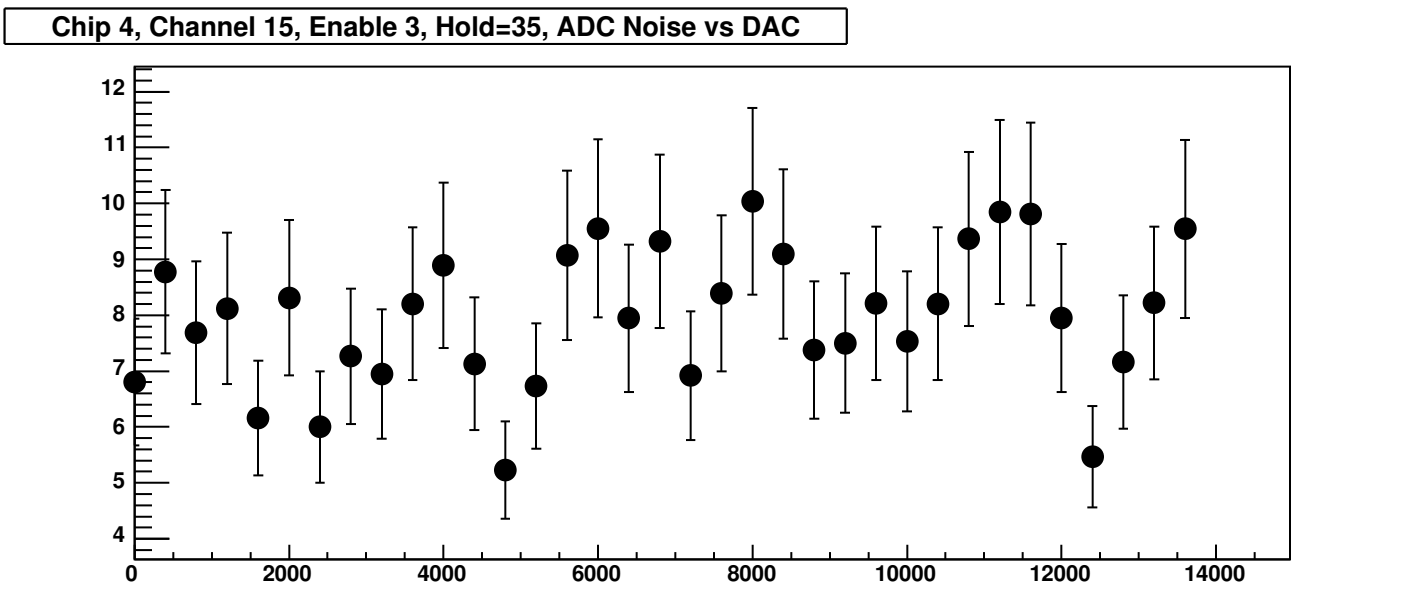


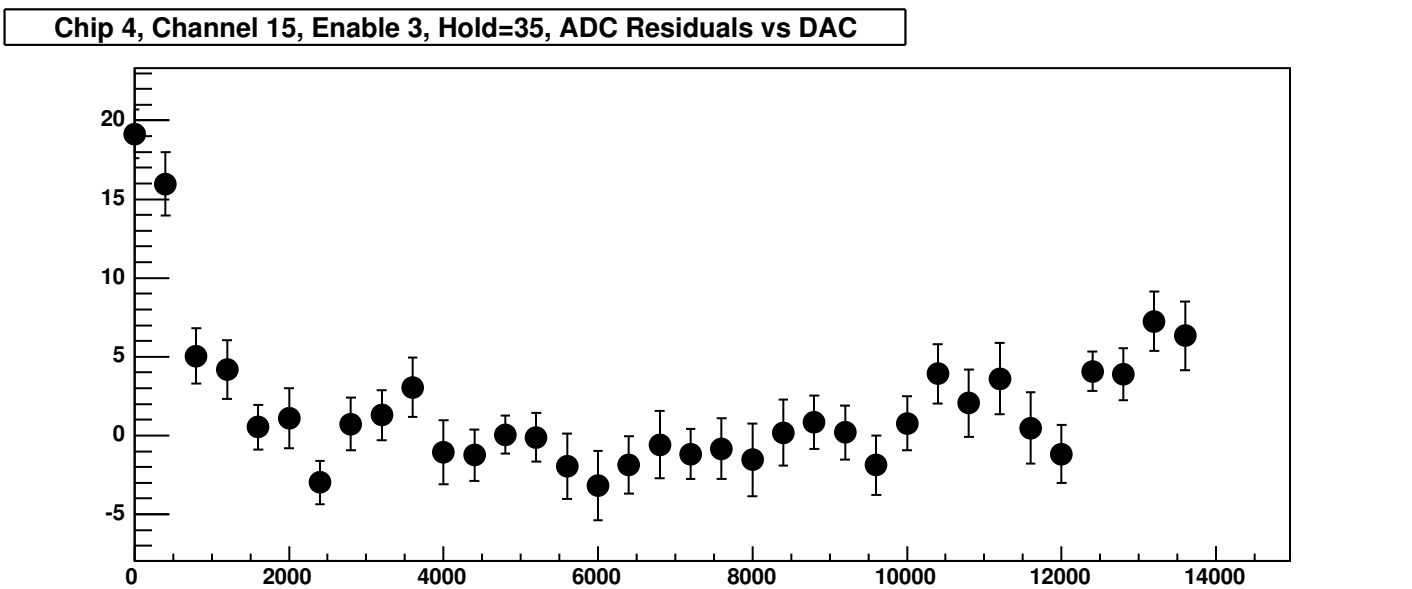

Chip 4, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 414.9 / 23
p0 262.3 ± 3.075
p1 1.965 ± 0.001881
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

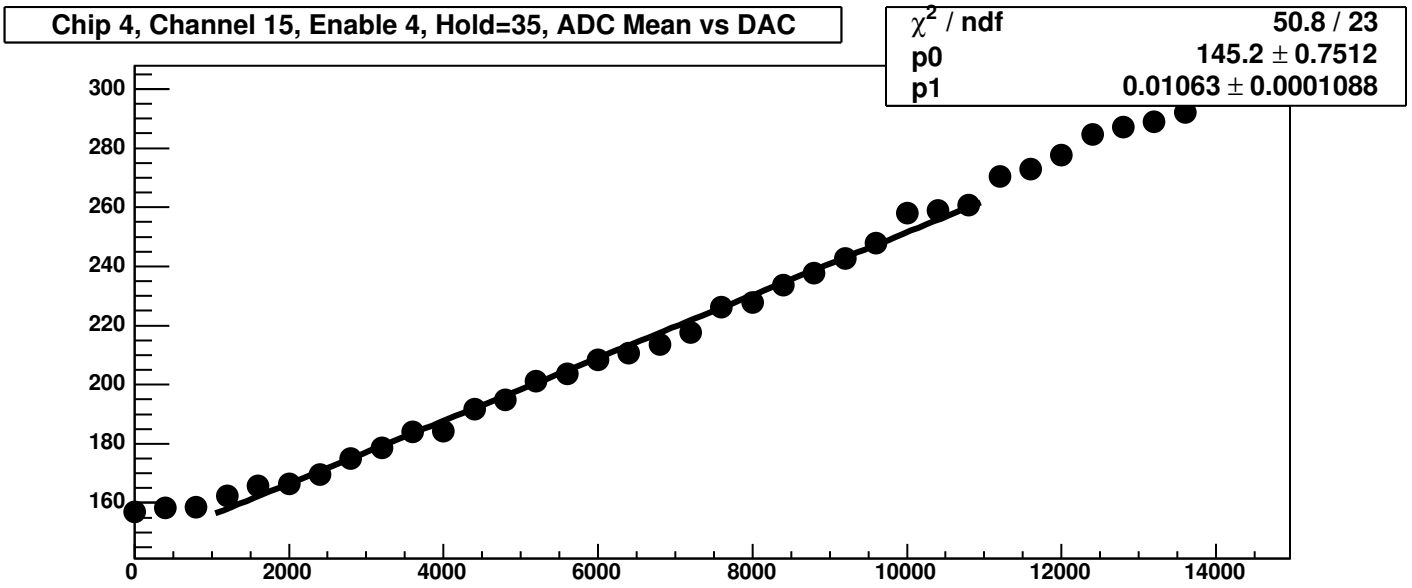

Chip 4, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
900
800
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

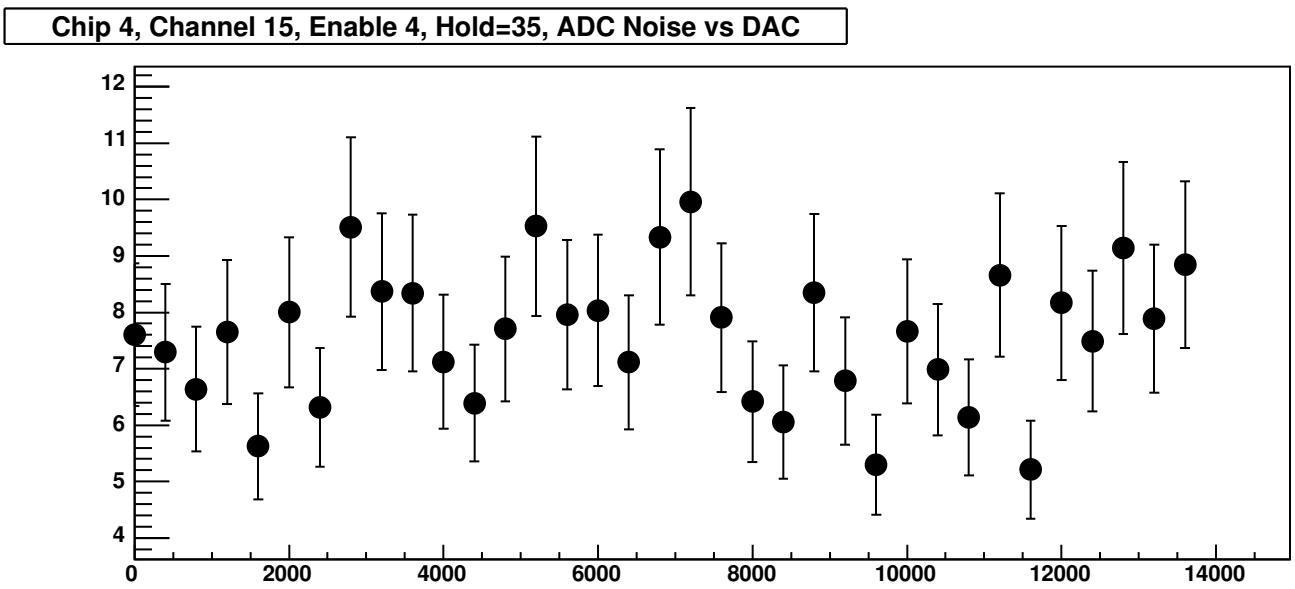

Chip 4, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

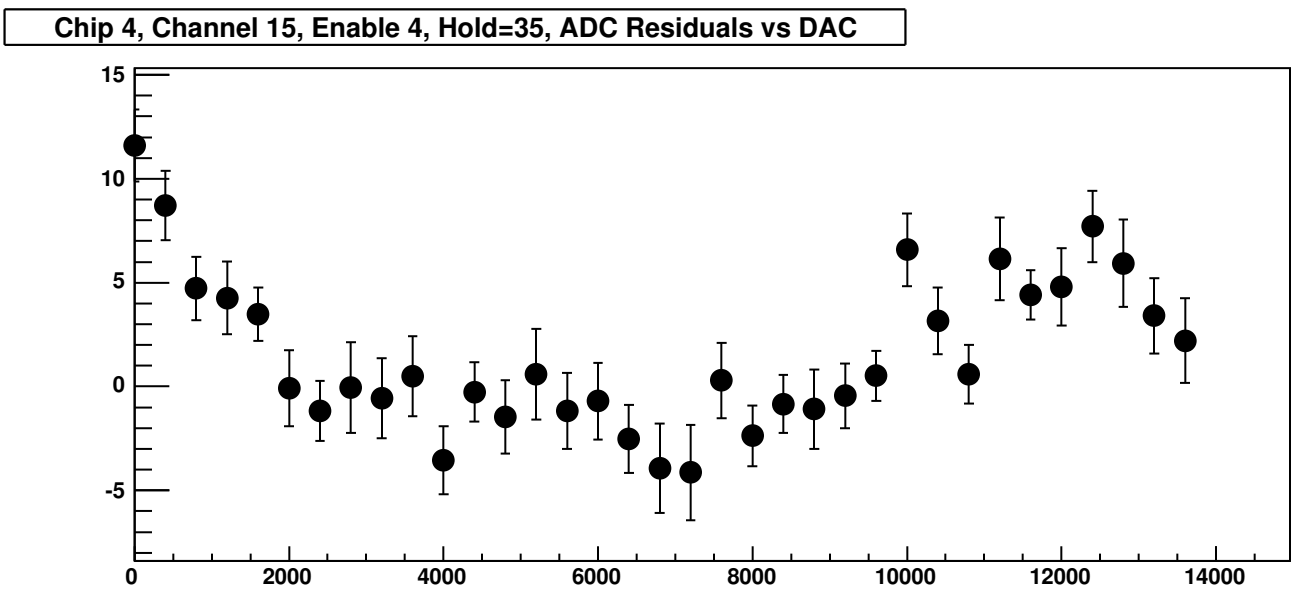

Chip 4, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.55 / 23
p0 141.4 ± 0.7704
p1 0.01189 ± 0.000122
320
300
280
260
240
220
200
180
160
140
0
2000
4000
6000
8000
10000
12000
14000

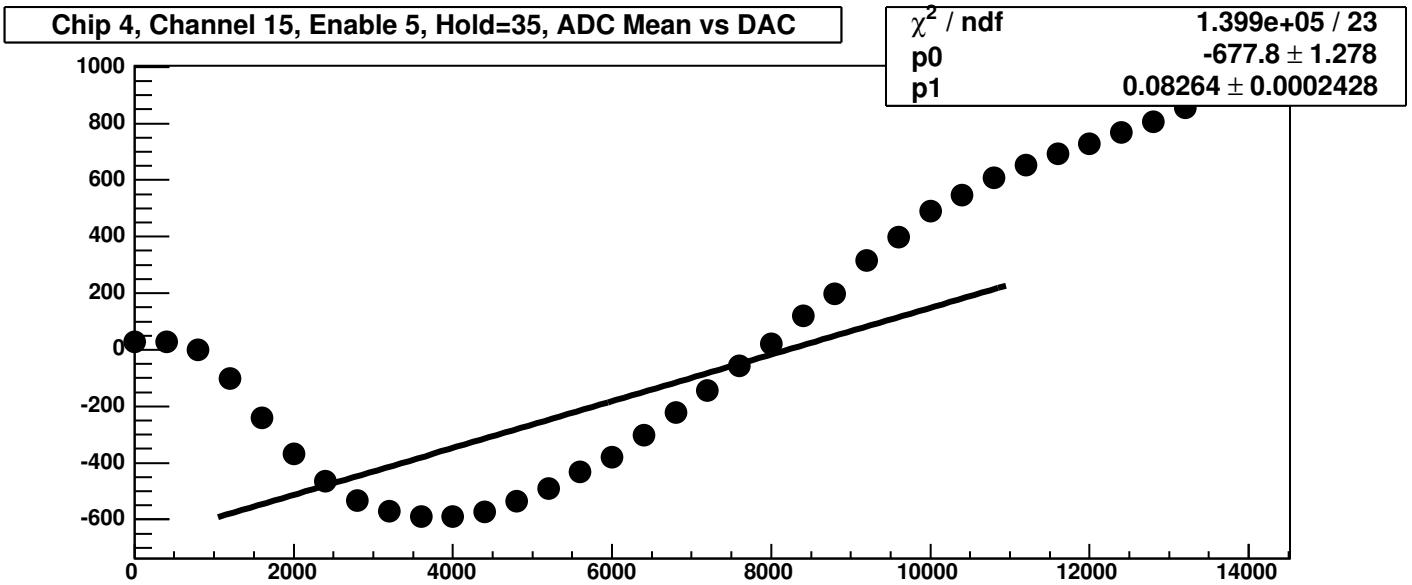

Chip 4, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

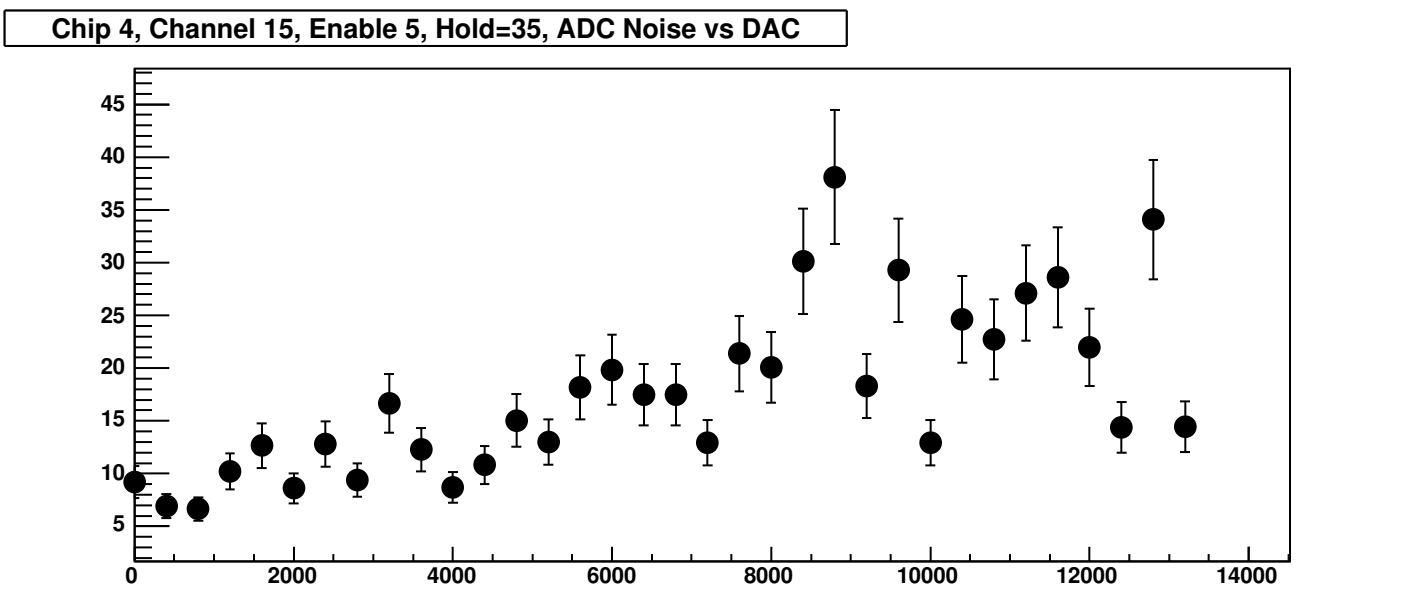

Chip 4, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

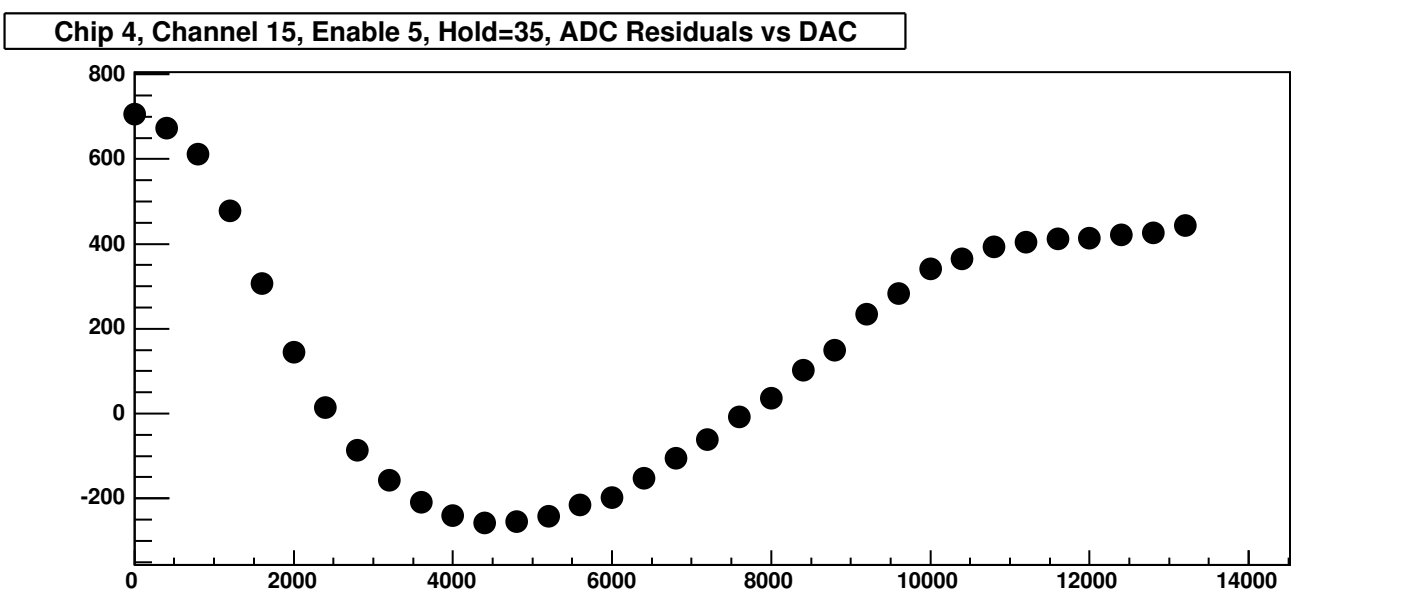

Chip 4, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 50.8 / 23
p0 145.2 ± 0.7512
p1 0.01063 ± 0.0001088
300
280
260
240
220
200
180
160
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

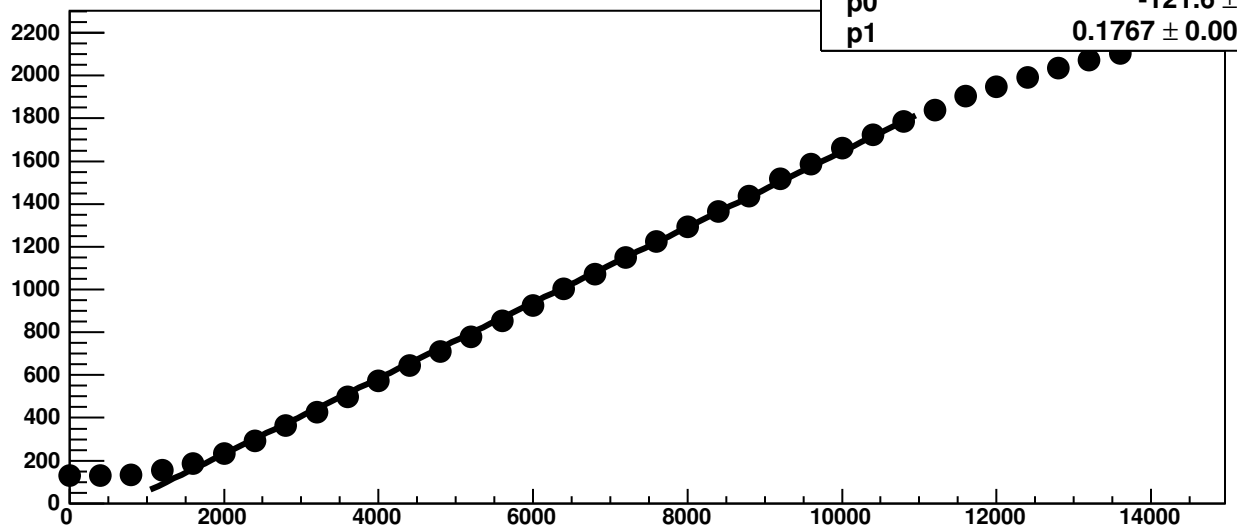

Chip 4, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.399e+05 / 23
p0 -677.8 ± 1.278
p1 0.08264 ± 0.0002428
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 4, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

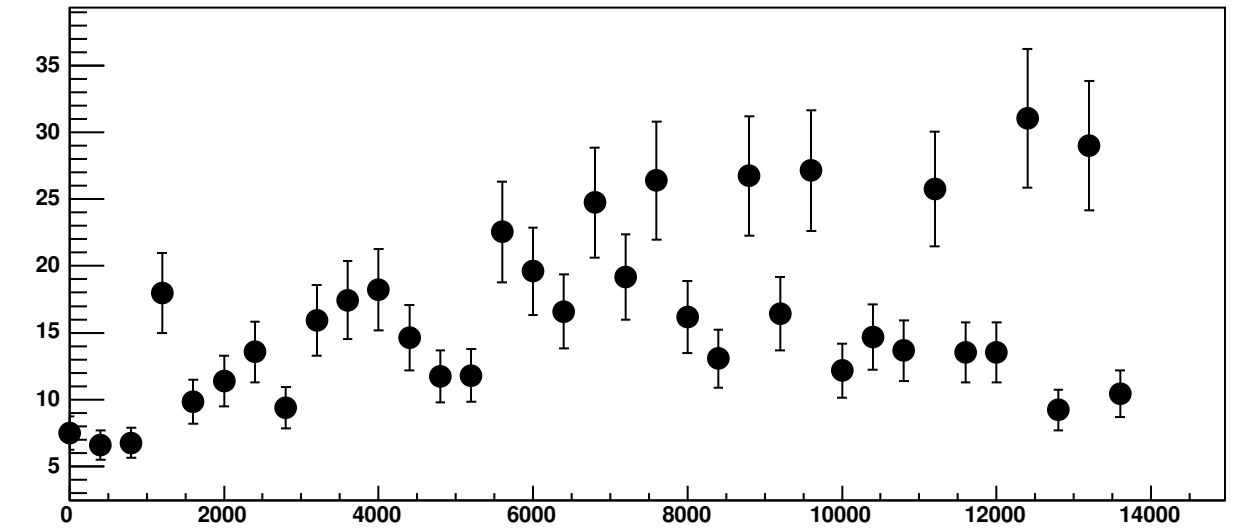

Chip 4, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

Chip 4, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

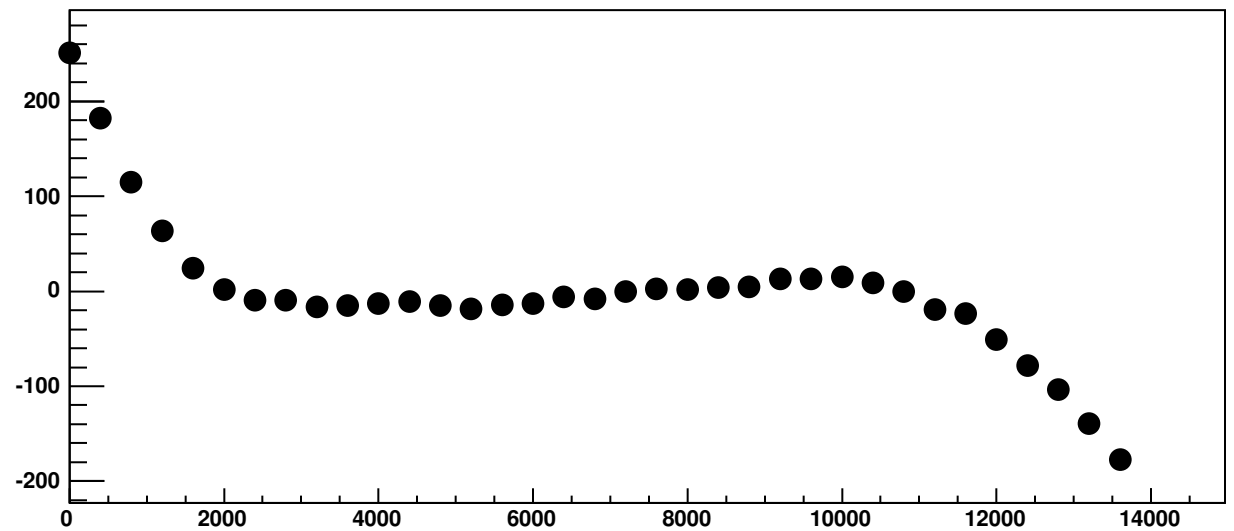
χ^2 / ndf	590.4 / 23
p0	-121.6 ± 1.372
p1	0.1767 ± 0.0002216

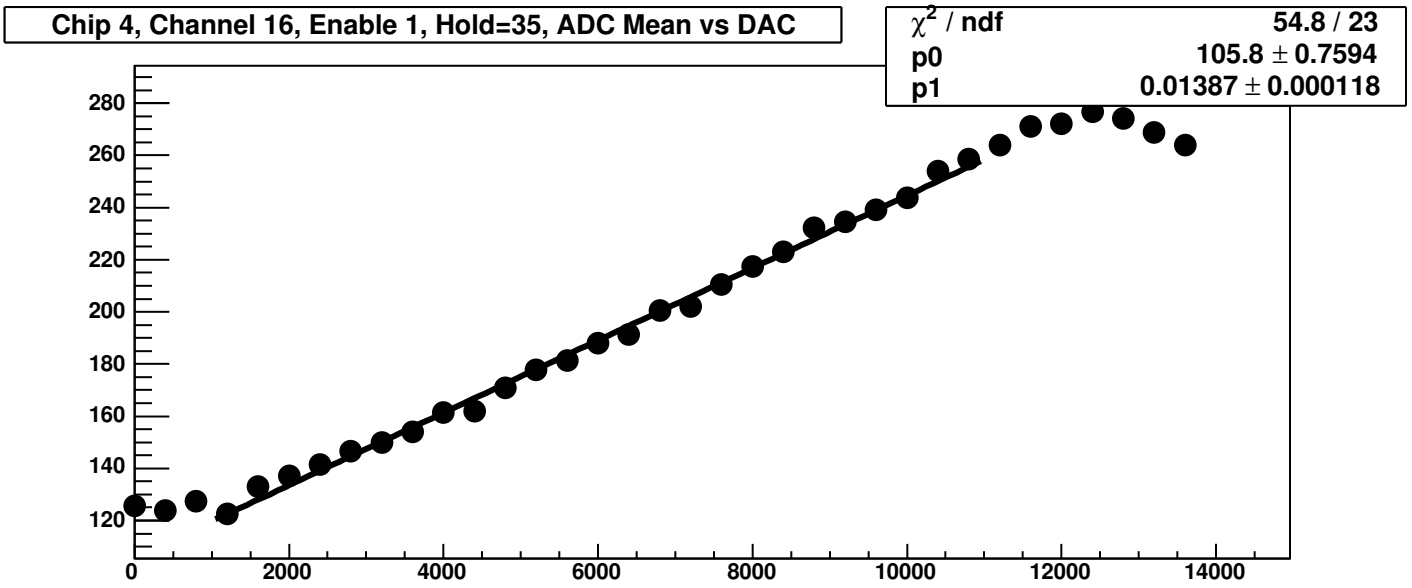


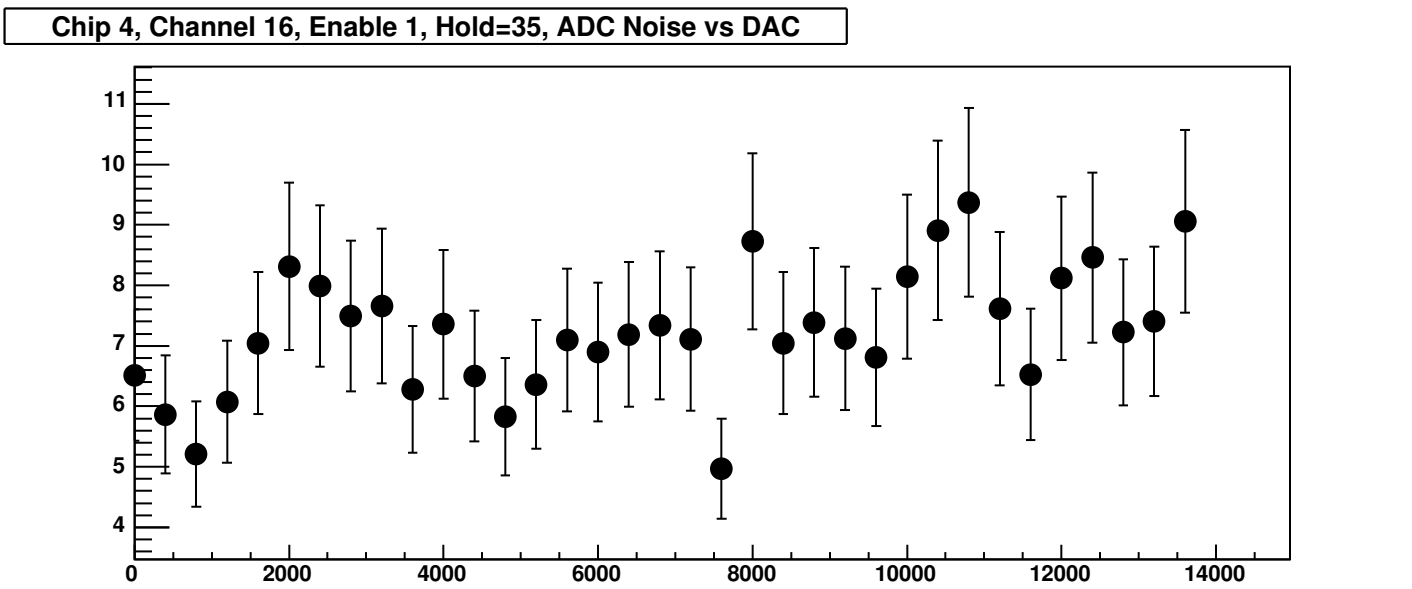
Chip 4, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC

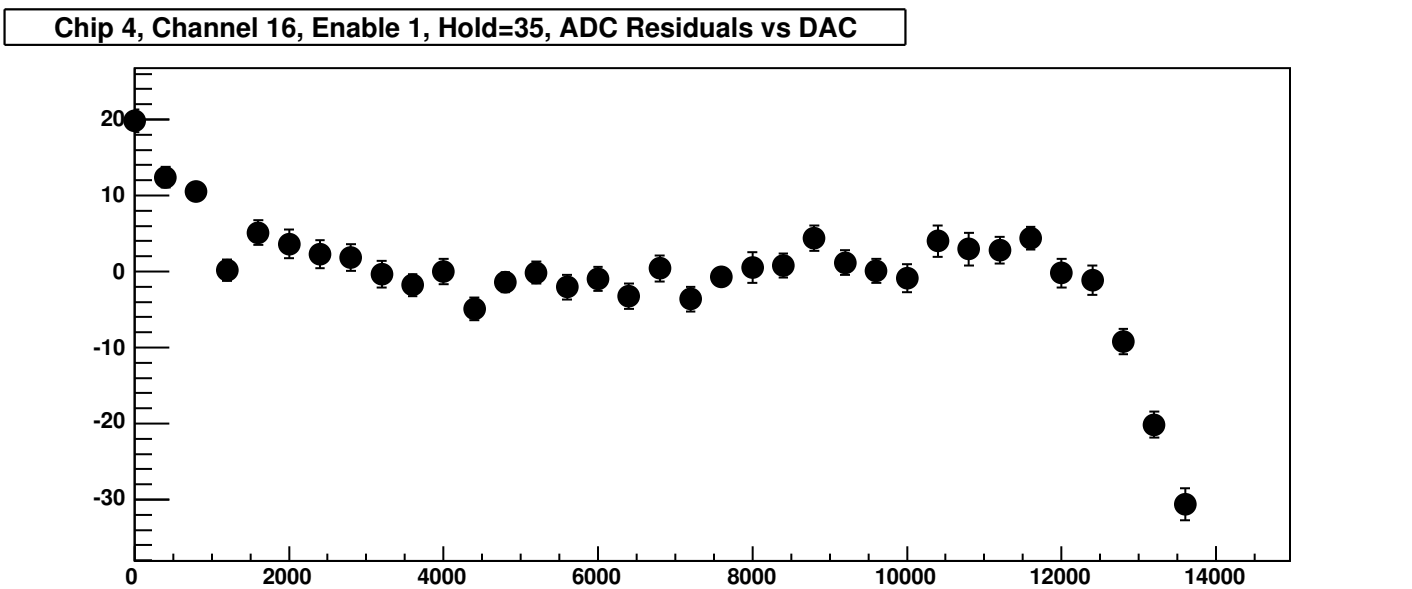


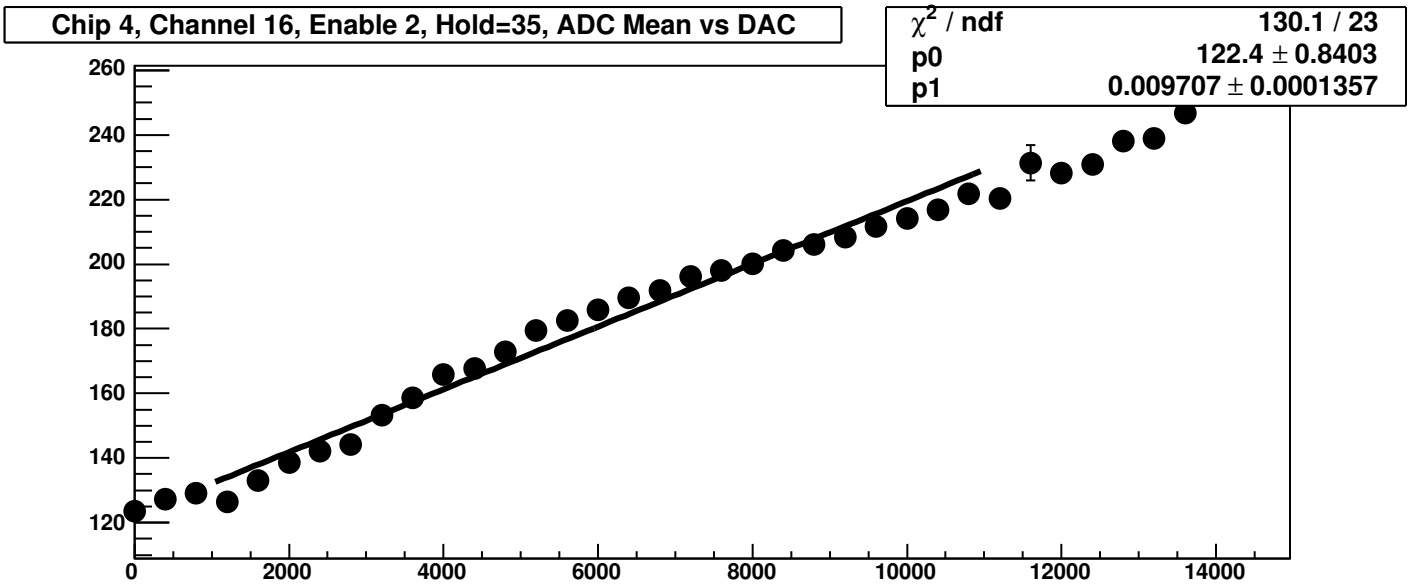
Chip 4, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

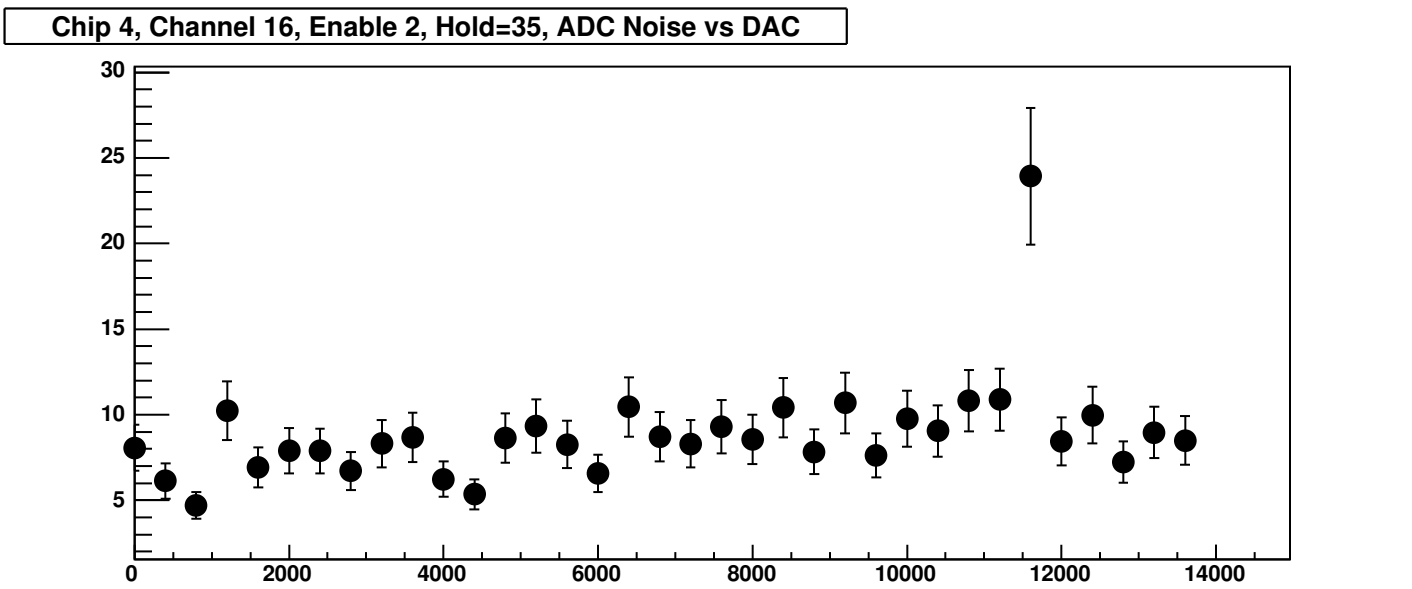


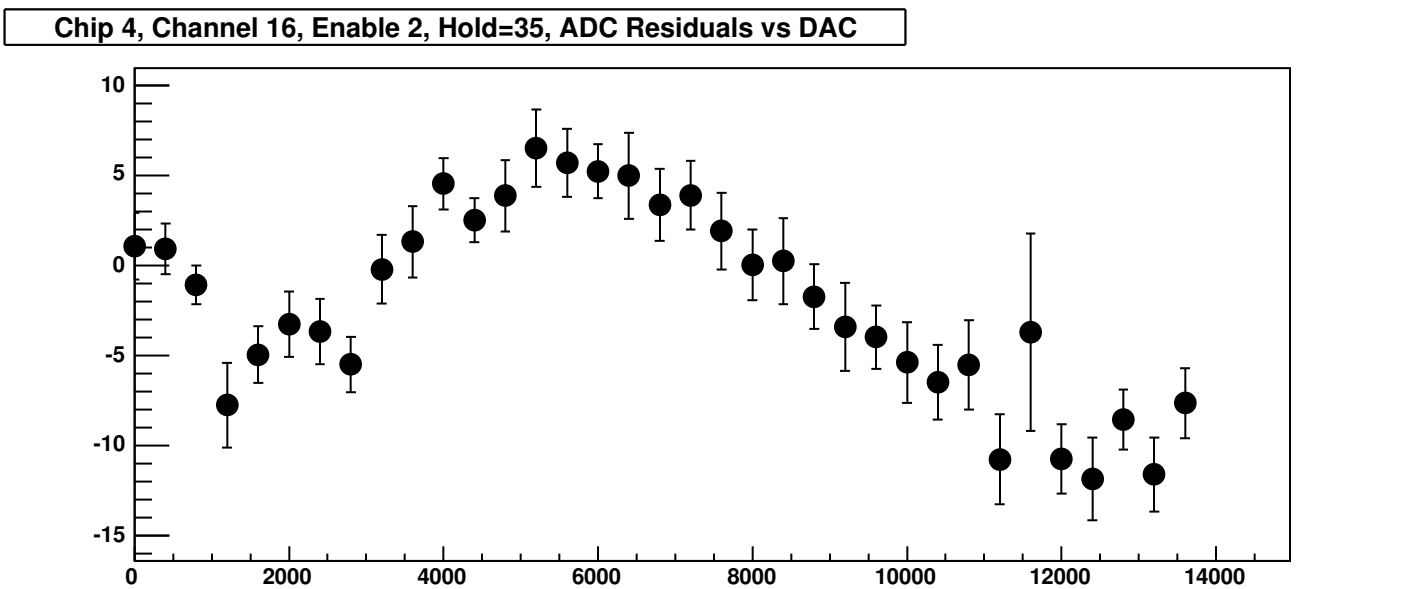

Chip 4, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 54.8 / 23
p0 105.8 ± 0.7594
p1 0.01387 ± 0.000118
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

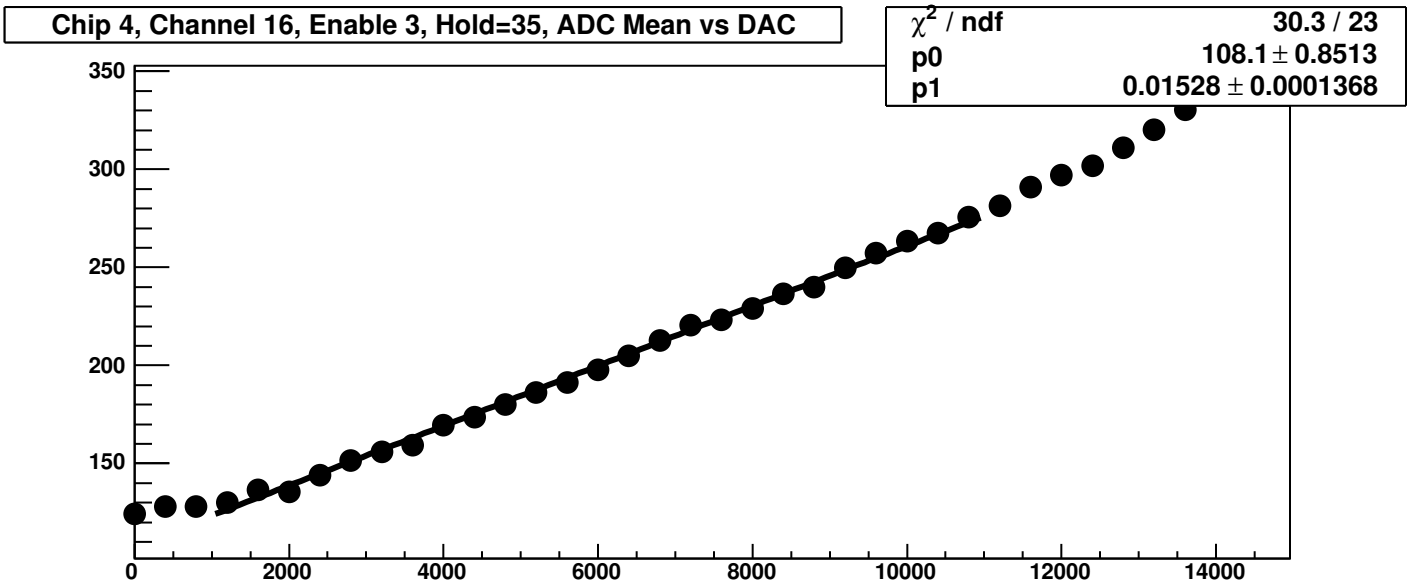

Chip 4, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

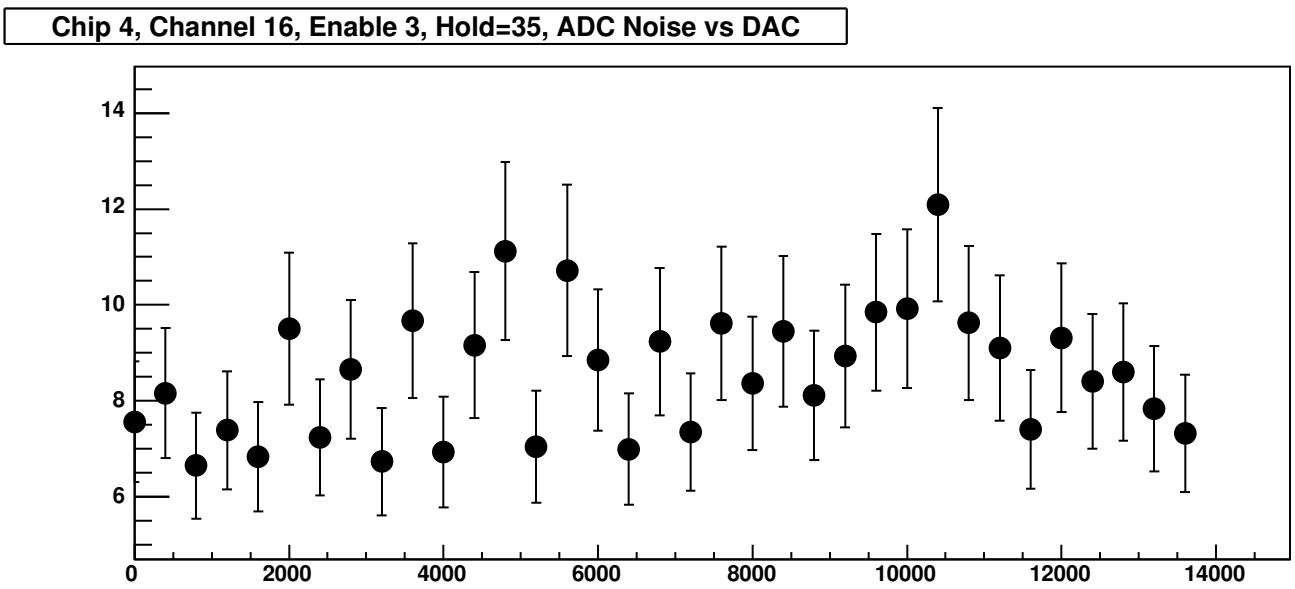

Chip 4, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

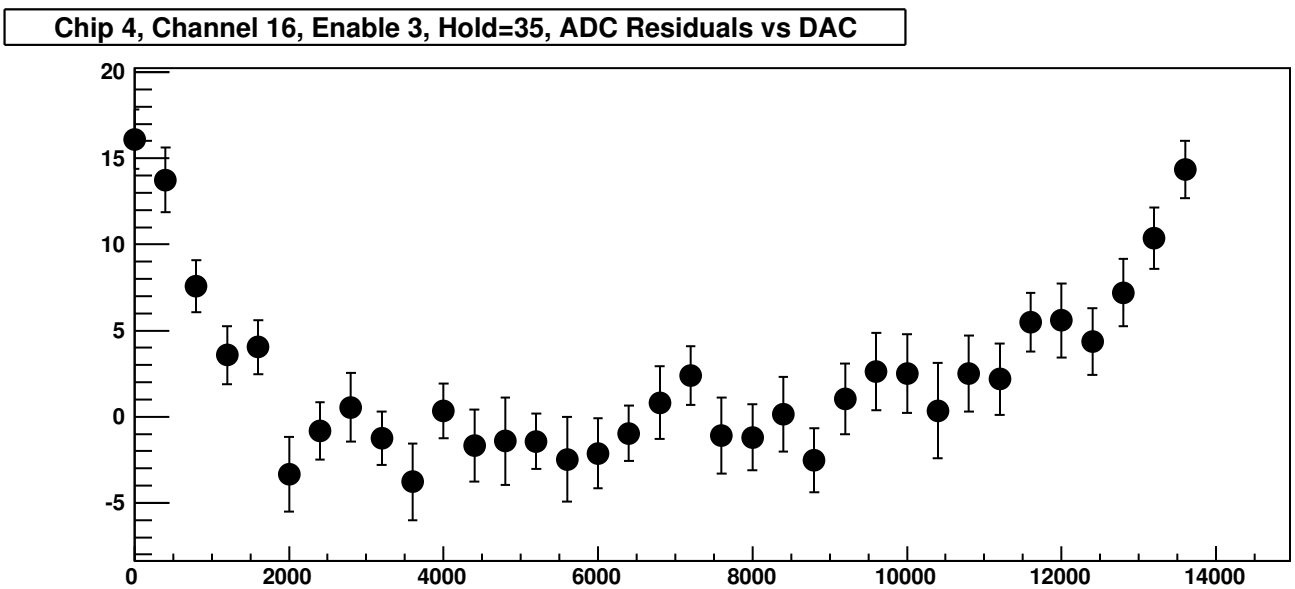

Chip 4, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 130.1 / 23
p0 122.4 ± 0.8403
p1 0.009707 ± 0.0001357
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

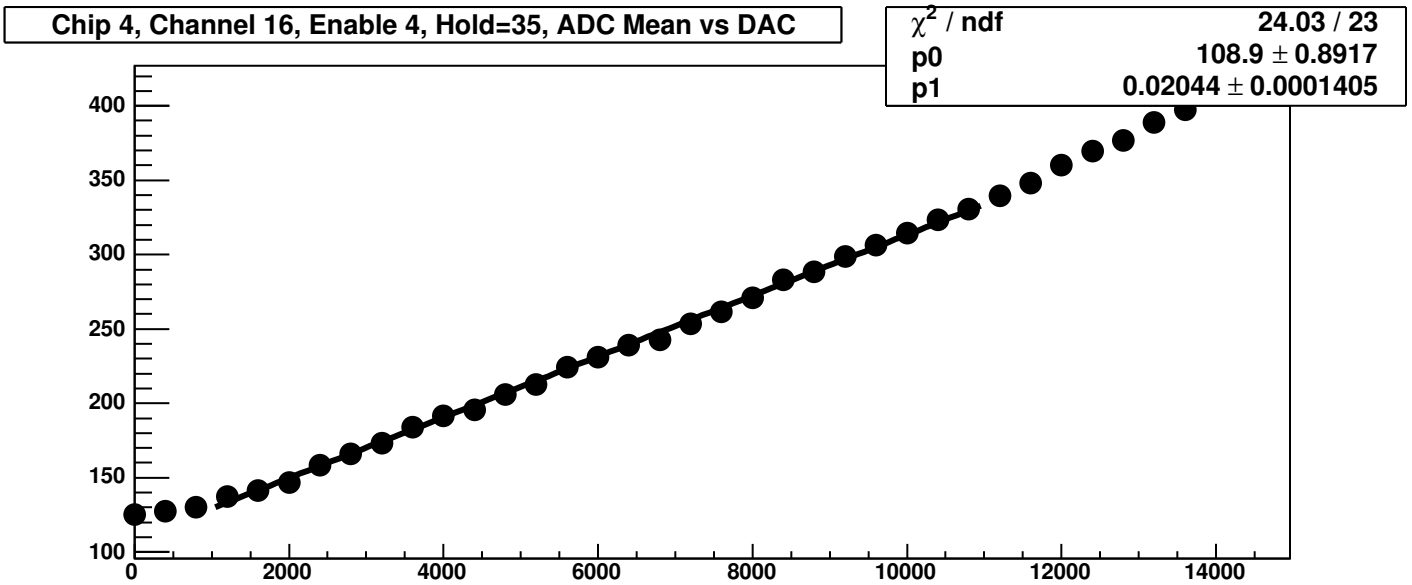

Chip 4, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

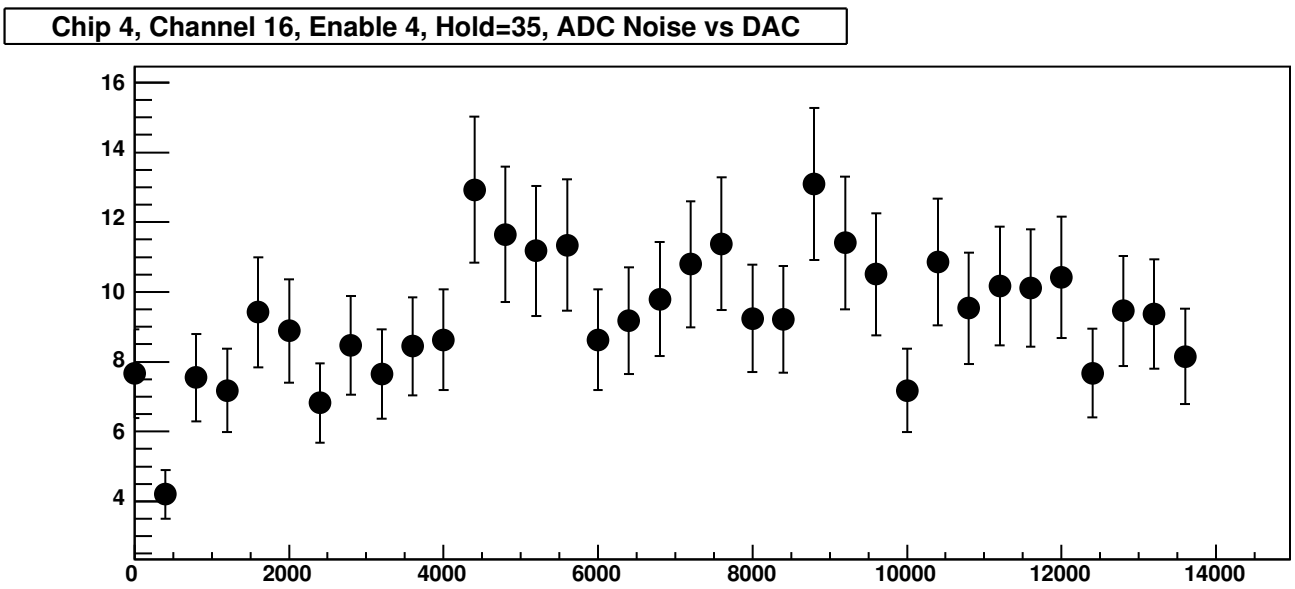

Chip 4, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

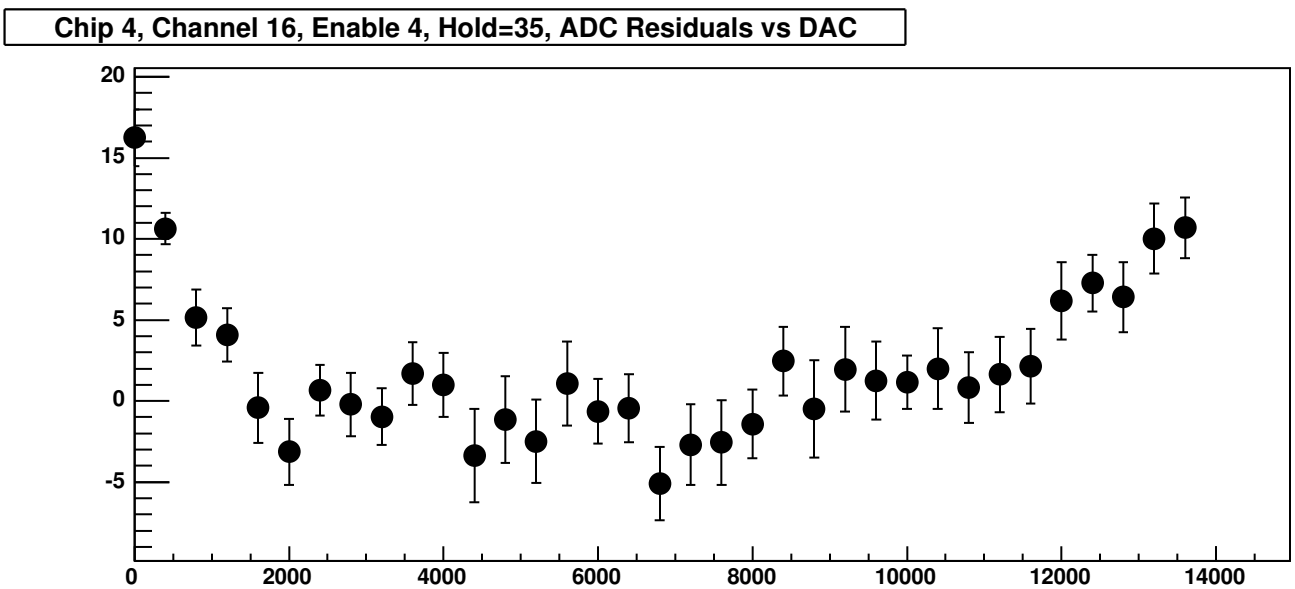

Chip 4, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 30.3 / 23
p0 108.1 ± 0.8513
p1 0.01528 ± 0.0001368
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

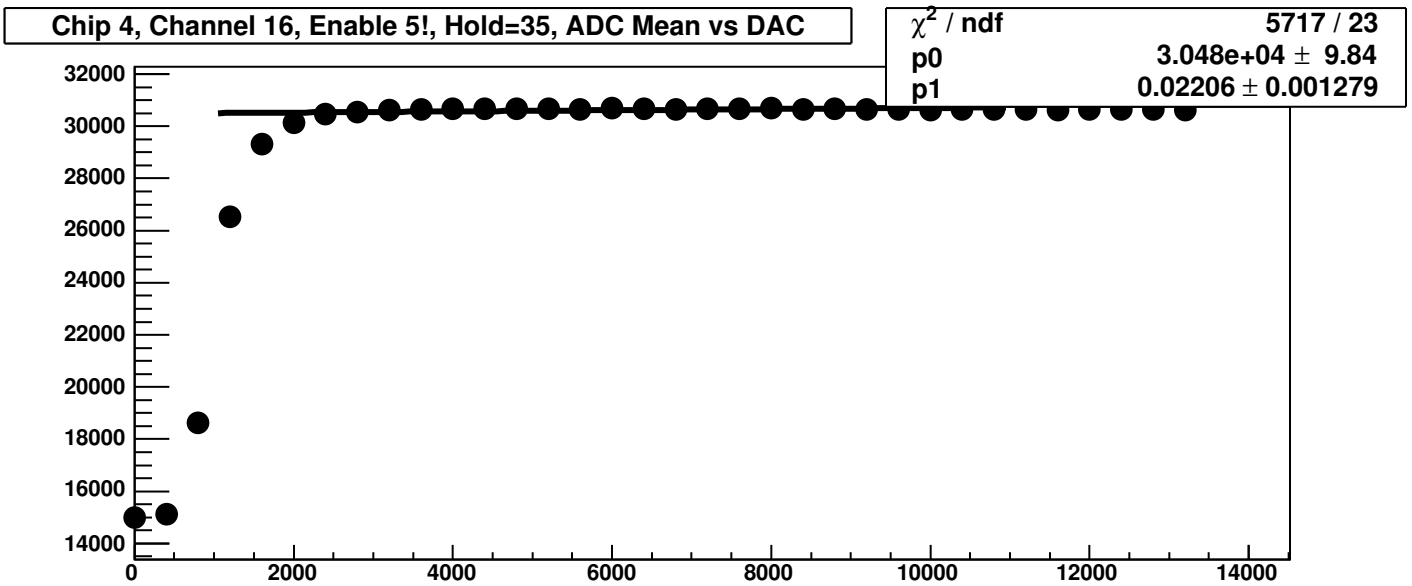

Chip 4, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

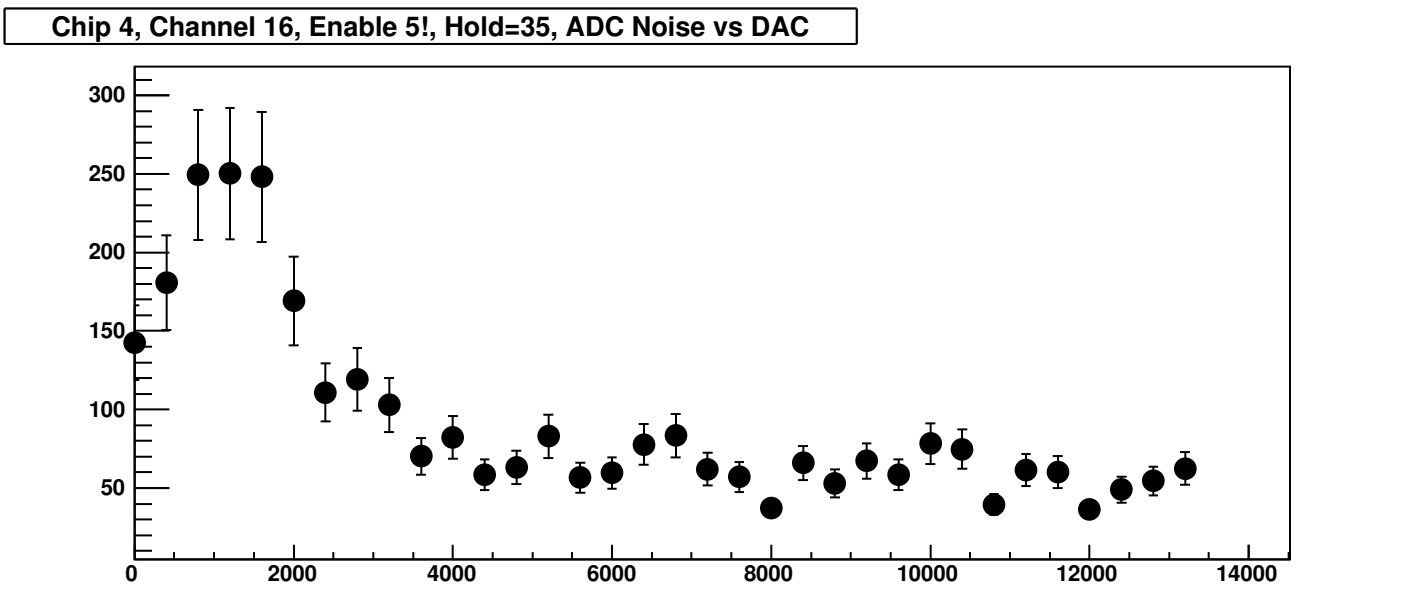

Chip 4, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

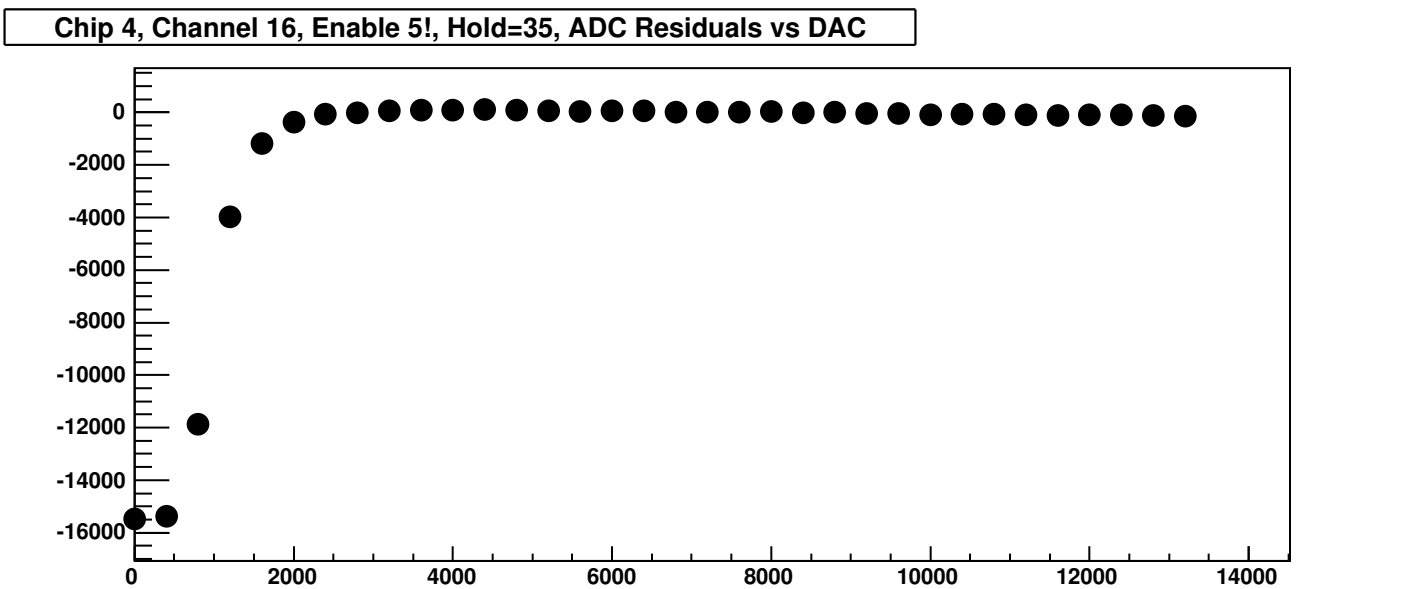

Chip 4, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.03 / 23
p0 108.9 ± 0.8917
p1 0.02044 ± 0.0001405
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

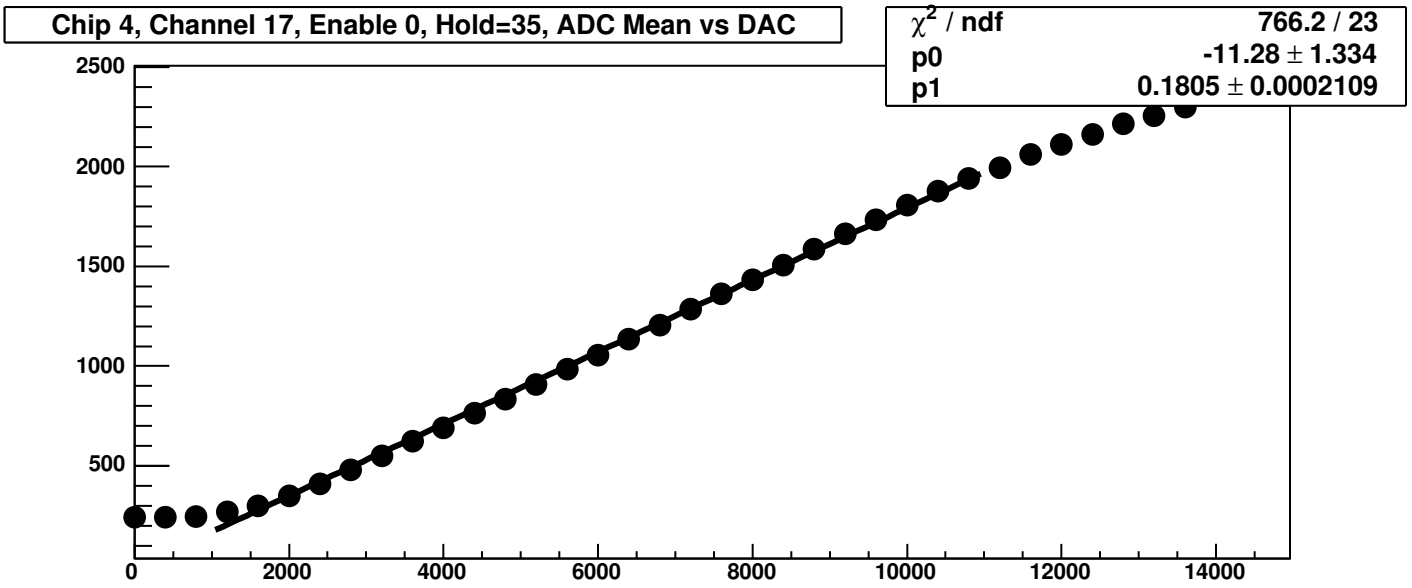

Chip 4, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

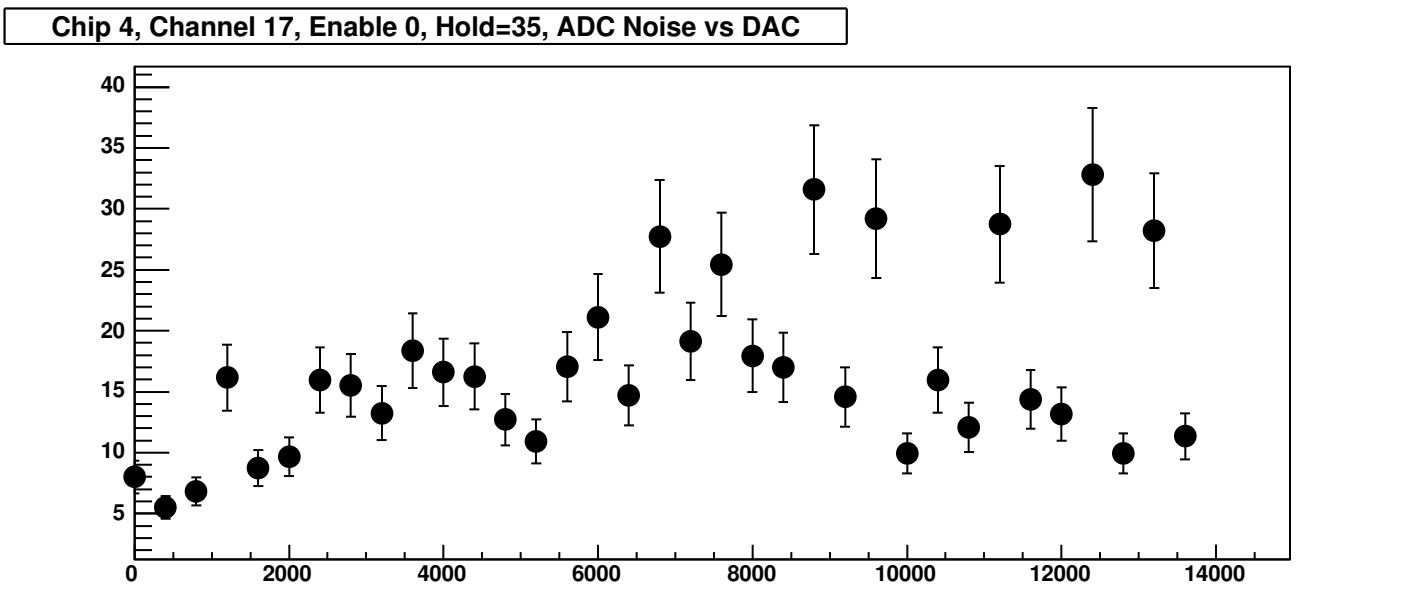

Chip 4, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

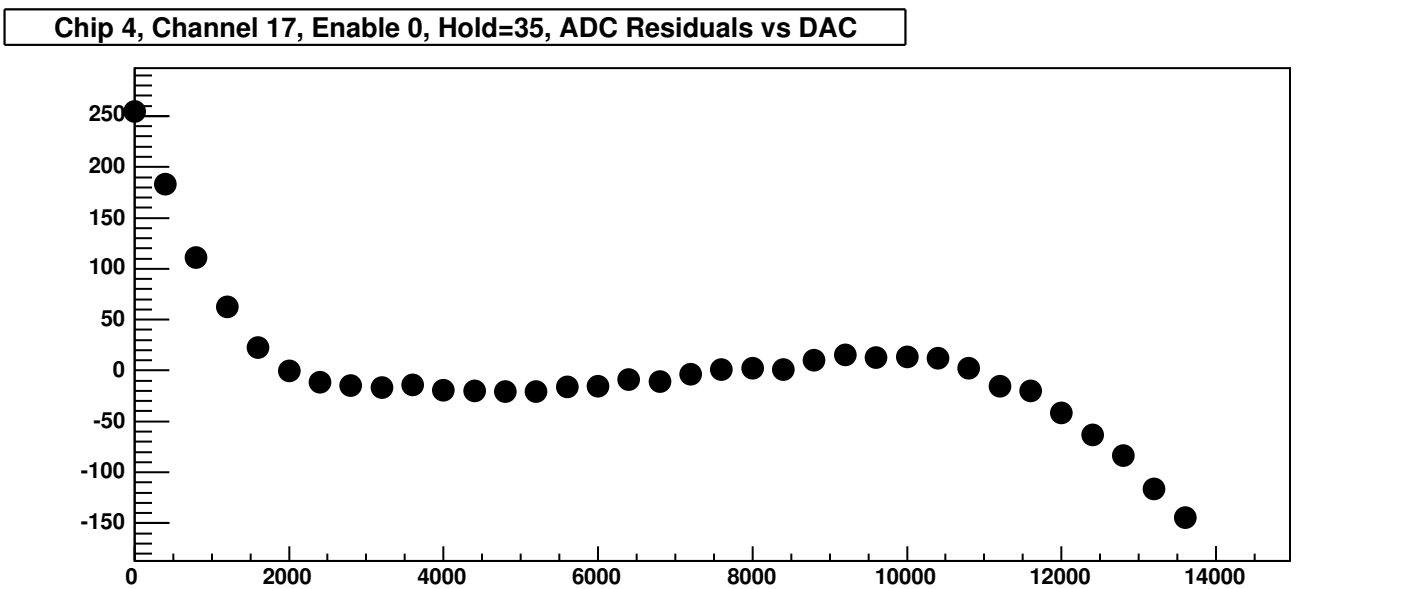

Chip 4, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 5717 / 23
p0 3.048e+04 ± 9.84
p1 0.02206 ± 0.001279
32000
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

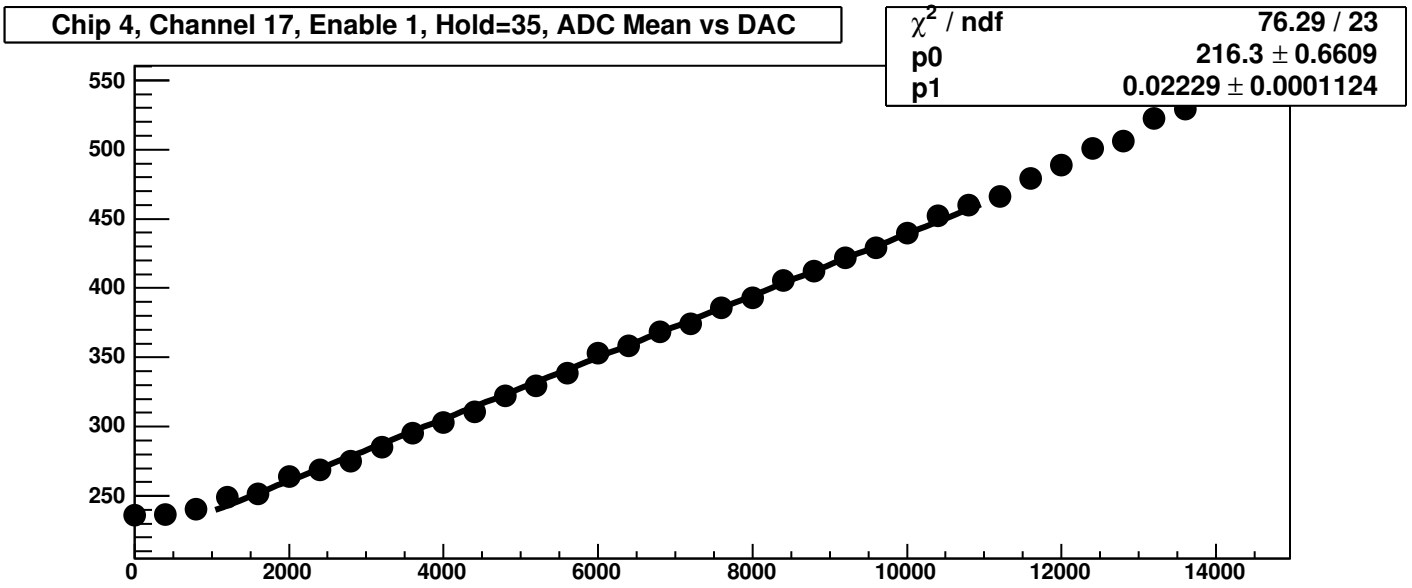

Chip 4, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

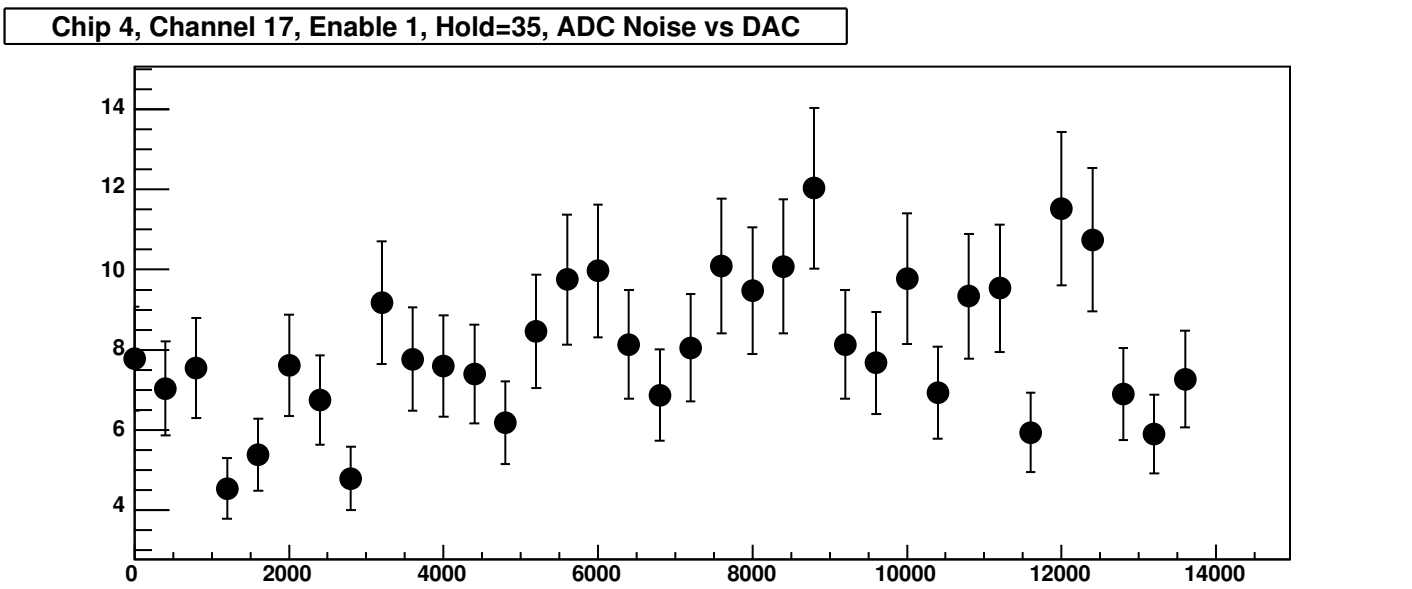

Chip 4, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

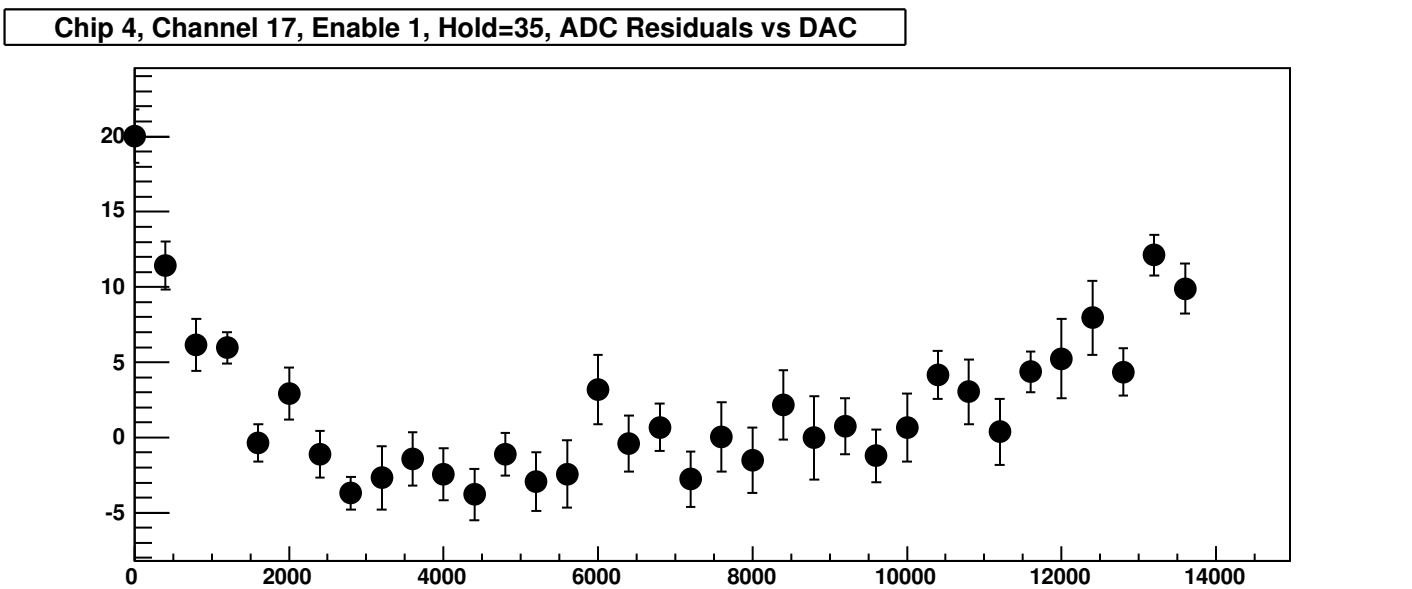

Chip 4, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 766.2 / 23
p0 -11.28 ± 1.334
p1 0.1805 ± 0.0002109
2500
2000
1500
1000
500
0
2000
4000
6000
8000
10000
12000
14000

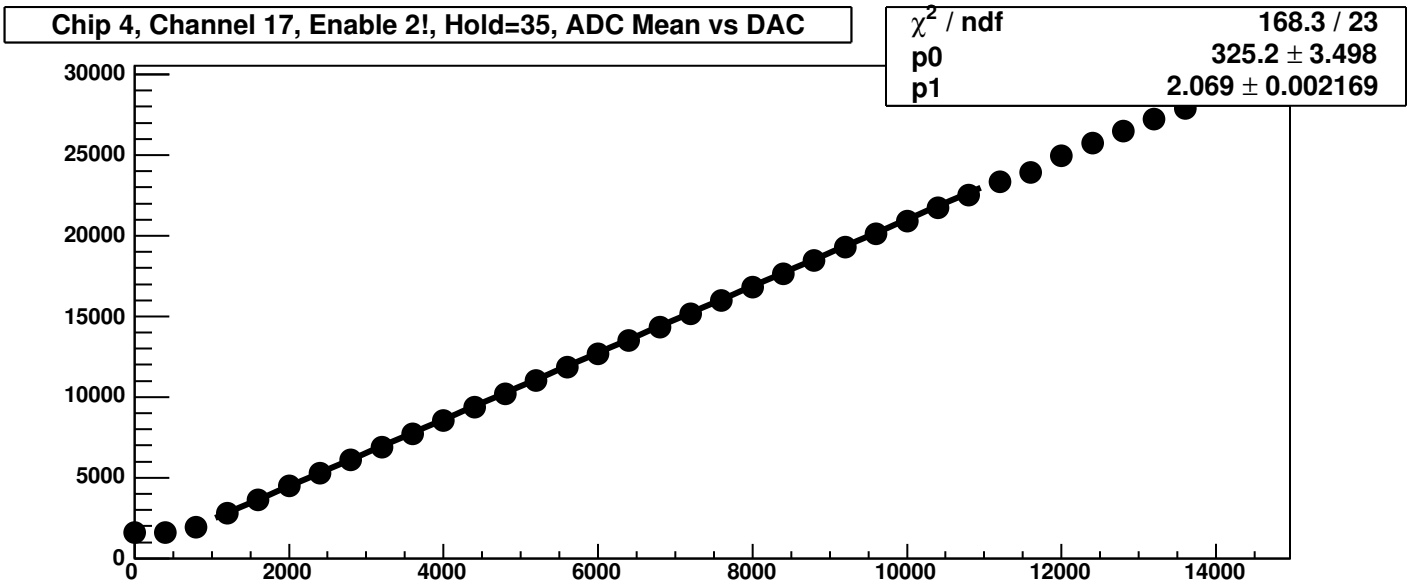

Chip 4, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

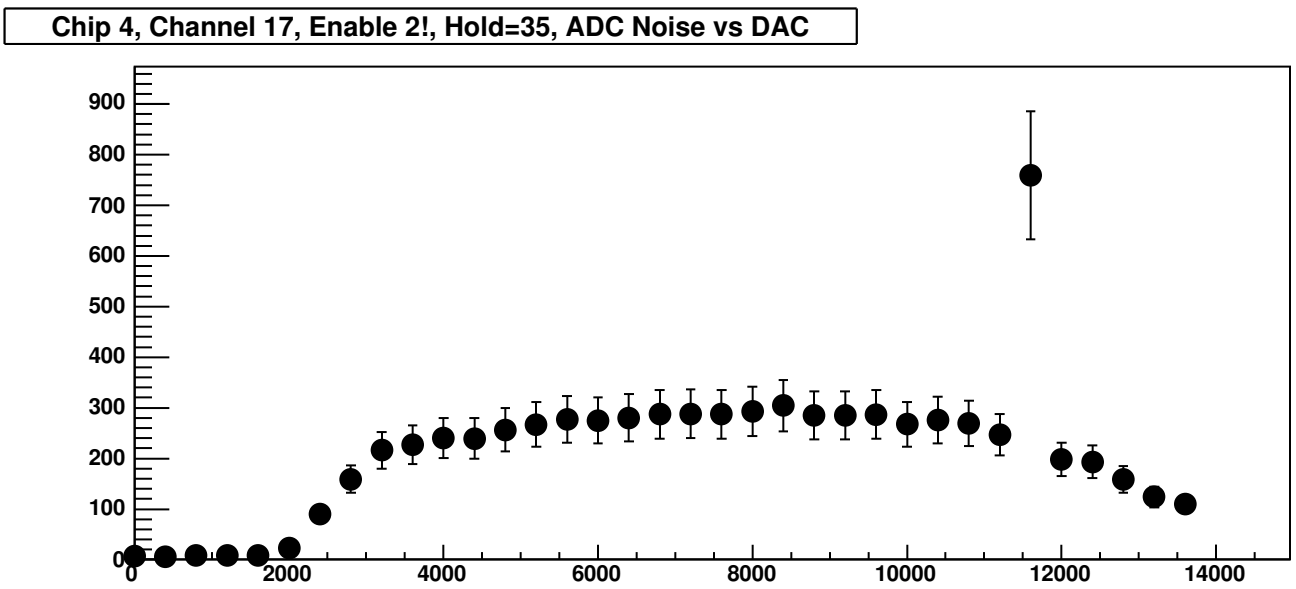

Chip 4, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

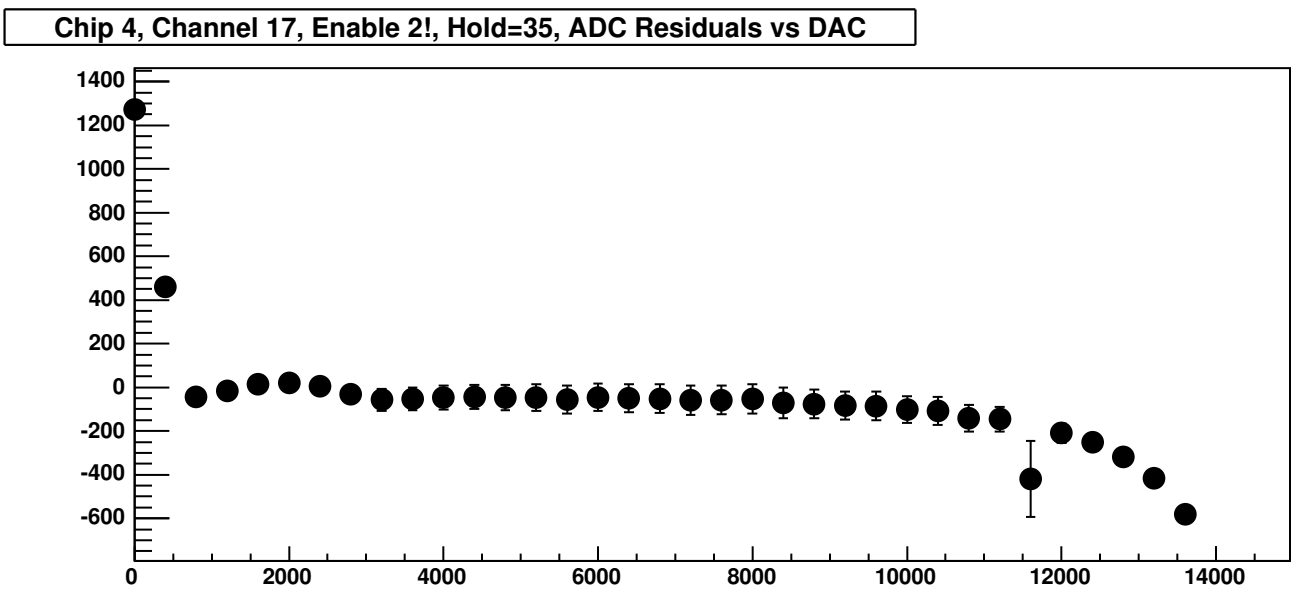

Chip 4, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 76.29 / 23
p0 216.3 ± 0.6609
p1 0.02229 ± 0.0001124
550
500
450
400
350
300
250
0
2000
4000
6000
8000
10000
12000
14000

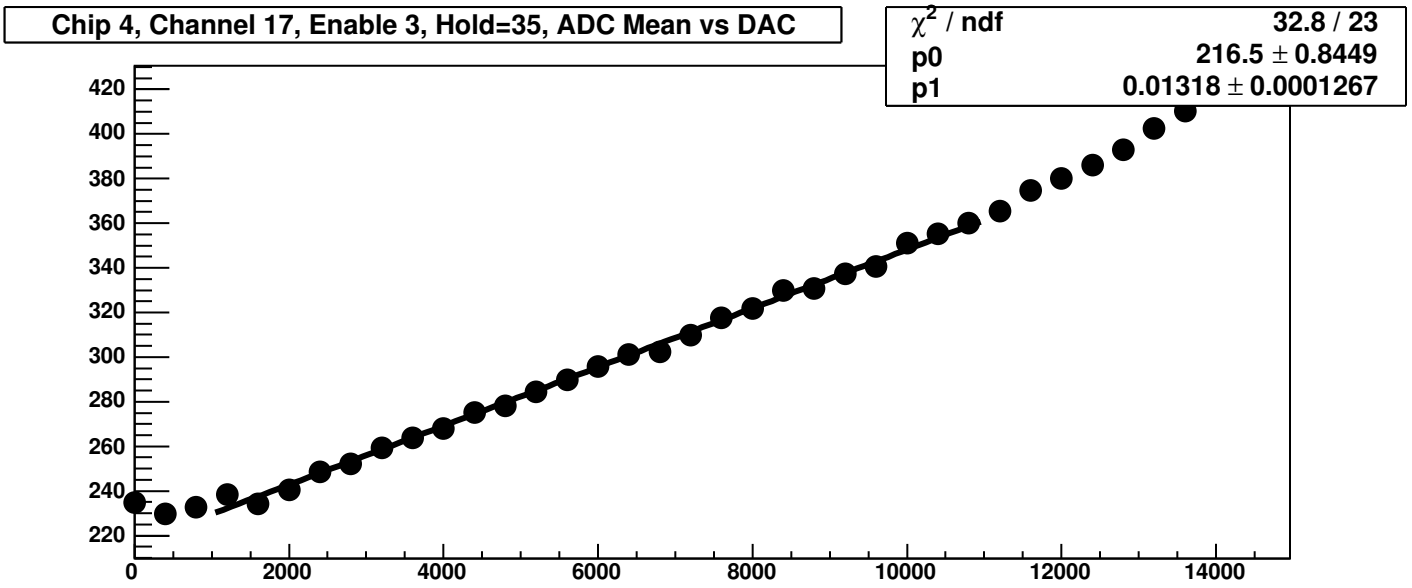

Chip 4, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

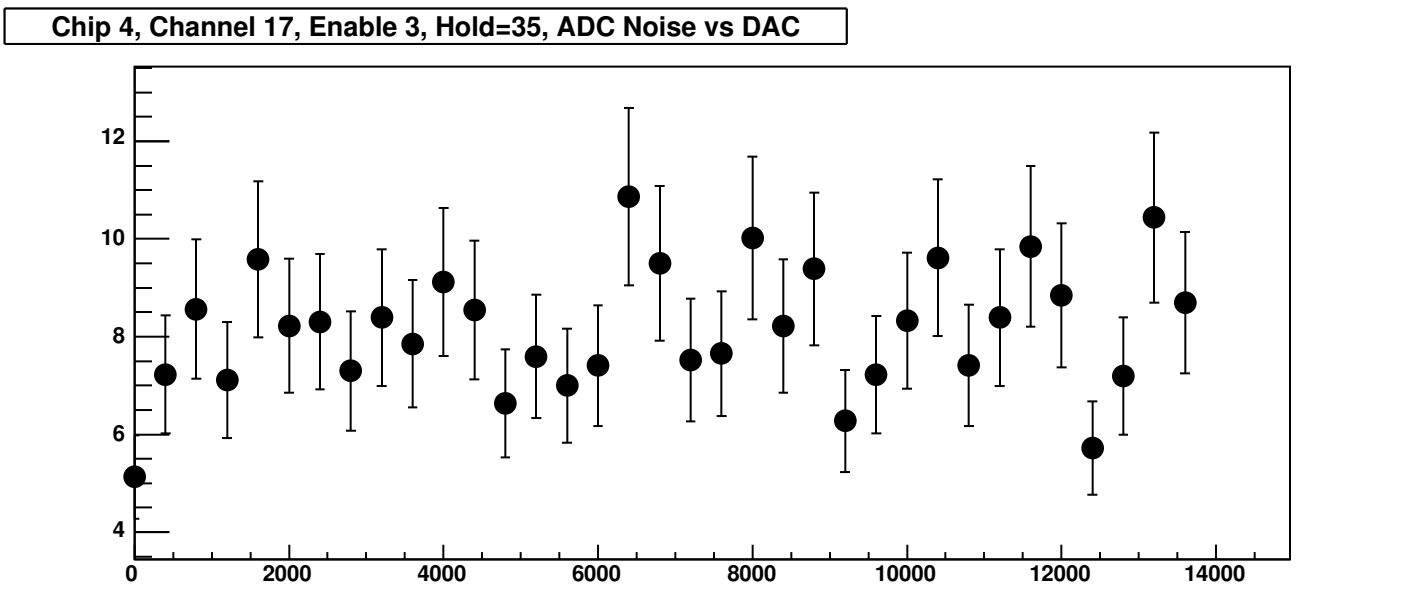

Chip 4, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

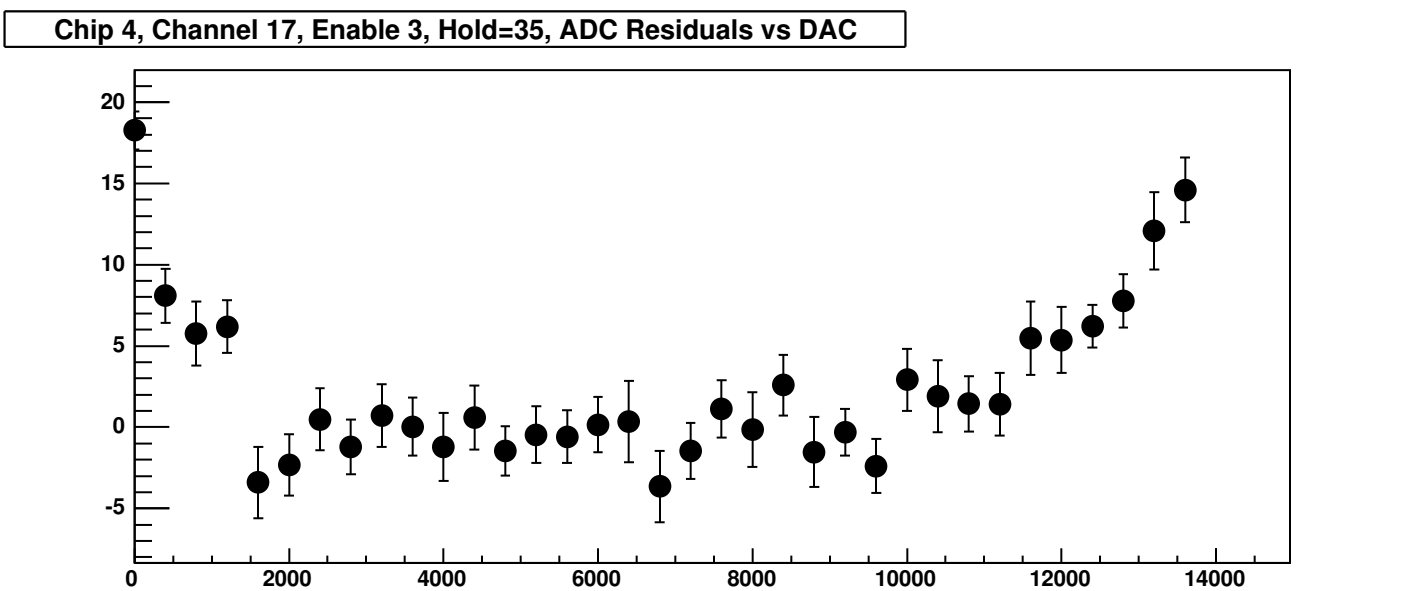

Chip 4, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 168.3 / 23
p0 325.2 ± 3.498
p1 2.069 ± 0.002169
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

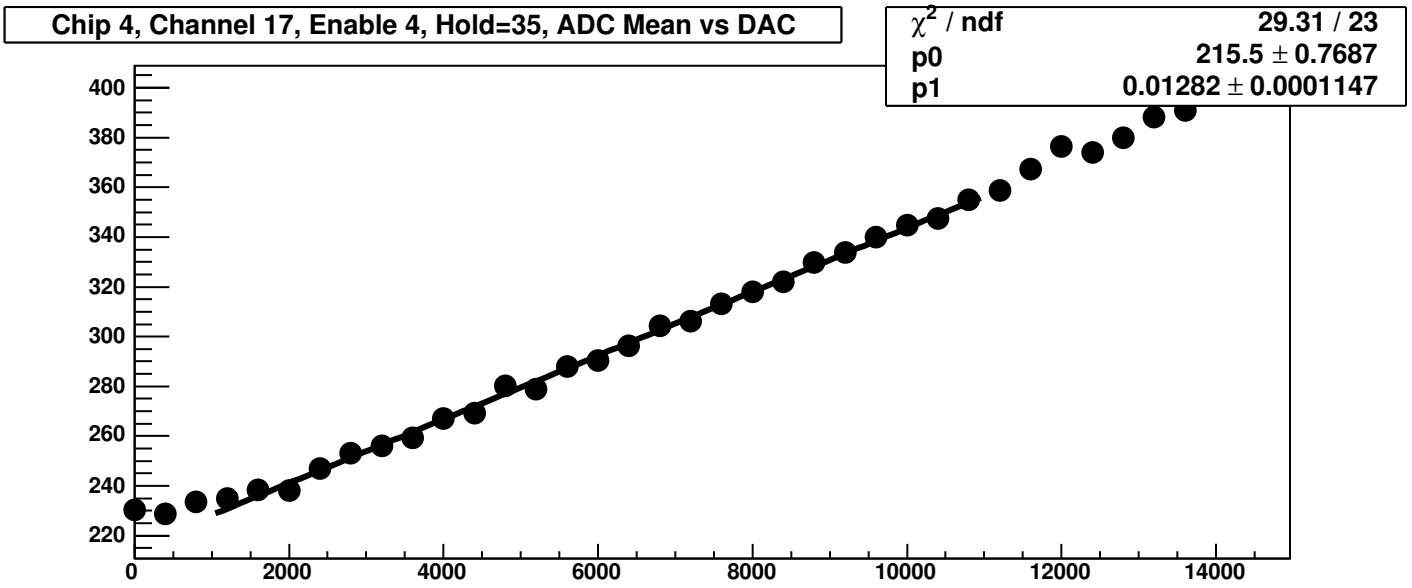

Chip 4, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
900
800
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

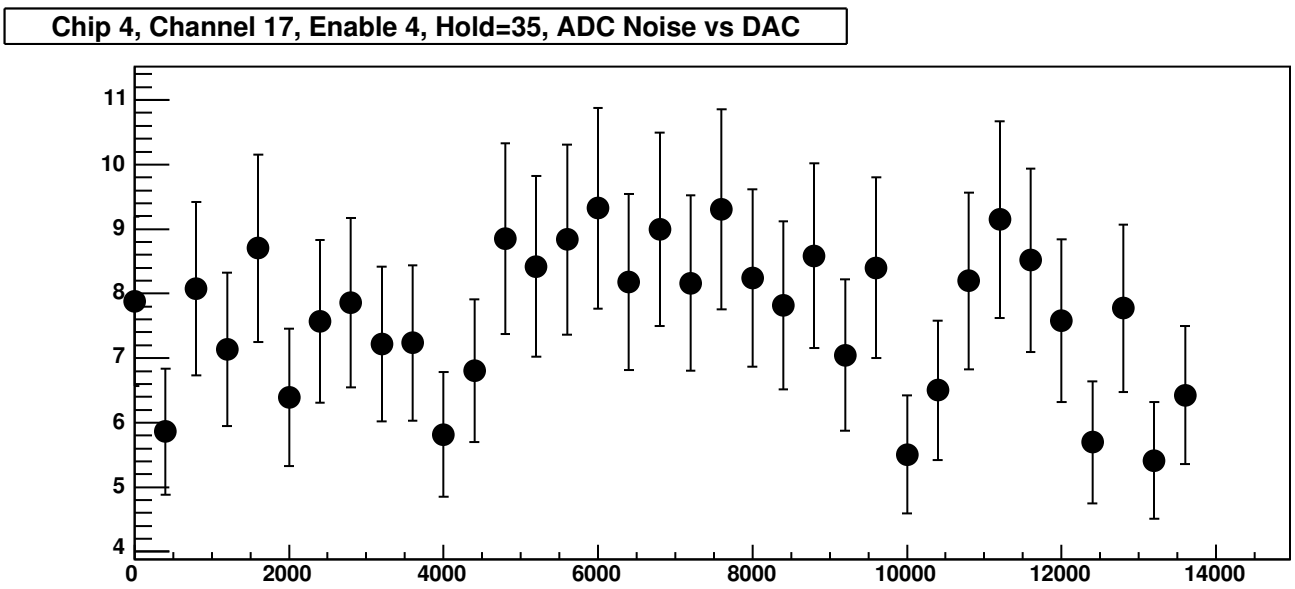

Chip 4, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

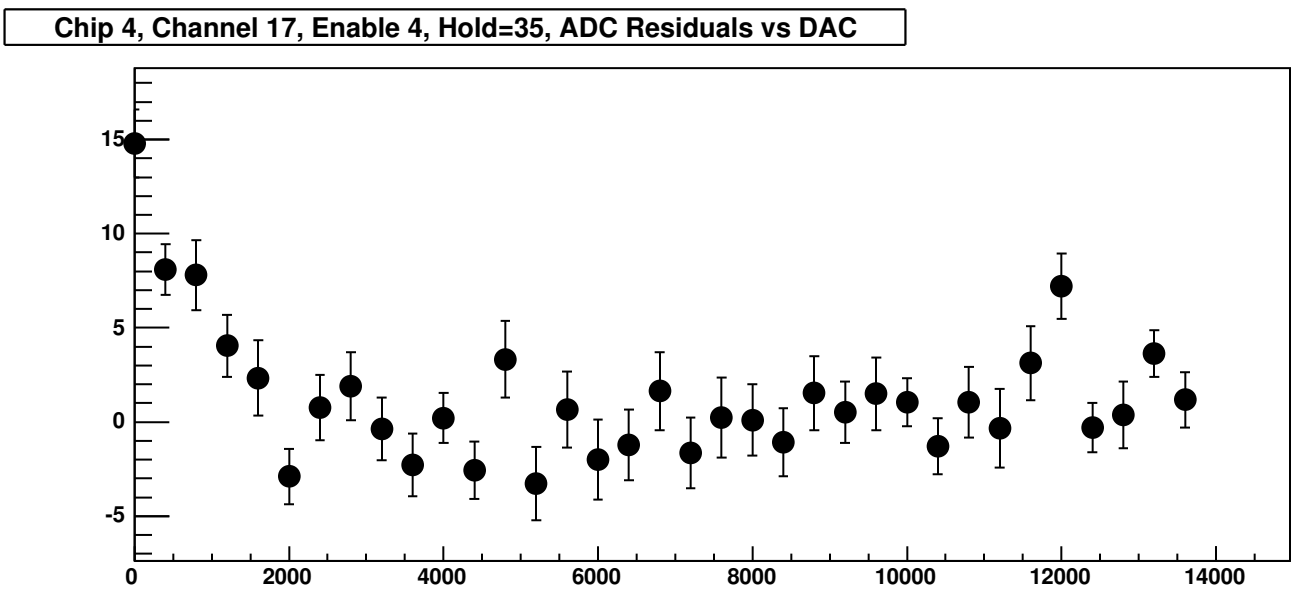

Chip 4, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 32.8 / 23
p0 216.5 ± 0.8449
p1 0.01318 ± 0.0001267
420
400
380
360
340
320
300
280
260
240
220
0
2000
4000
6000
8000
10000
12000
14000

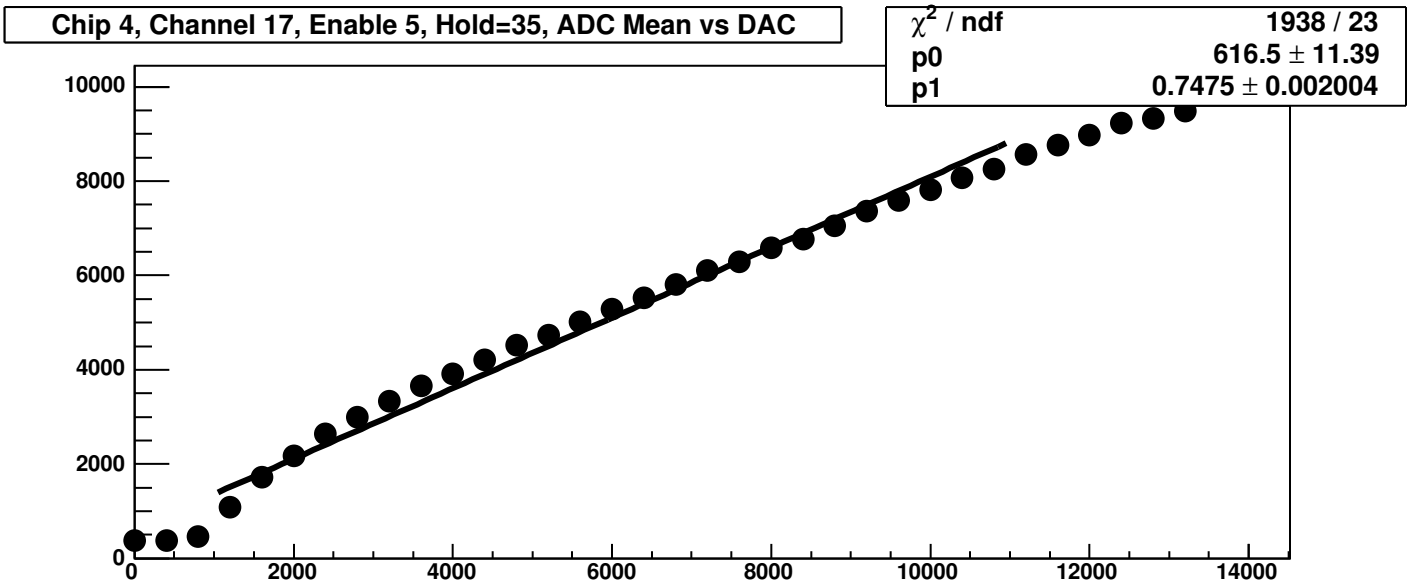

Chip 4, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

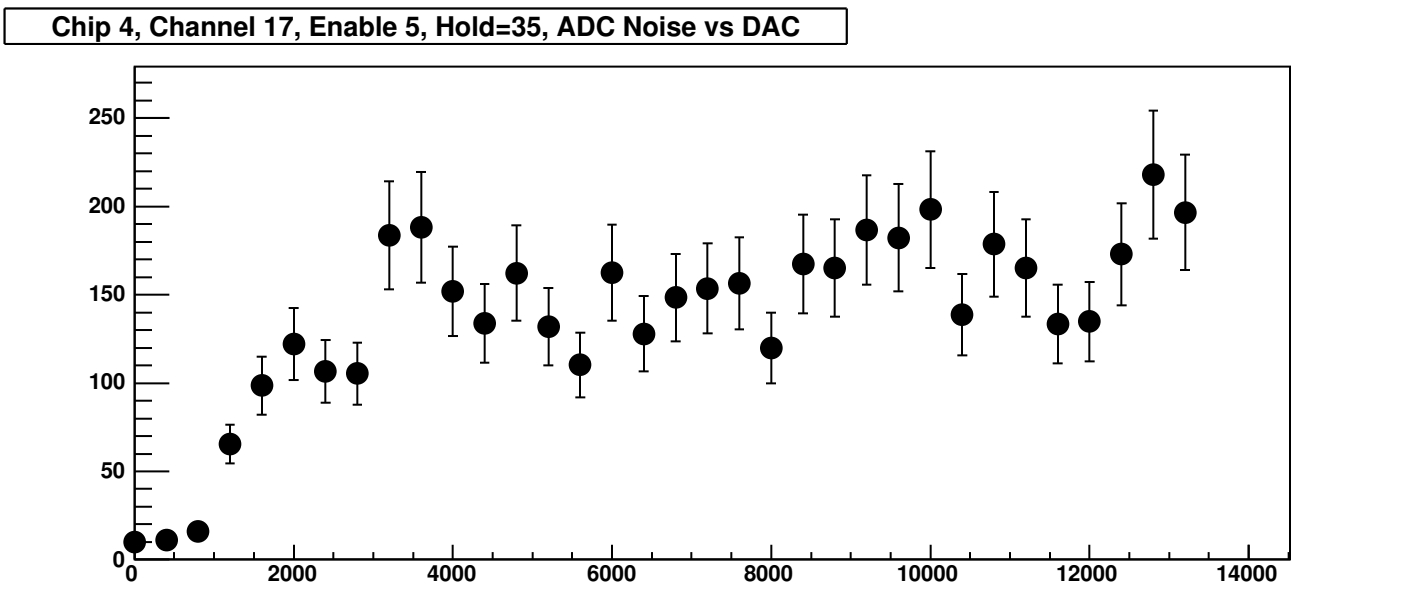

Chip 4, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

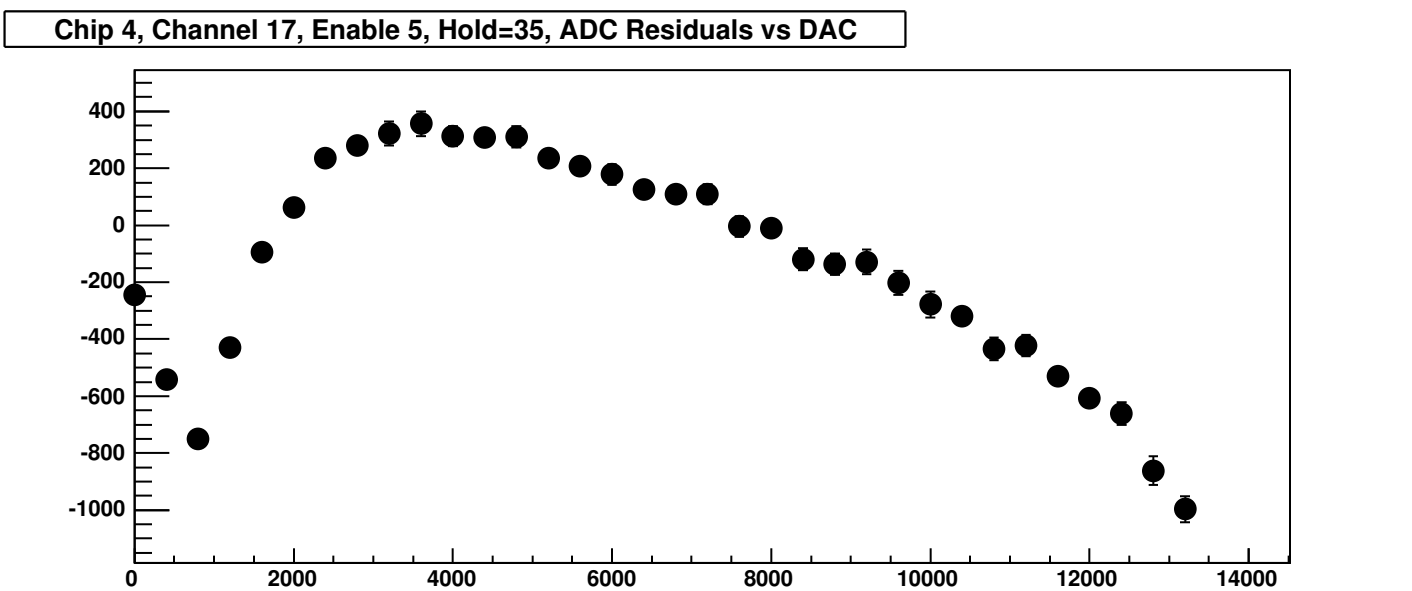

Chip 4, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.31 / 23
p0 215.5 ± 0.7687
p1 0.01282 ± 0.0001147
400
380
360
340
320
300
280
260
240
220
0
2000
4000
6000
8000
10000
12000
14000

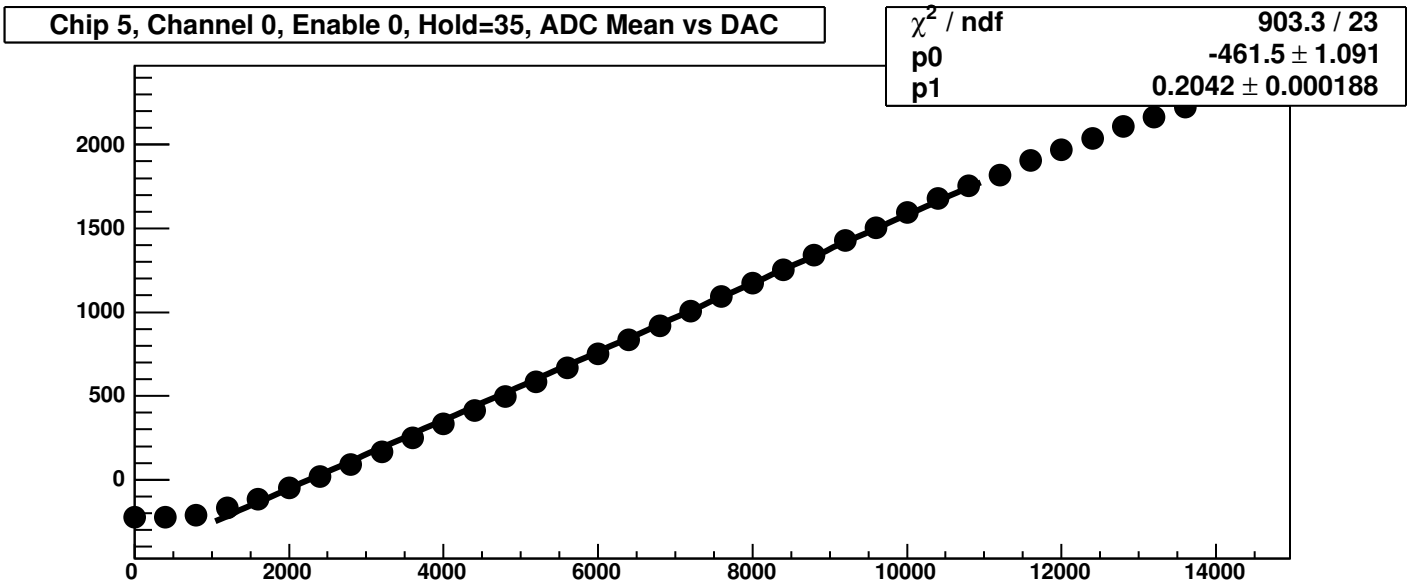

Chip 4, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

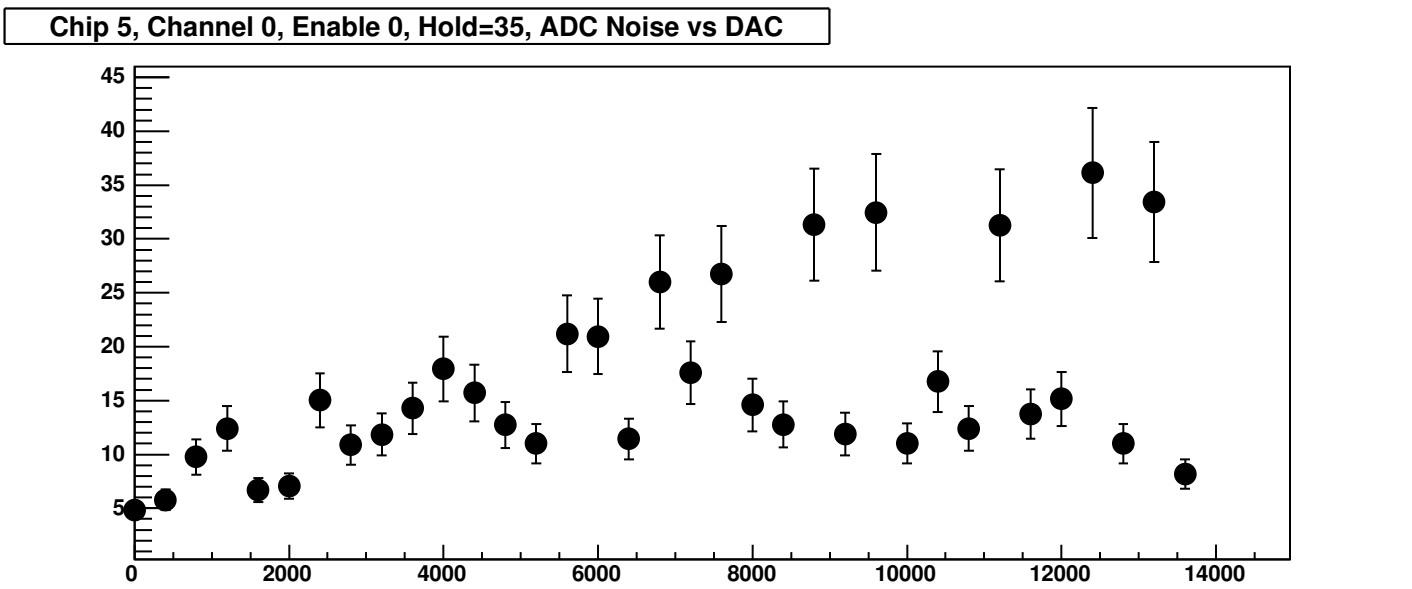

Chip 4, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

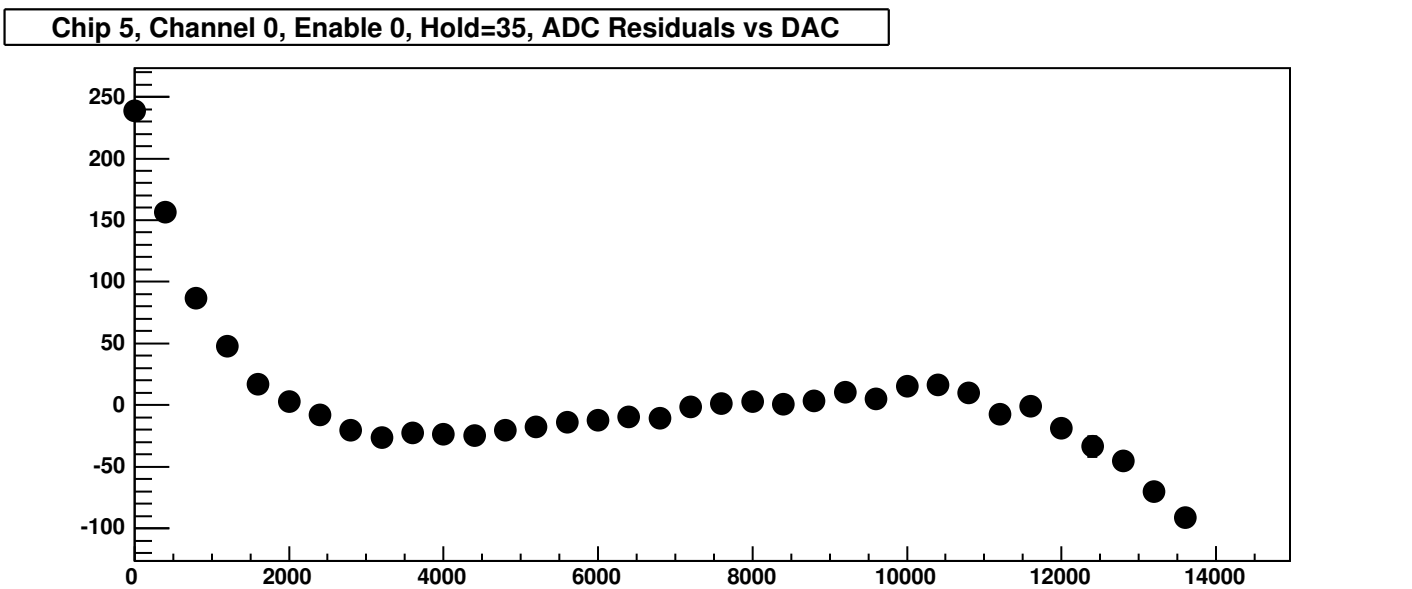

Chip 4, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1938 / 23
p0 616.5 ± 11.39
p1 0.7475 ± 0.002004
10000
8000
6000
4000
2000
0
0
2000
4000
6000
8000
10000
12000
14000

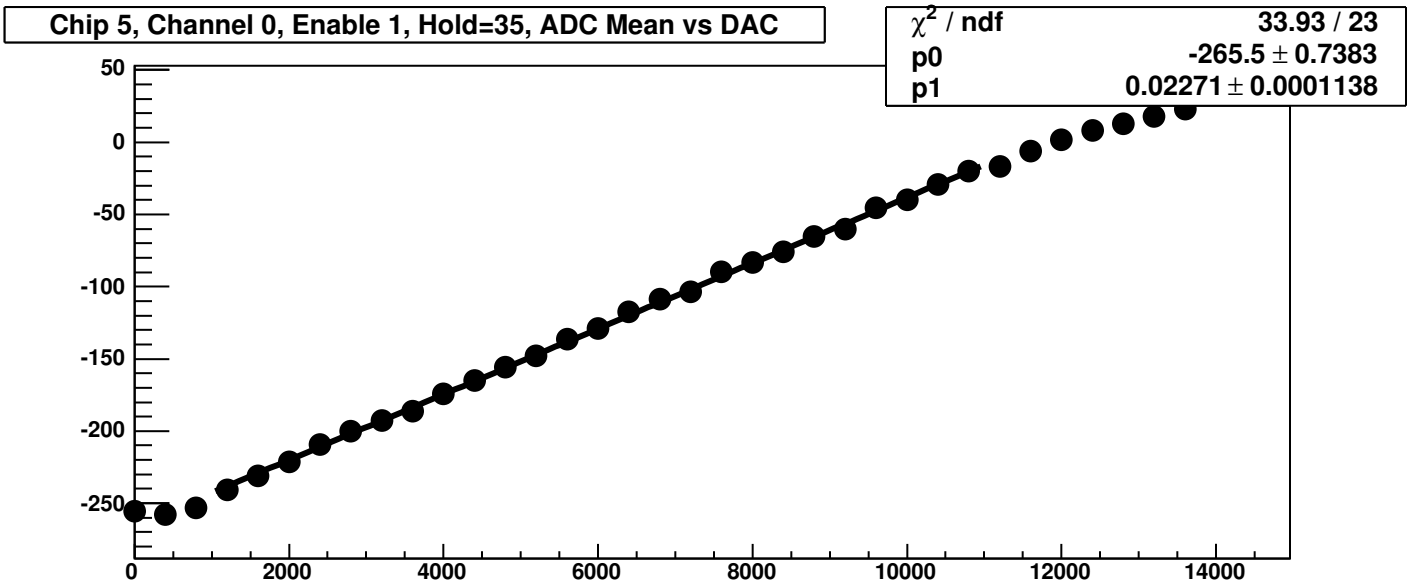

Chip 4, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

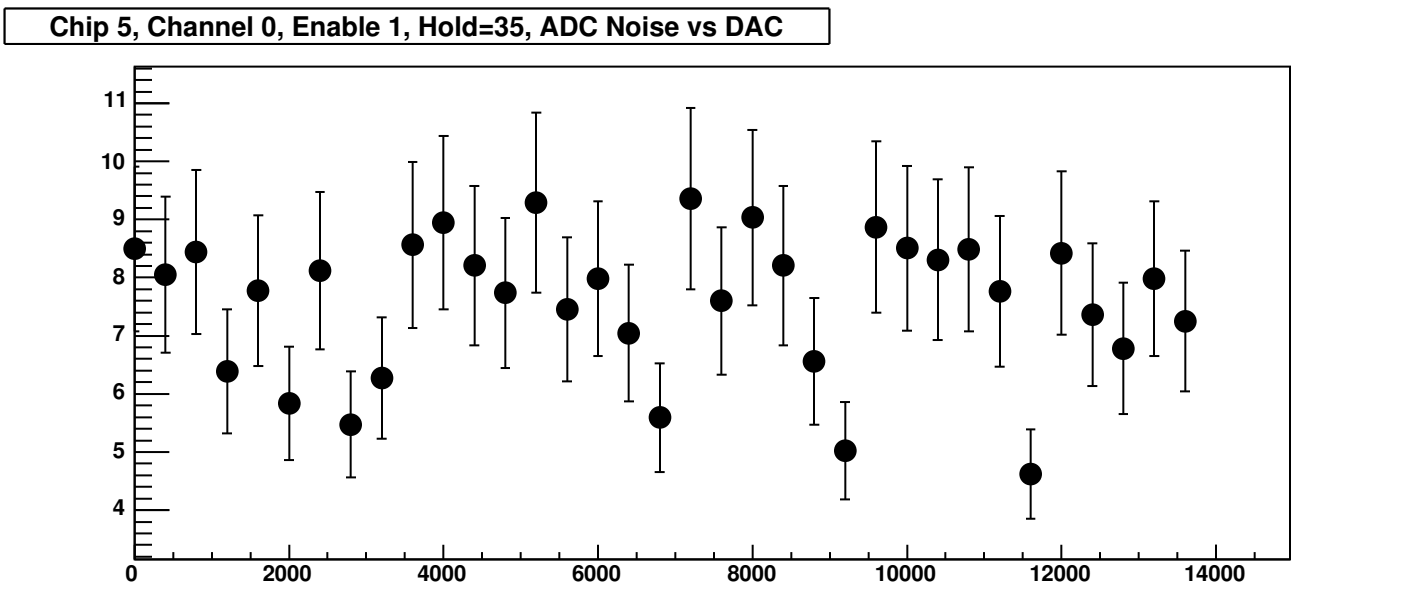

Chip 4, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

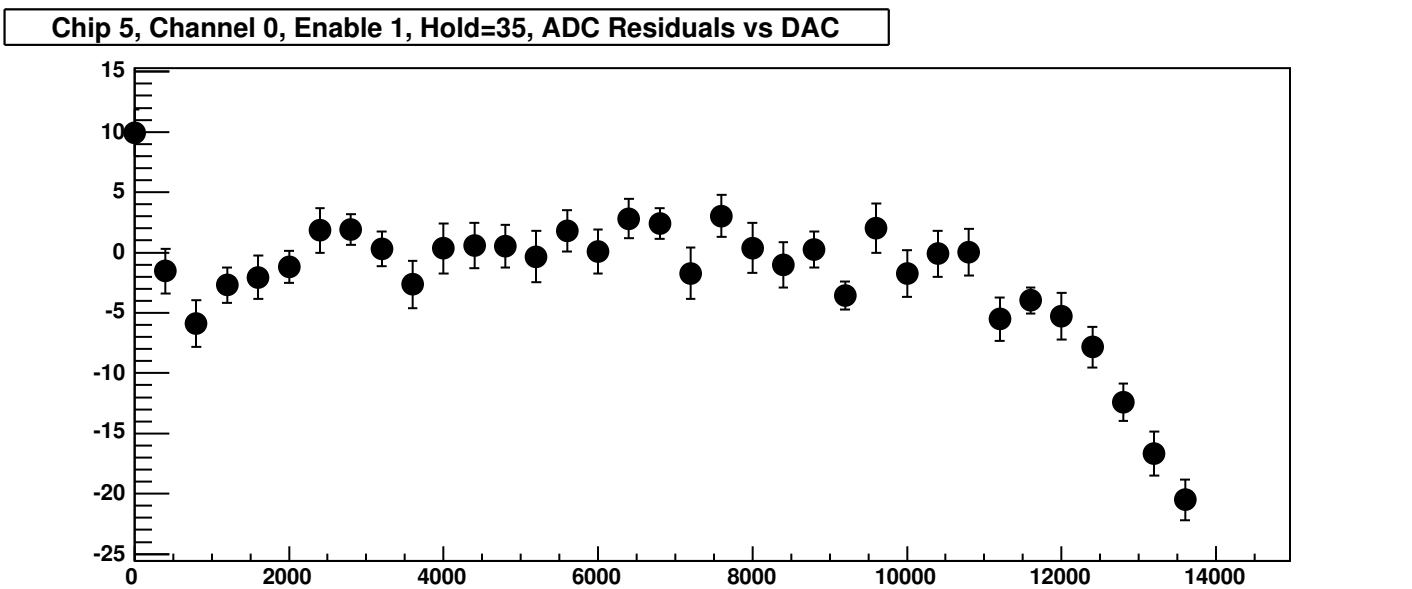

Chip 5, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 903.3 / 23
p0 -461.5 ± 1.091
p1 0.2042 ± 0.000188
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

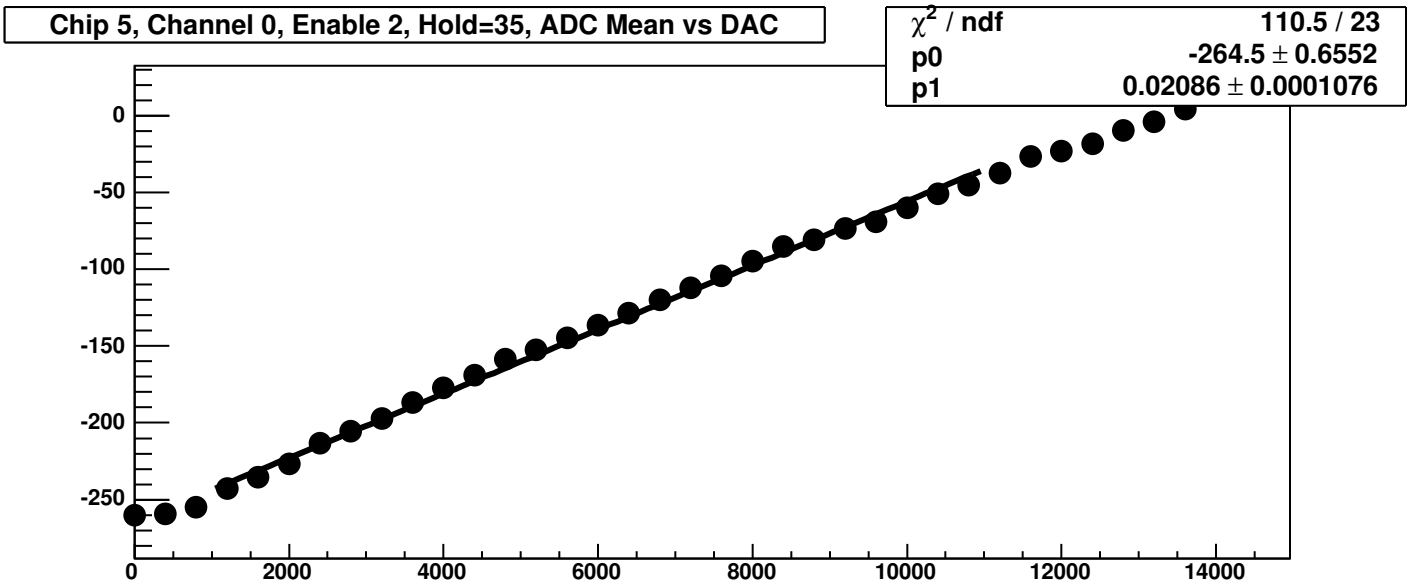

Chip 5, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

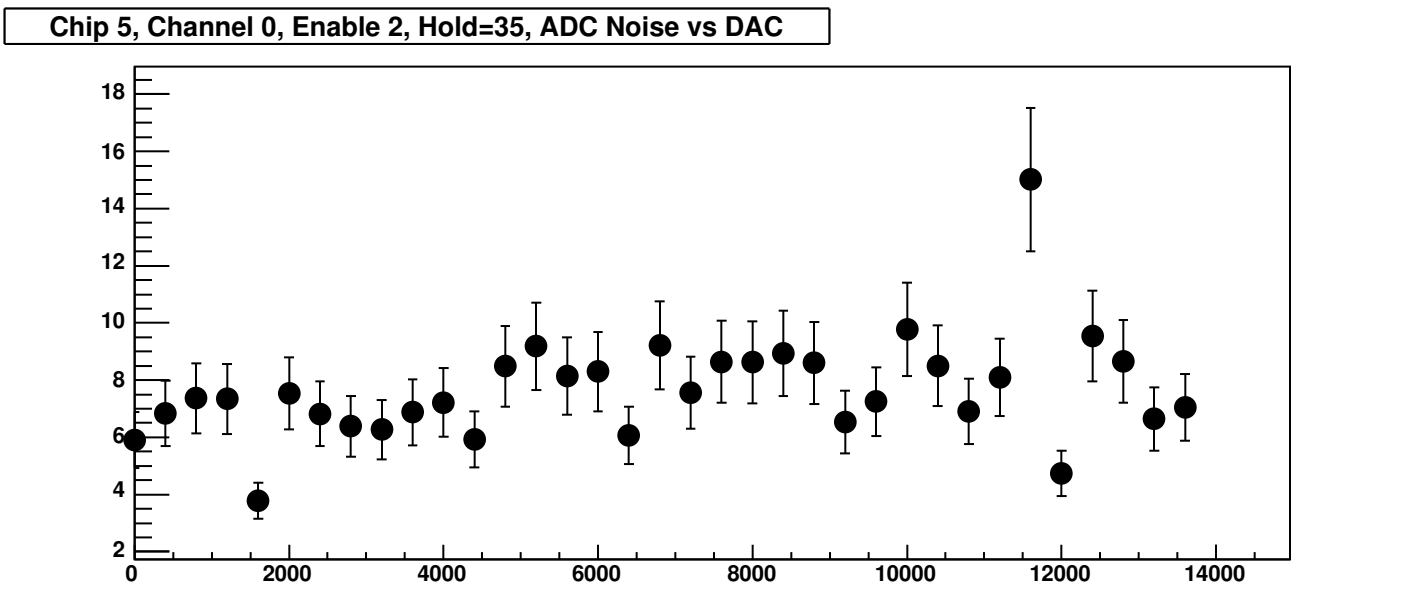

Chip 5, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

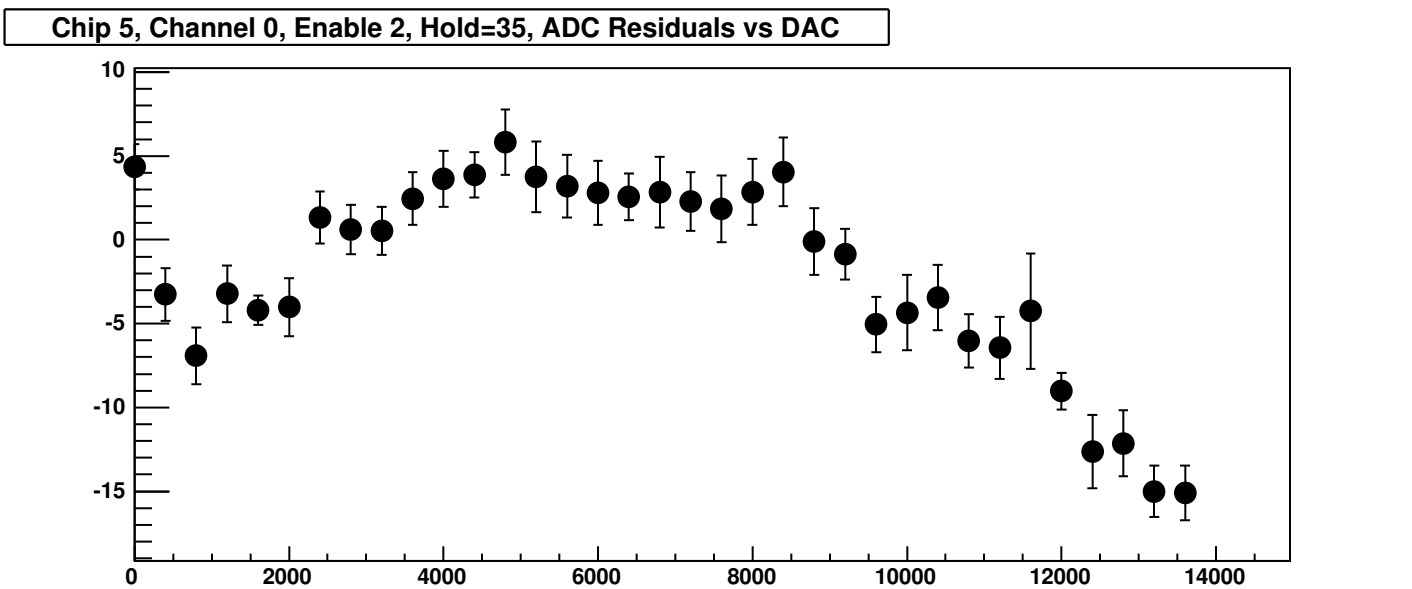

Chip 5, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.93 / 23
p0 -265.5 ± 0.7383
p1 0.02271 ± 0.0001138
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

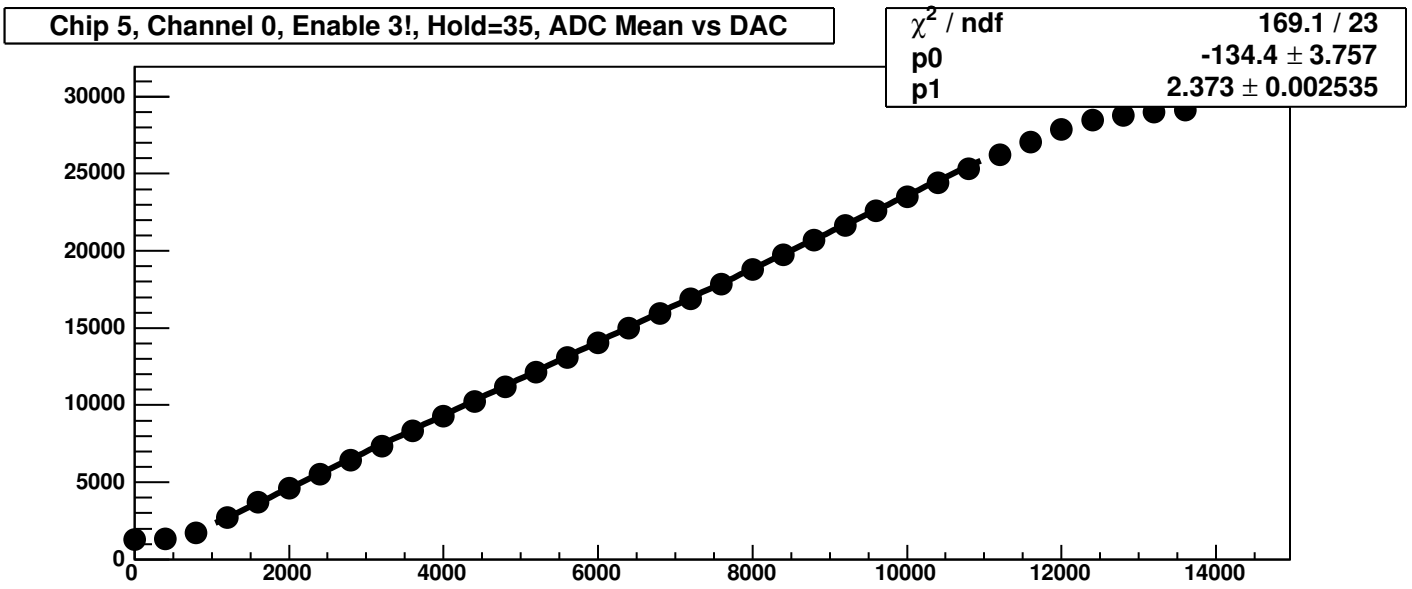

Chip 5, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

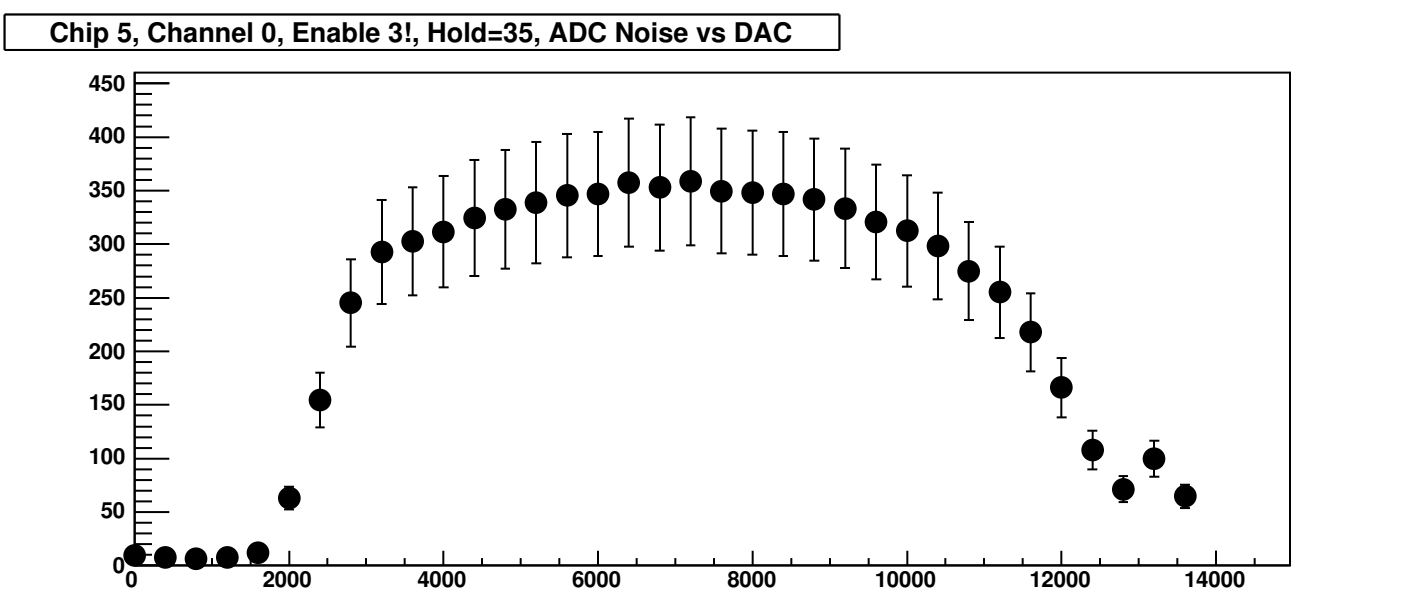

Chip 5, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

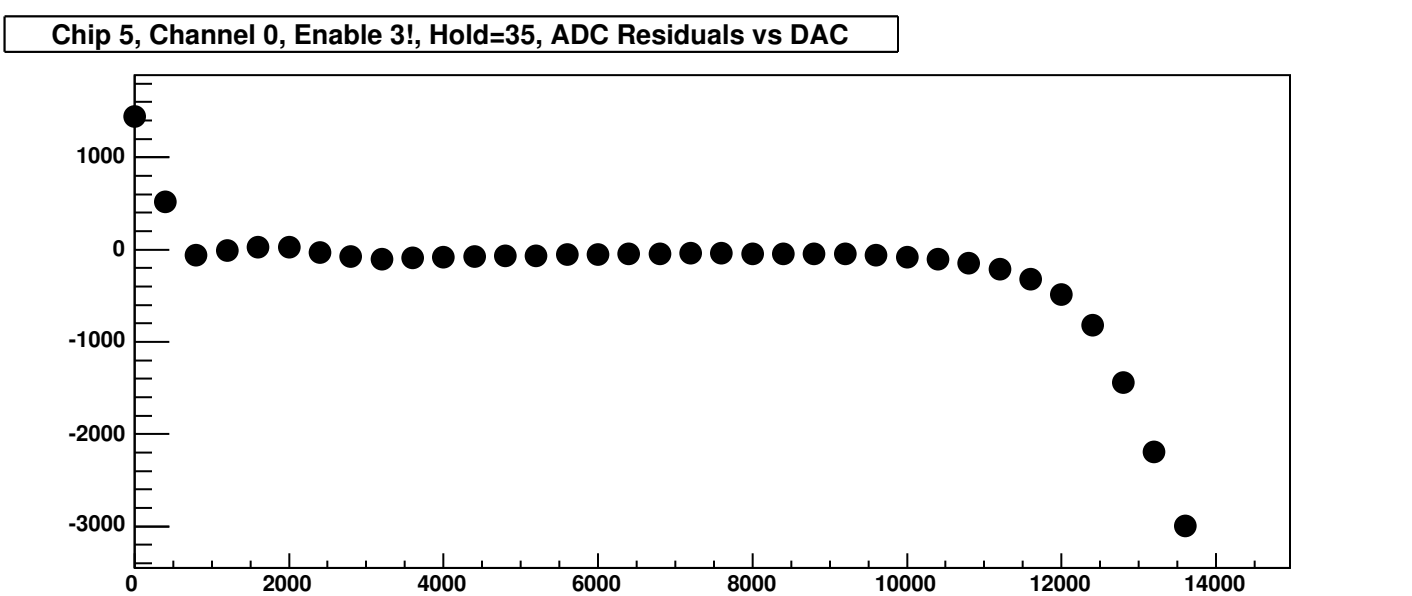

Chip 5, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 110.5 / 23
p0 -264.5 ± 0.6552
p1 0.02086 ± 0.0001076
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

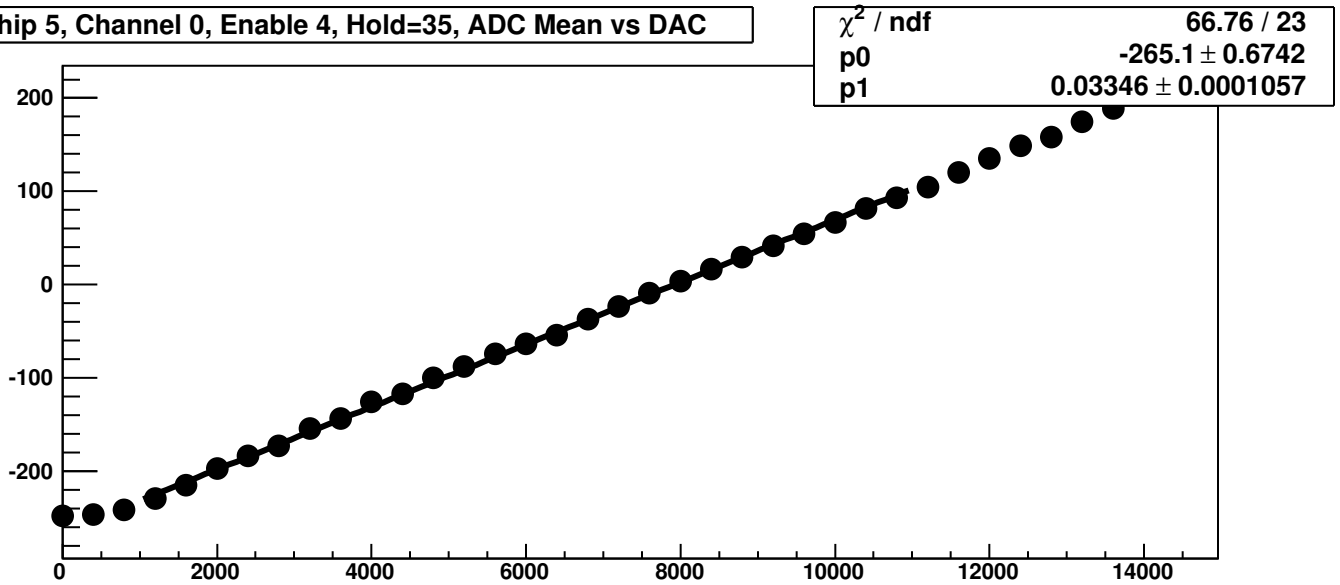

Chip 5, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 169.1 / 23
p0 -134.4 ± 3.757
p1 2.373 ± 0.002535
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

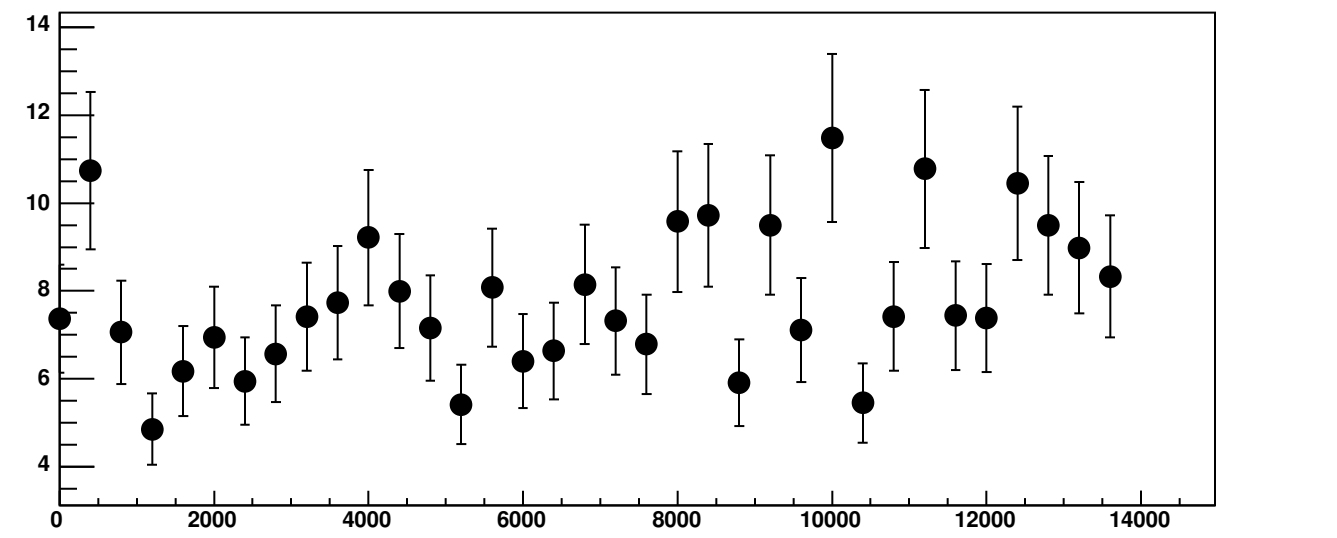

Chip 5, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

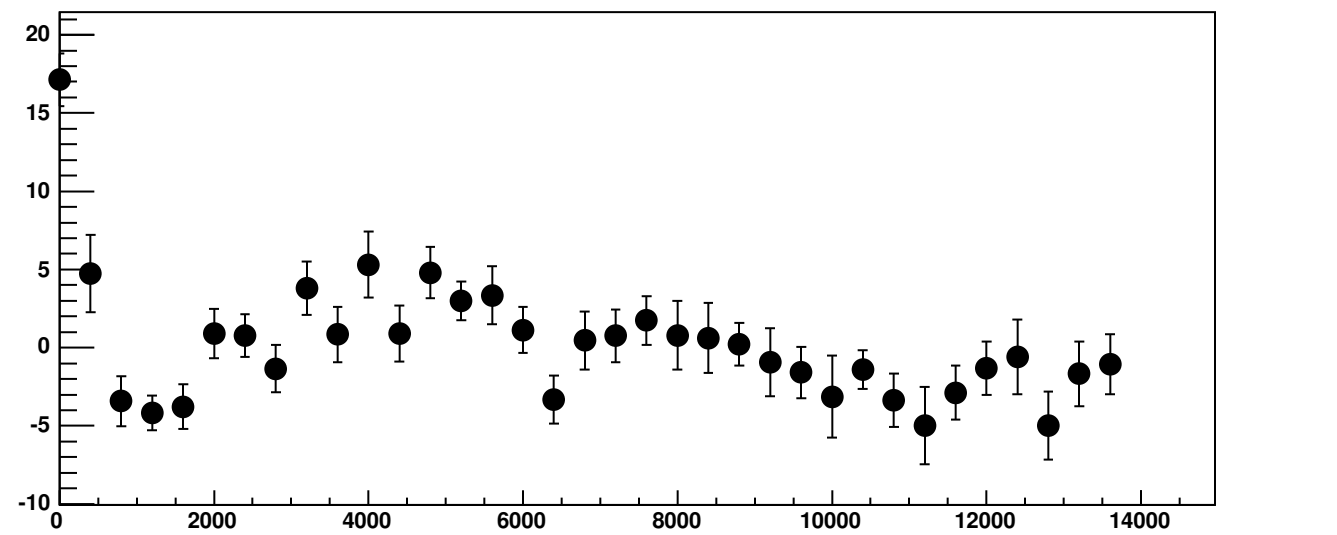
Chip 5, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

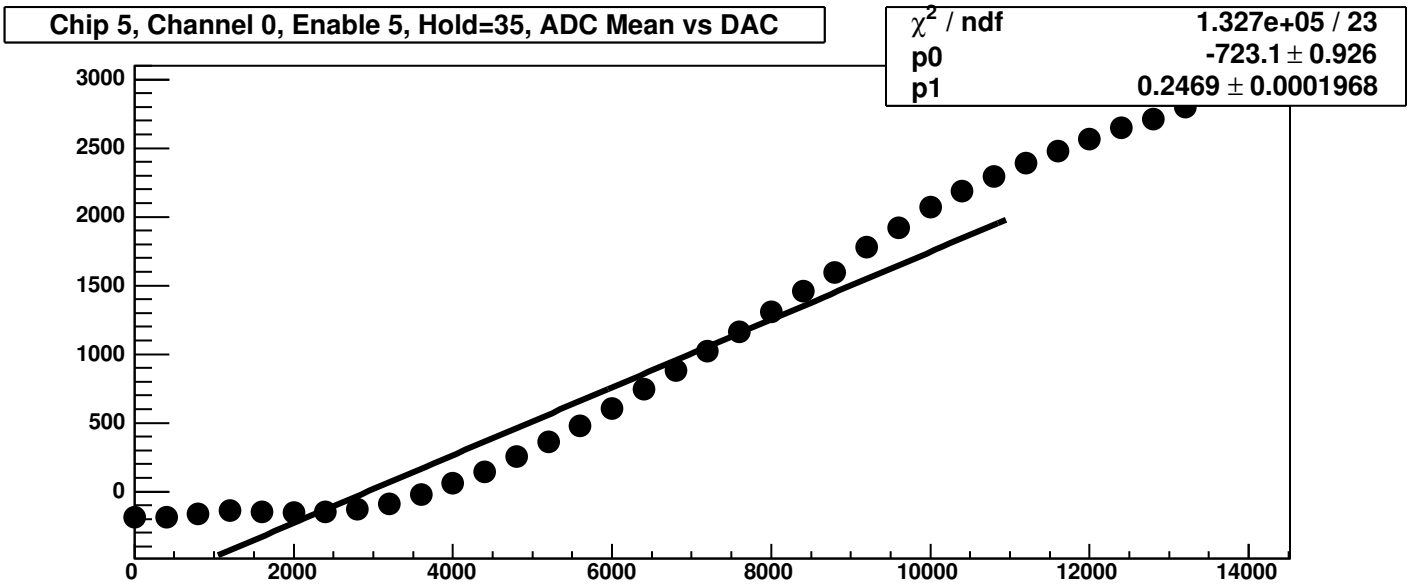


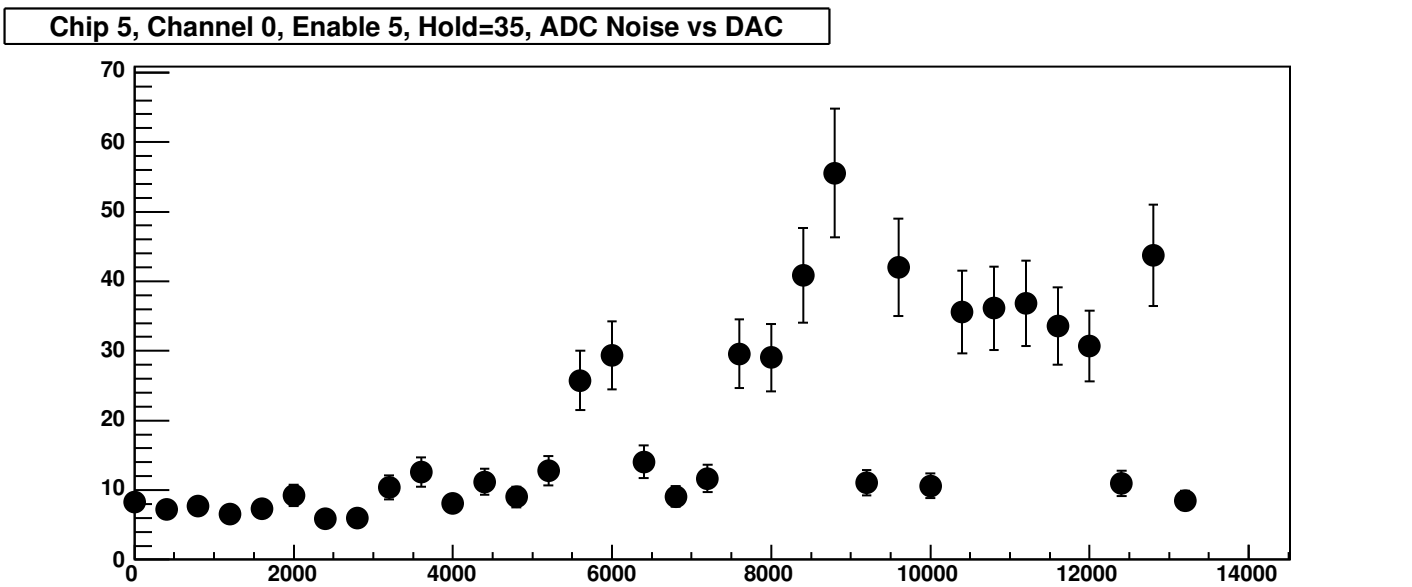
Chip 5, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

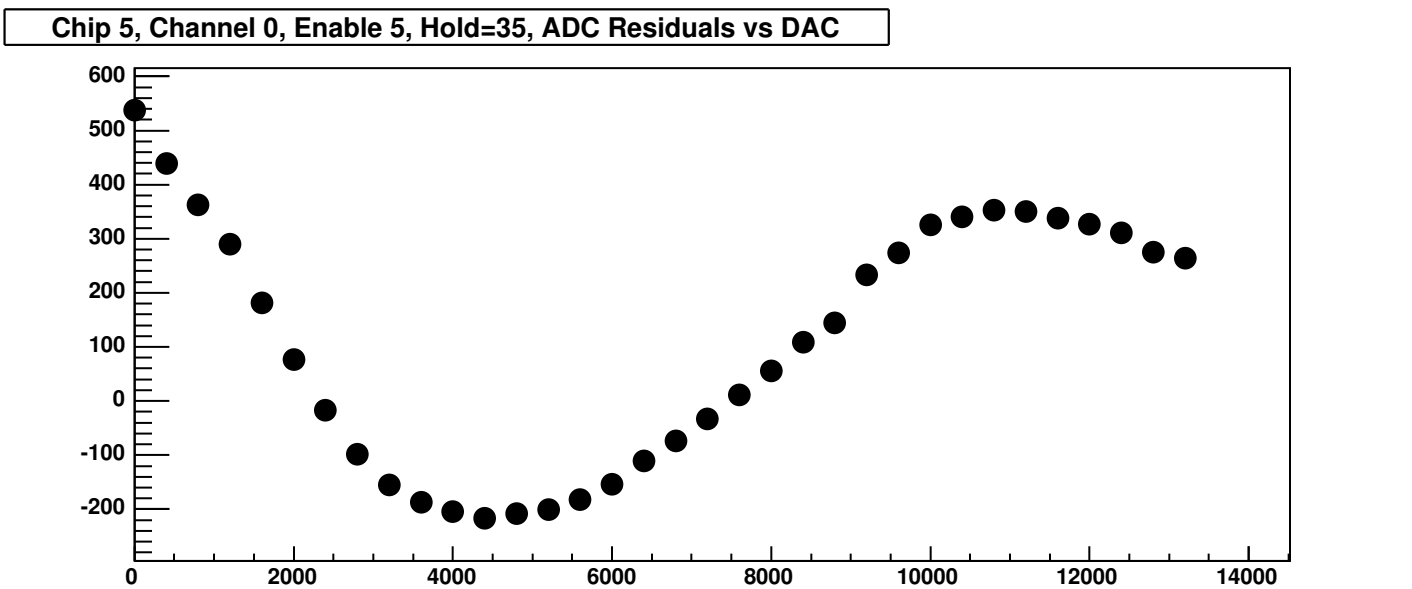


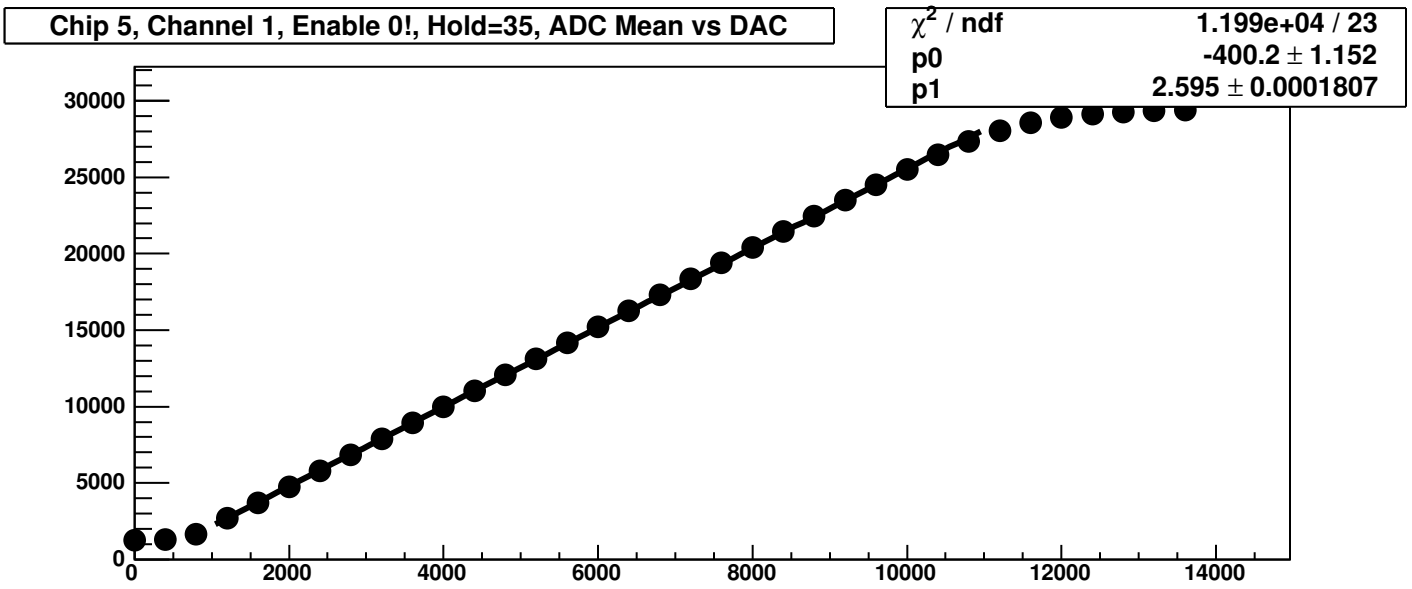
Chip 5, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

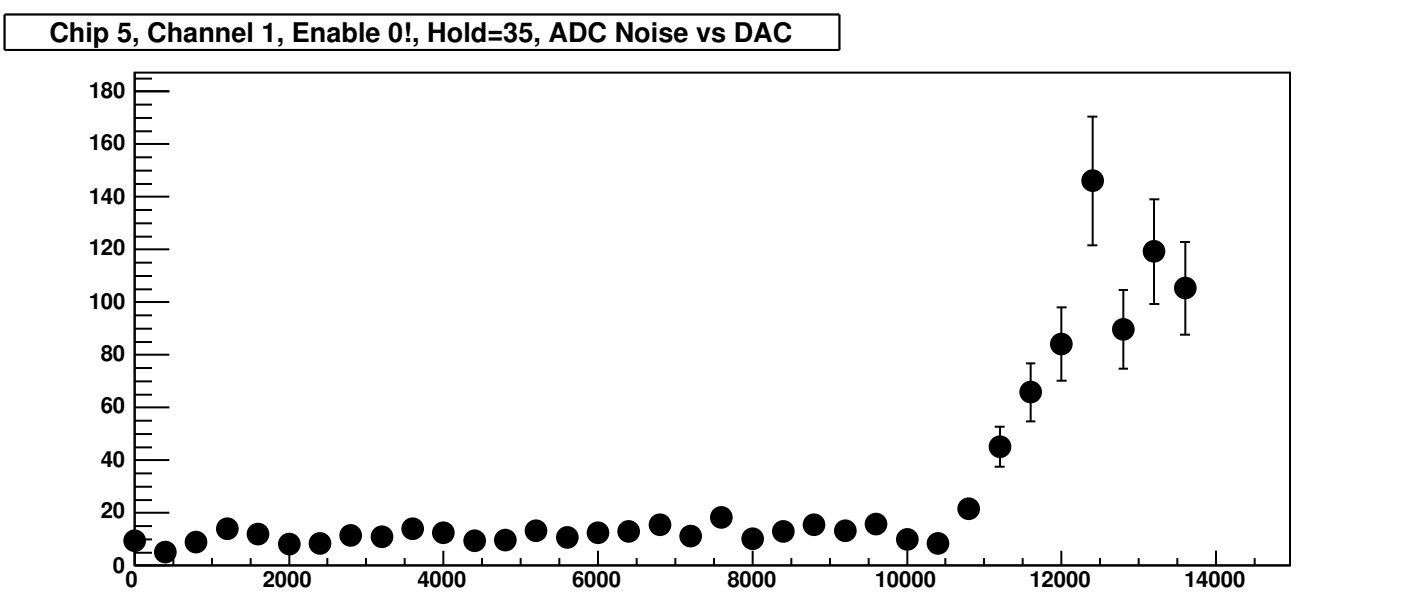


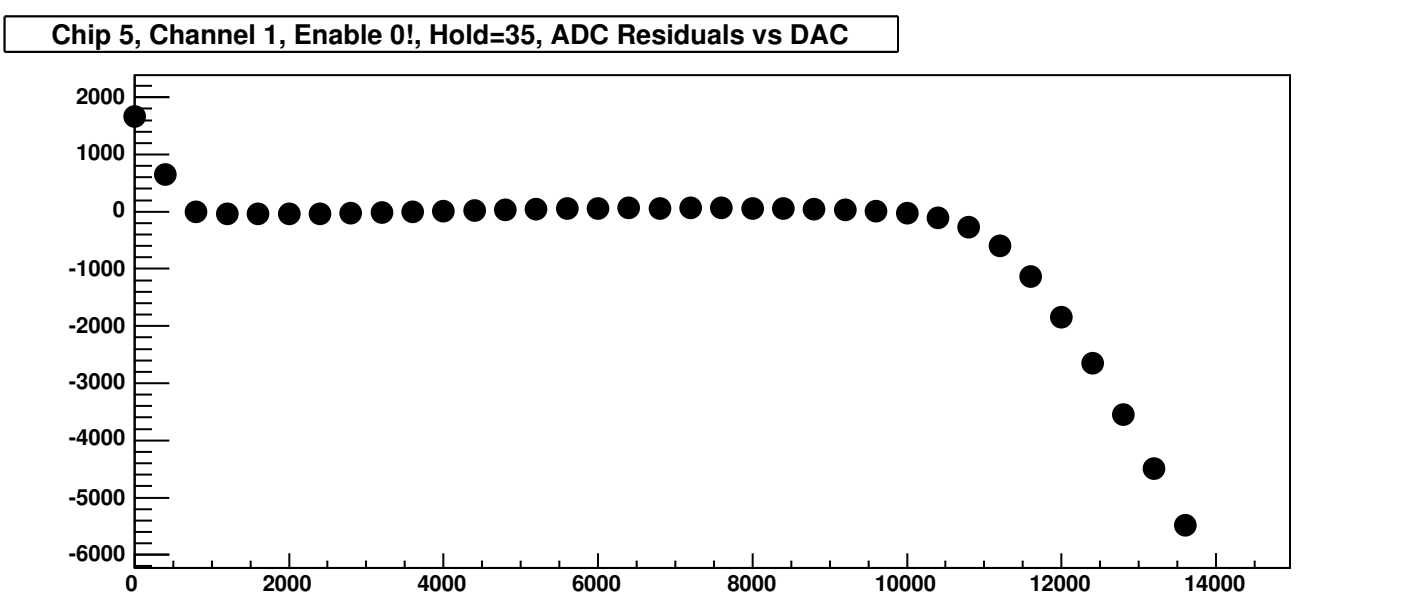

Chip 5, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.327e+05 / 23
p0 -723.1 ± 0.926
p1 0.2469 ± 0.0001968
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

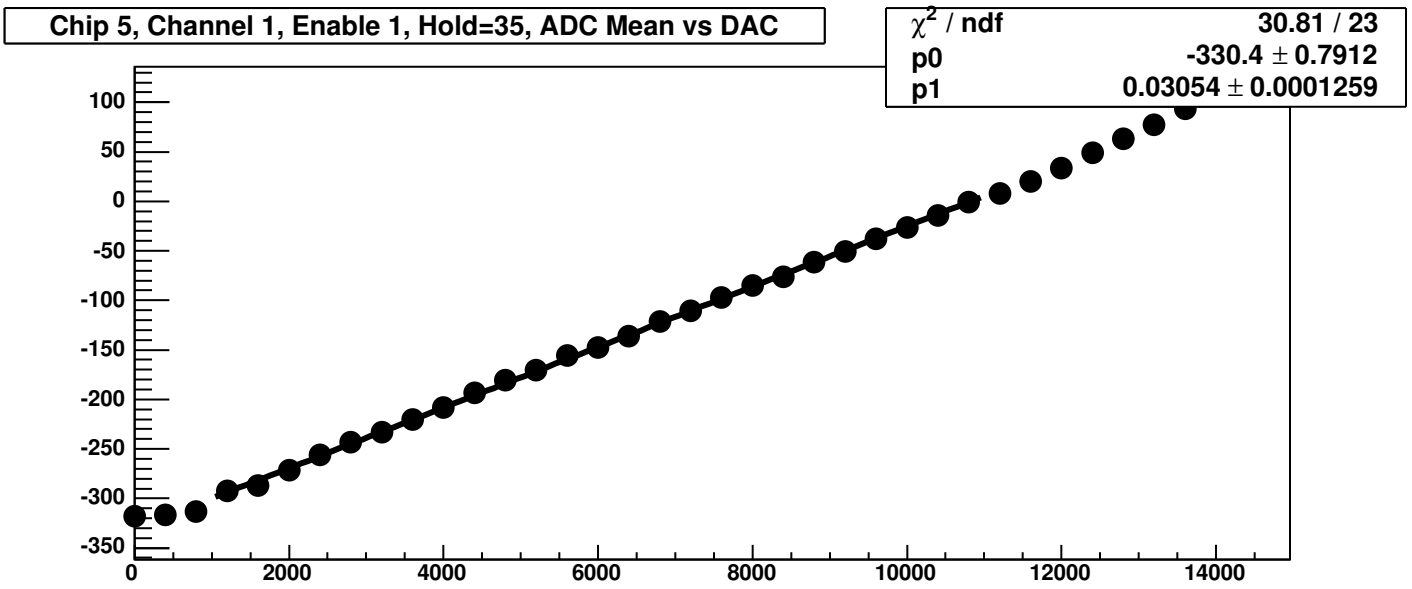

Chip 5, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

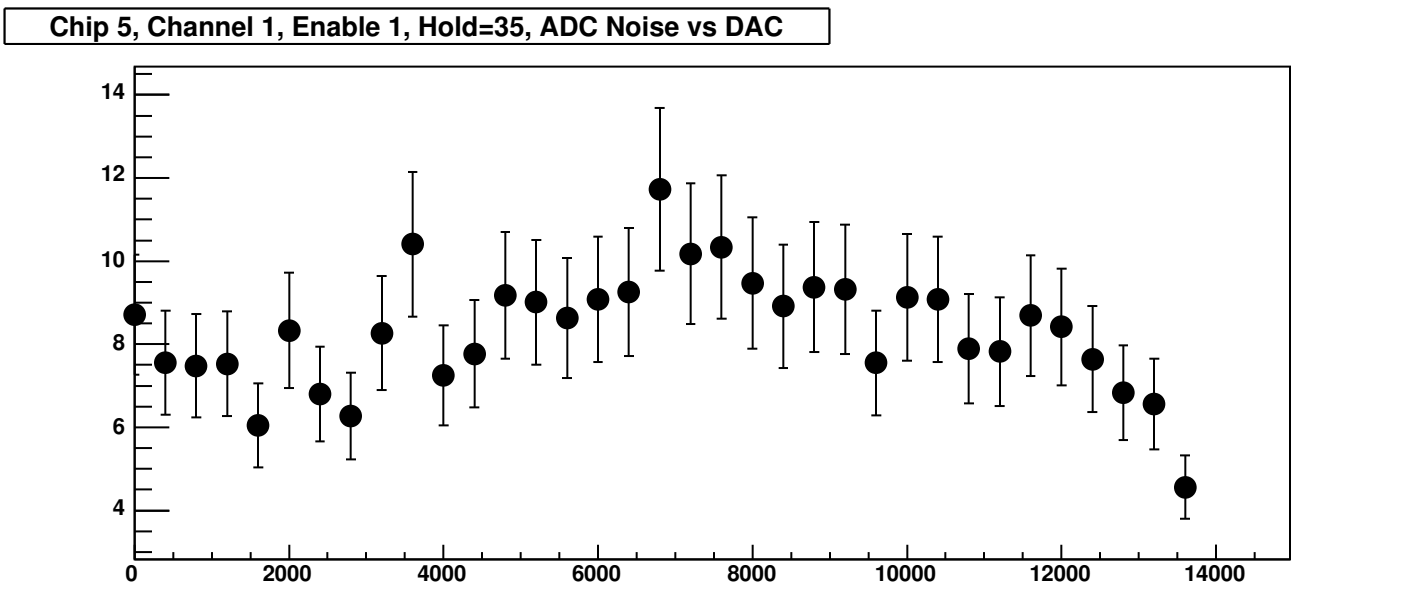

Chip 5, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

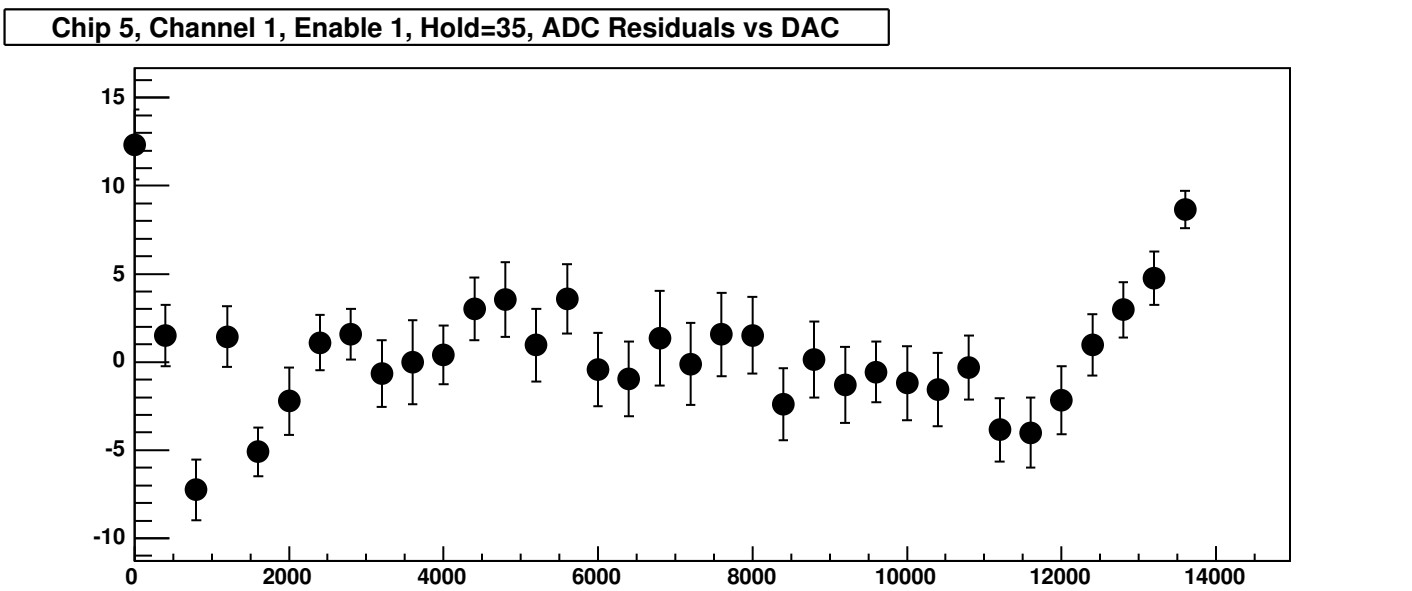

Chip 5, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1.199e+04 / 23
p0 -400.2 ± 1.152
p1 2.595 ± 0.0001807
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

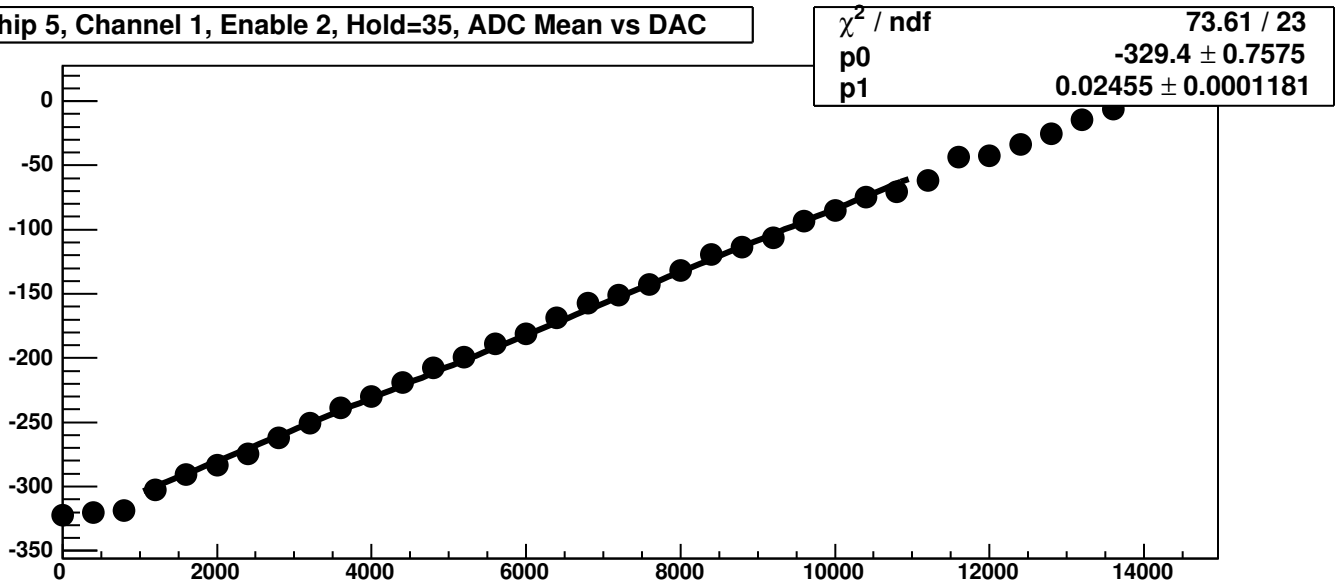

Chip 5, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 30.81 / 23
p0 -330.4 ± 0.7912
p1 0.03054 ± 0.0001259
100
50
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

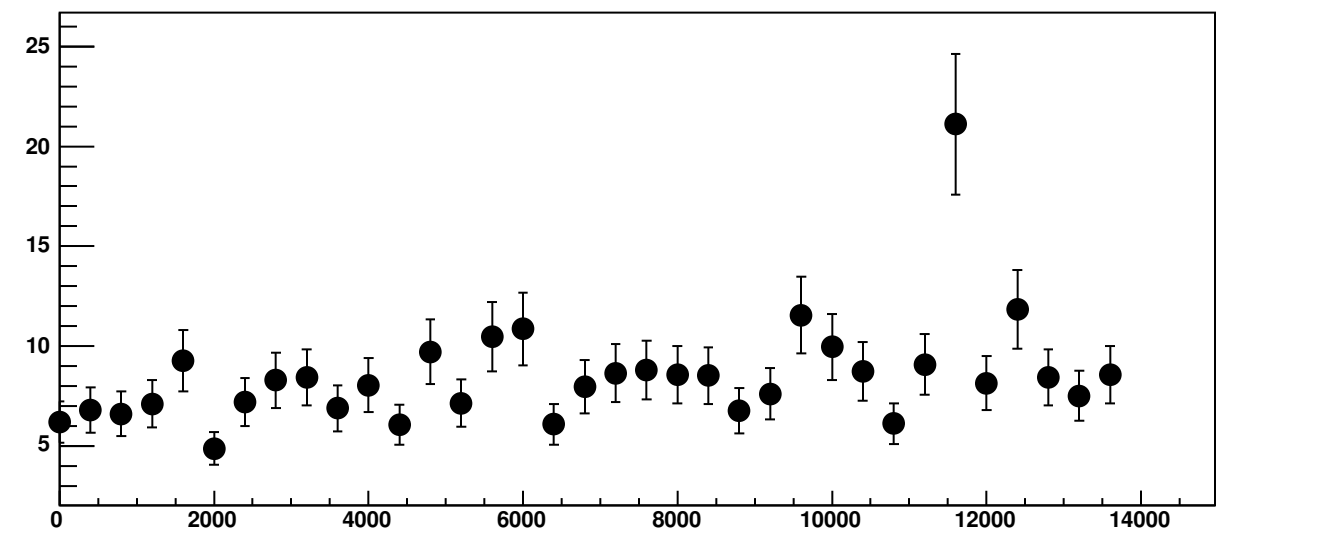

Chip 5, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

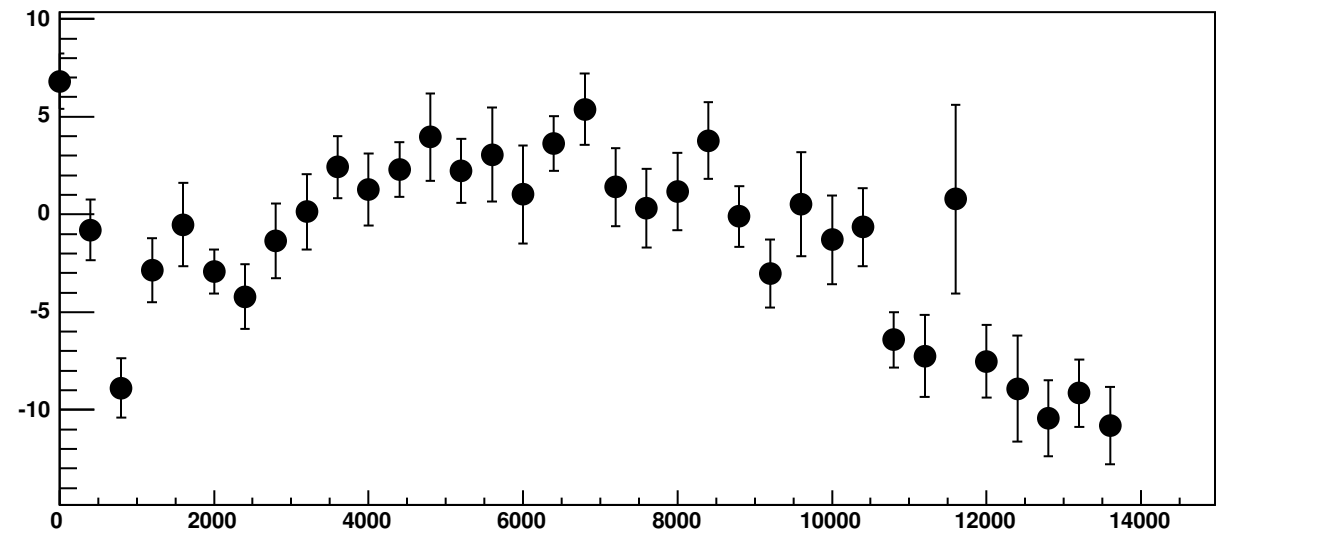
Chip 5, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC



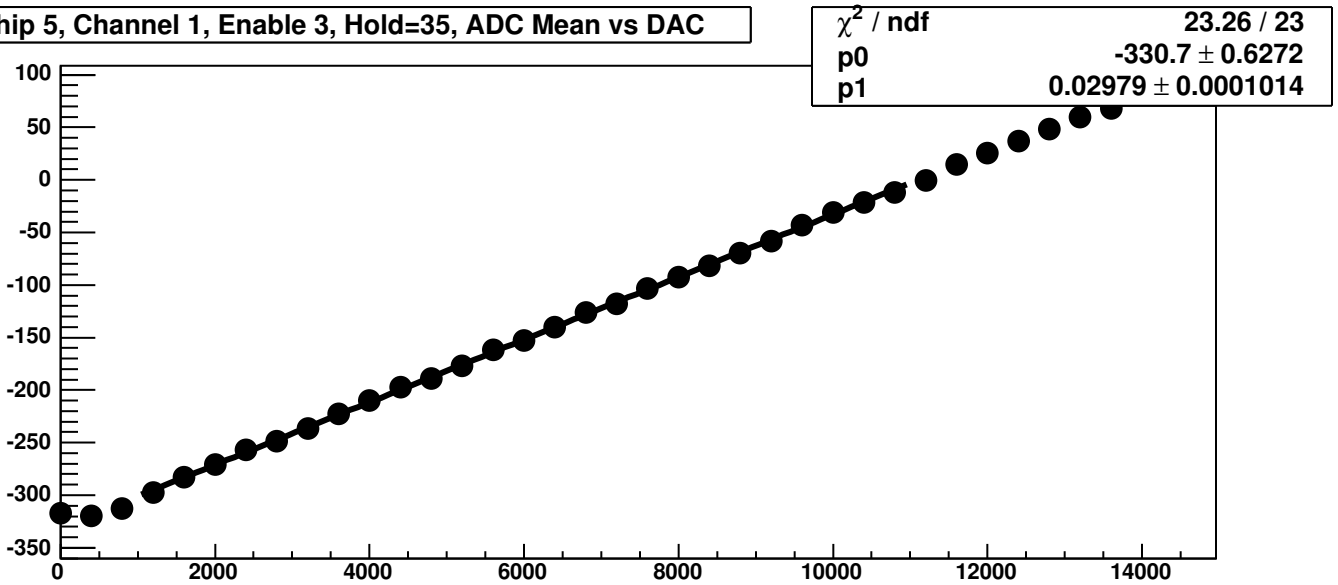
Chip 5, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC



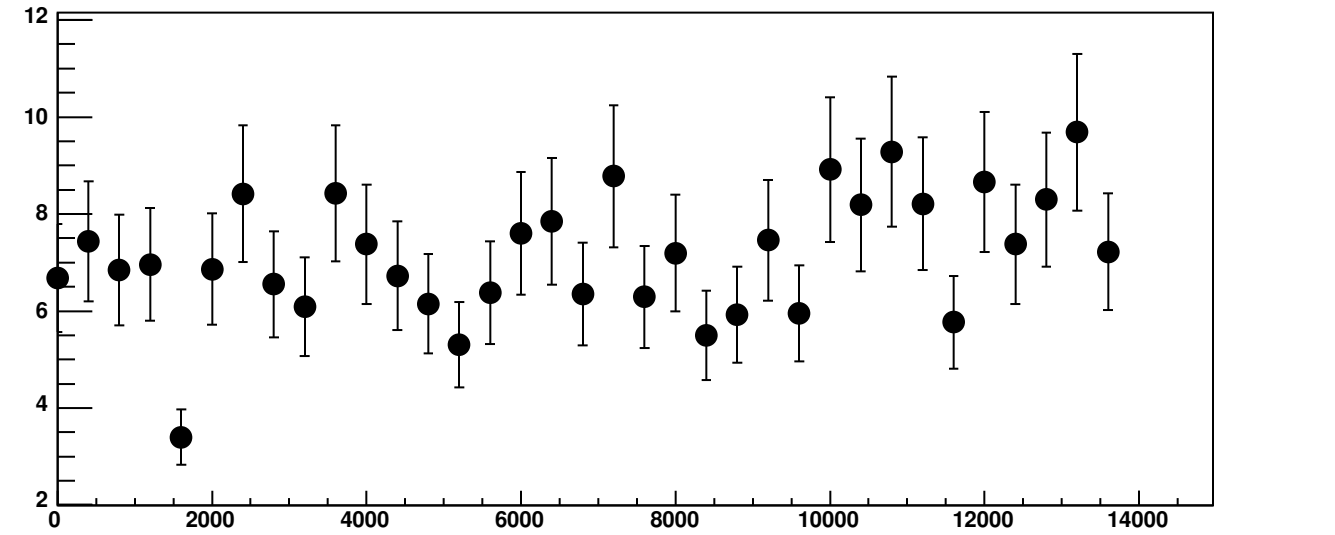
Chip 5, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC



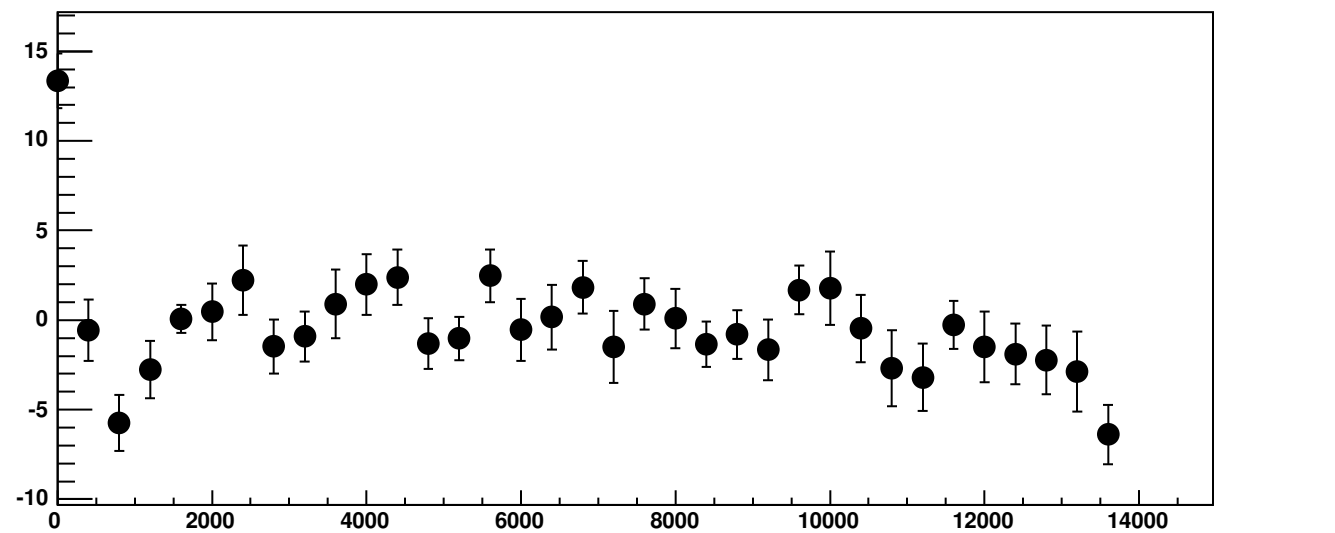
Chip 5, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC

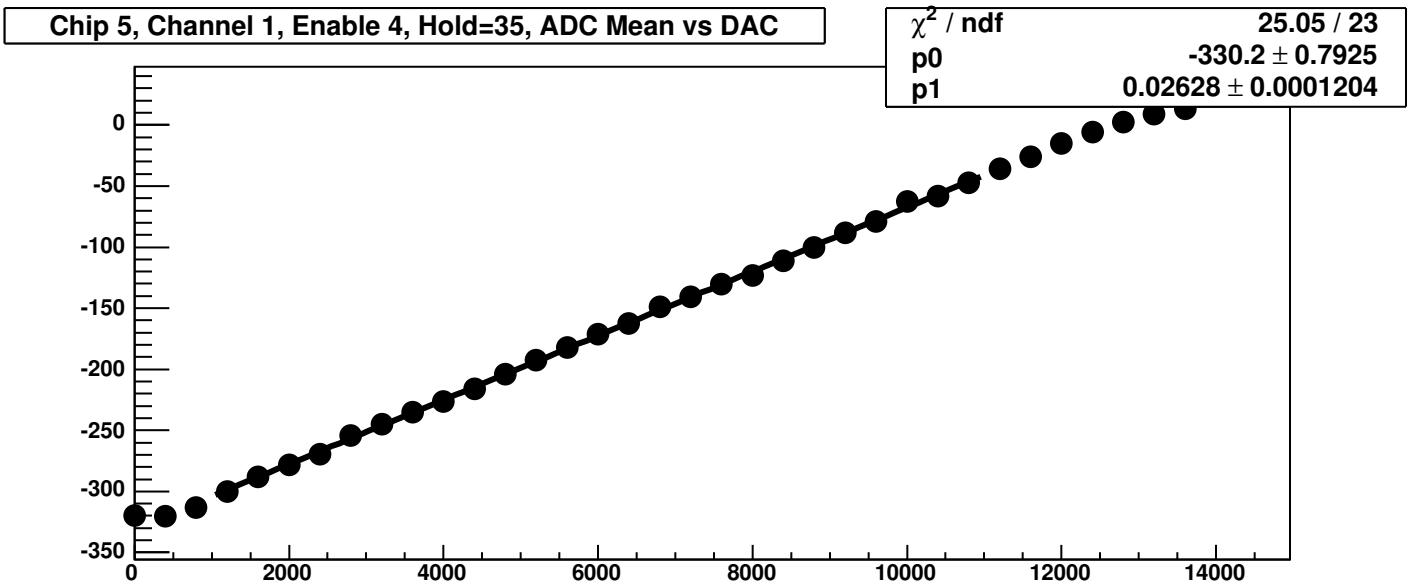


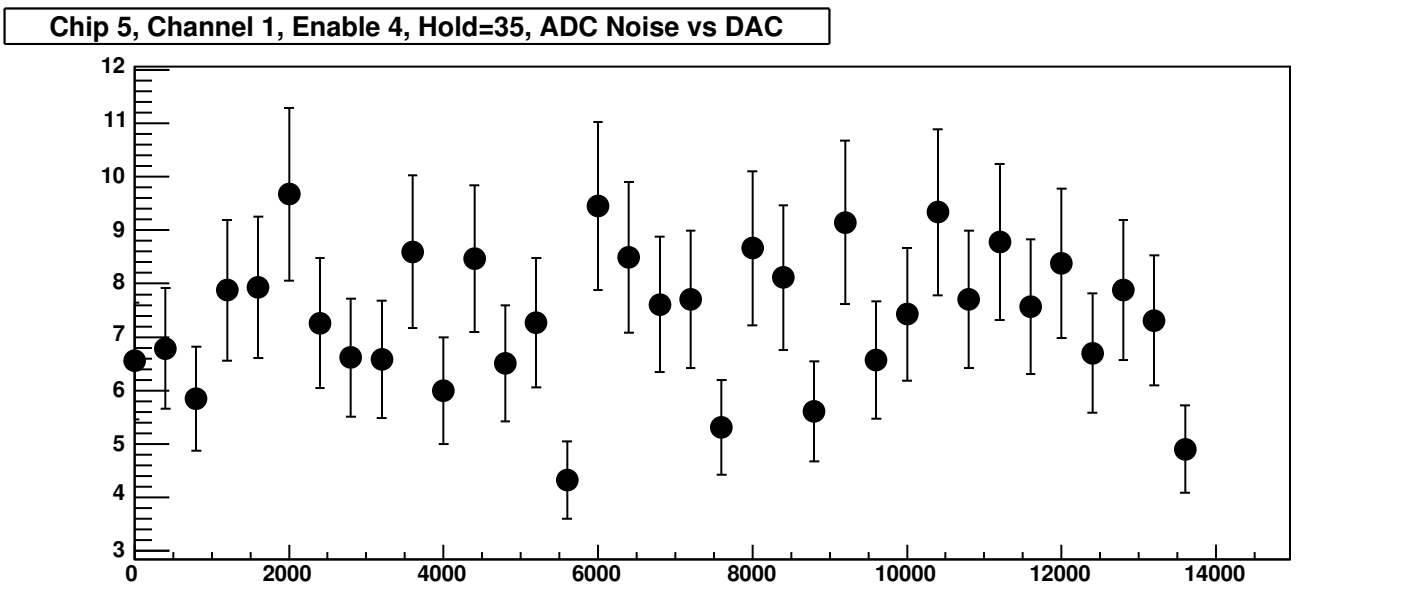
Chip 5, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC

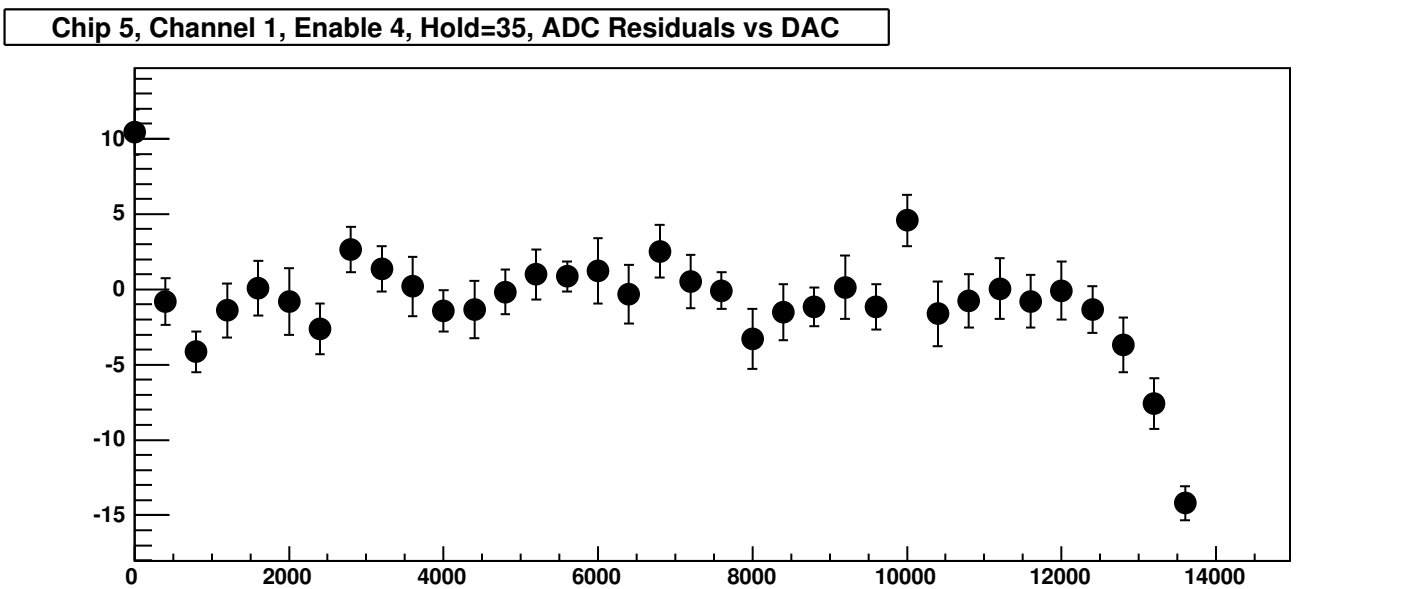


Chip 5, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC

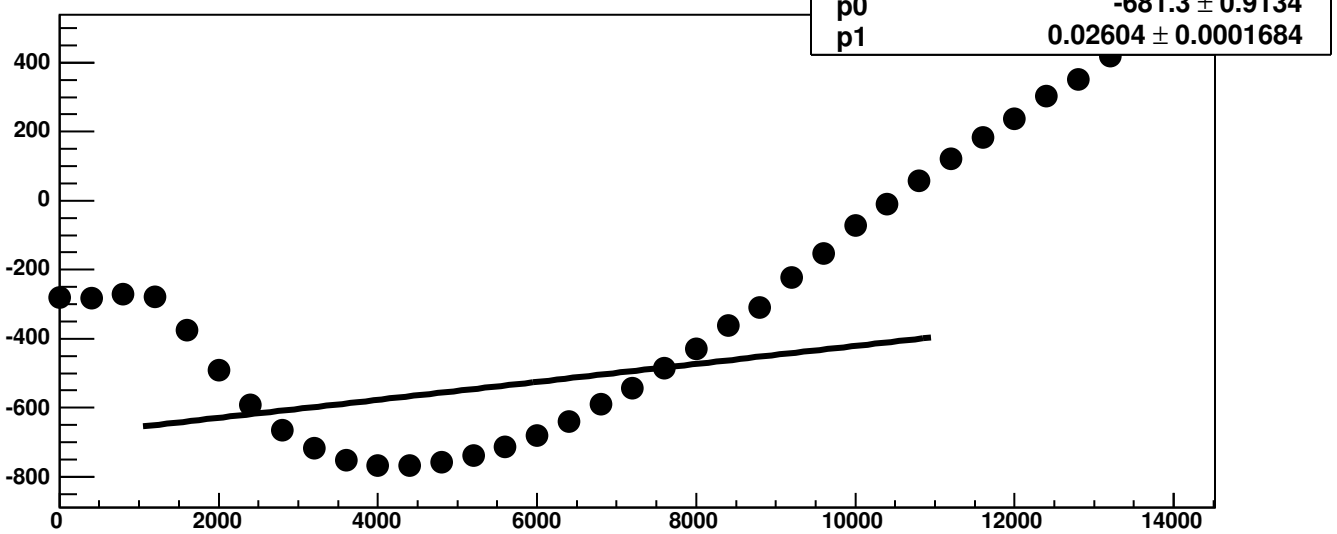



Chip 5, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 25.05 / 23
p0 -330.2 ± 0.7925
p1 0.02628 ± 0.0001204
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

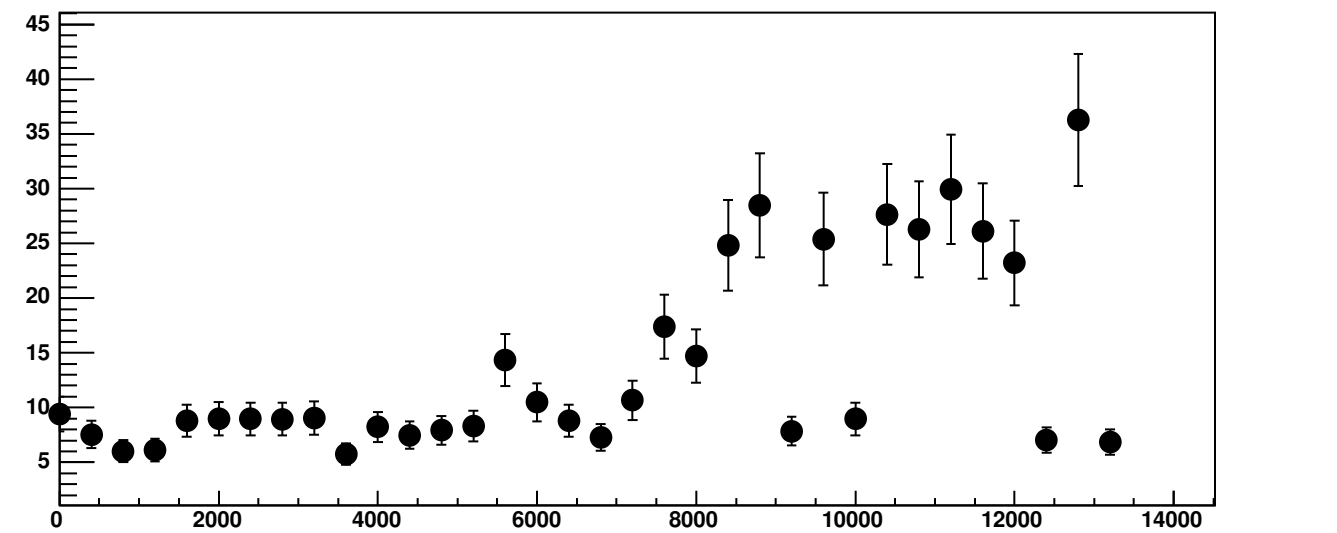

Chip 5, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

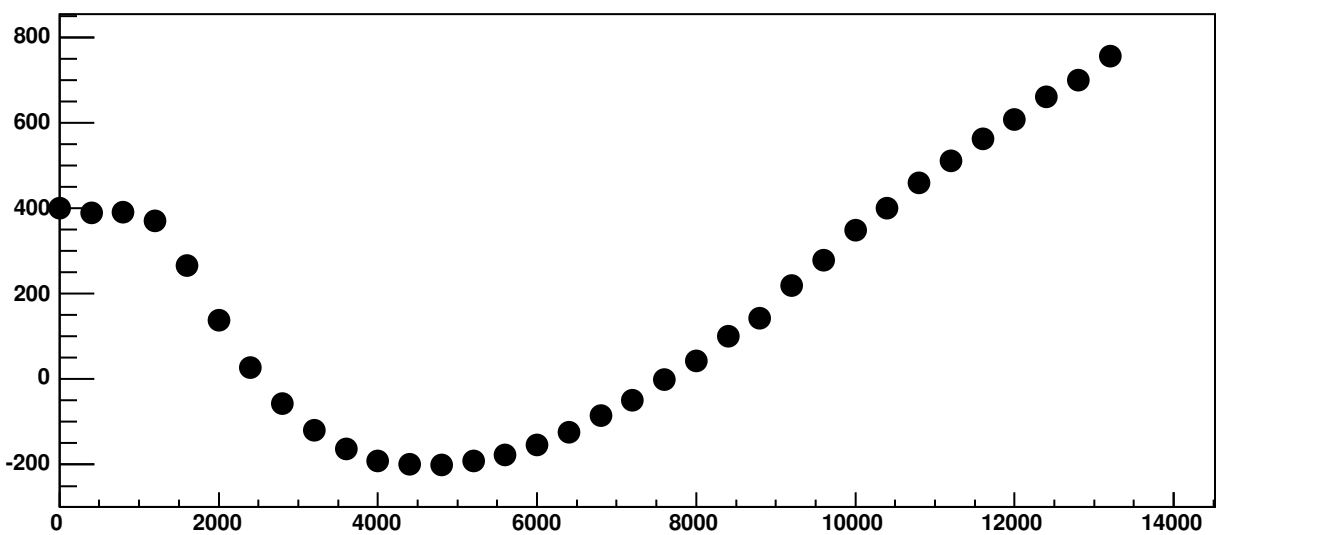
Chip 5, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

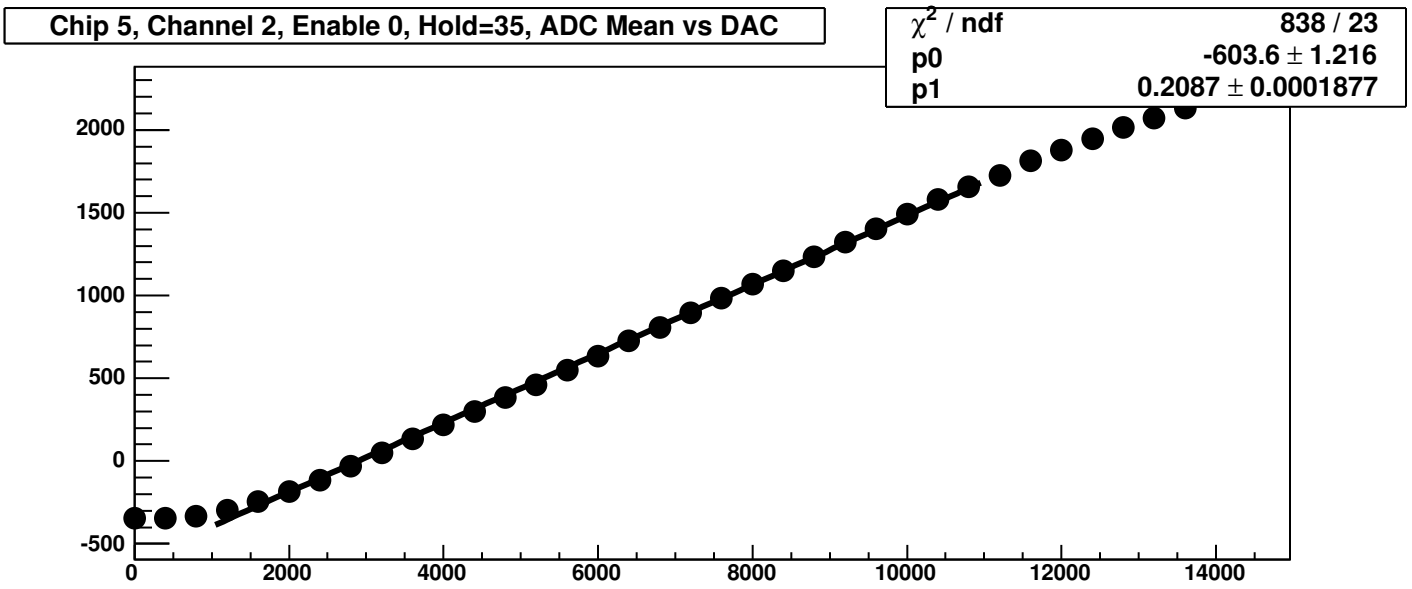


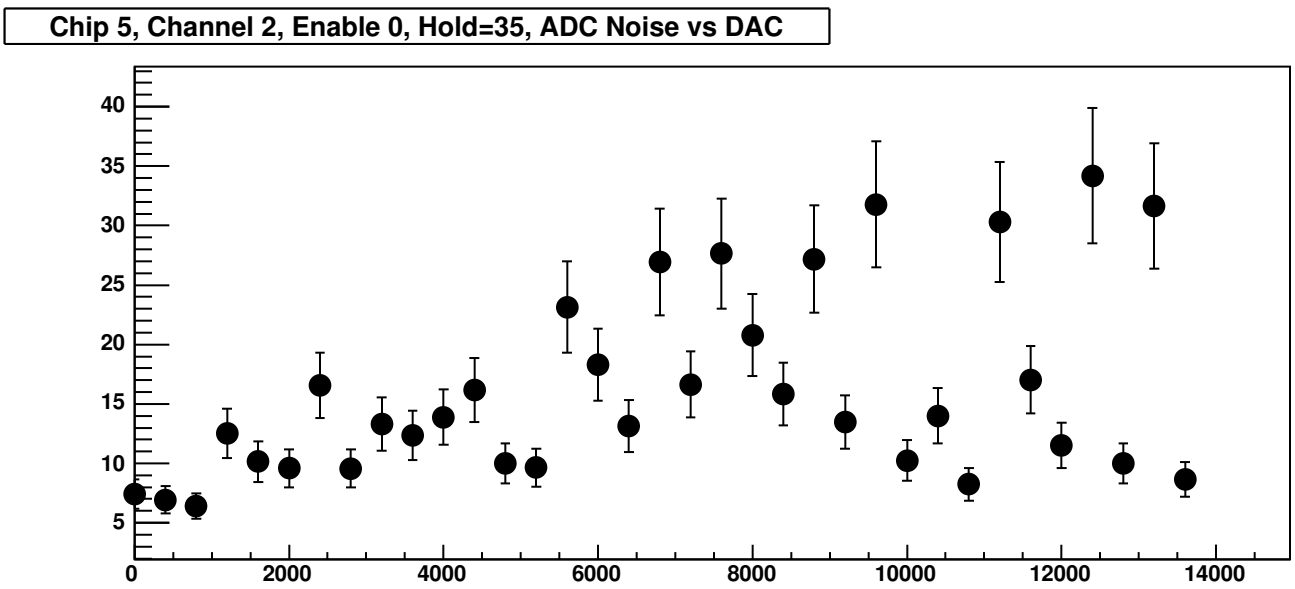
Chip 5, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC

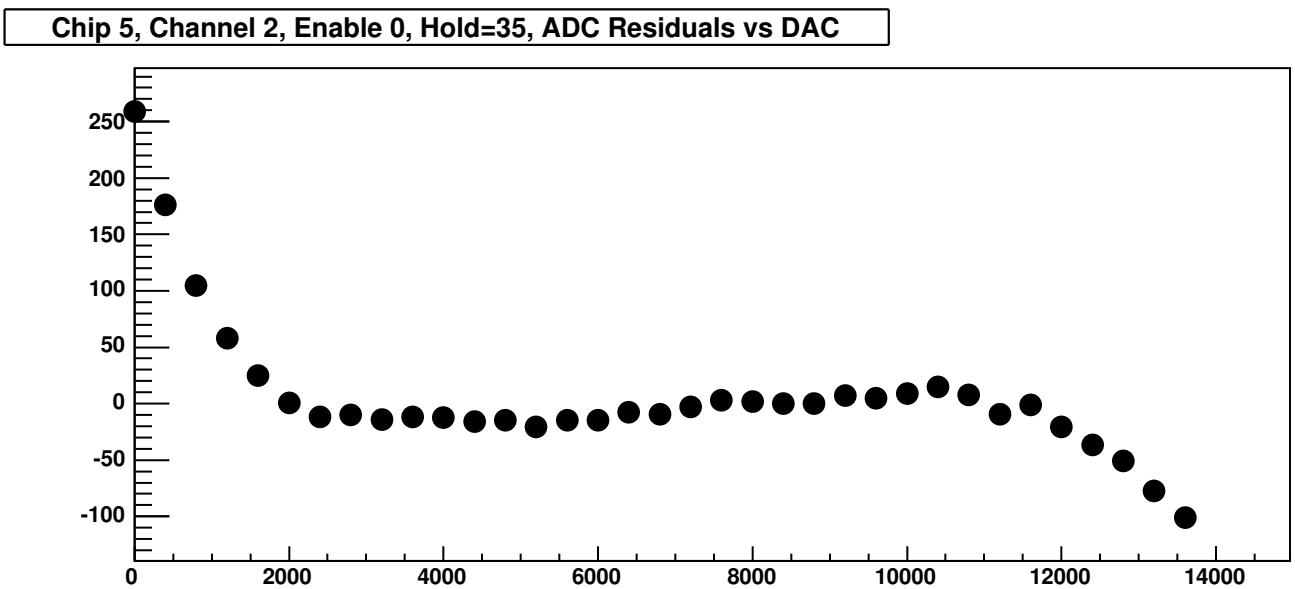


Chip 5, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

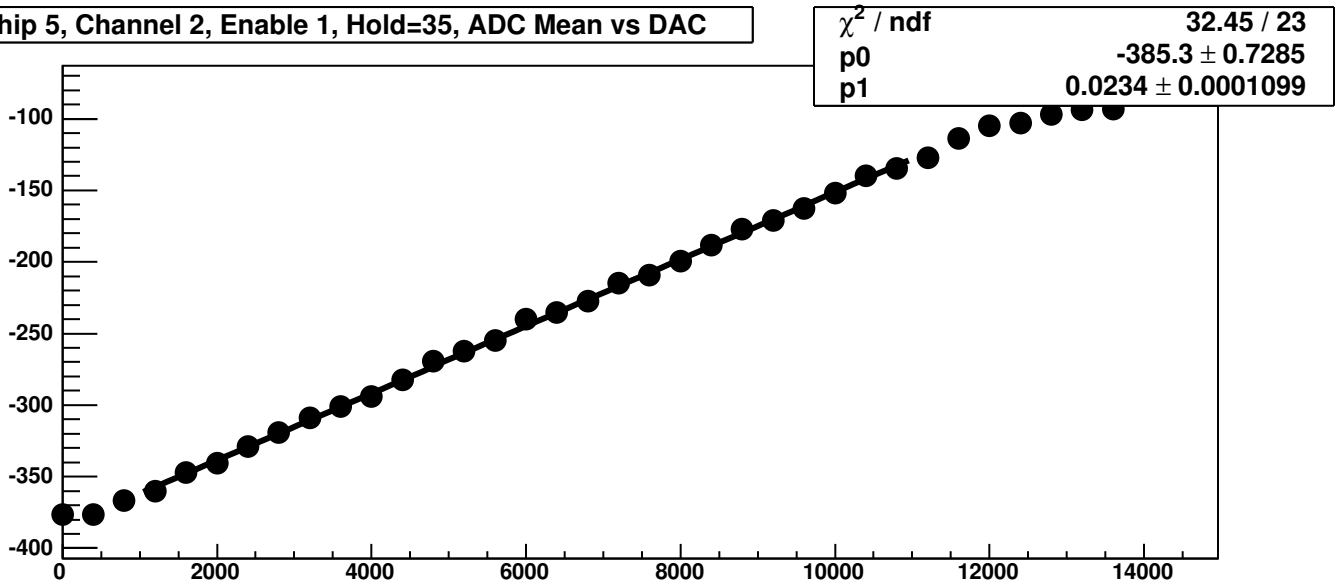



Chip 5, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 838 / 23
p0 -603.6 ± 1.216
p1 0.2087 ± 0.0001877
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

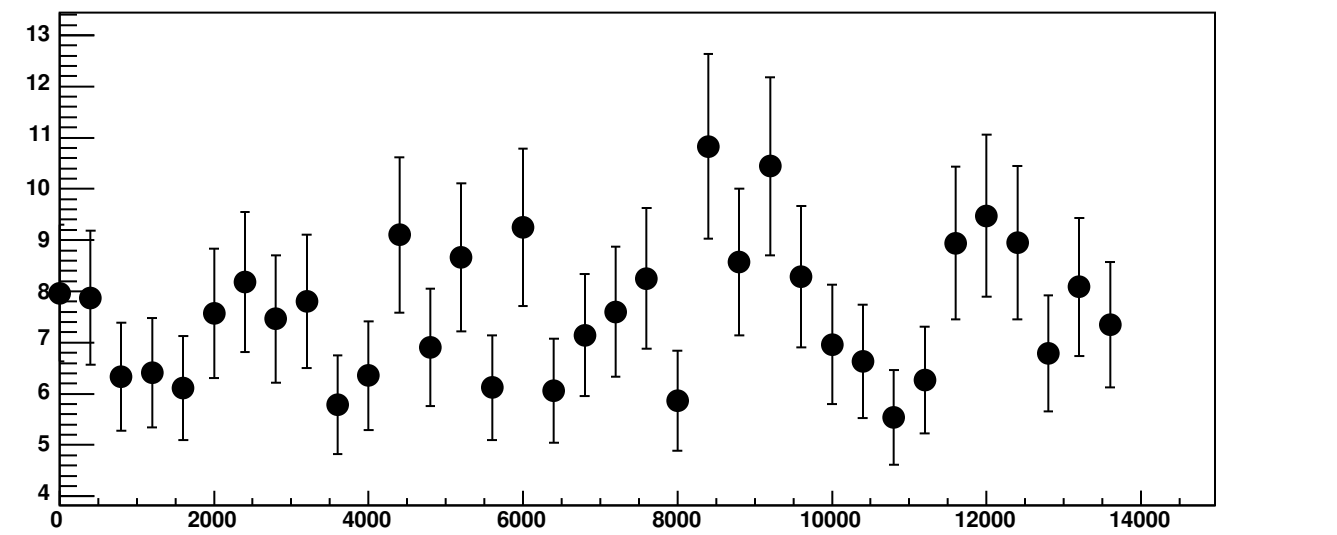

Chip 5, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

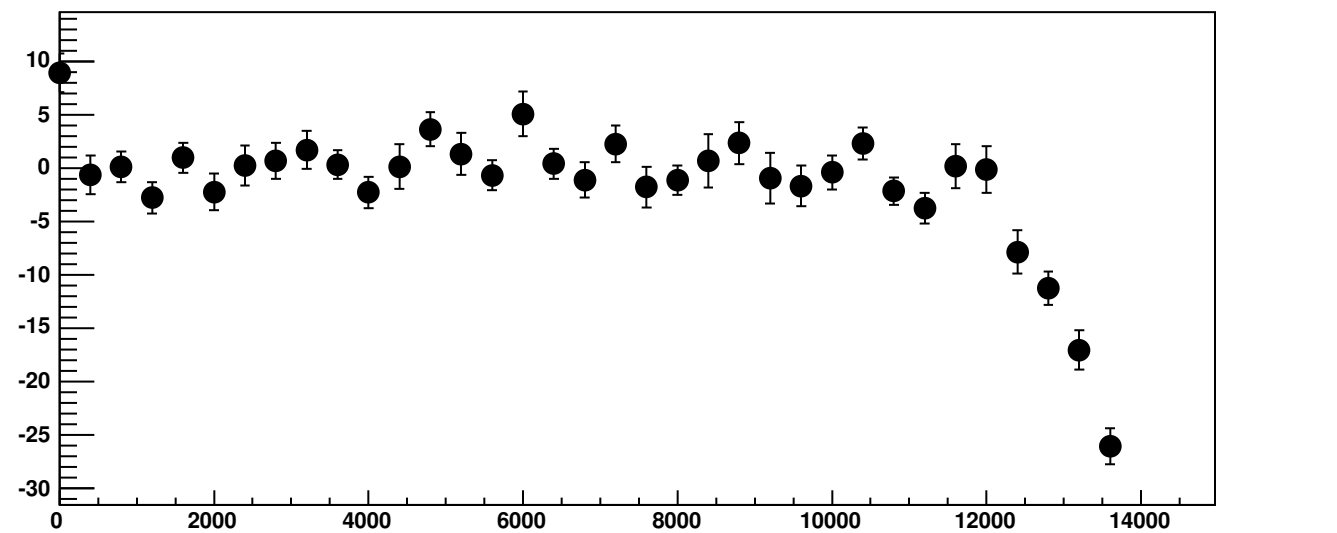
Chip 5, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

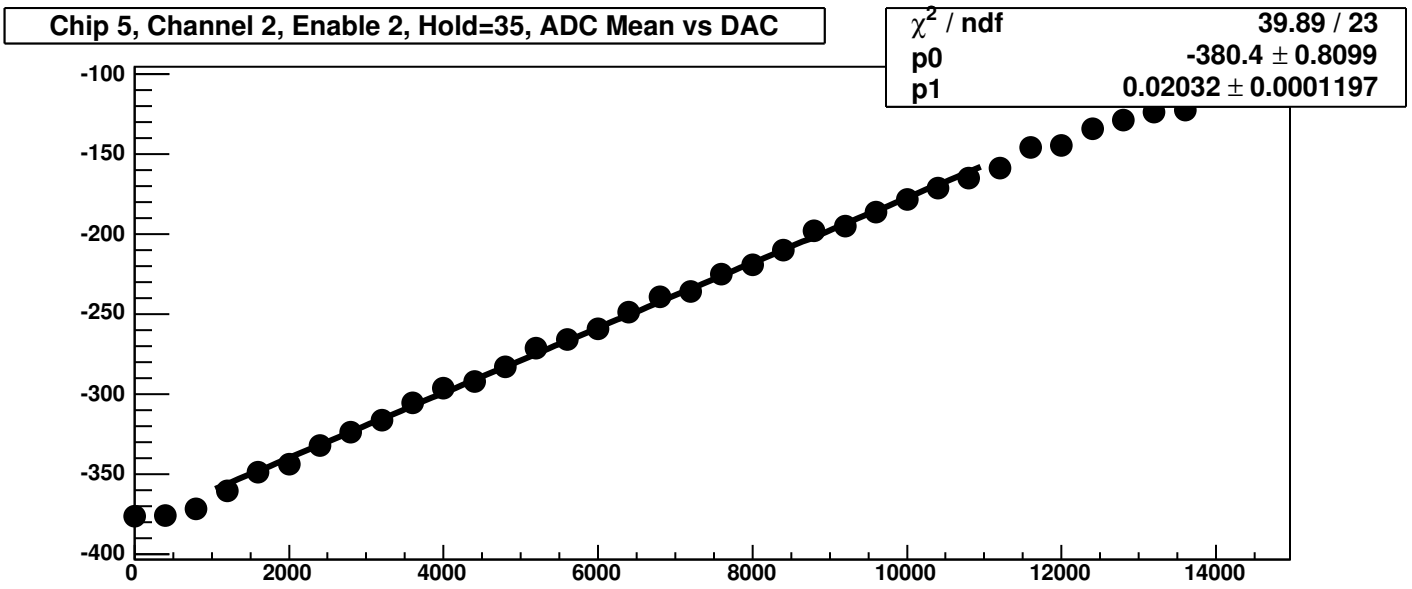


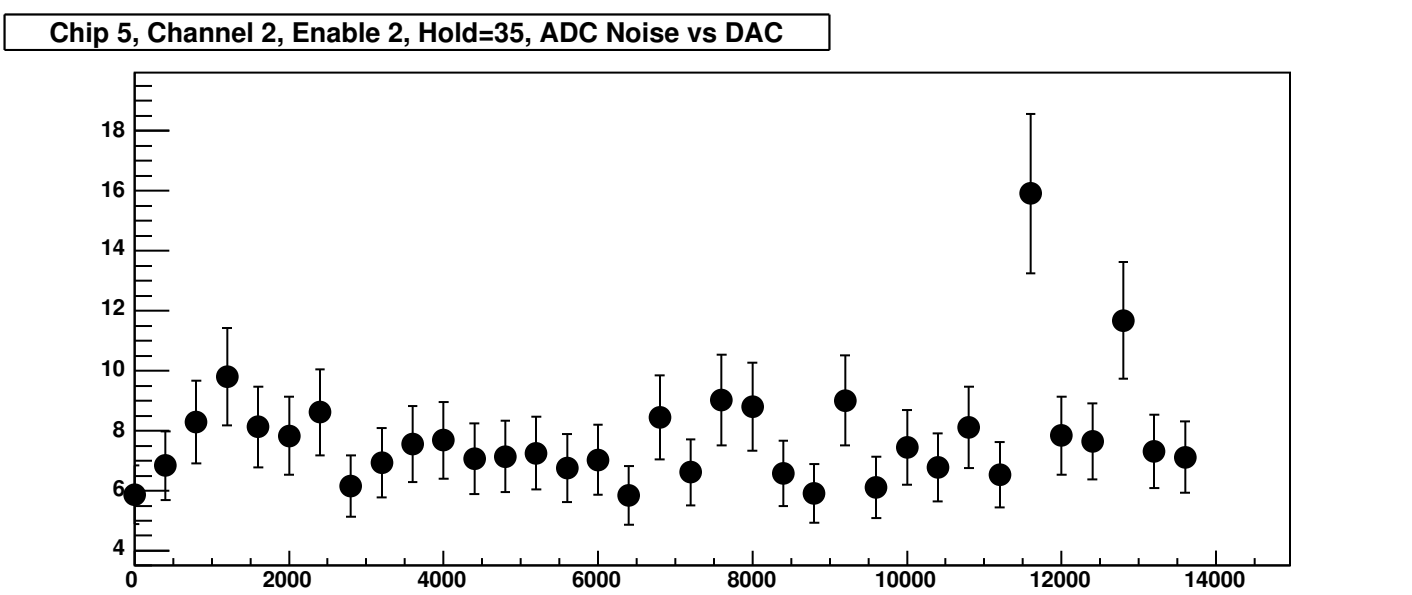
Chip 5, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC

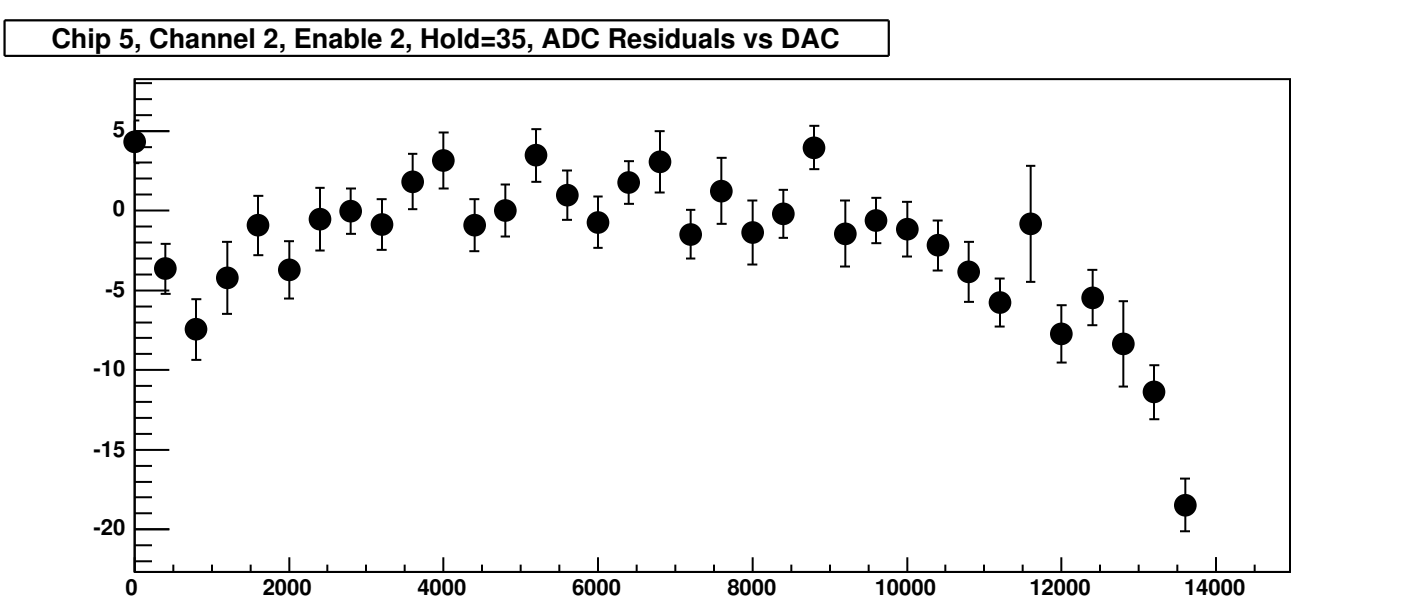


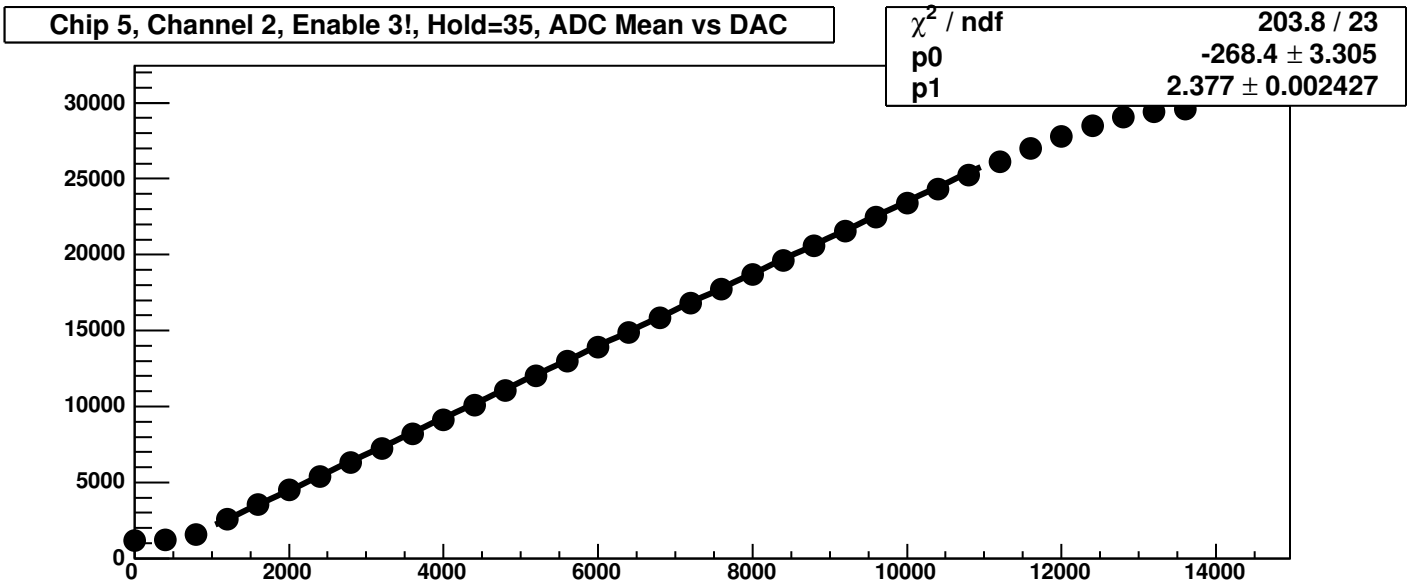
Chip 5, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

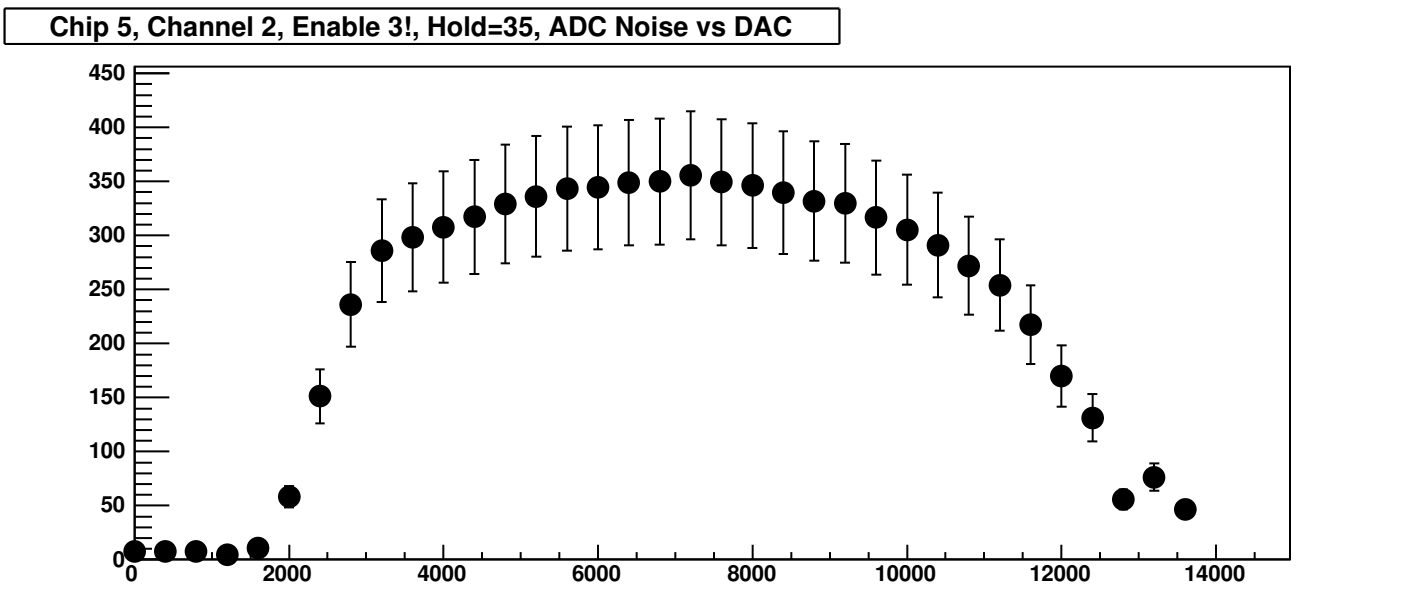


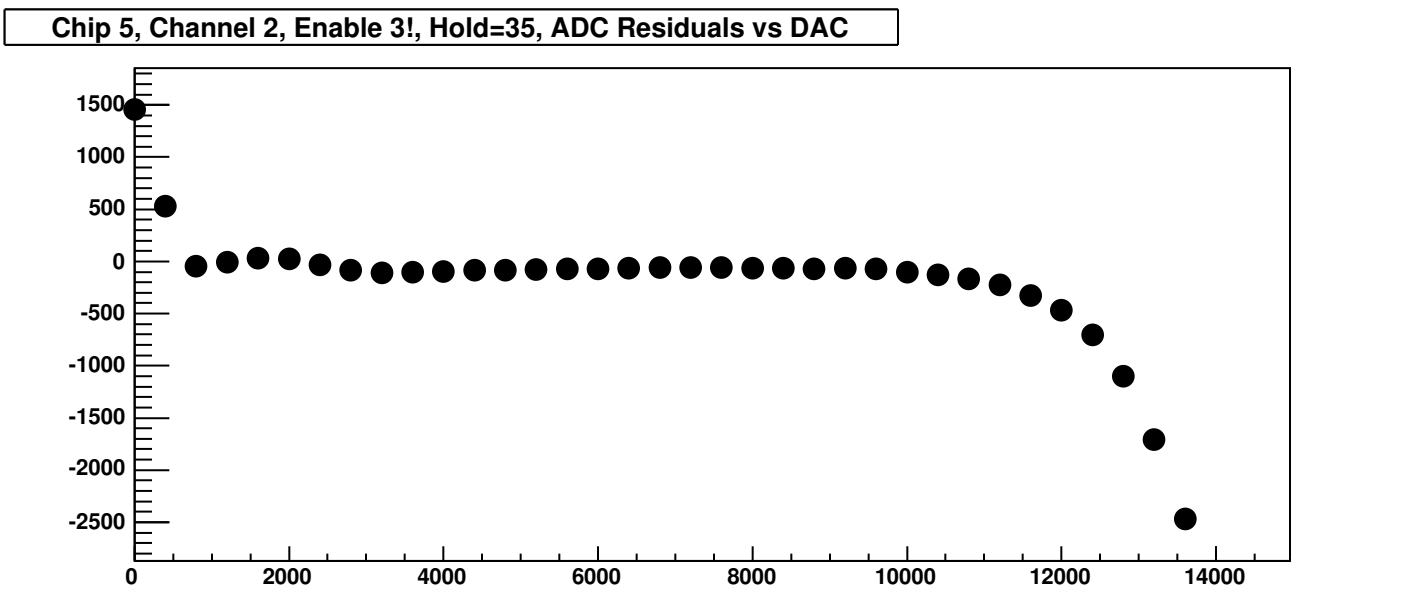

Chip 5, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 39.89 / 23
p0 -380.4 ± 0.8099
p1 0.02032 ± 0.0001197
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

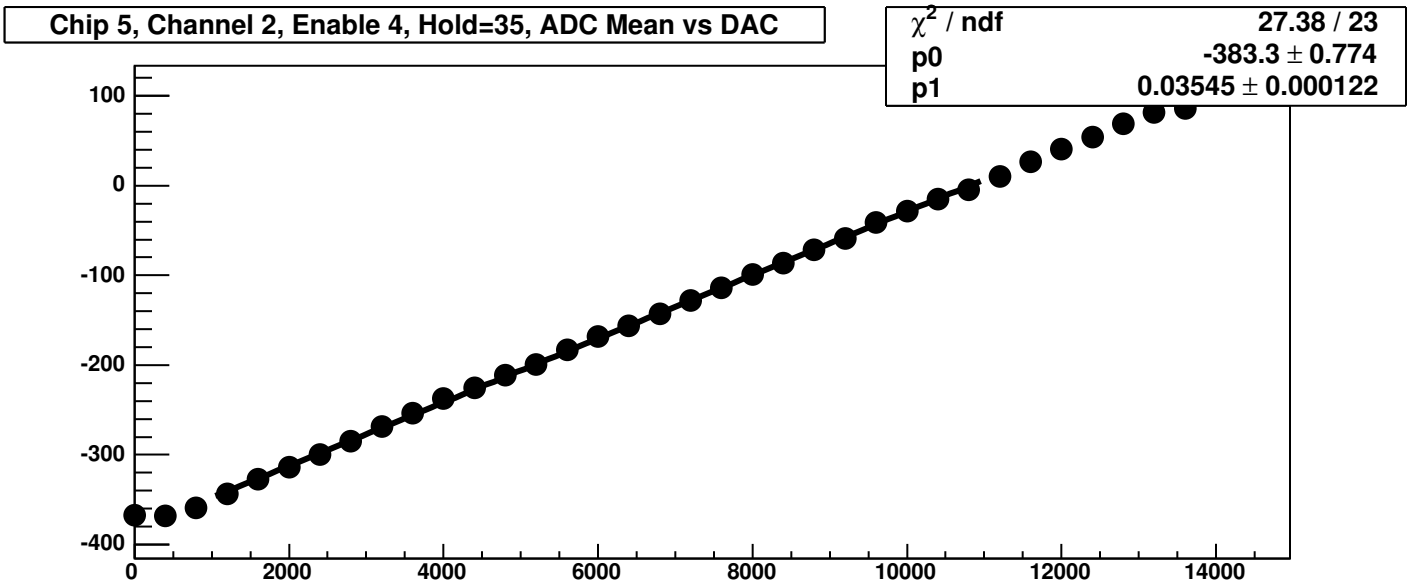

Chip 5, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

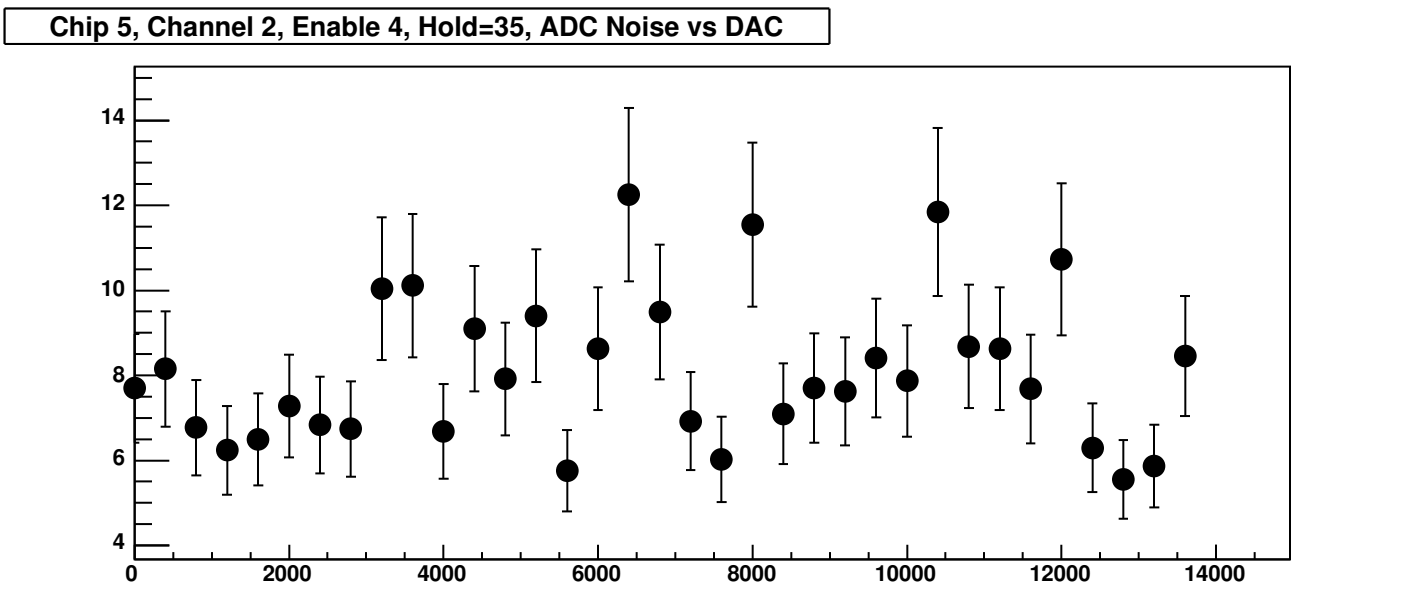

Chip 5, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

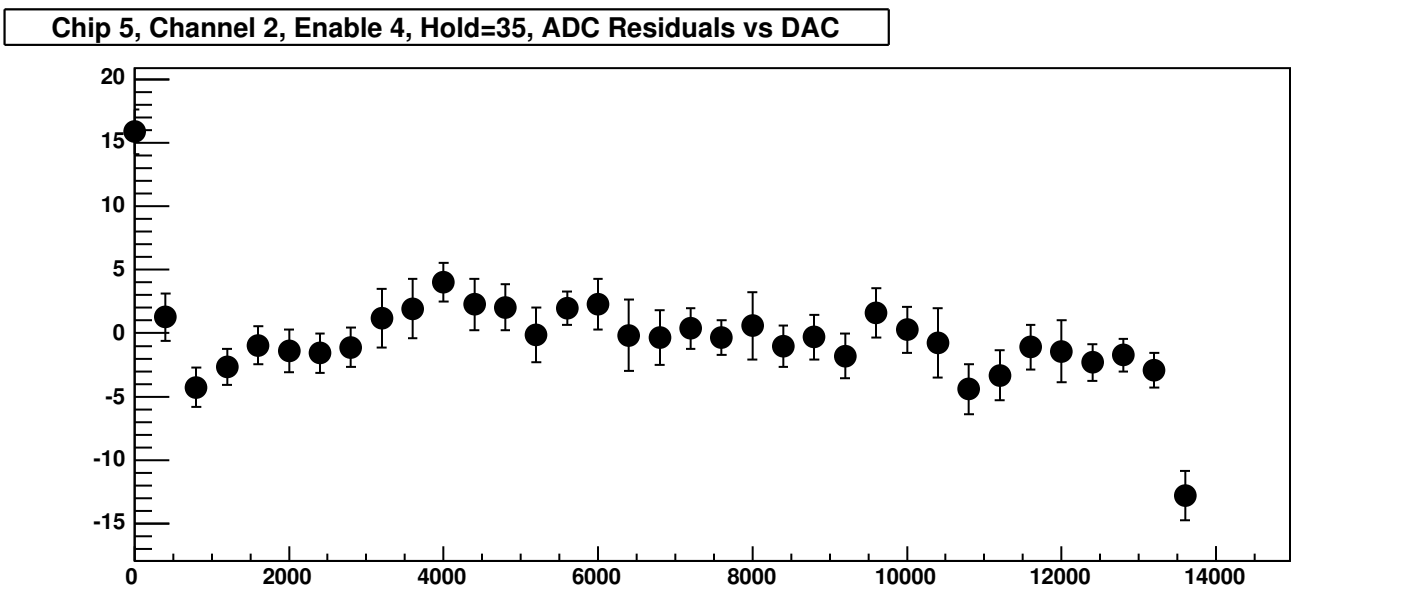

Chip 5, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 203.8 / 23
p0 -268.4 ± 3.305
p1 2.377 ± 0.002427
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

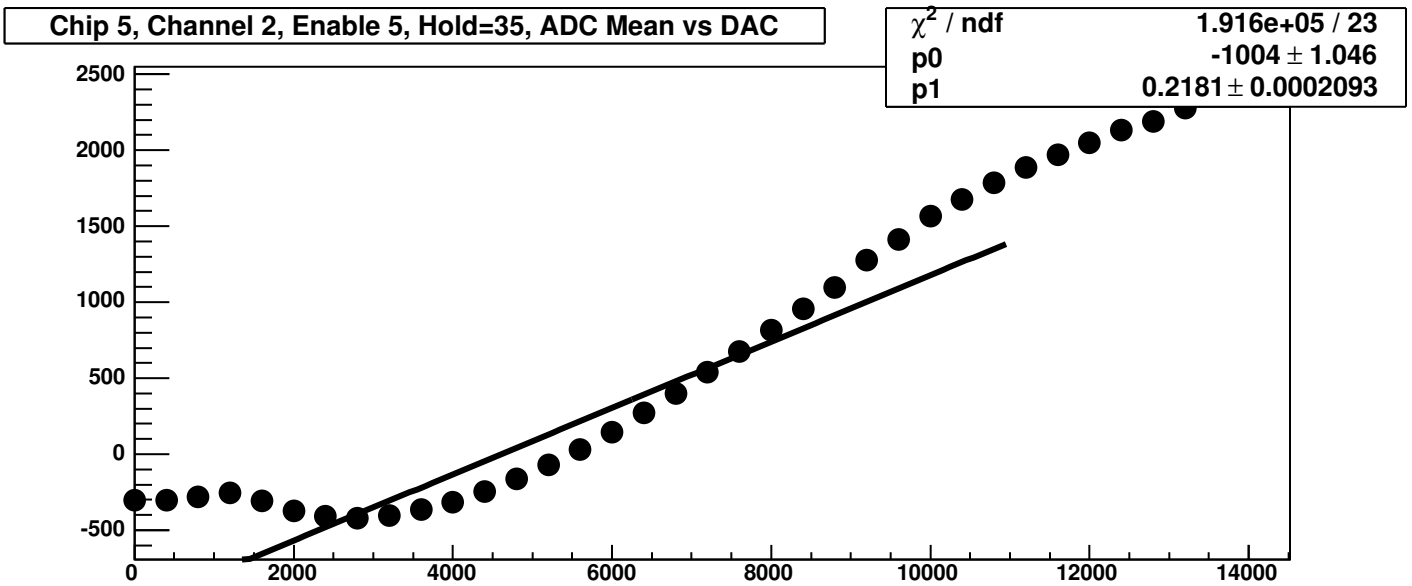

Chip 5, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

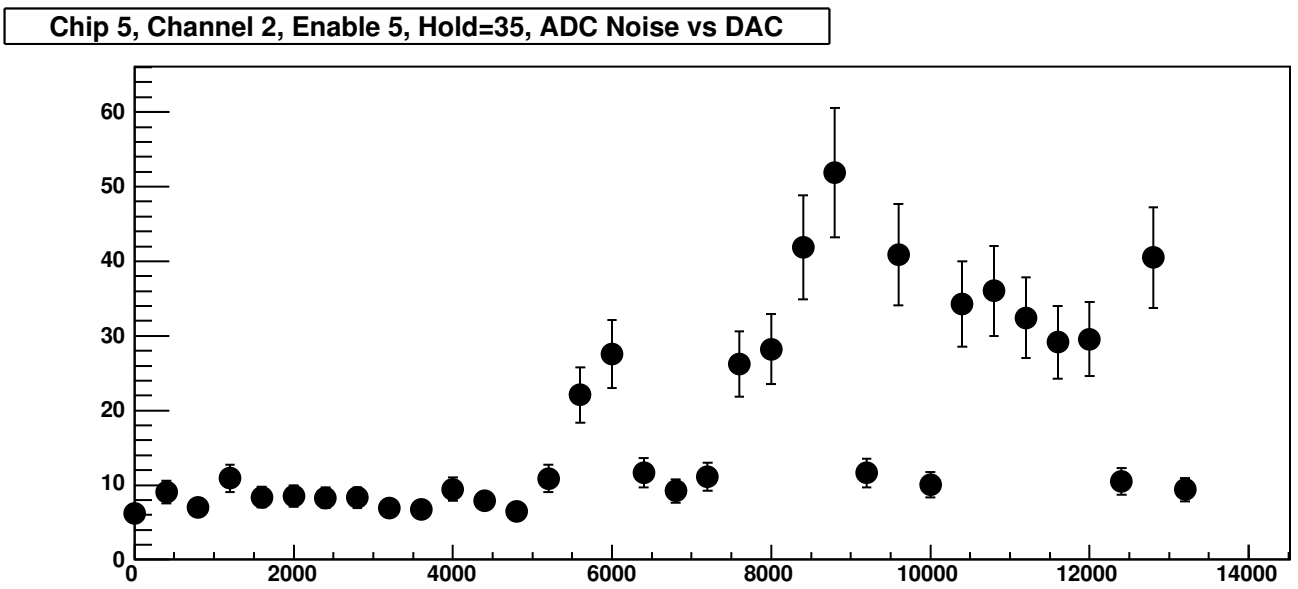

Chip 5, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

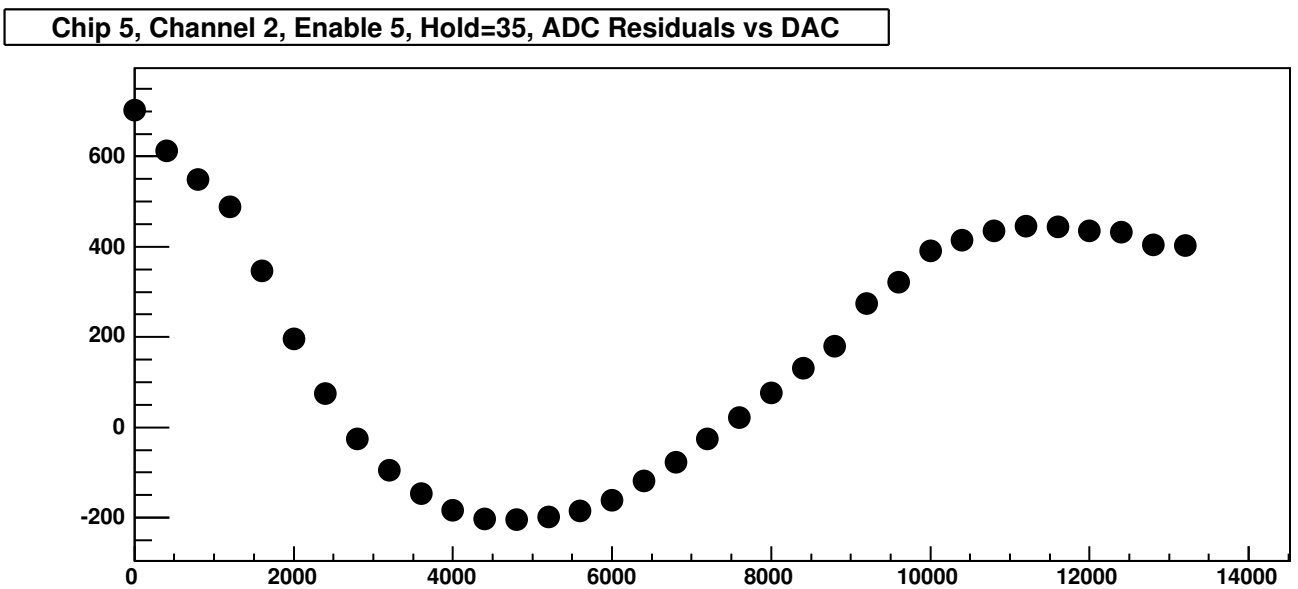

Chip 5, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.38 / 23
p0 -383.3 ± 0.774
p1 0.03545 ± 0.000122
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

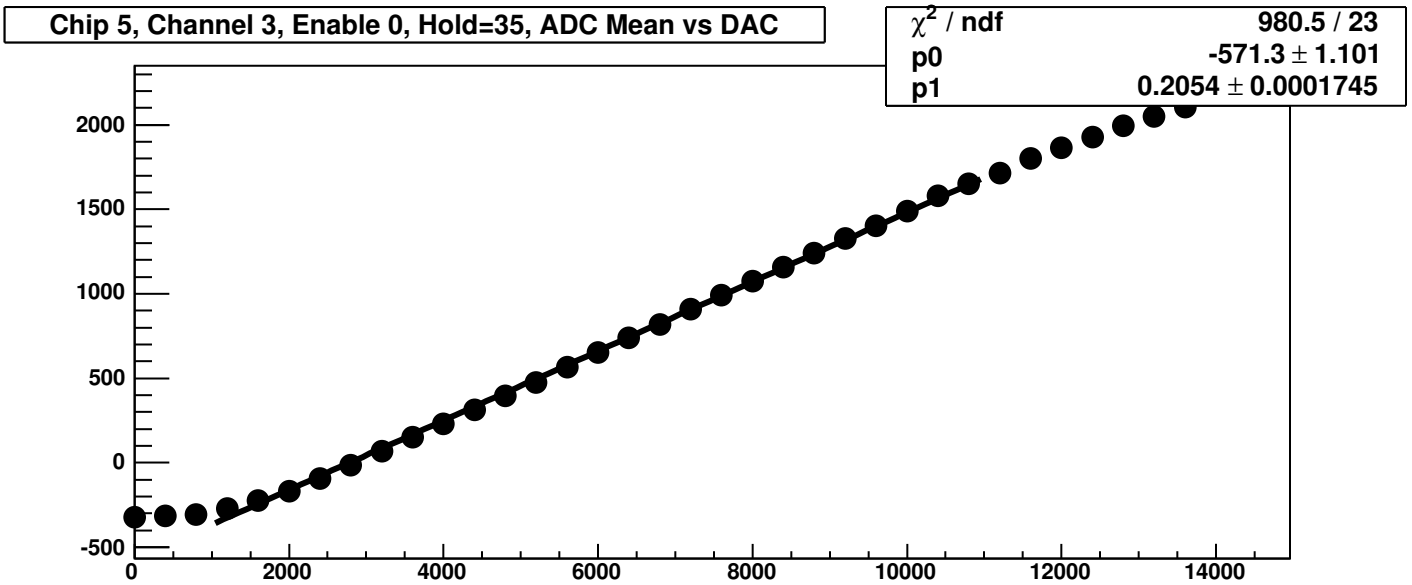

Chip 5, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

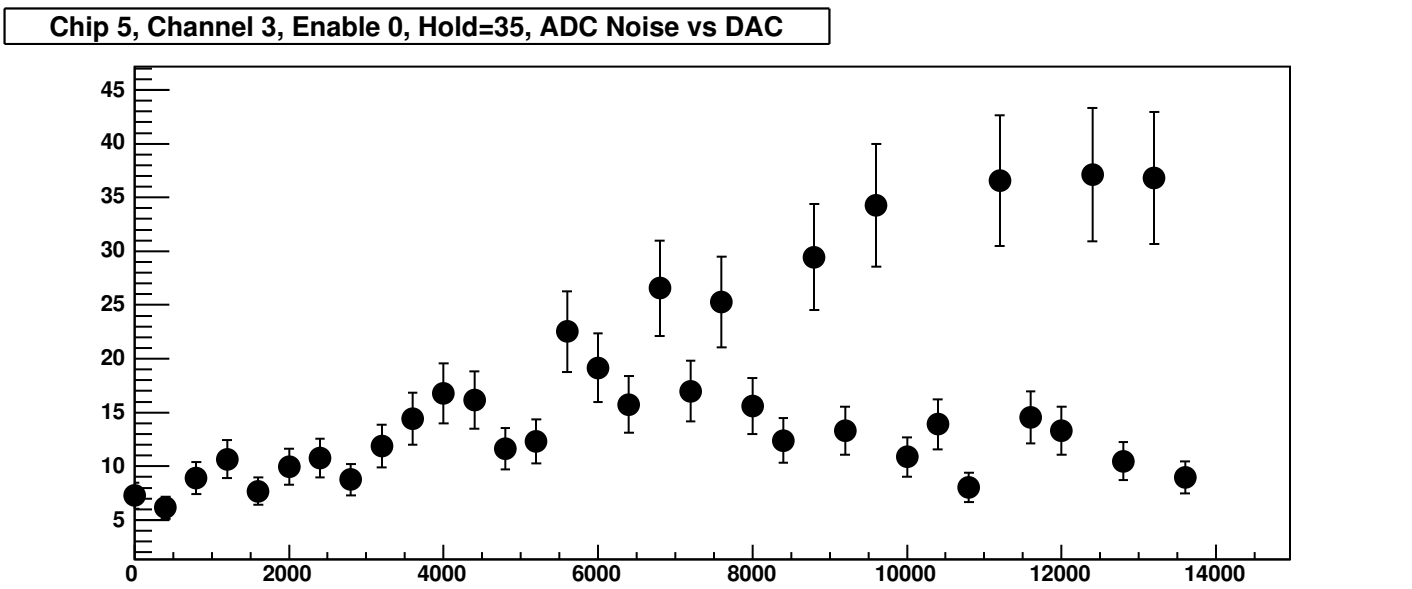

Chip 5, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

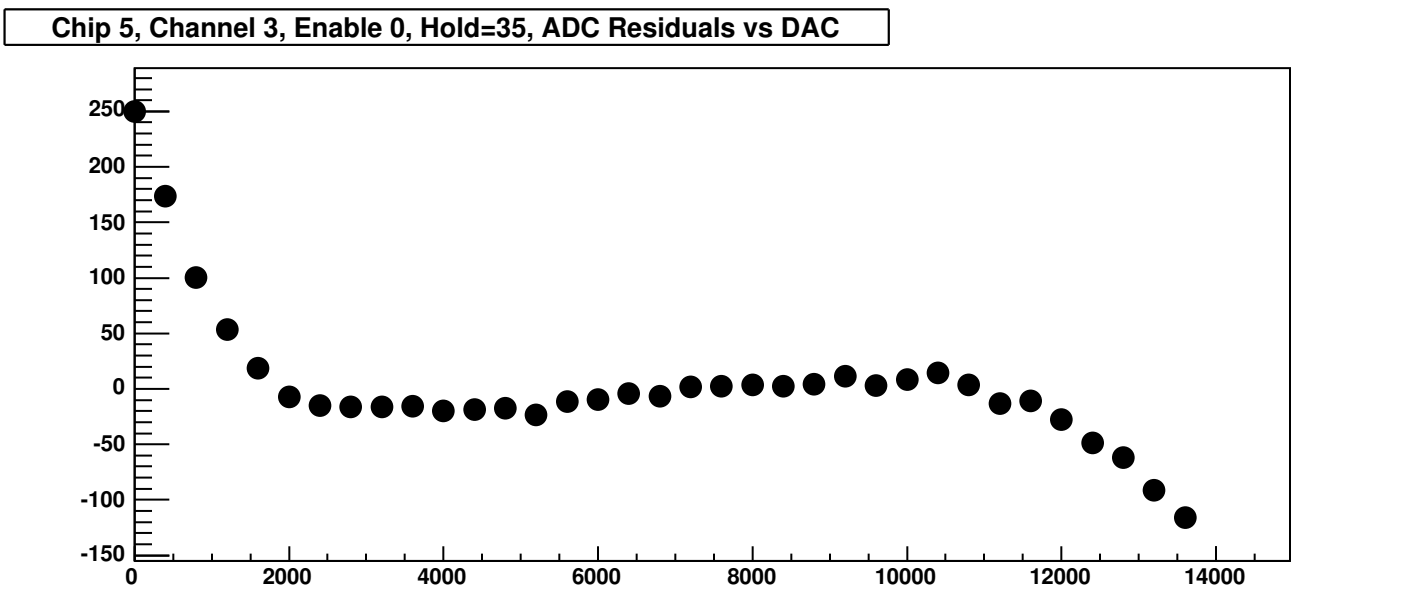

Chip 5, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.916e+05 / 23
p0 -1004 ± 1.046
p1 0.2181 ± 0.0002093
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

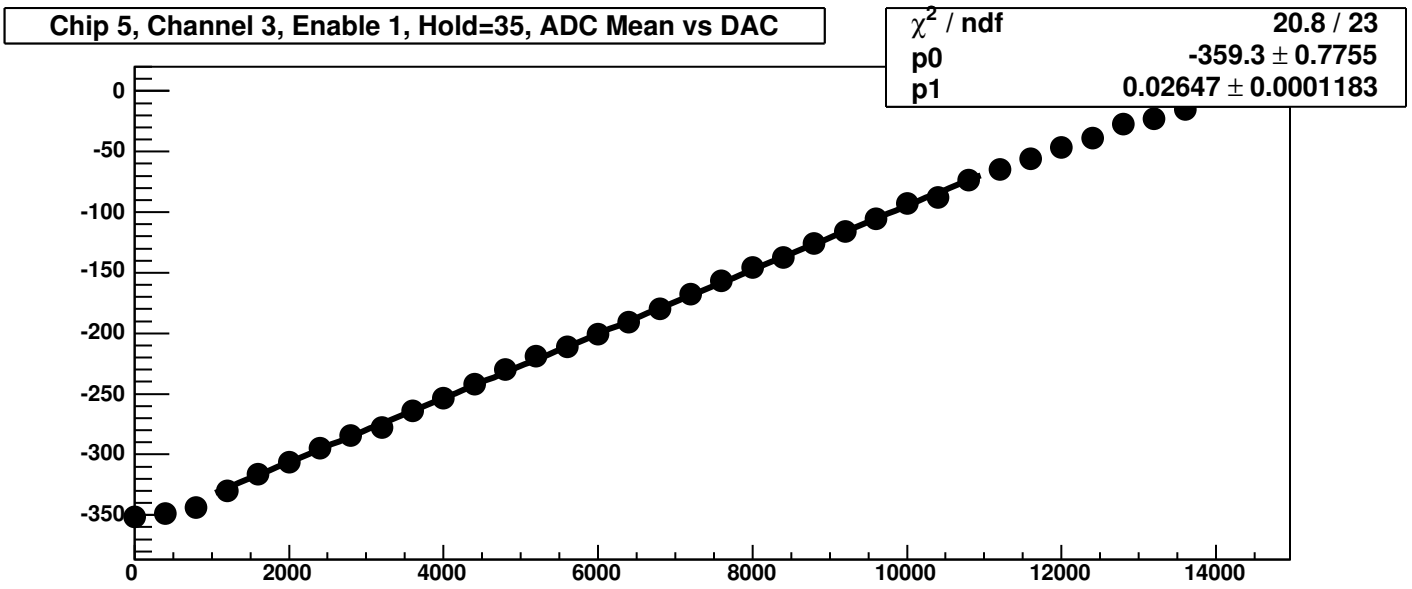

Chip 5, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

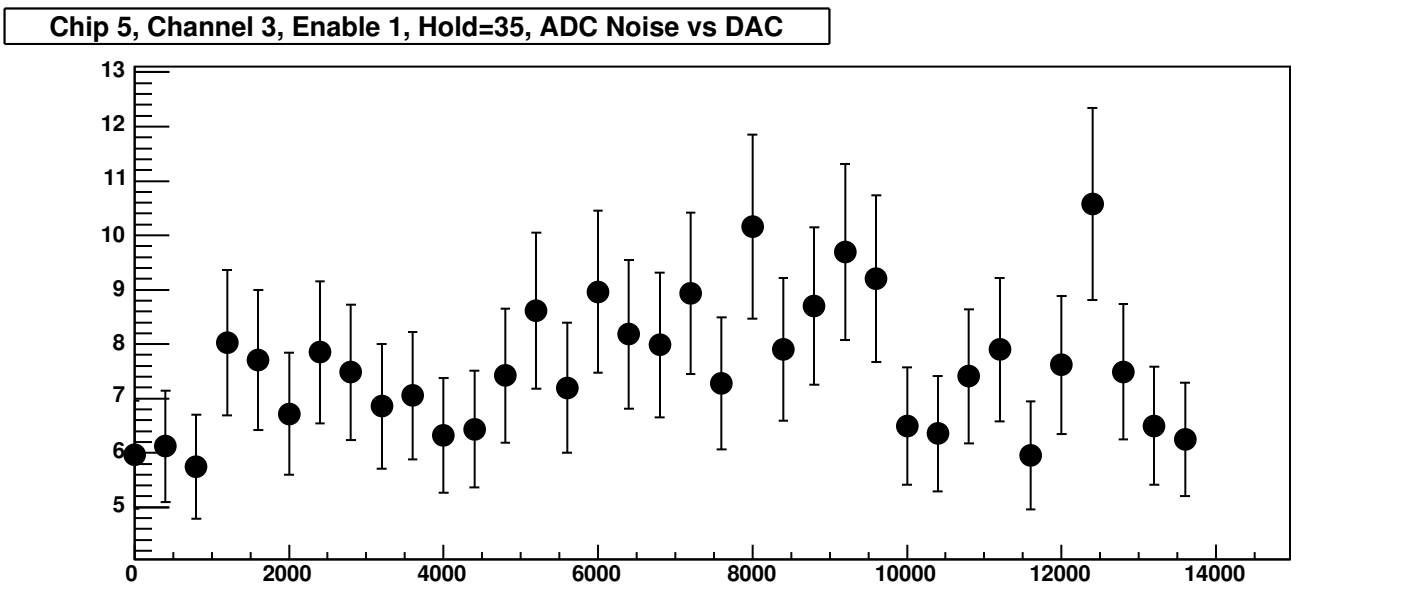

Chip 5, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

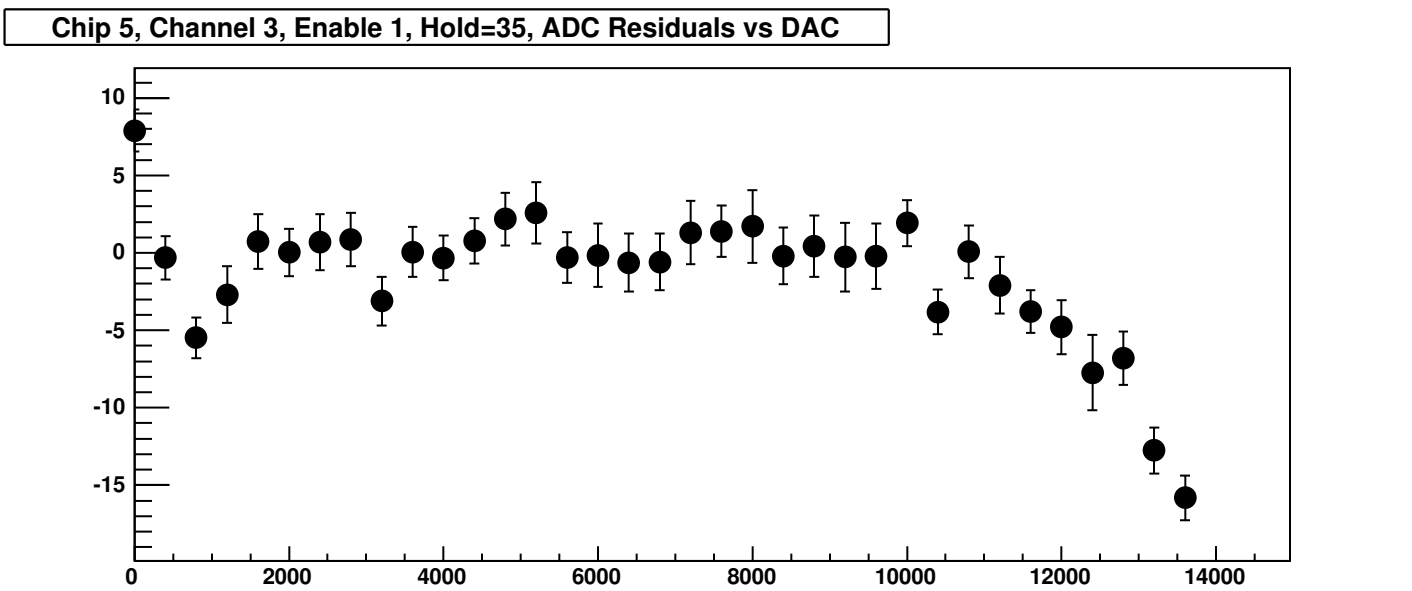

Chip 5, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 980.5 / 23
p0 -571.3 ± 1.101
p1 0.2054 ± 0.0001745
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

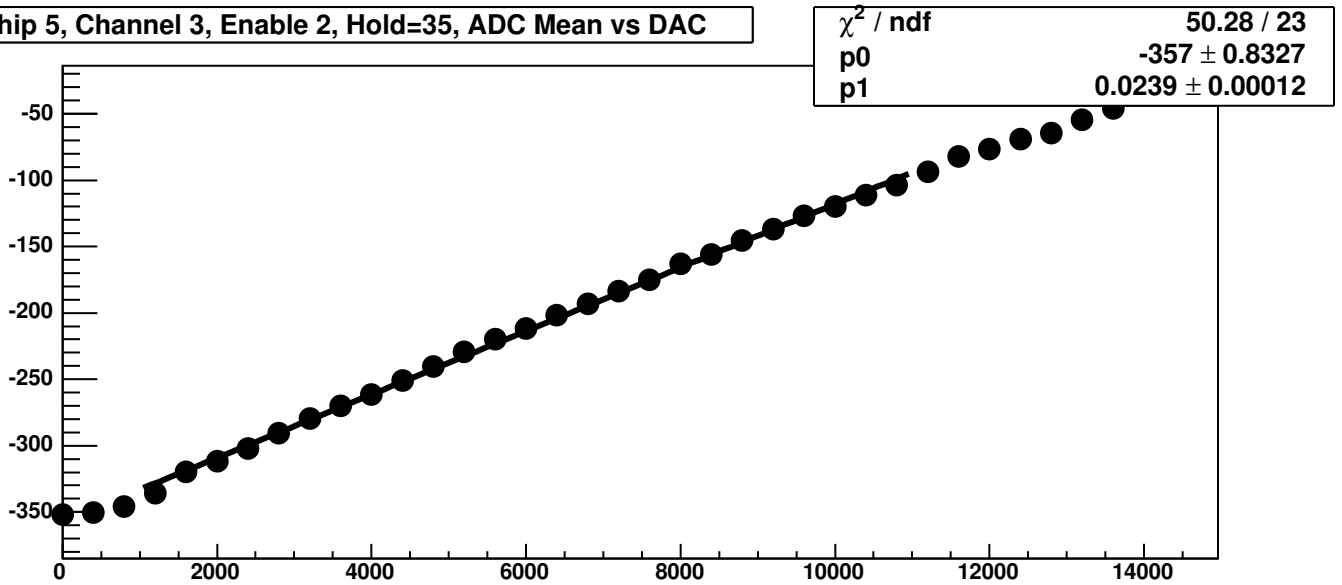

Chip 5, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 20.8 / 23
p0 -359.3 ± 0.7755
p1 0.02647 ± 0.0001183
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

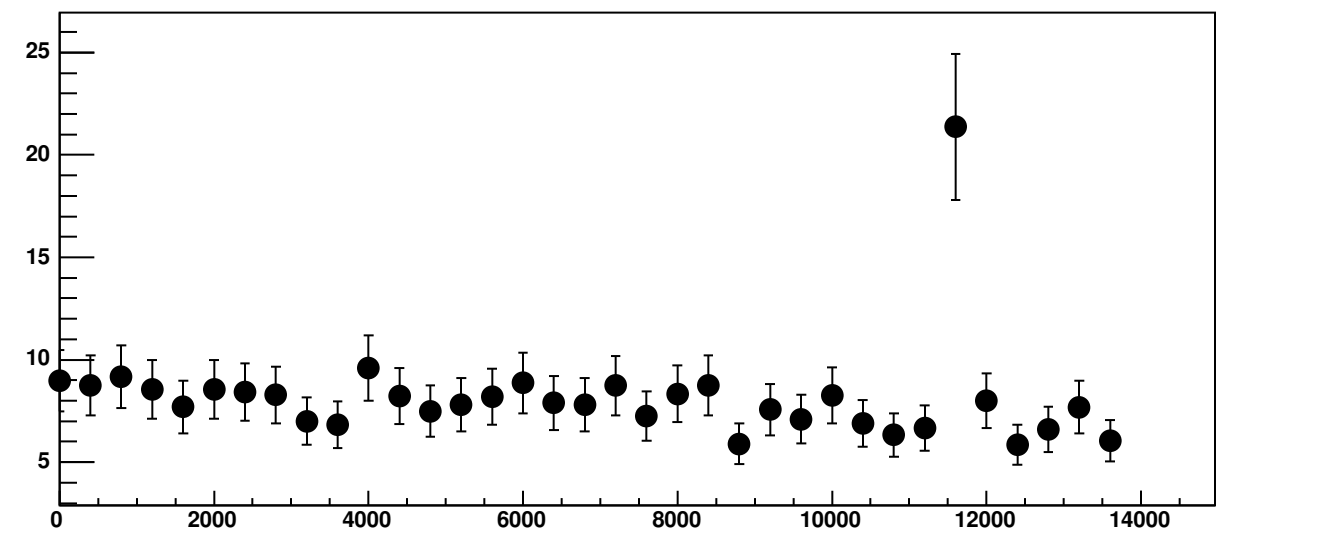

Chip 5, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

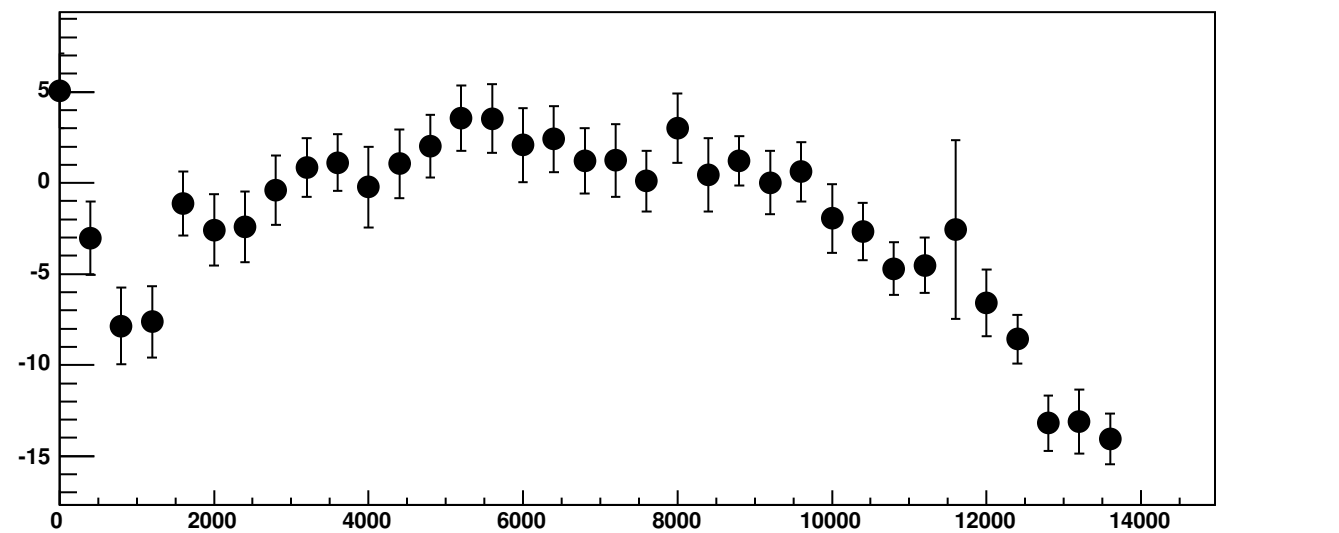
Chip 5, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

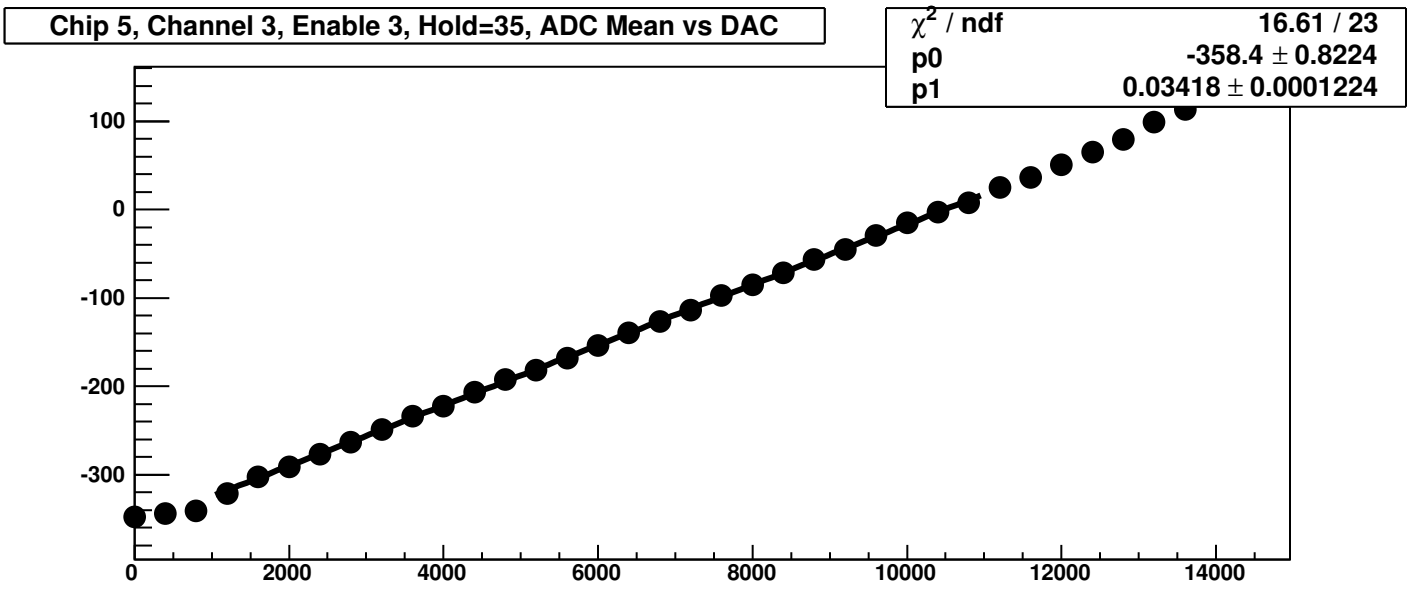


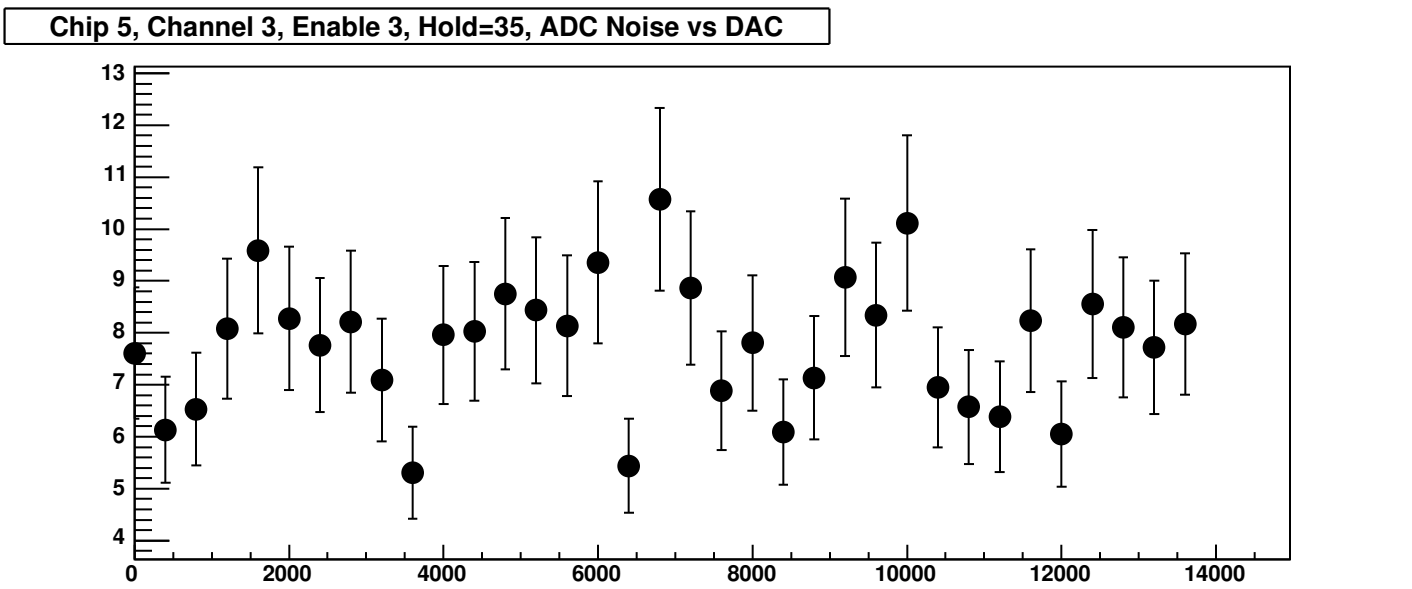
Chip 5, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC

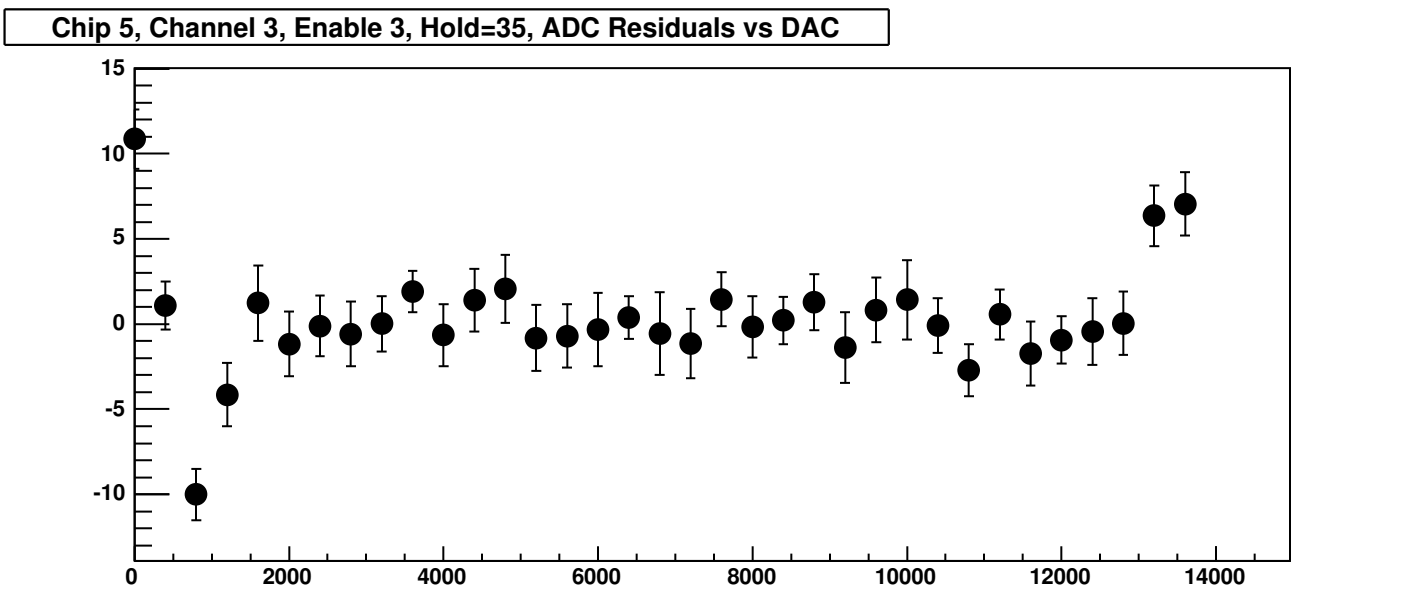


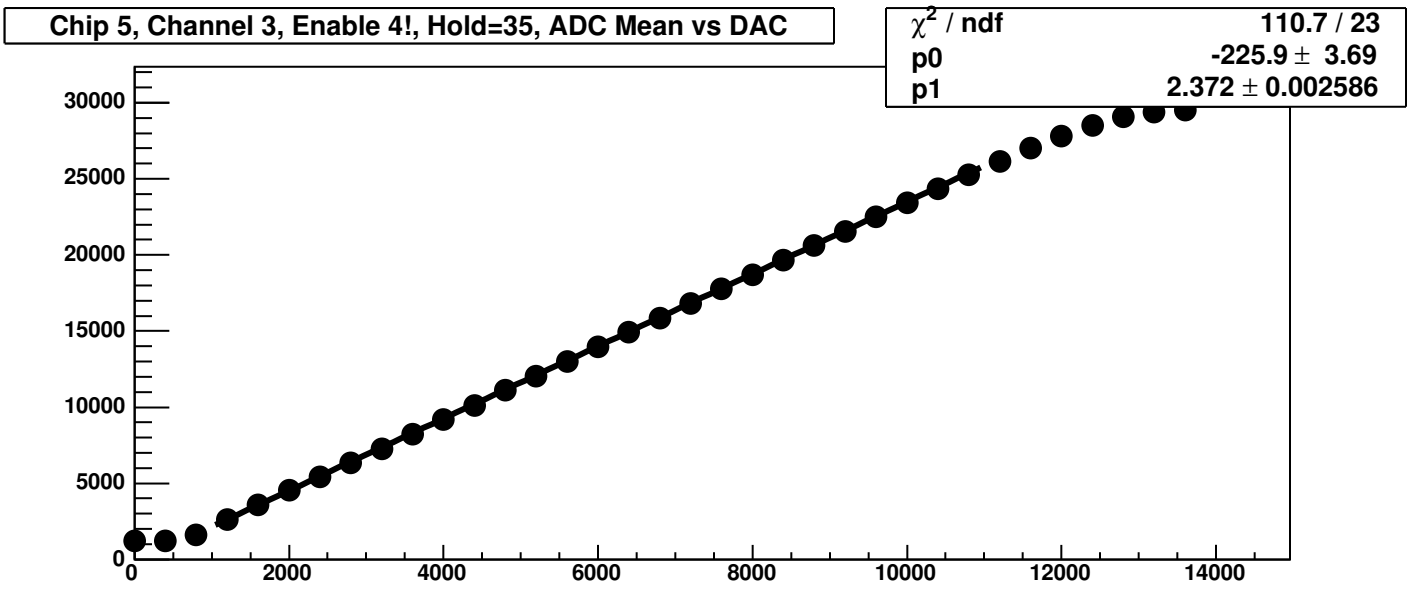
Chip 5, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

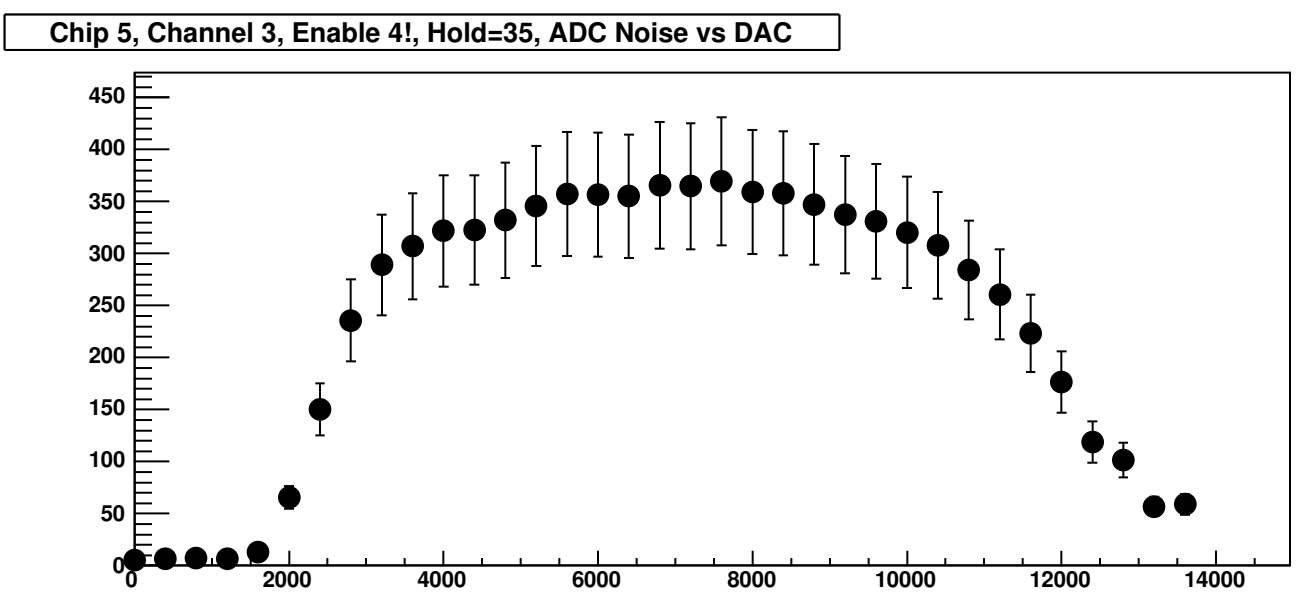


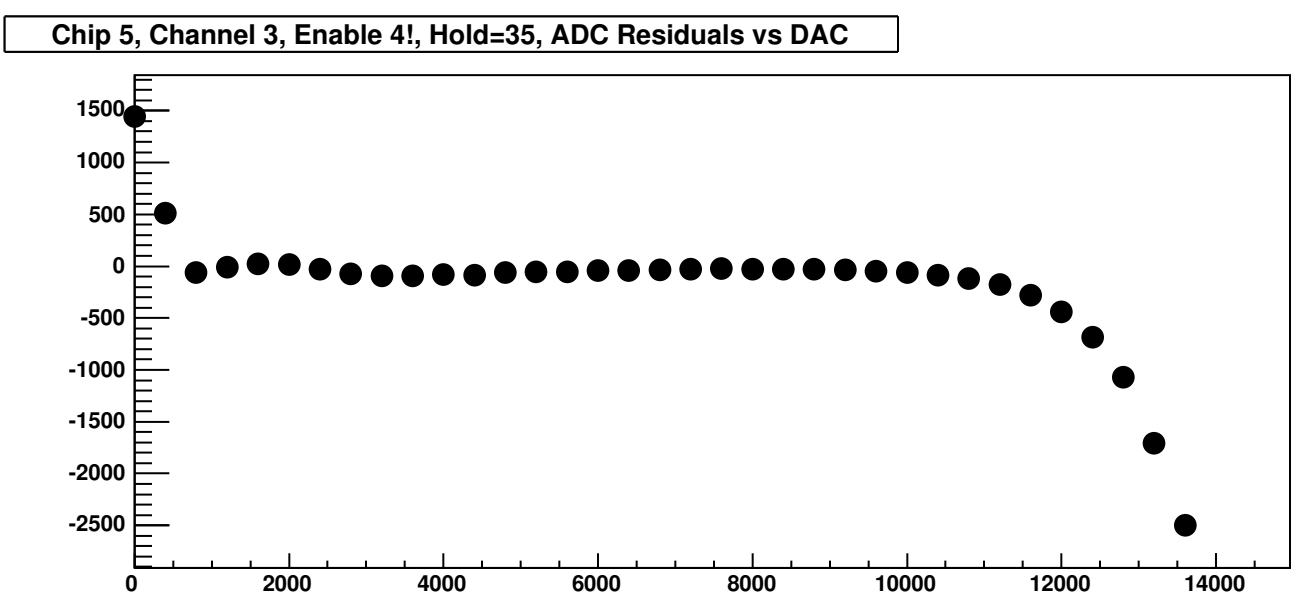

Chip 5, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 16.61 / 23
p0 -358.4 ± 0.8224
p1 0.03418 ± 0.0001224
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

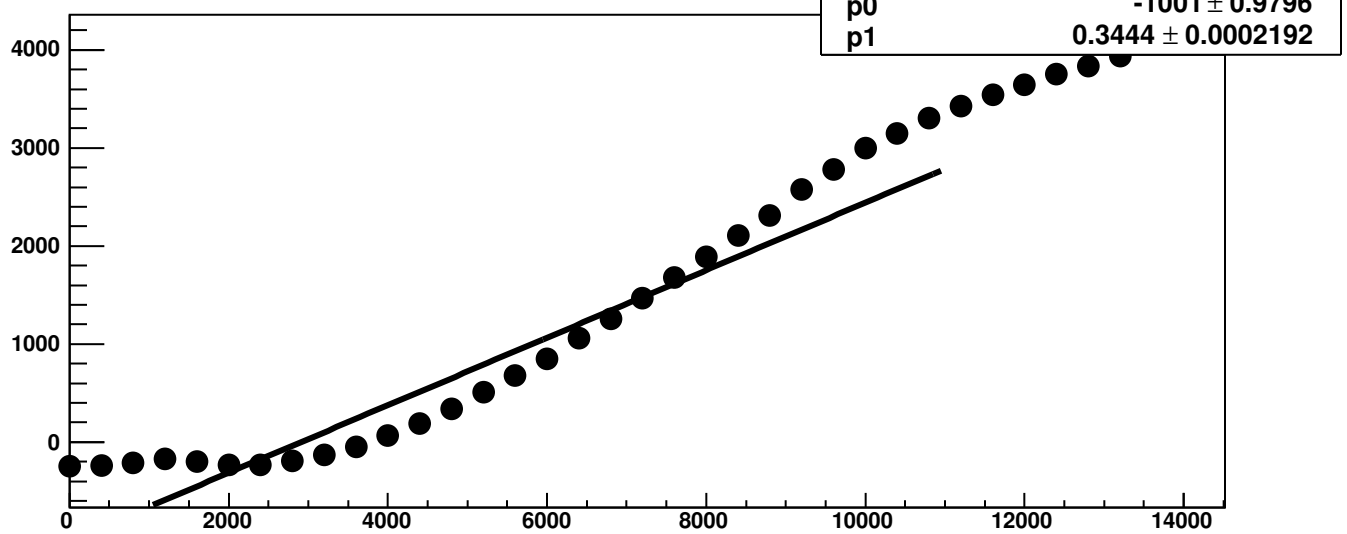

Chip 5, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 110.7 / 23
p0 -225.9 ± 3.69
p1 2.372 ± 0.002586
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

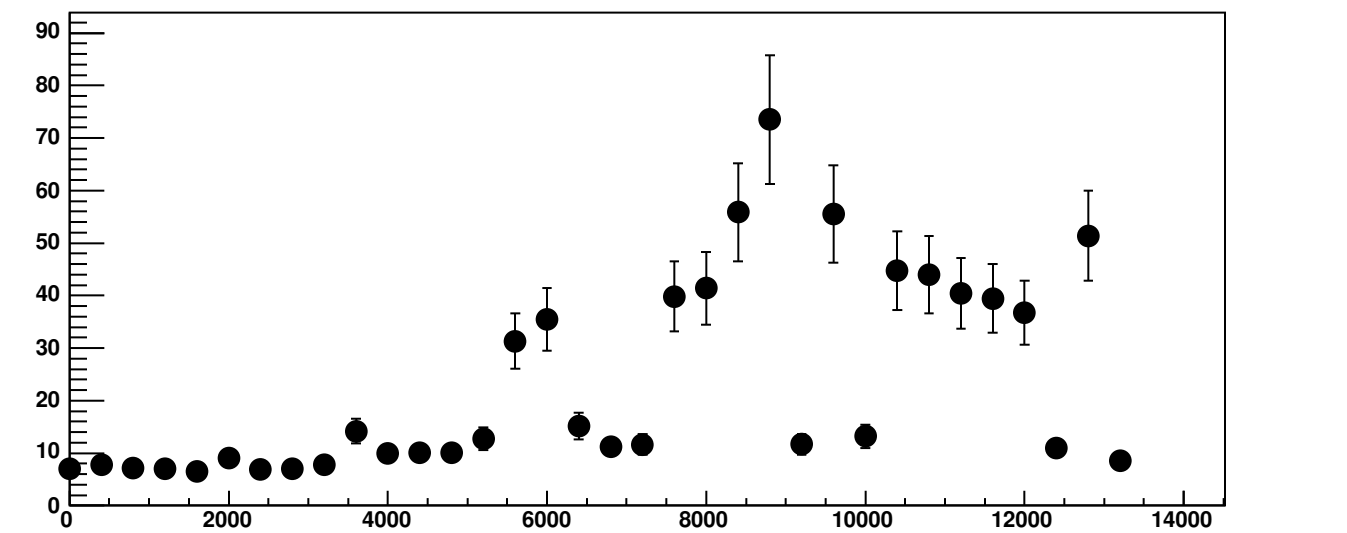

Chip 5, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

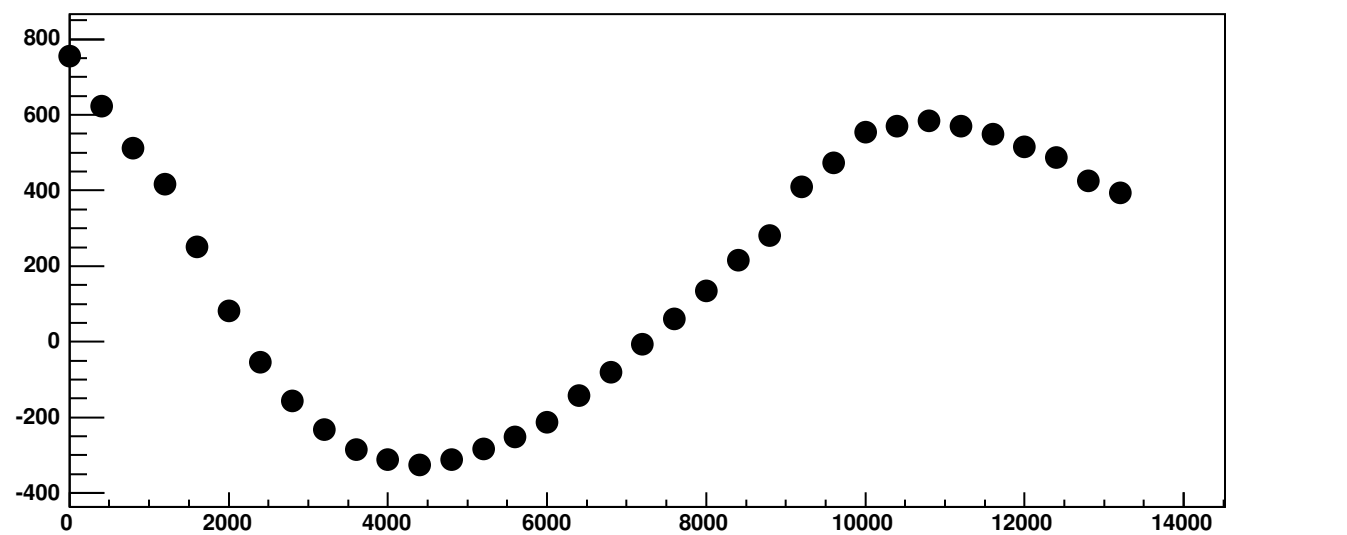
Chip 5, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

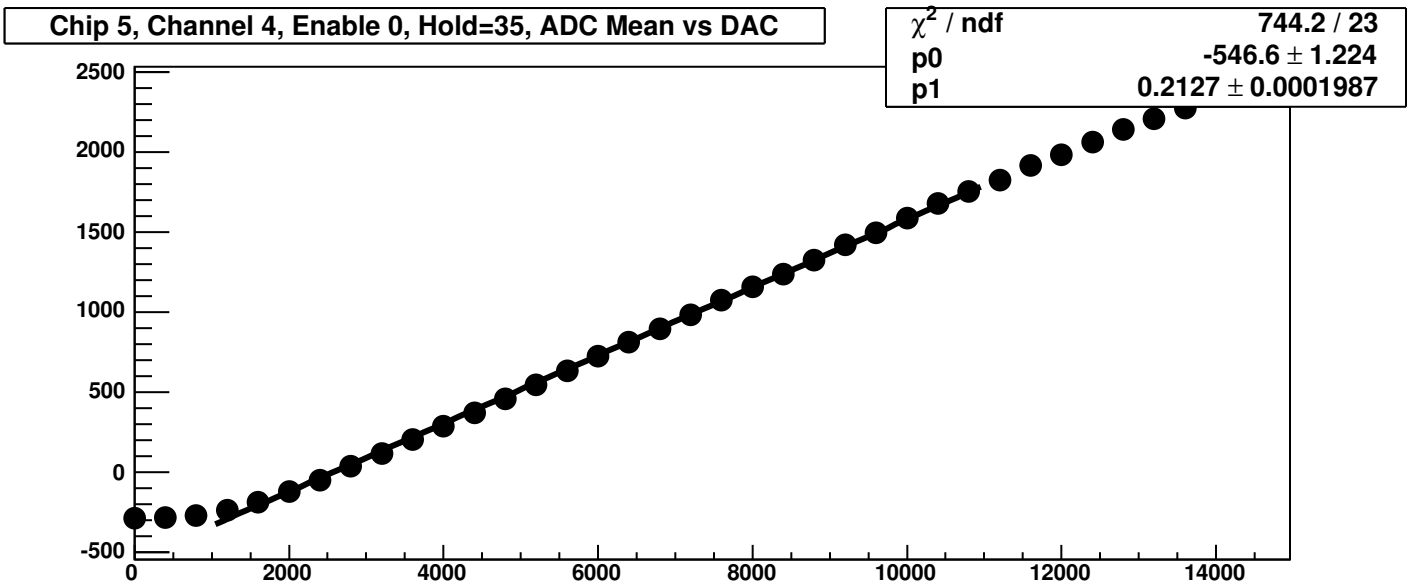


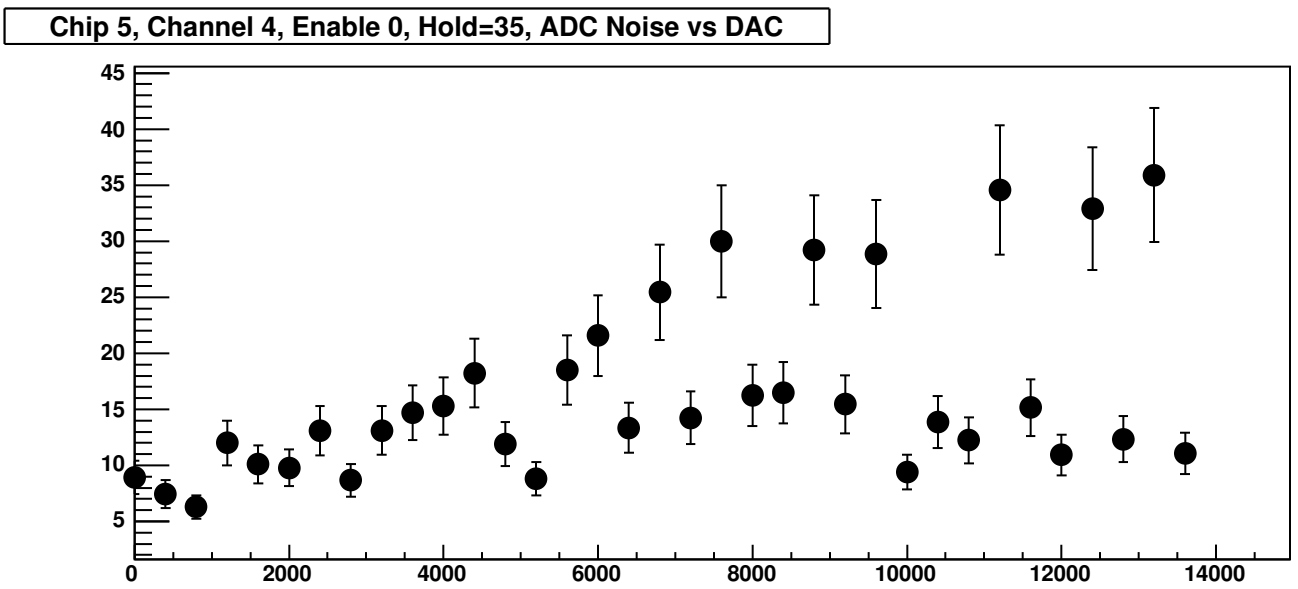
Chip 5, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC

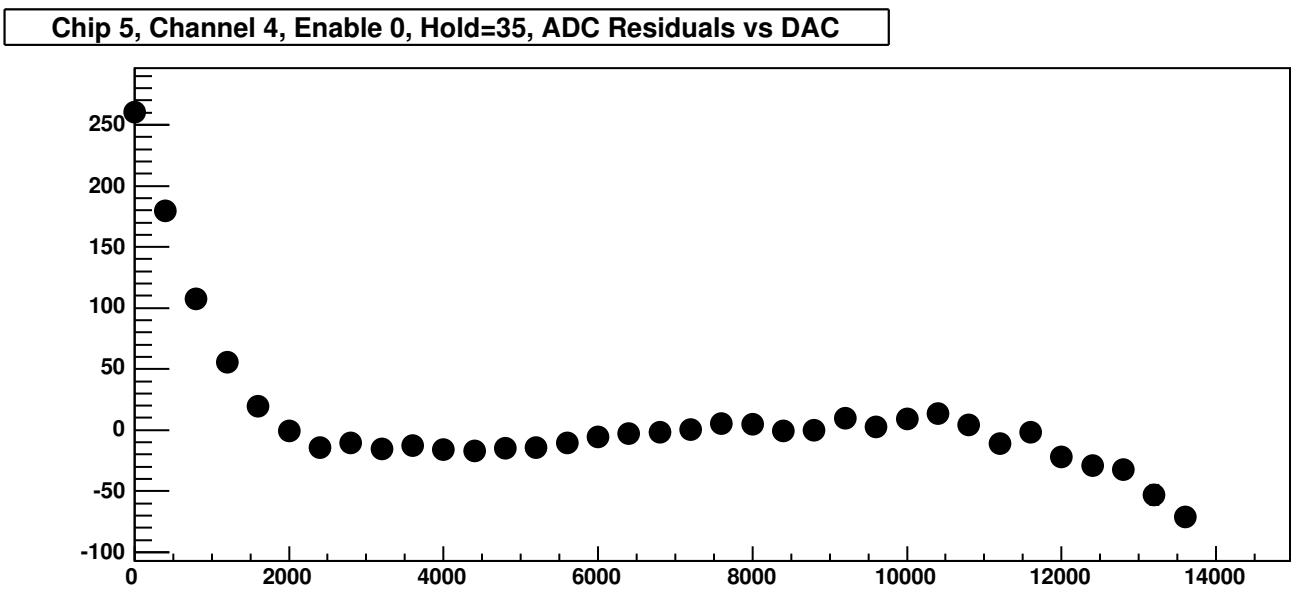


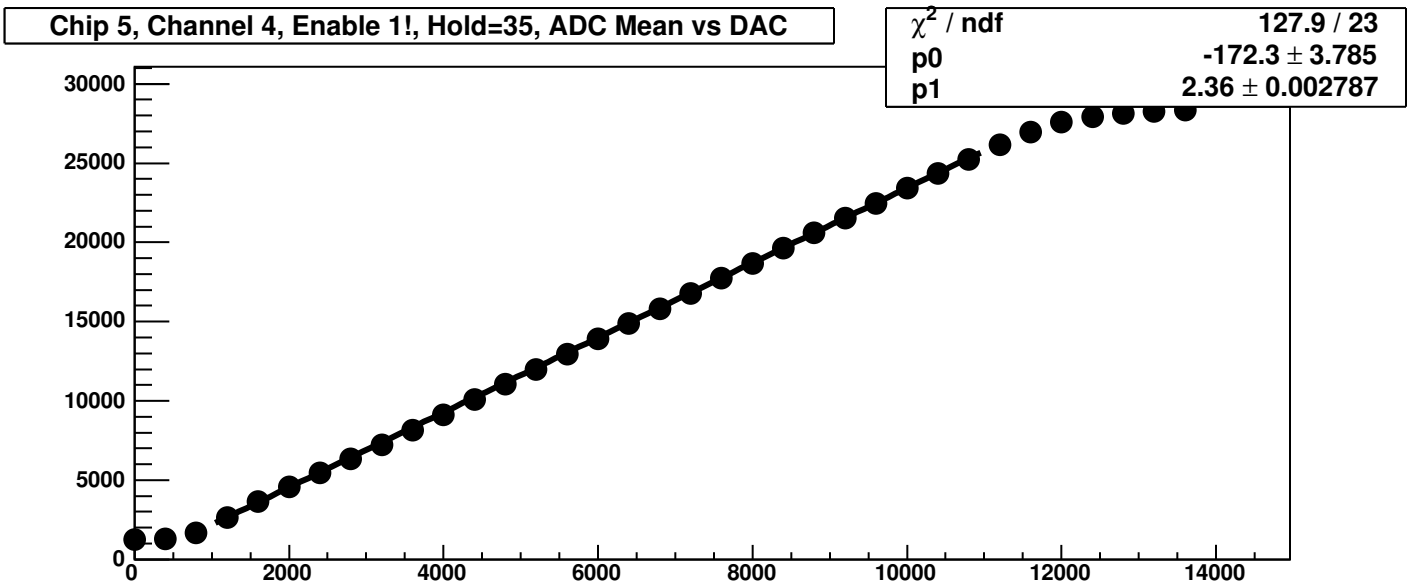
Chip 5, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

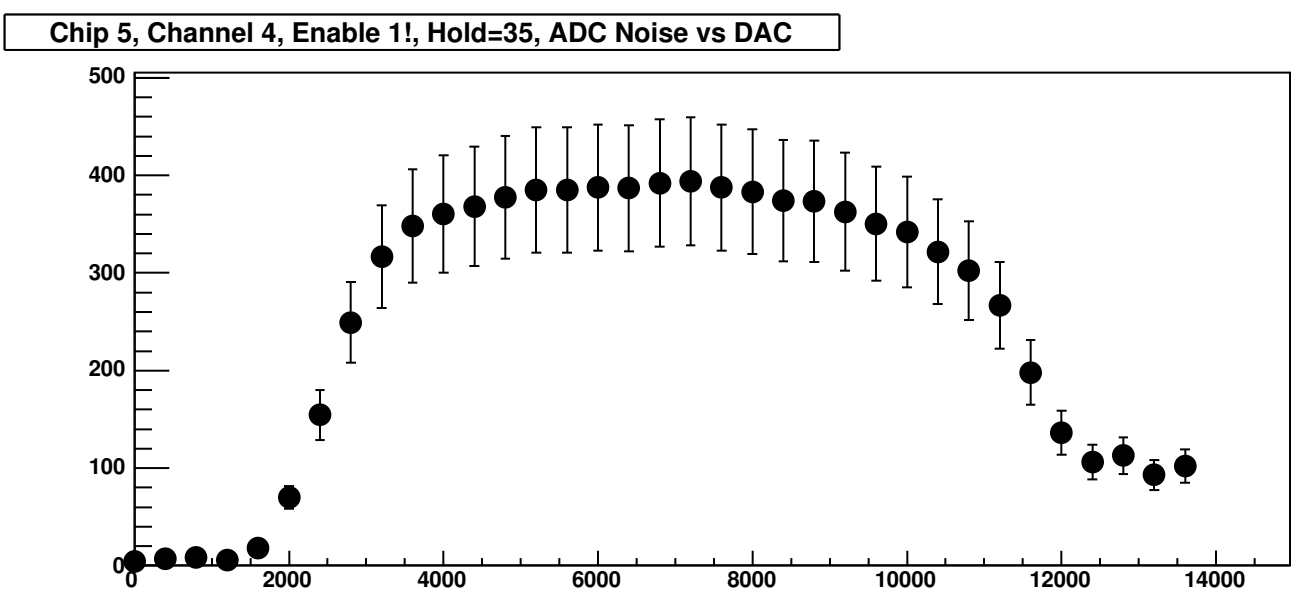


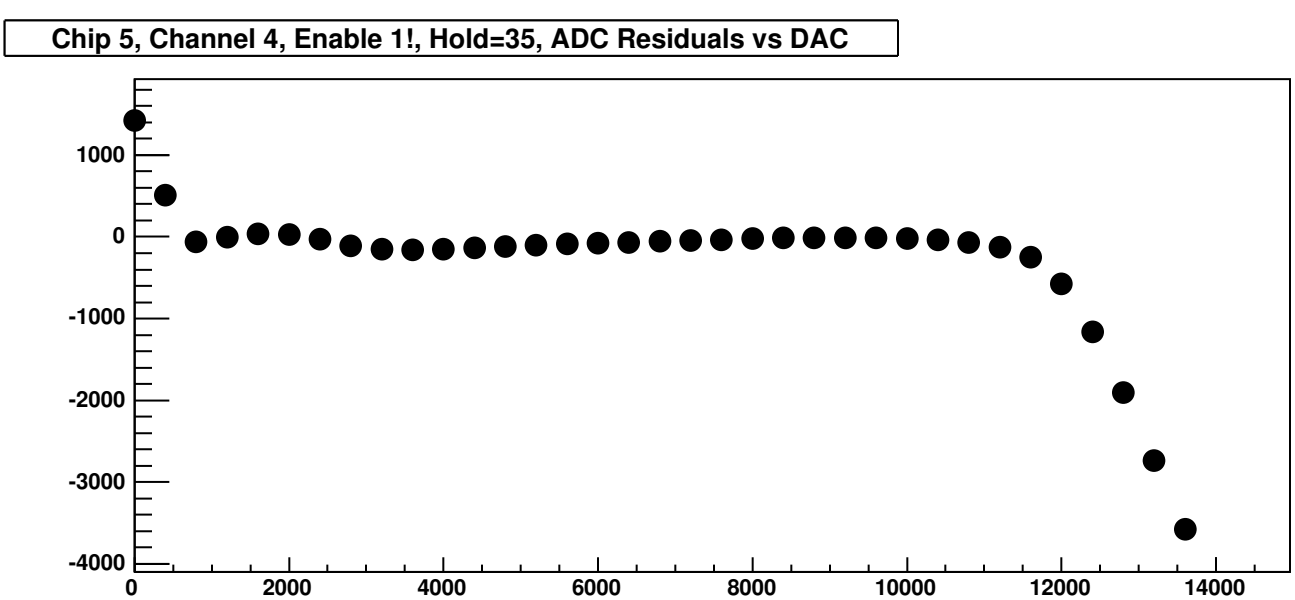

Chip 5, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 744.2 / 23
p0 -546.6 ± 1.224
p1 0.2127 ± 0.0001987
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

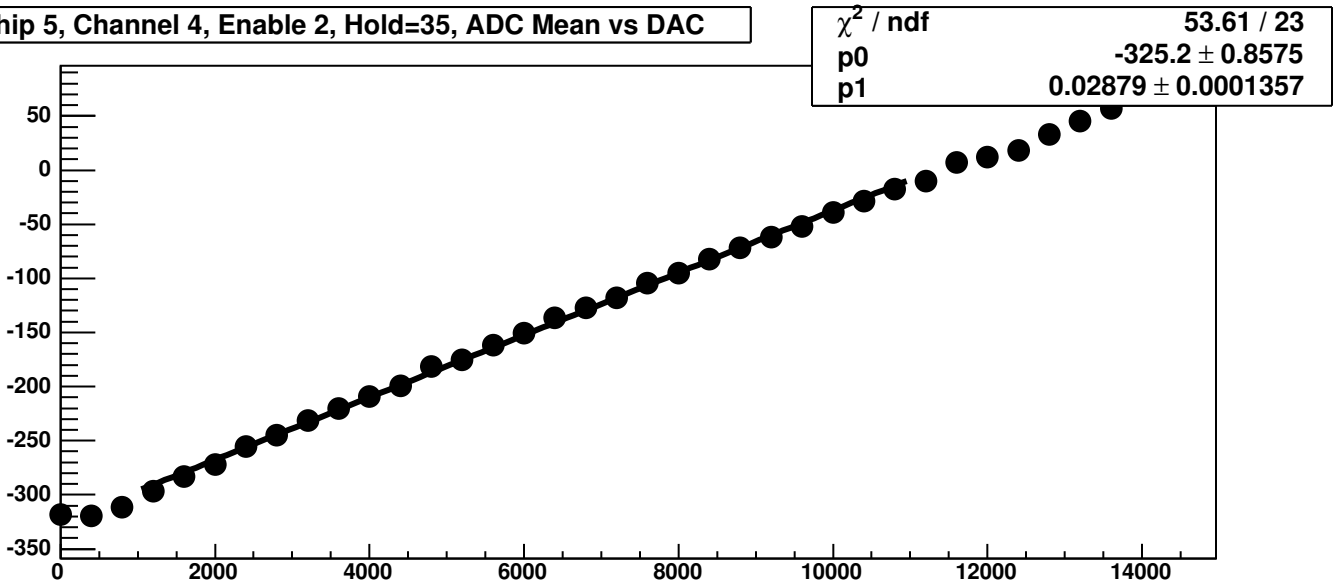

Chip 5, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 127.9 / 23
p0 -172.3 ± 3.785
p1 2.36 ± 0.002787
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

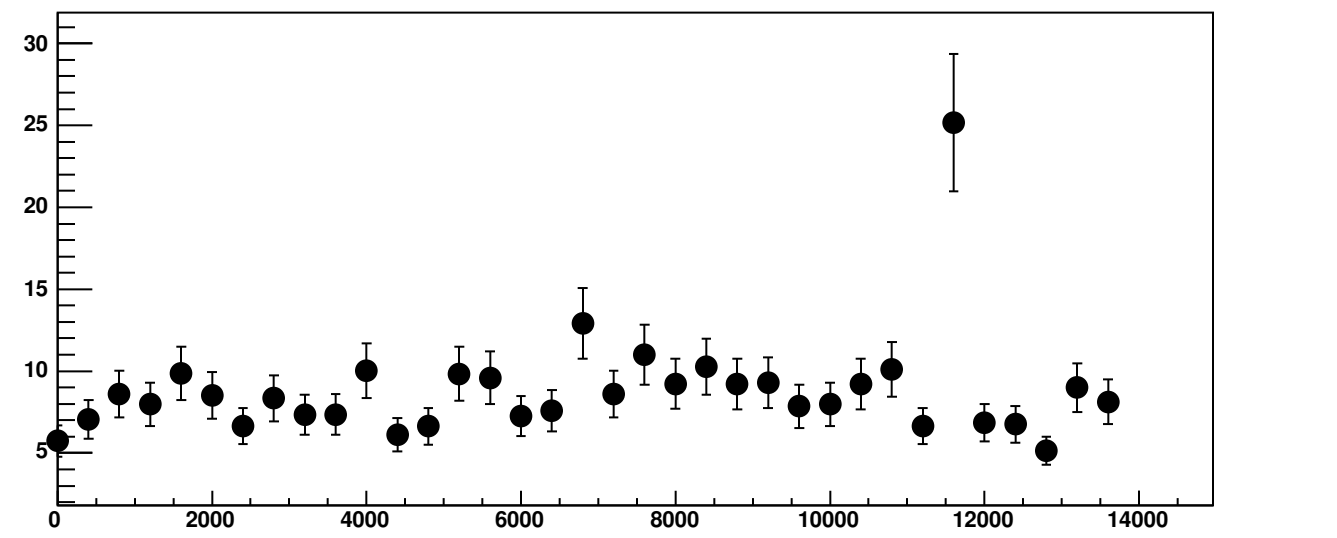

Chip 5, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

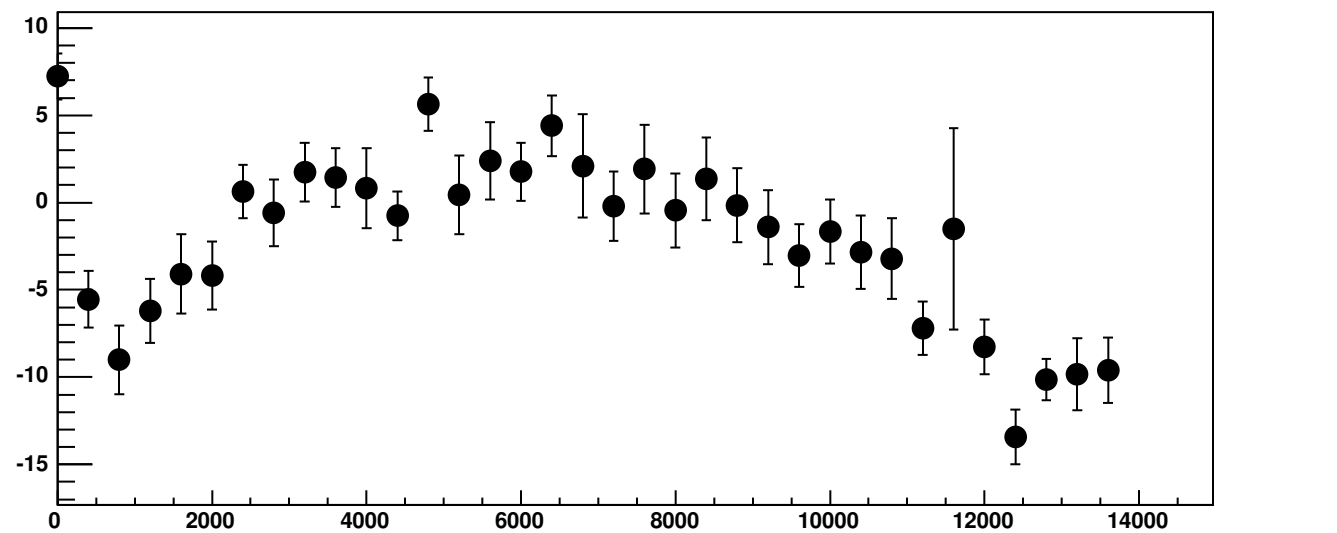
Chip 5, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



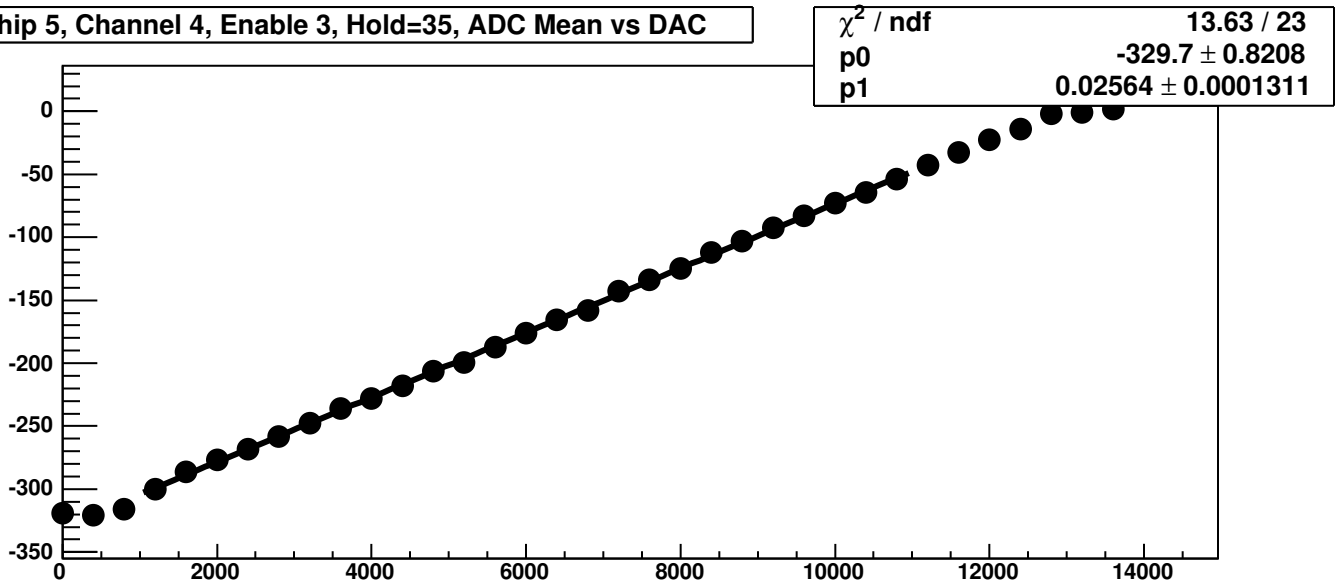
Chip 5, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



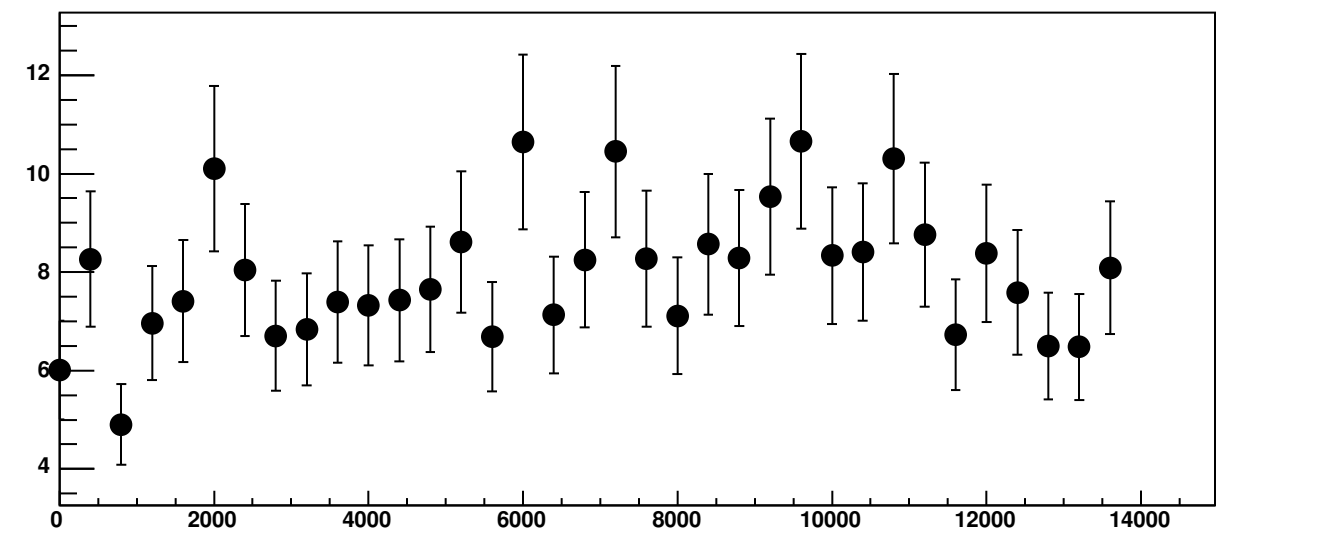
Chip 5, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



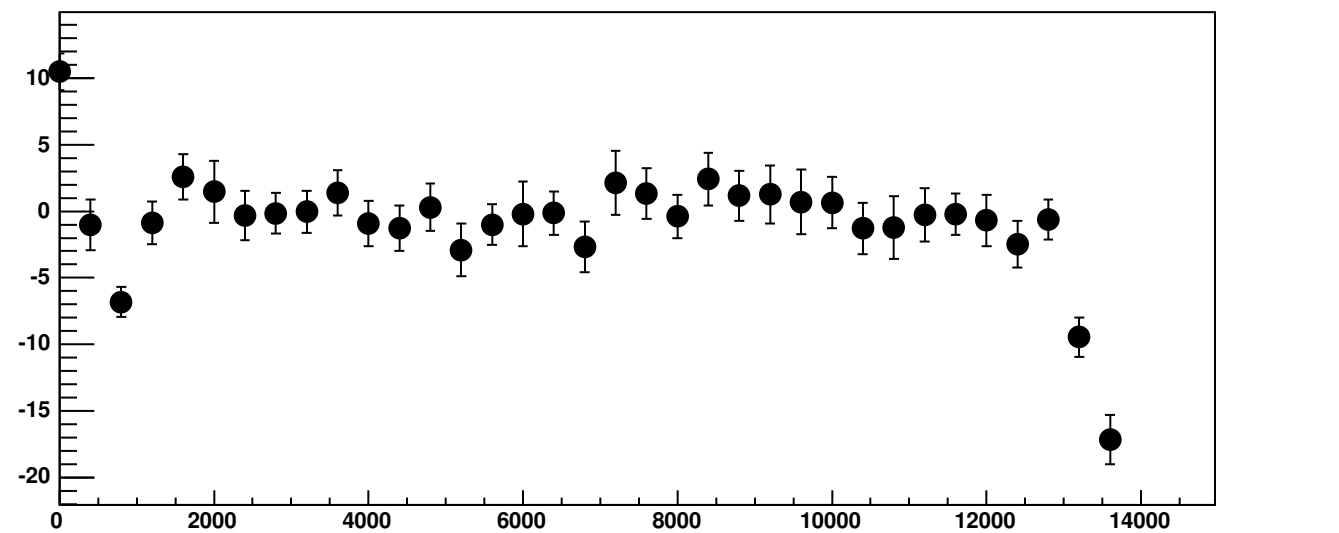
Chip 5, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



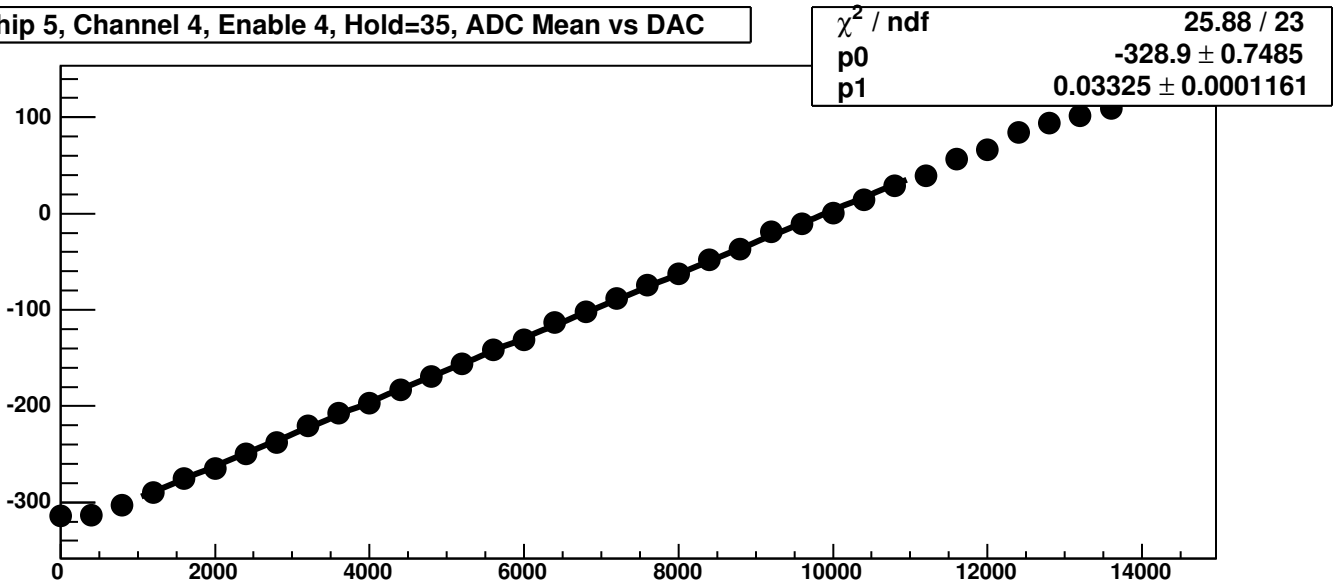
Chip 5, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



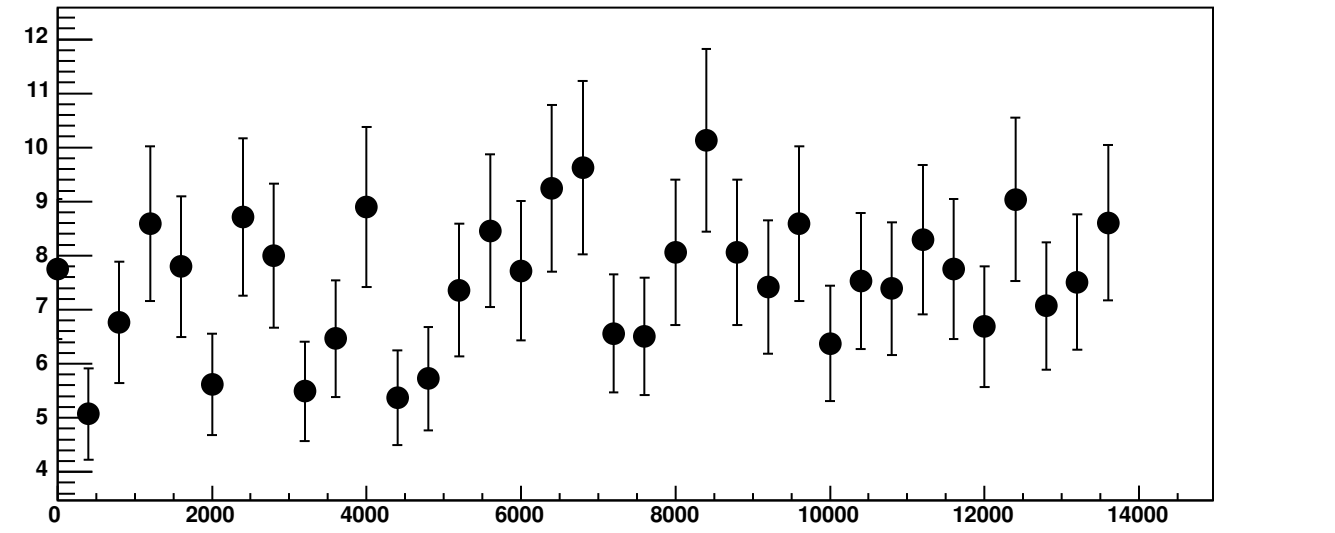
Chip 5, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



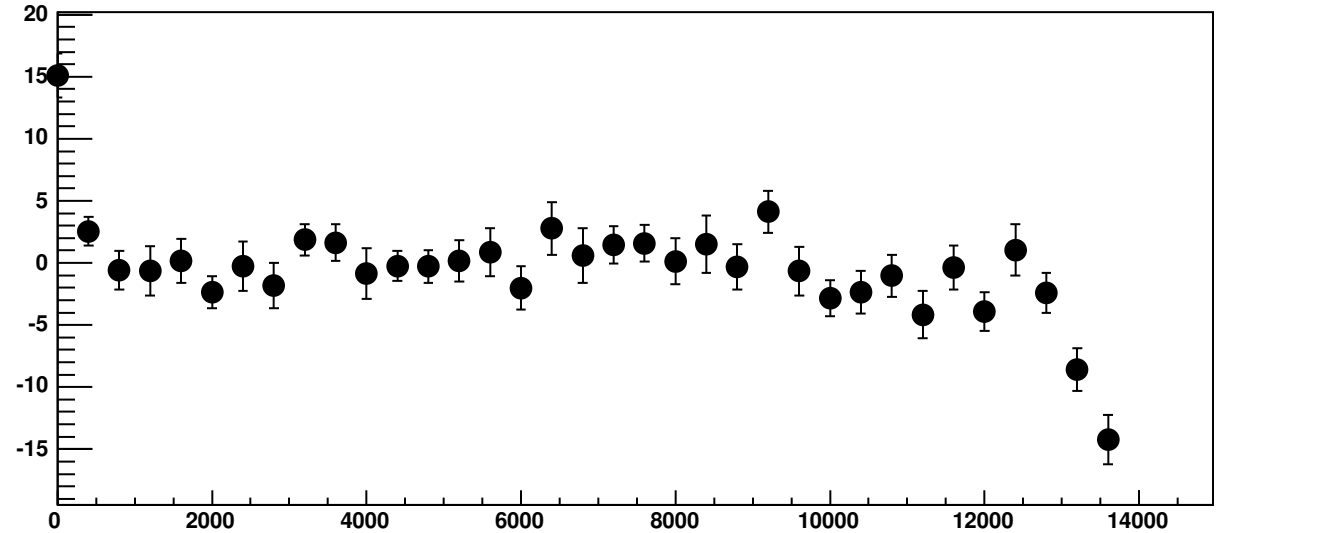
Chip 5, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC



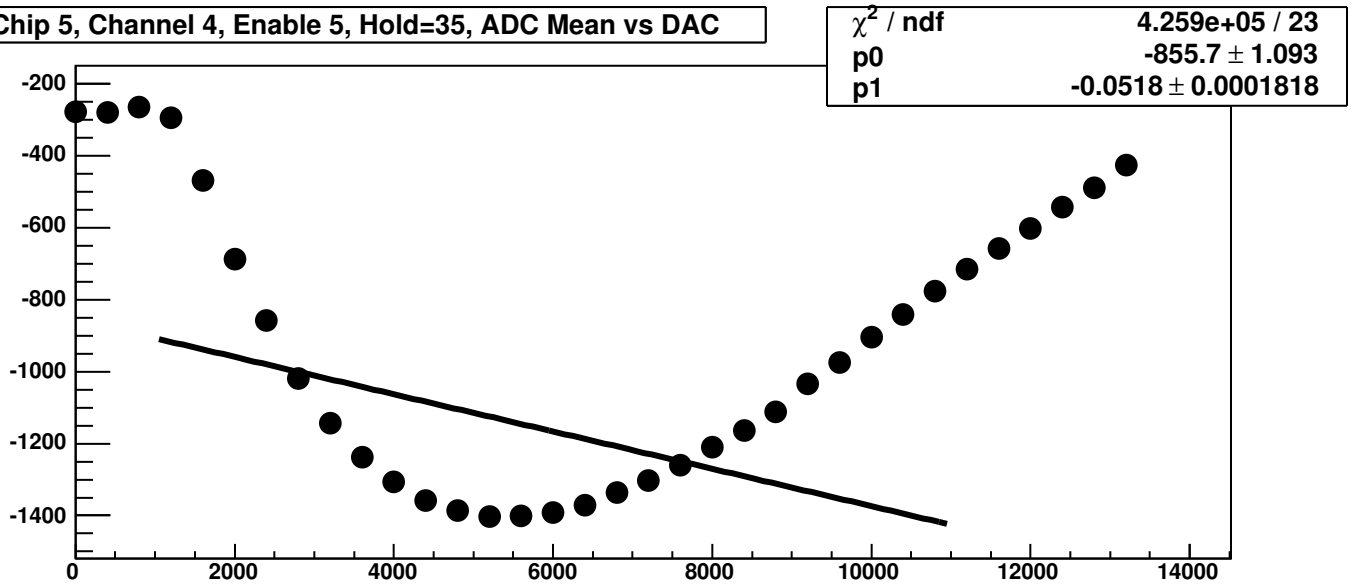
Chip 5, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC



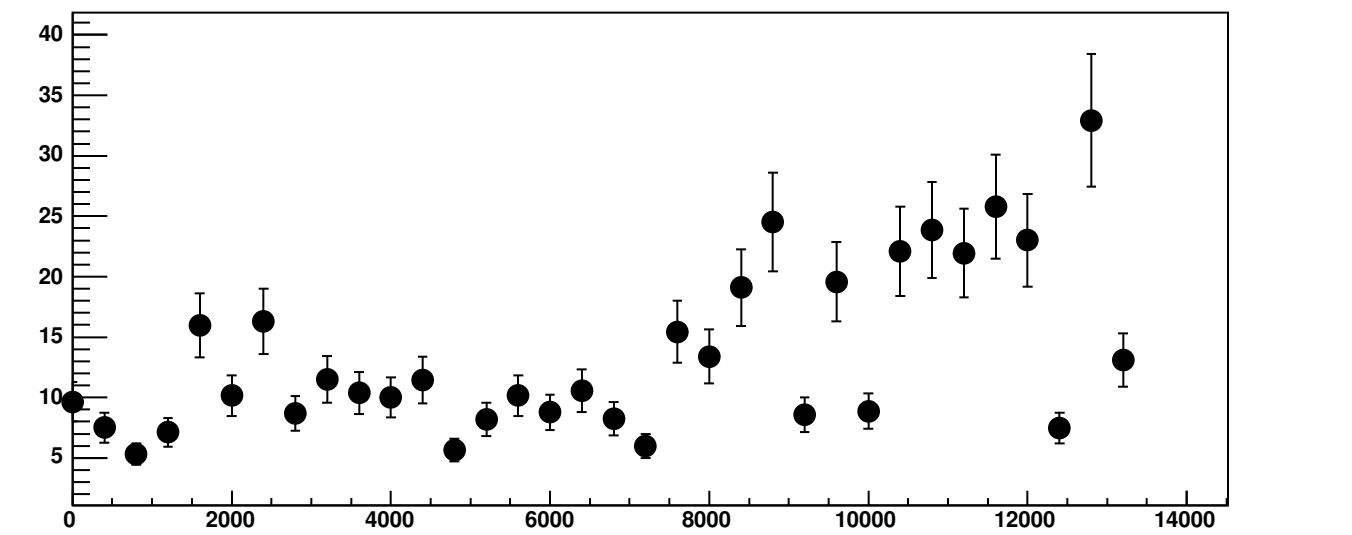
Chip 5, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC



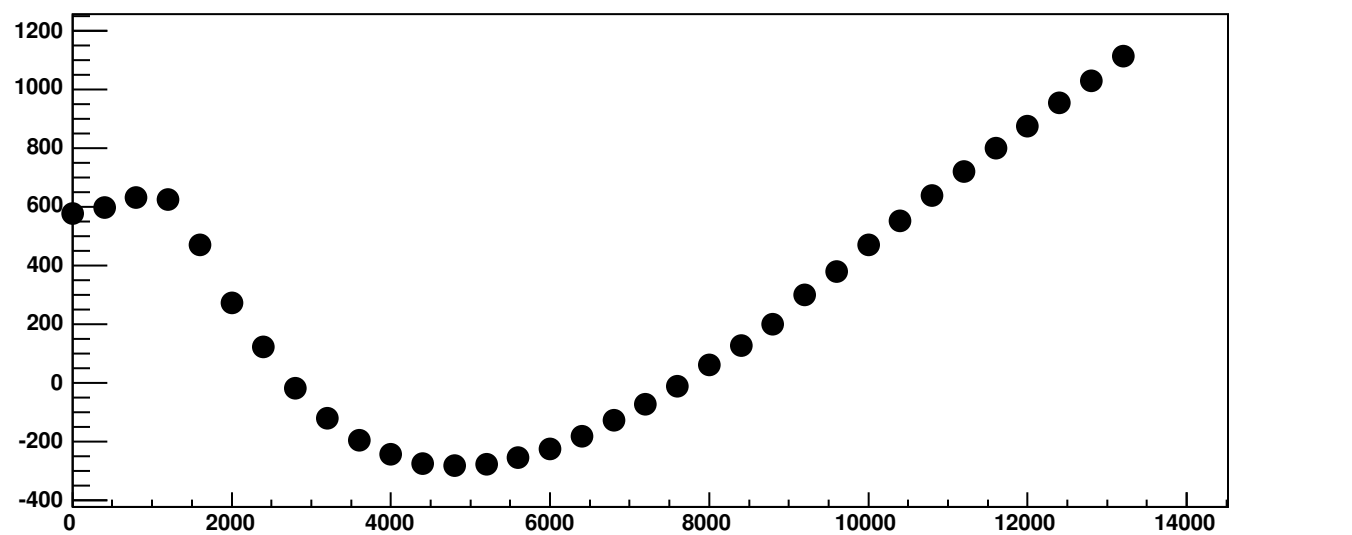
Chip 5, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

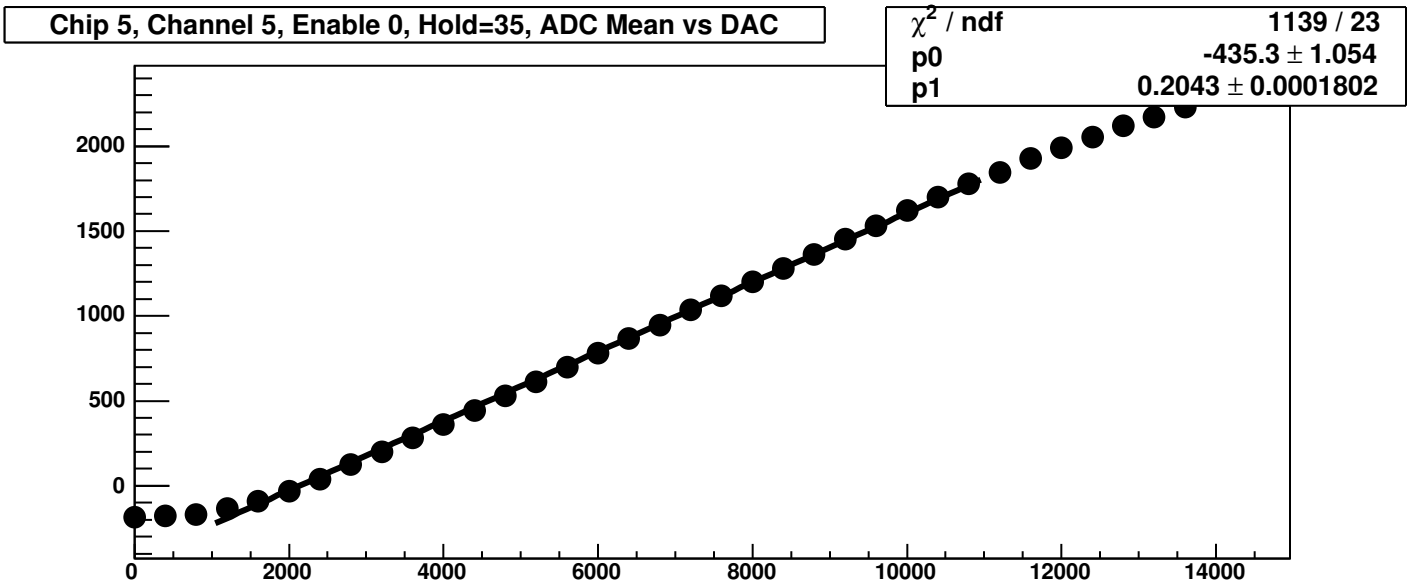


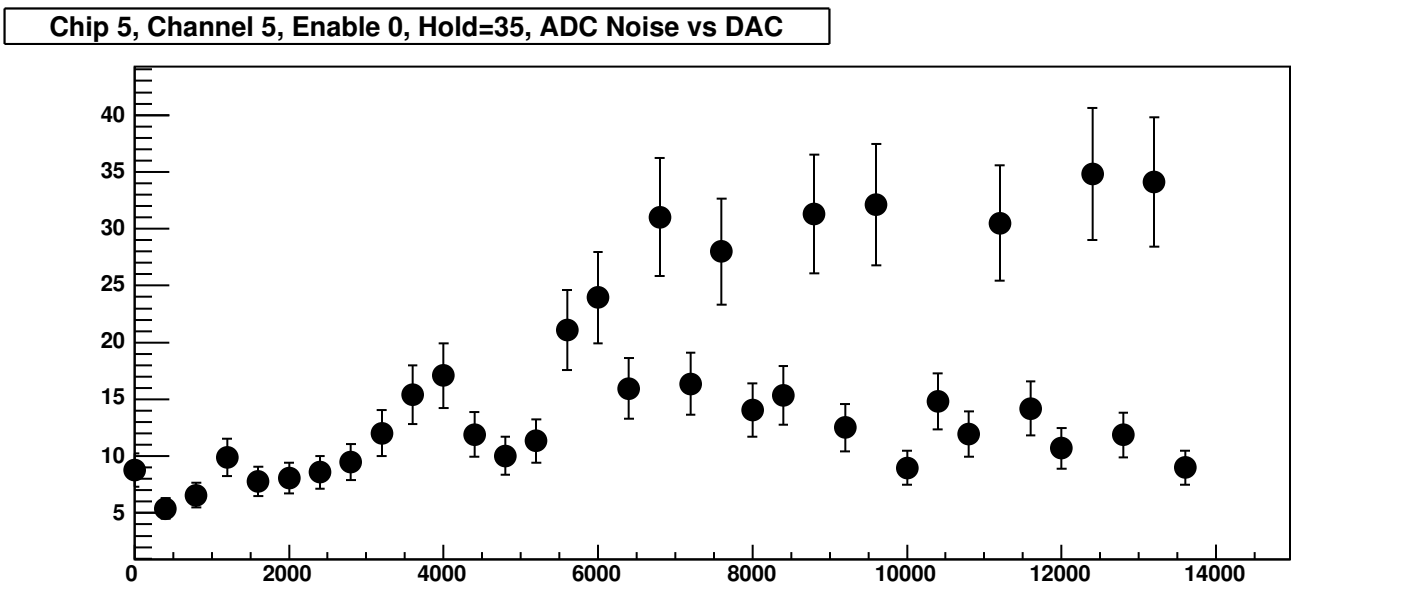
Chip 5, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC

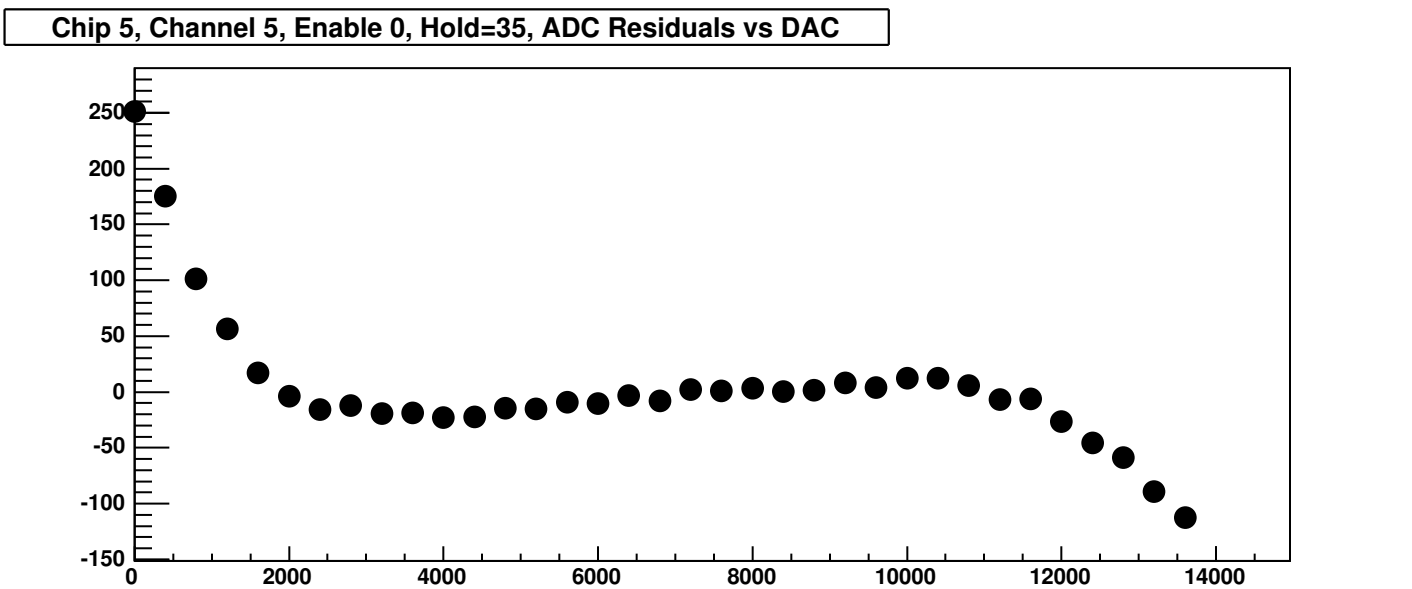


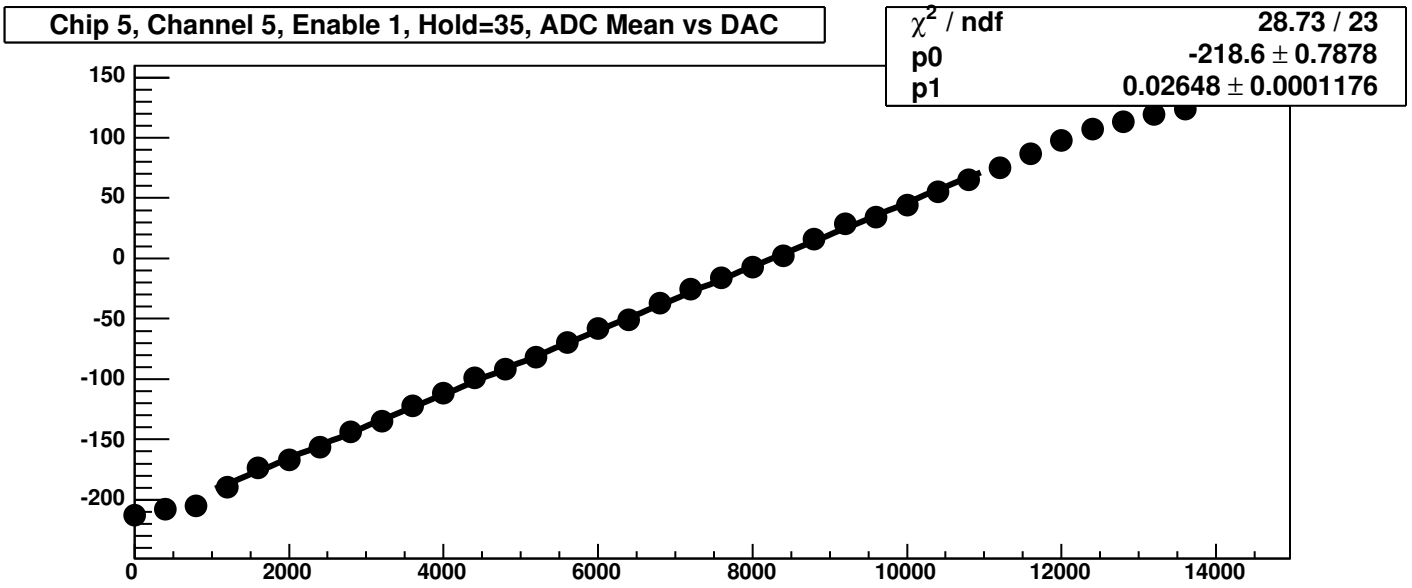
Chip 5, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

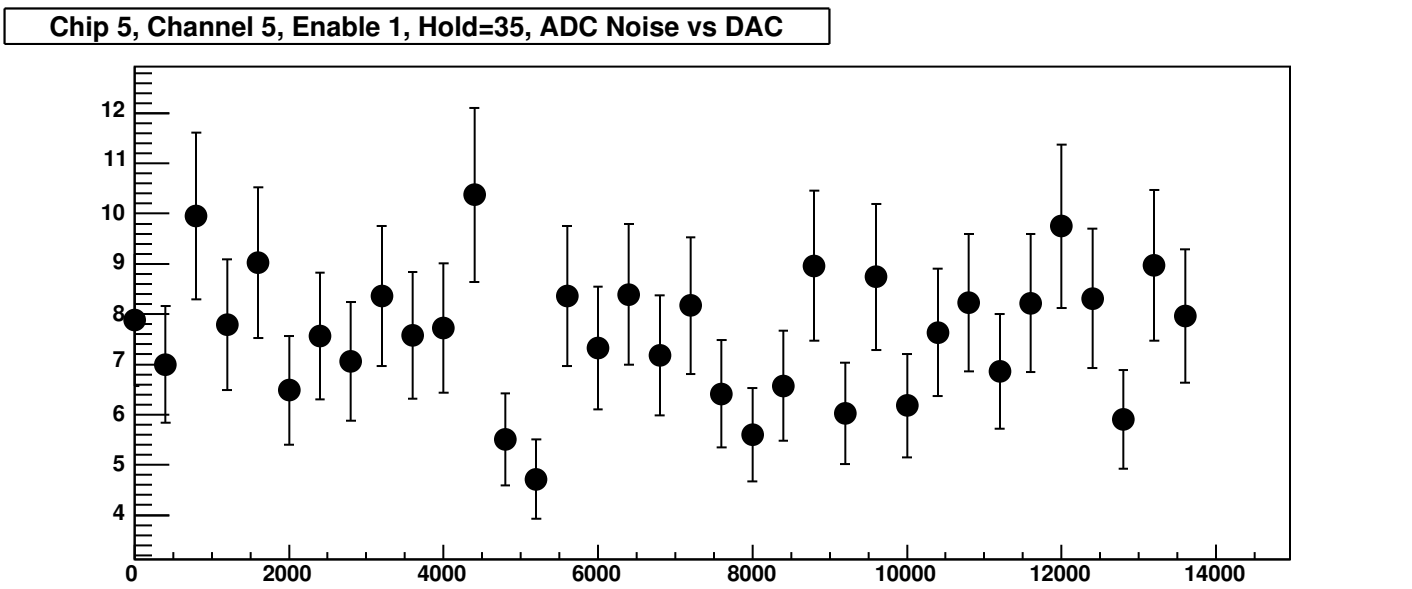


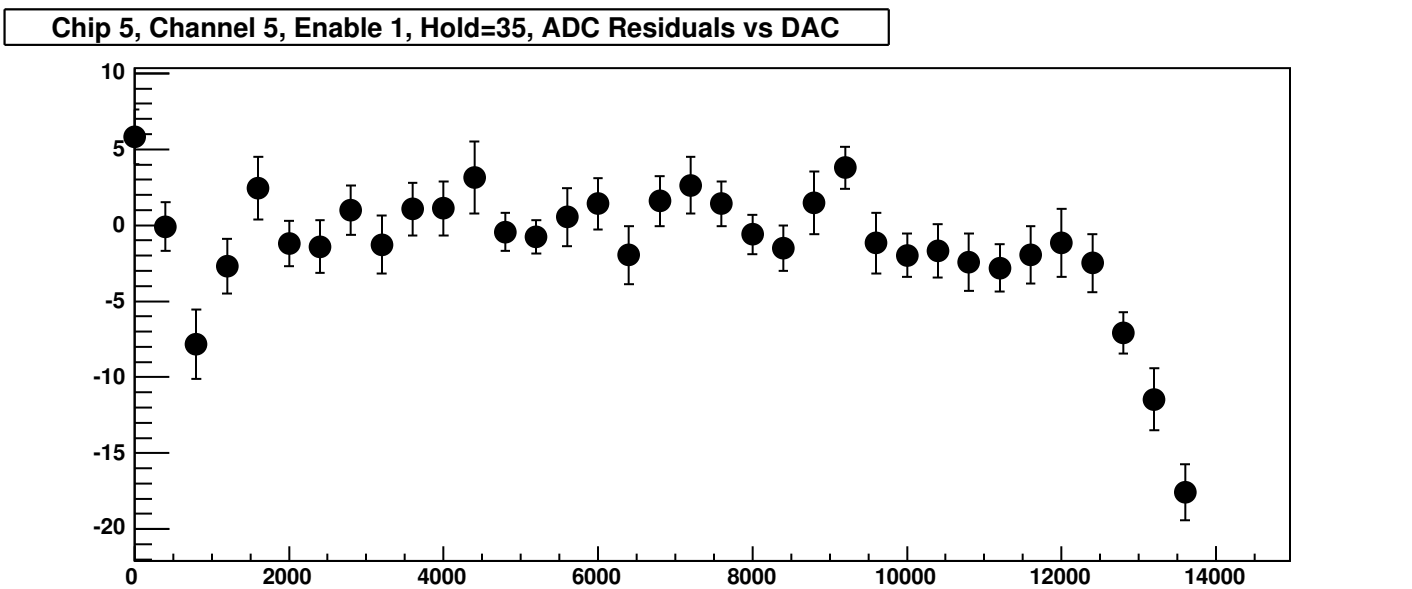

Chip 5, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1139 / 23
p0 -435.3 ± 1.054
p1 0.2043 ± 0.0001802
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

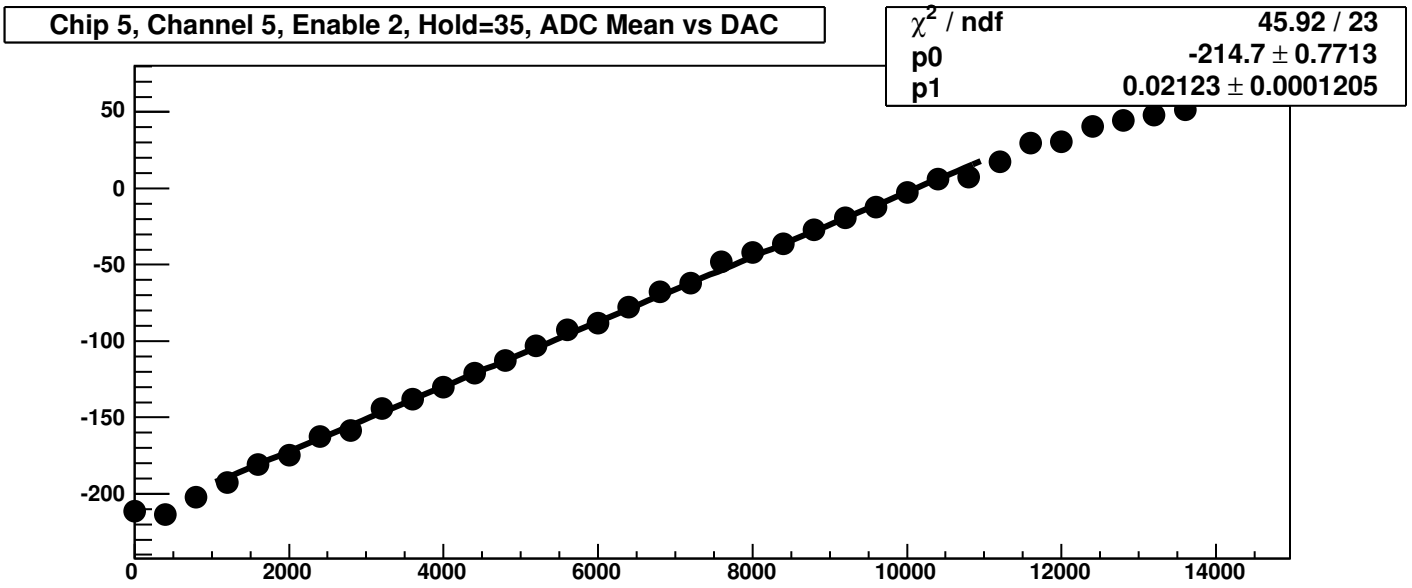

Chip 5, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

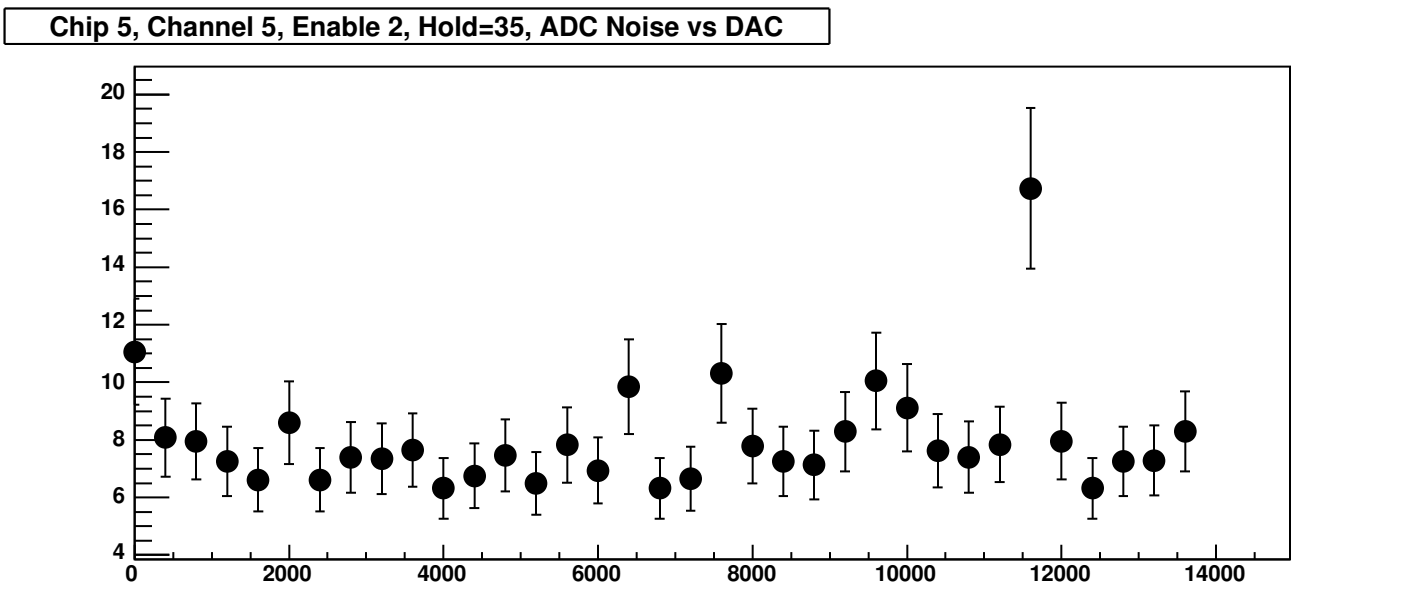

Chip 5, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

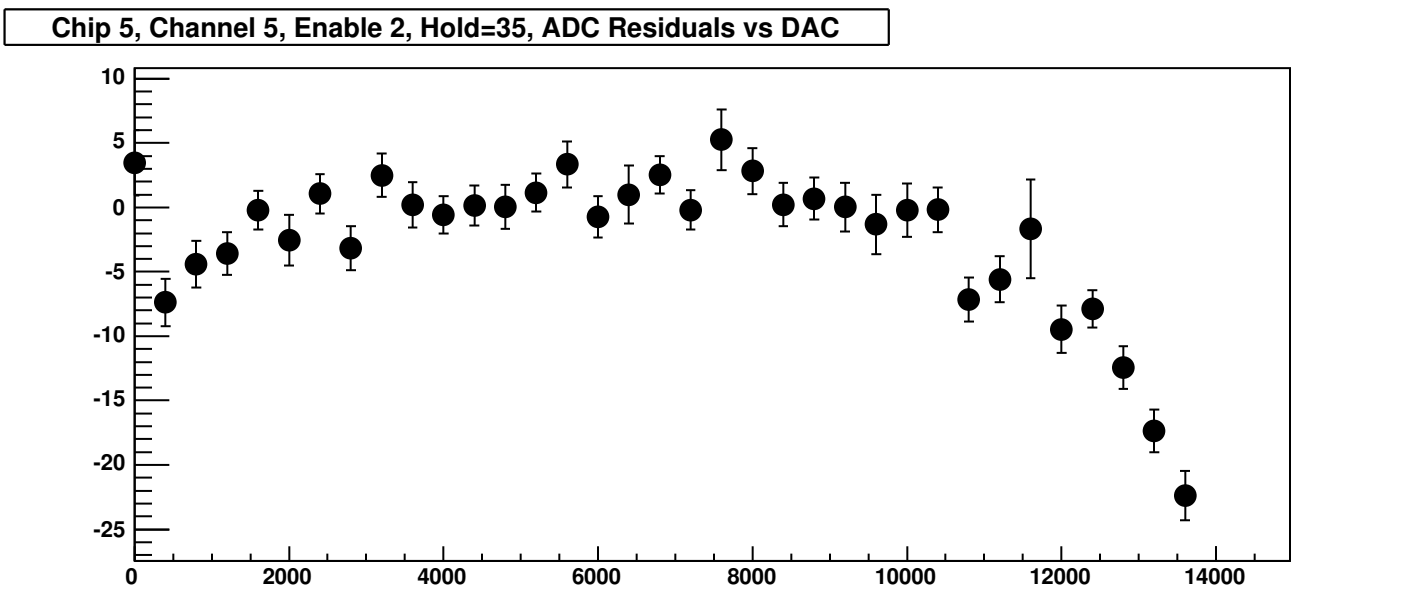

Chip 5, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 28.73 / 23
p0 -218.6 ± 0.7878
p1 0.02648 ± 0.0001176
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

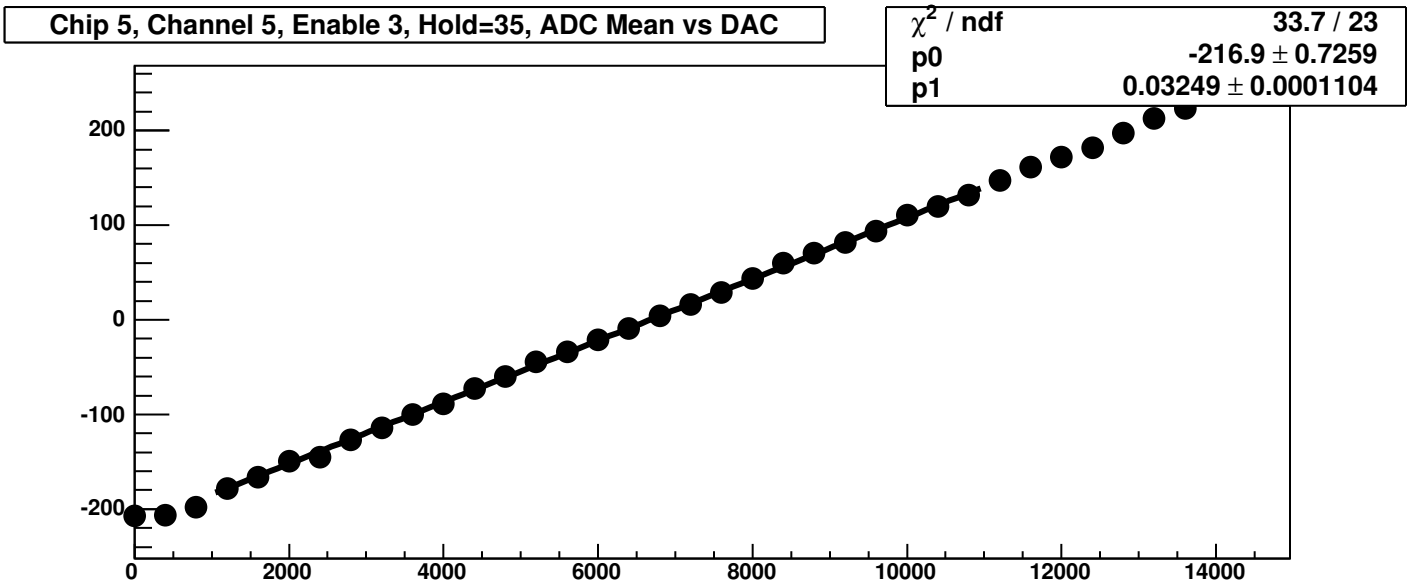

Chip 5, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

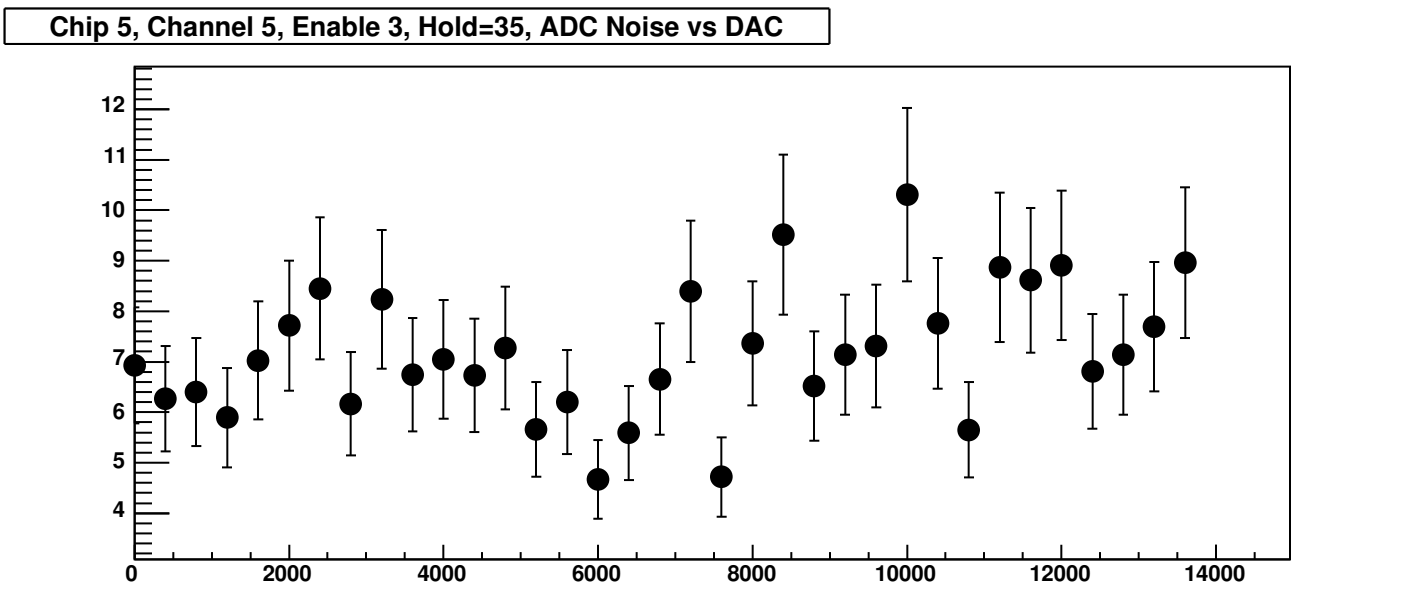

Chip 5, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

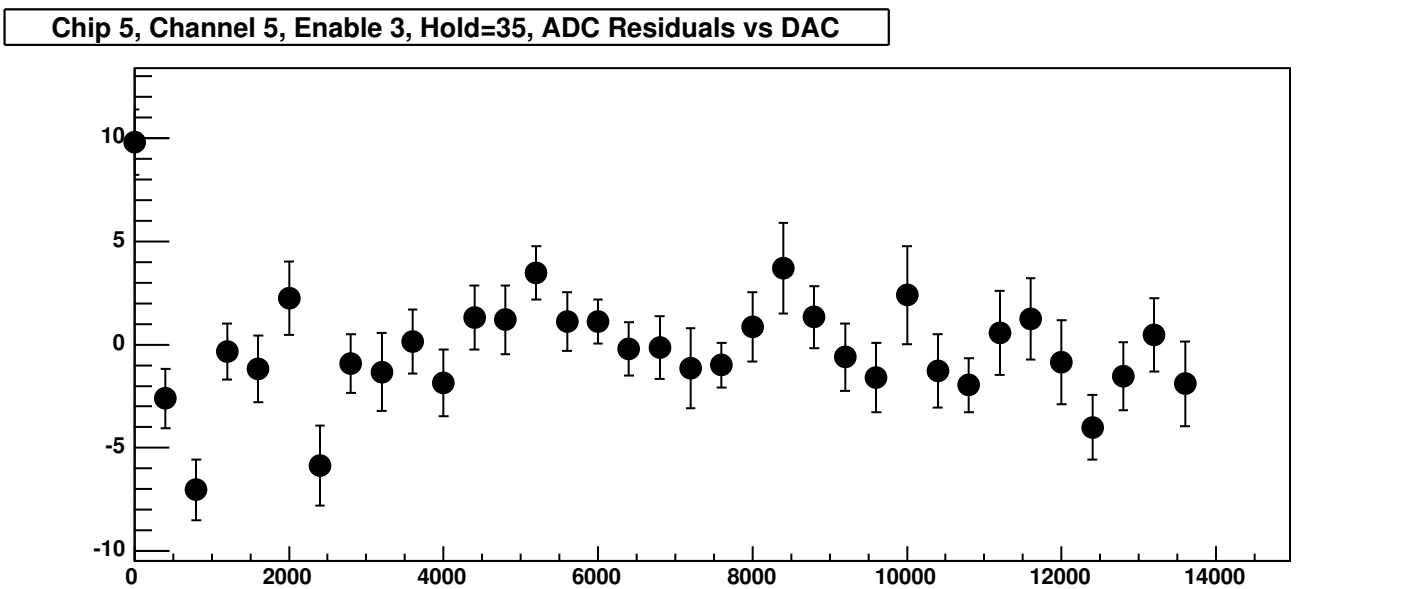

Chip 5, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 45.92 / 23
p0 -214.7 ± 0.7713
p1 0.02123 ± 0.0001205
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

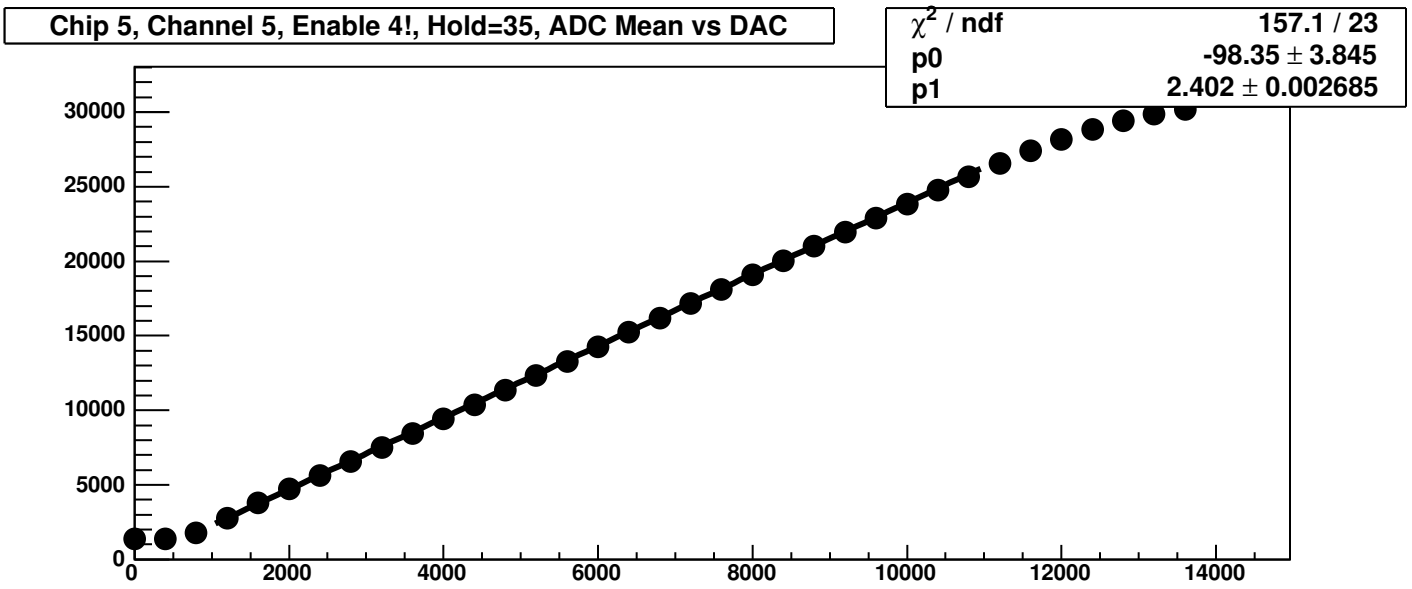

Chip 5, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

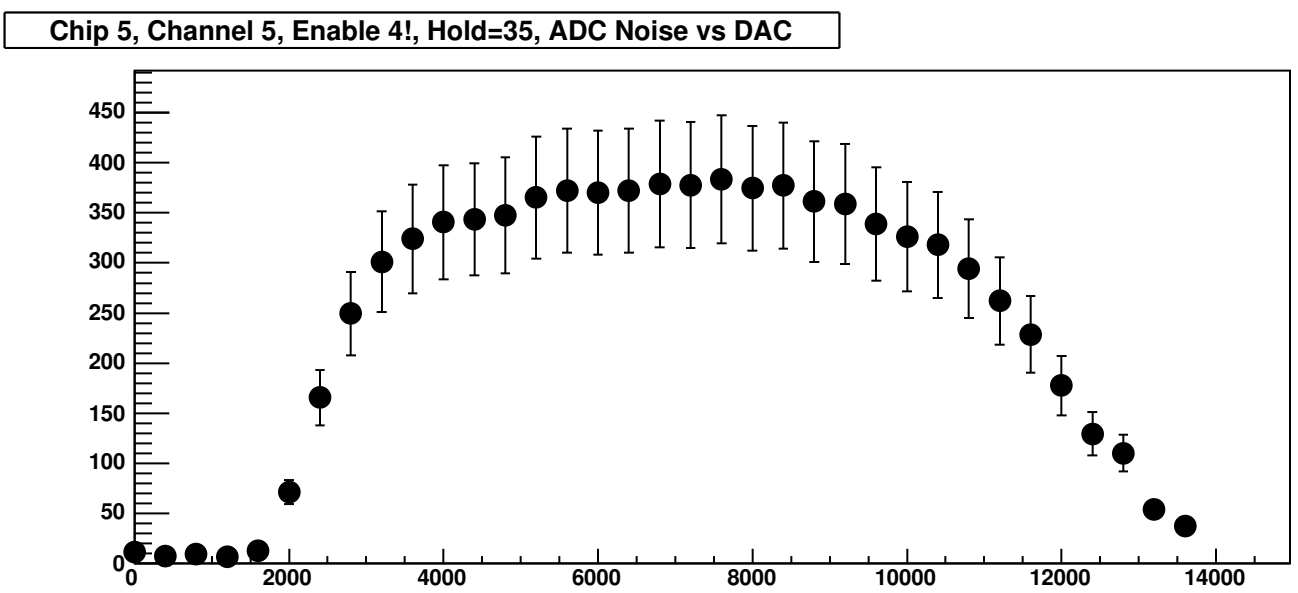

Chip 5, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

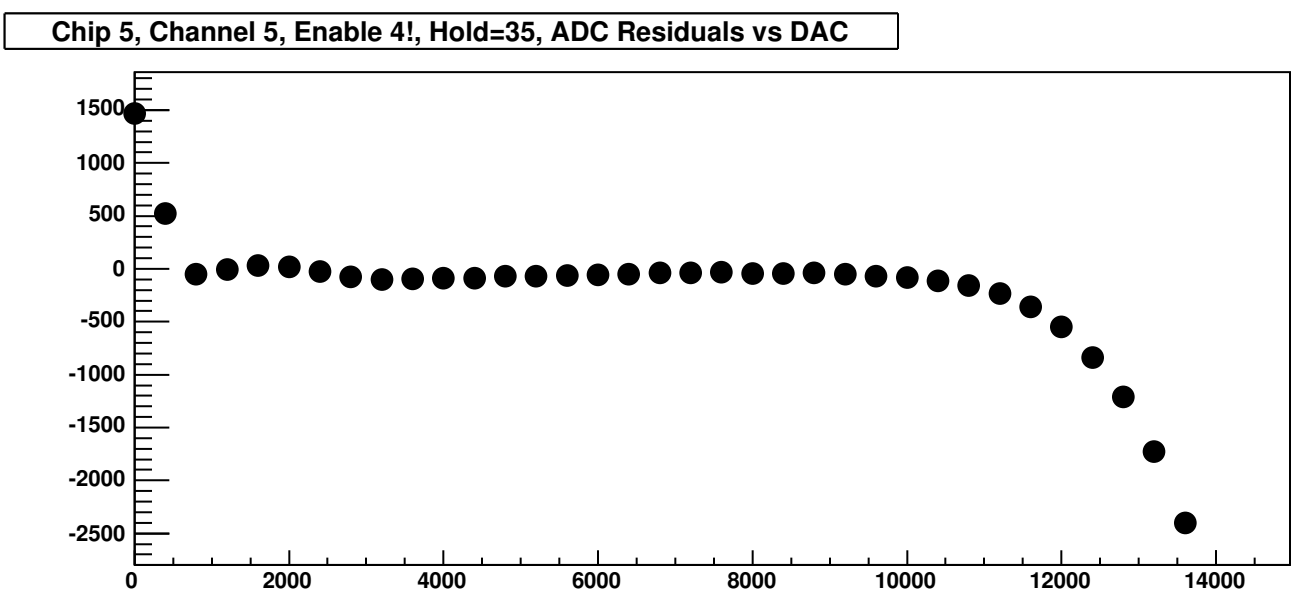

Chip 5, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 33.7 / 23
p0 -216.9 ± 0.7259
p1 0.03249 ± 0.0001104
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

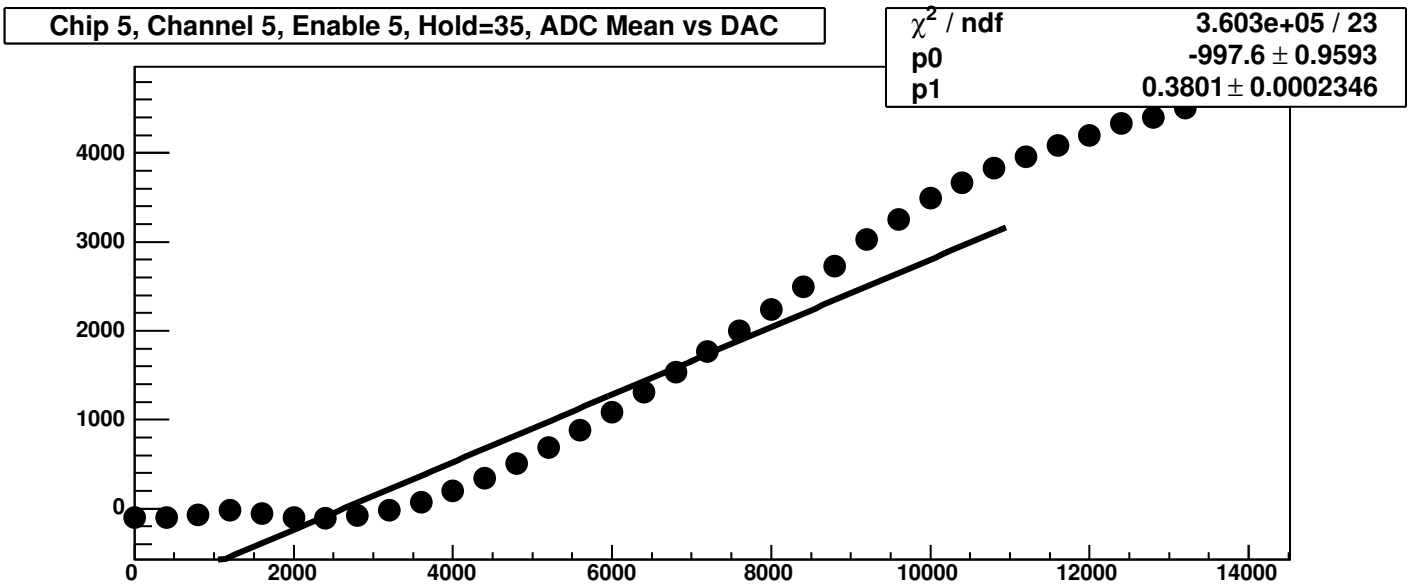

Chip 5, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

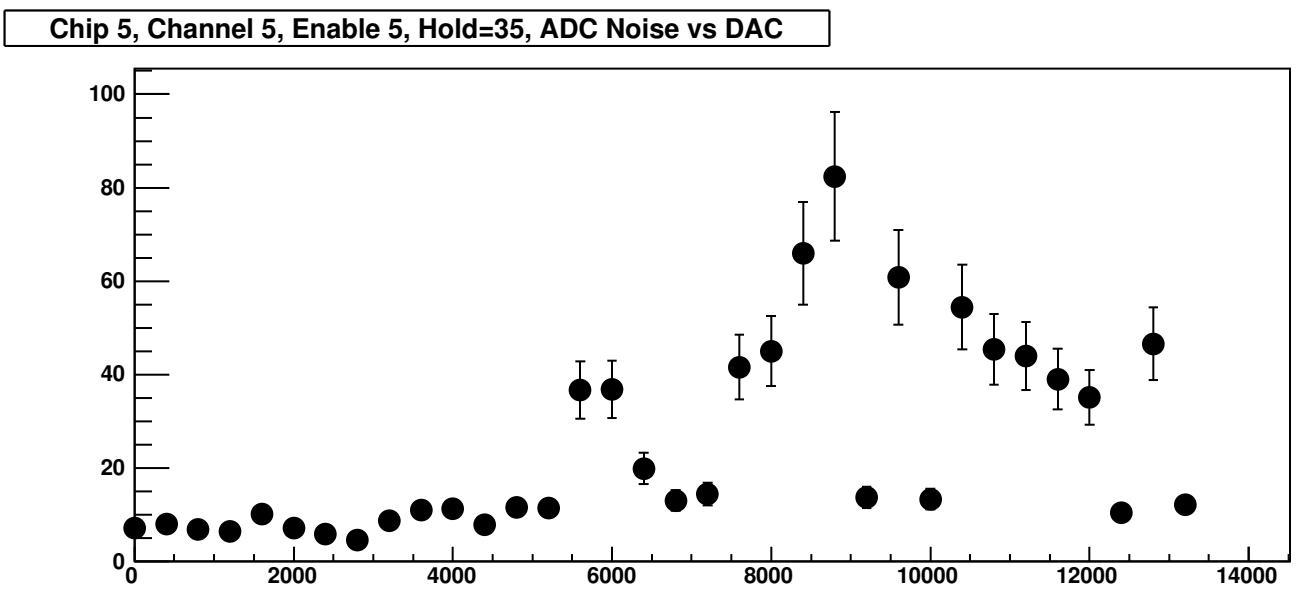

Chip 5, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

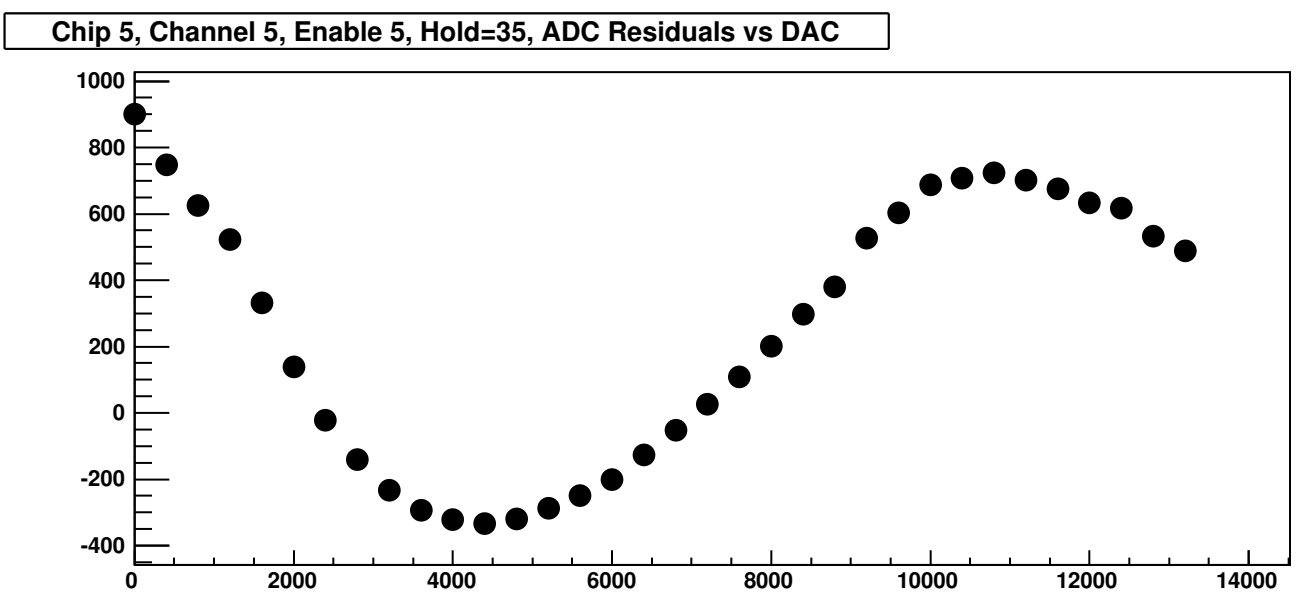

Chip 5, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 157.1 / 23
p0 -98.35 ± 3.845
p1 2.402 ± 0.002685
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

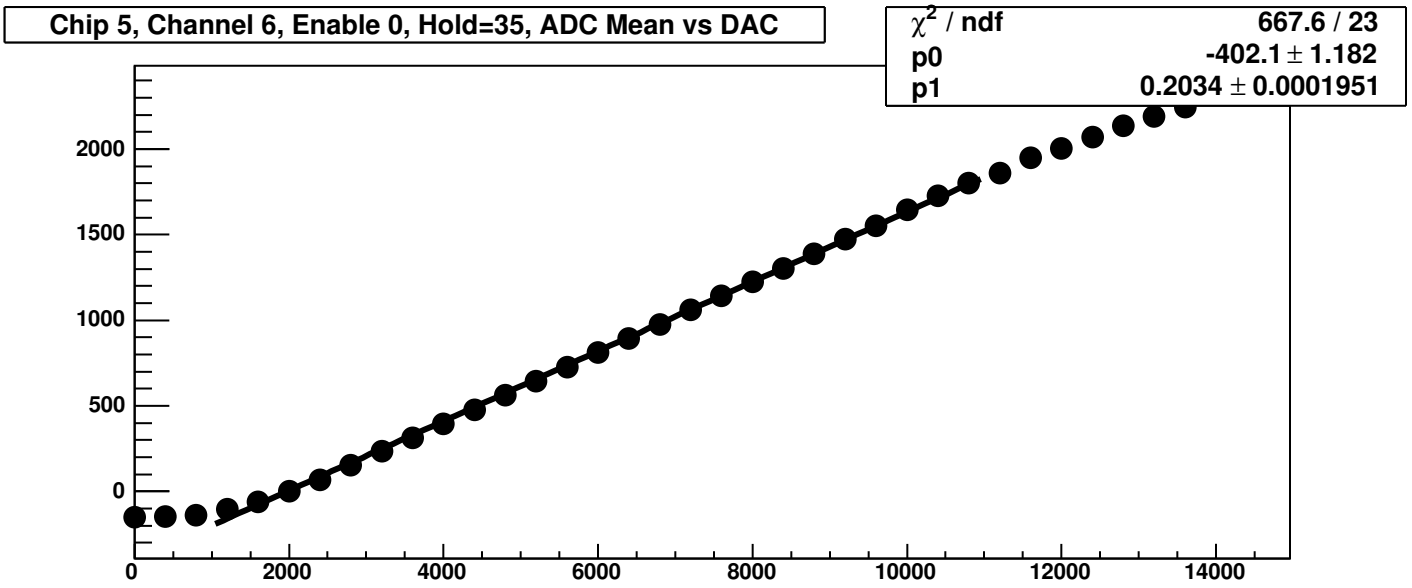

Chip 5, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

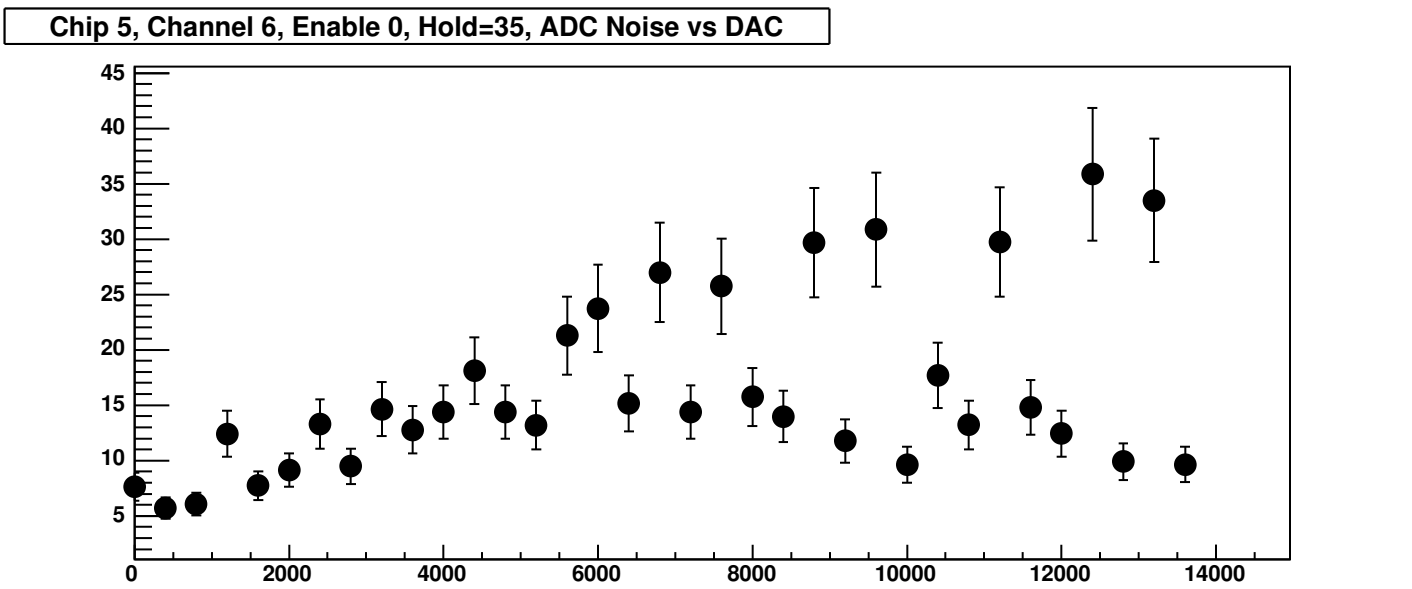

Chip 5, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

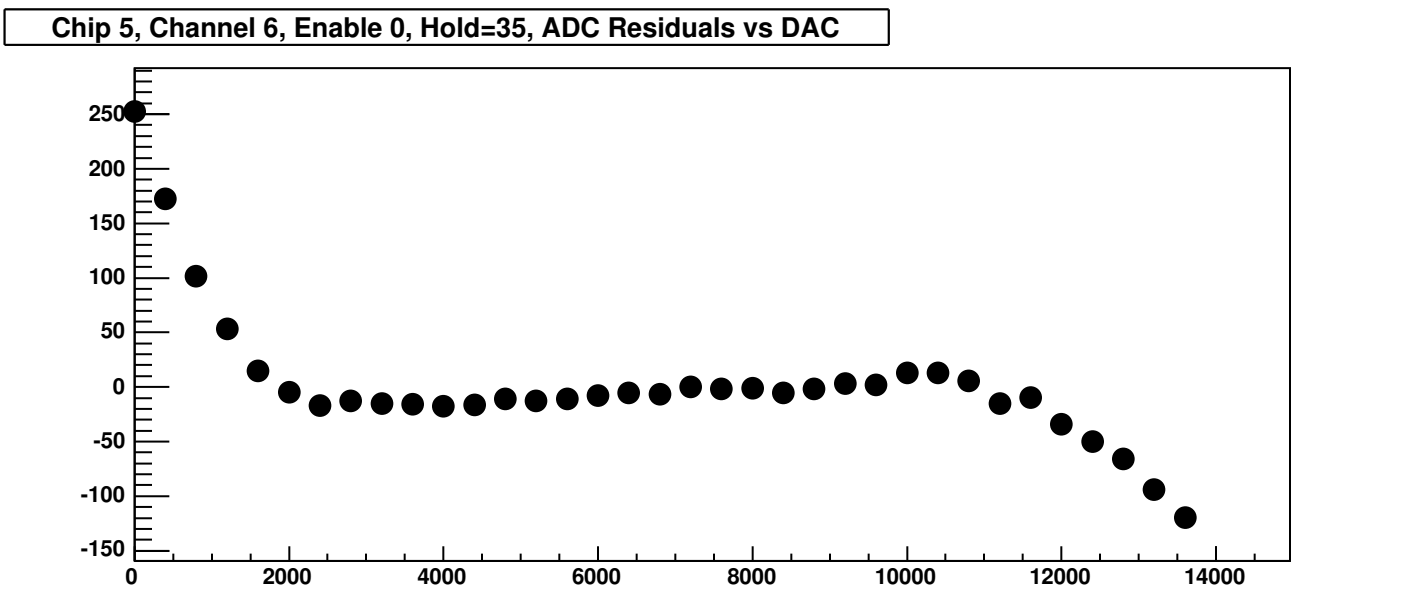

Chip 5, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.603e+05 / 23
p0 -997.6 ± 0.9593
p1 0.3801 ± 0.0002346
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

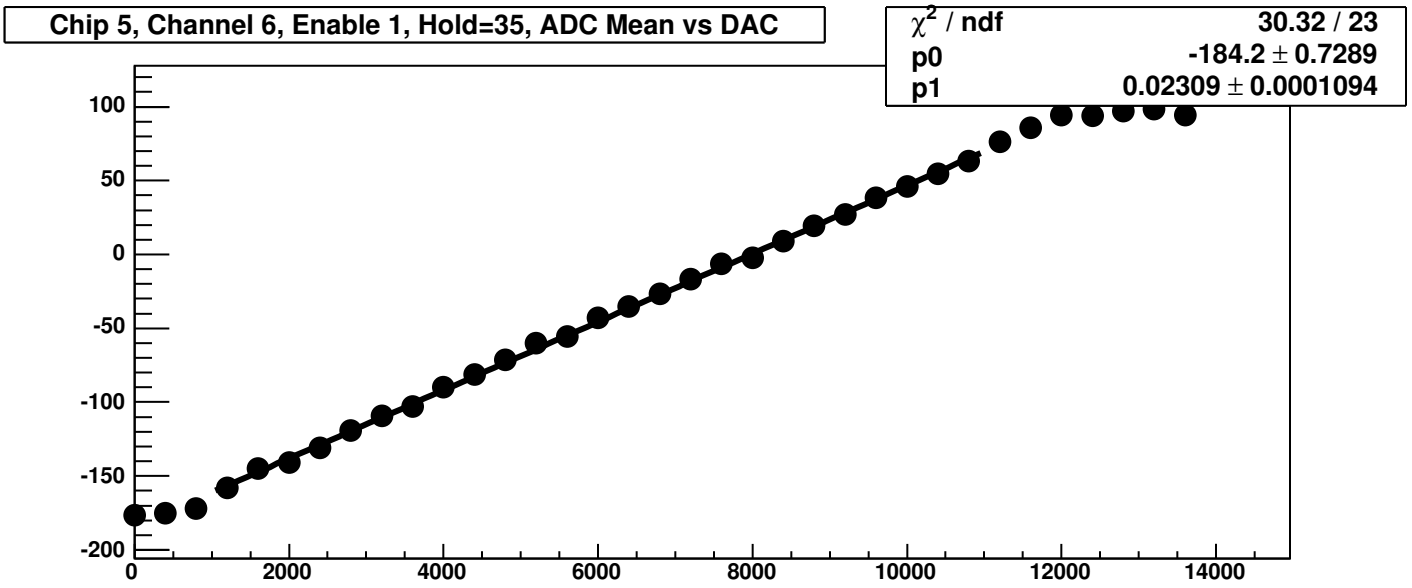

Chip 5, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

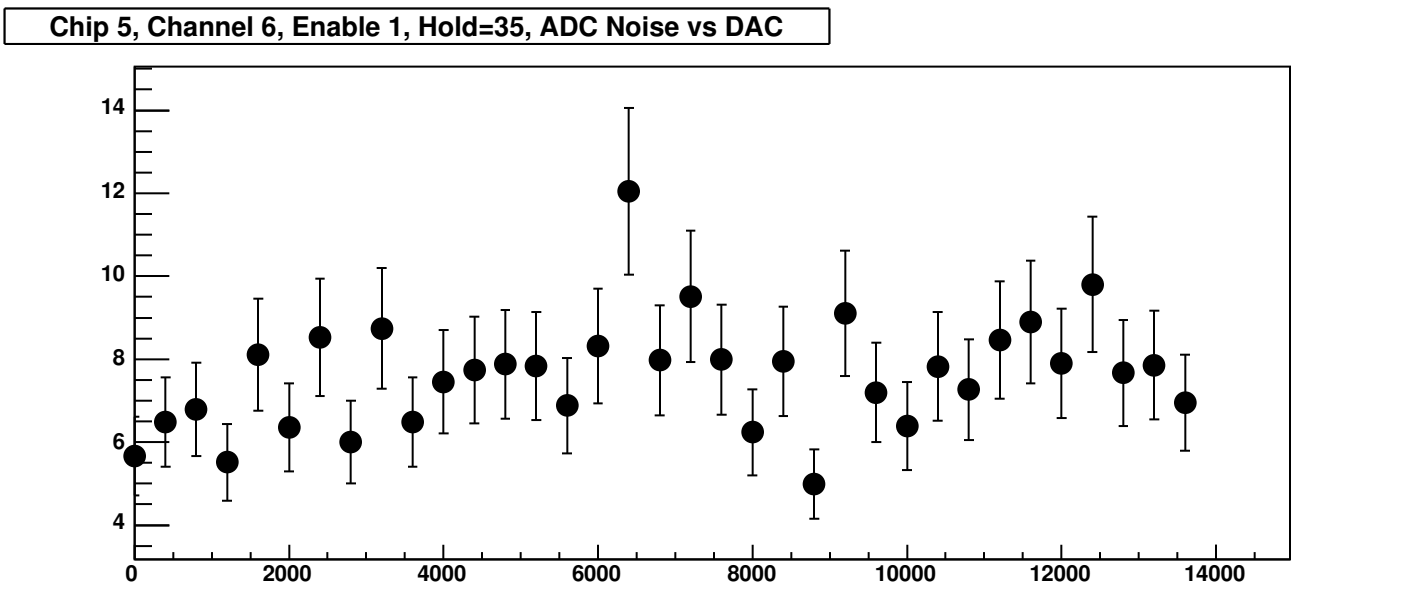

Chip 5, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

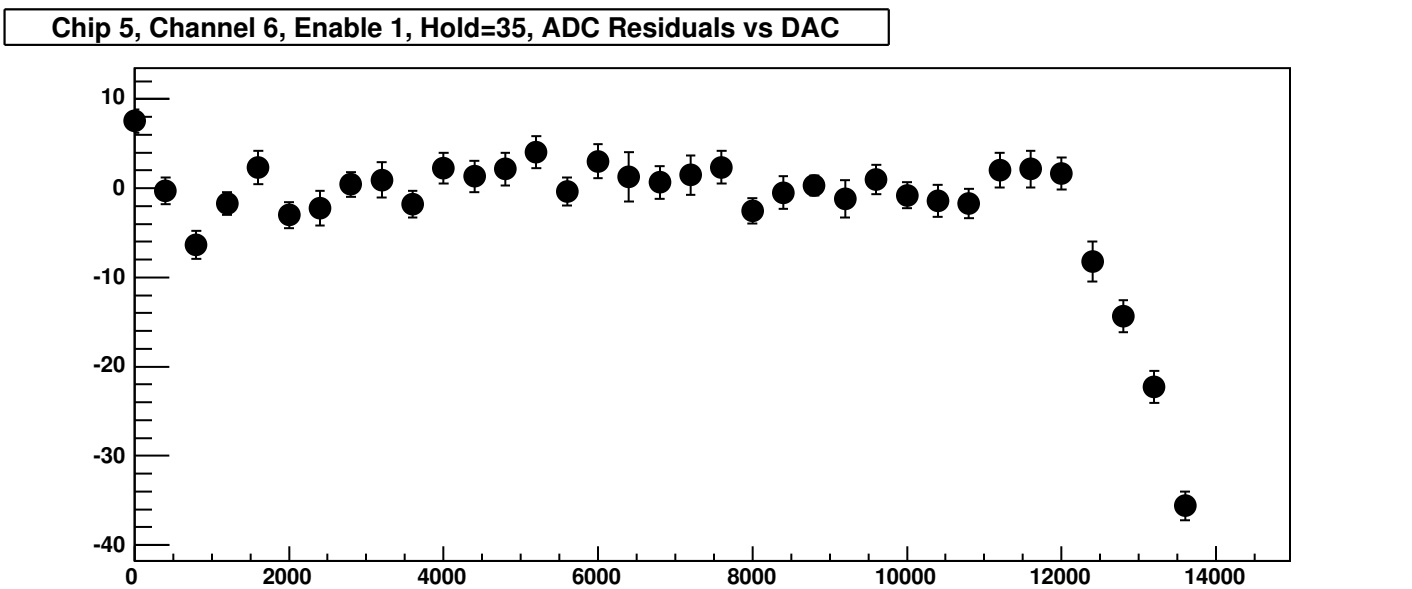

Chip 5, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 667.6 / 23
p0 -402.1 ± 1.182
p1 0.2034 ± 0.0001951
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

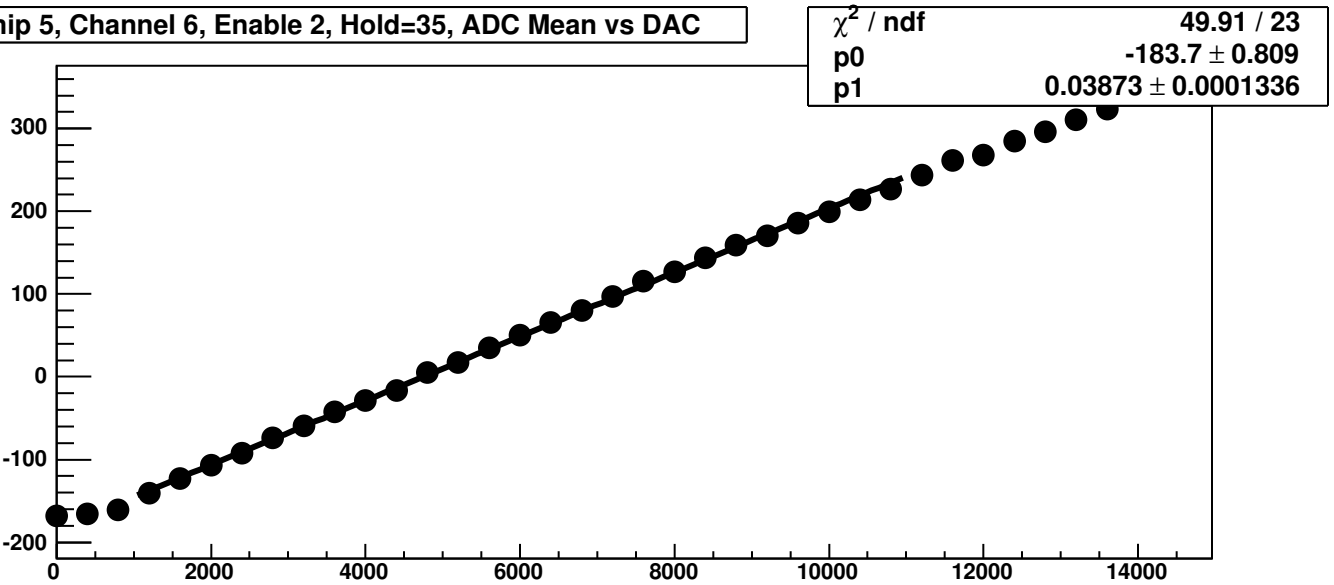

Chip 5, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 30.32 / 23
p0 -184.2 ± 0.7289
p1 0.02309 ± 0.0001094
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

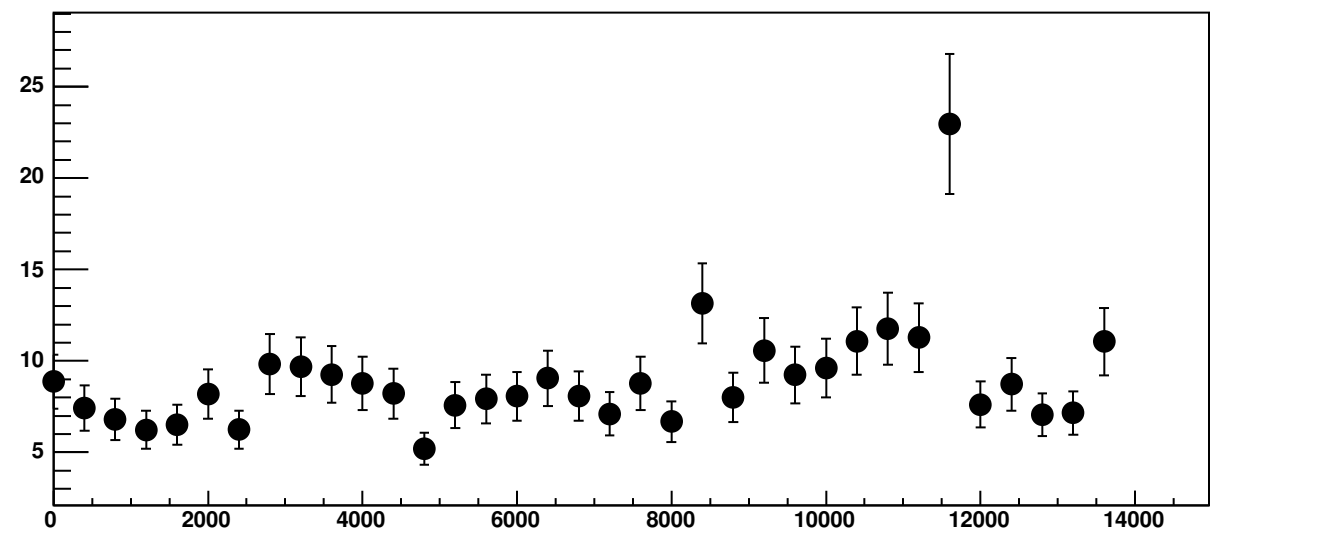

Chip 5, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

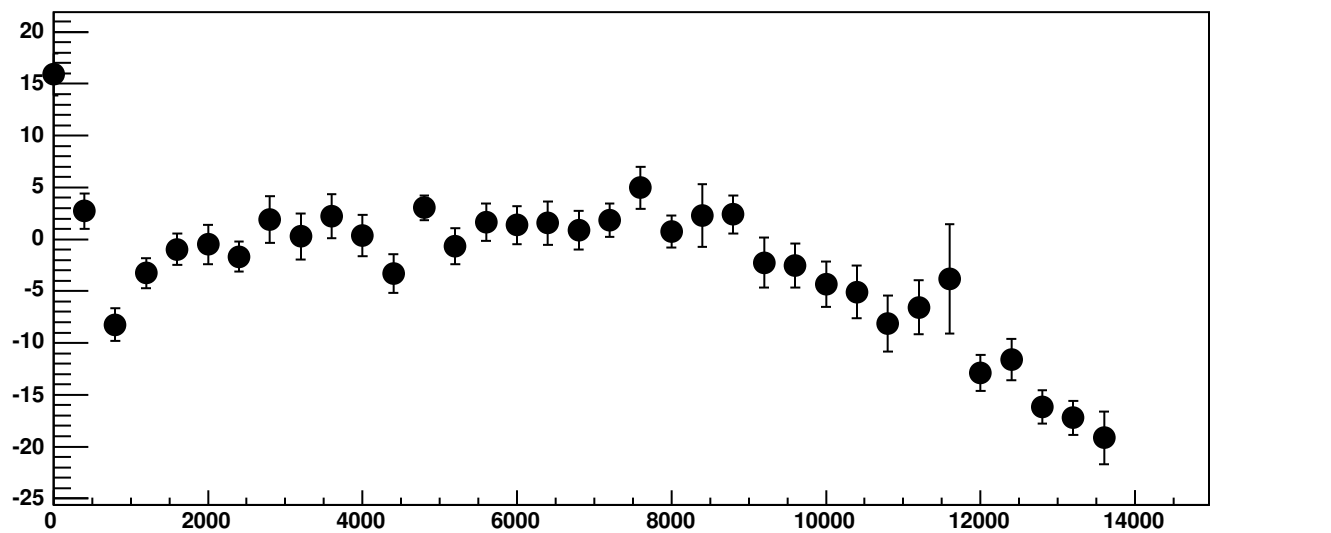
Chip 5, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC

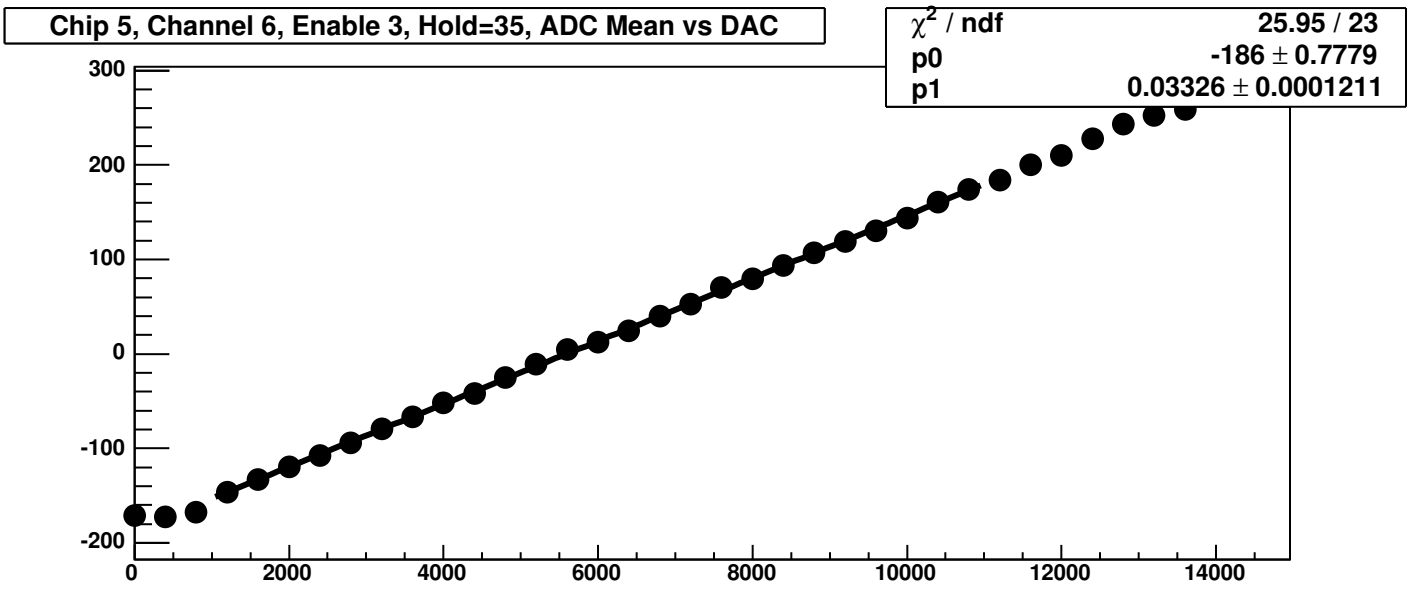


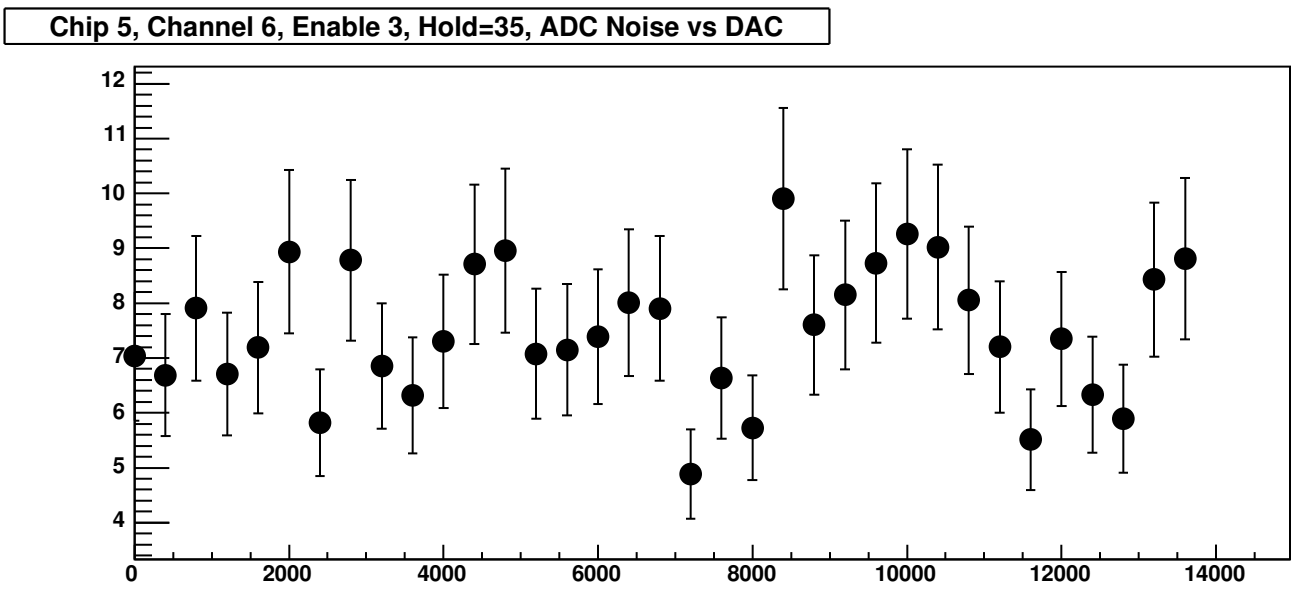
Chip 5, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC

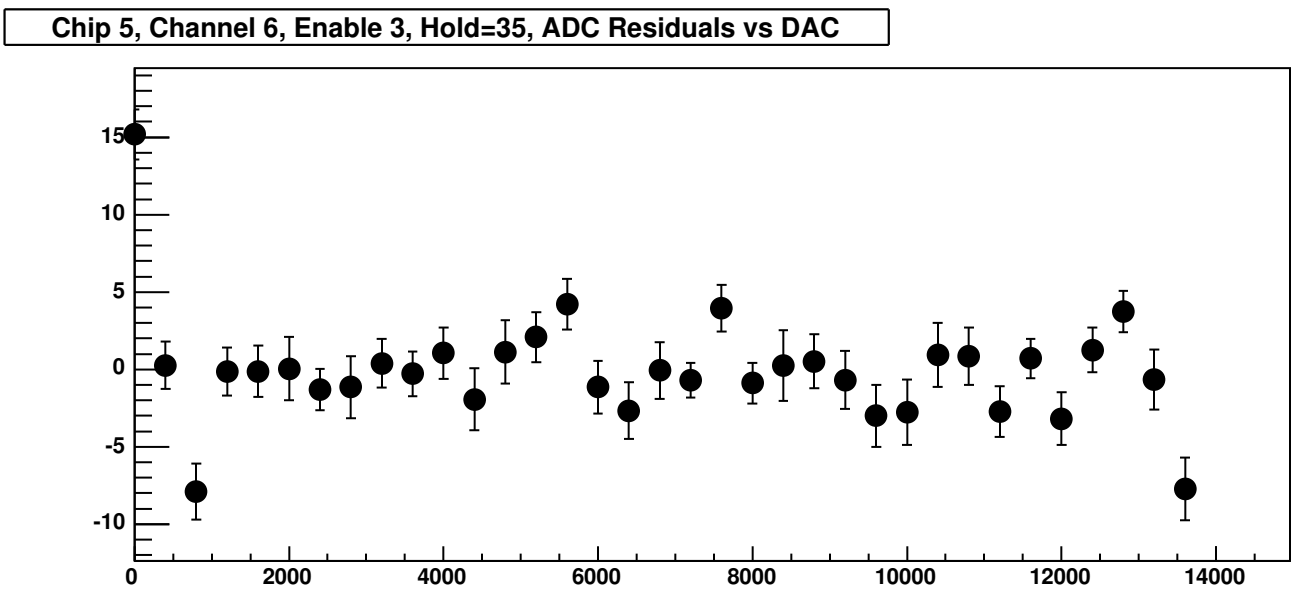


Chip 5, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC

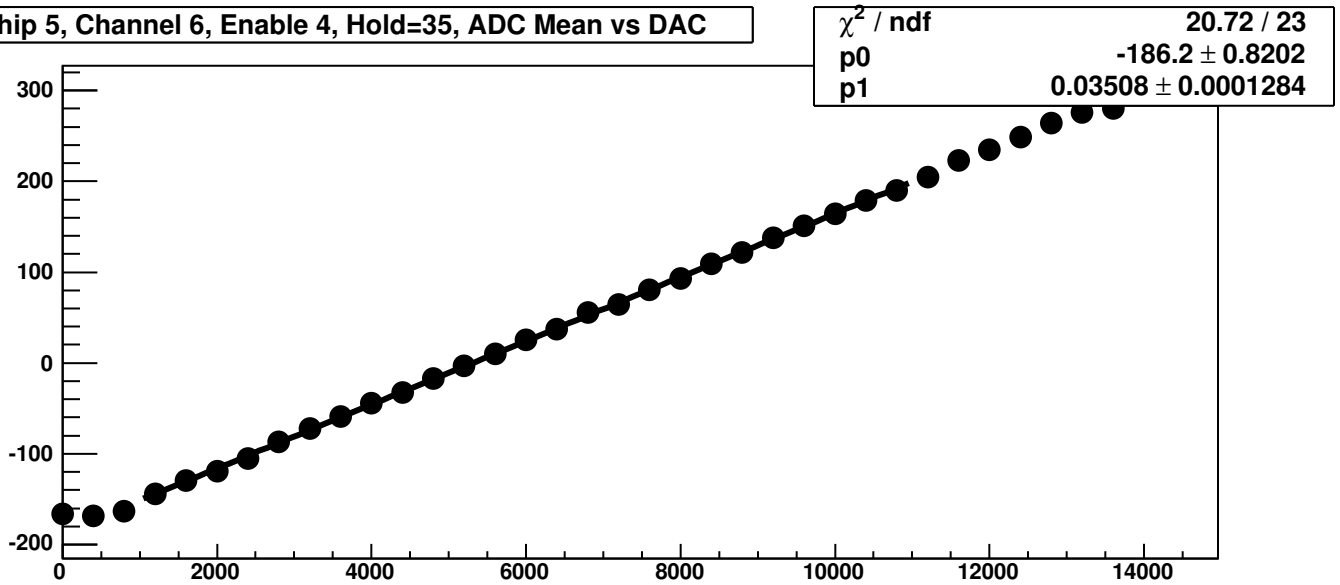



Chip 5, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.95 / 23
p0 -186 ± 0.7779
p1 0.03326 ± 0.0001211
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

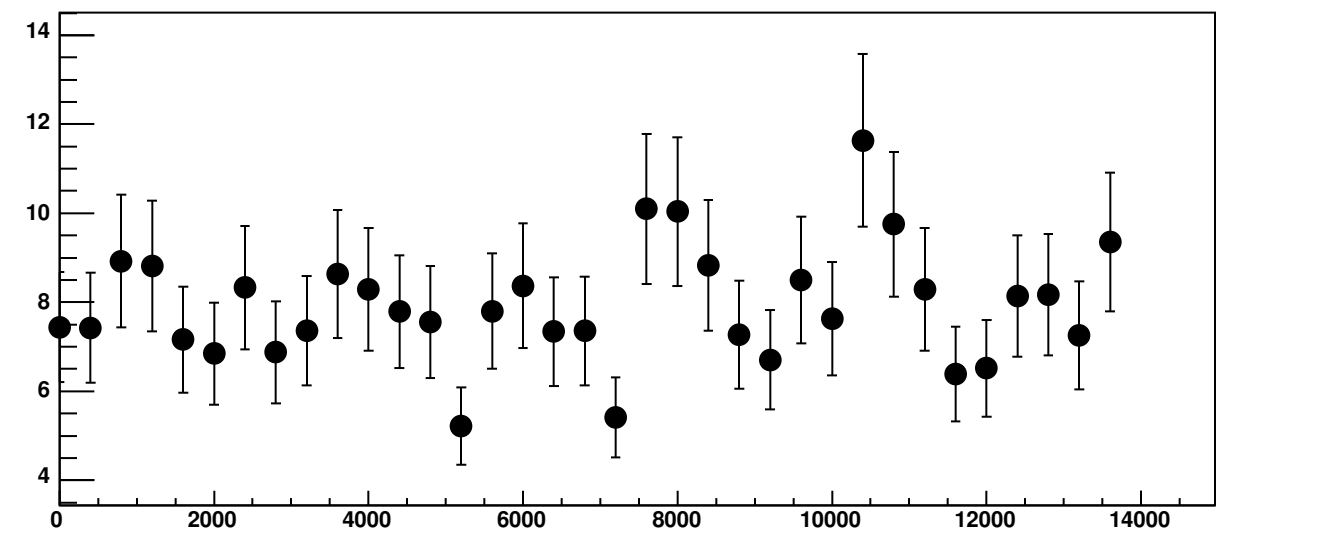

Chip 5, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

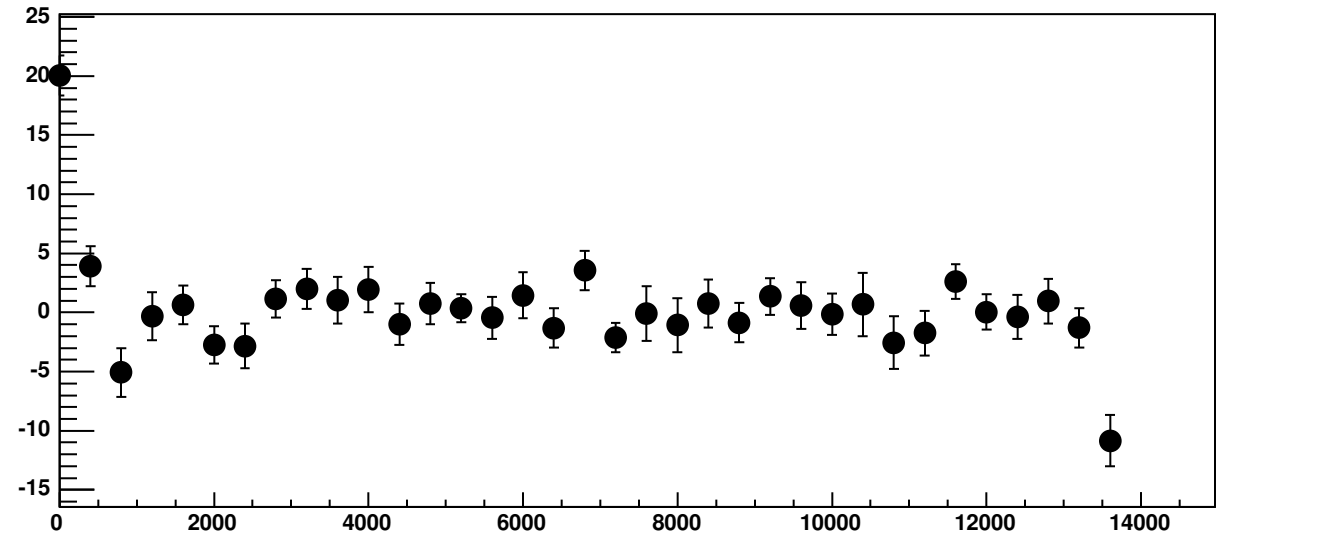
Chip 5, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

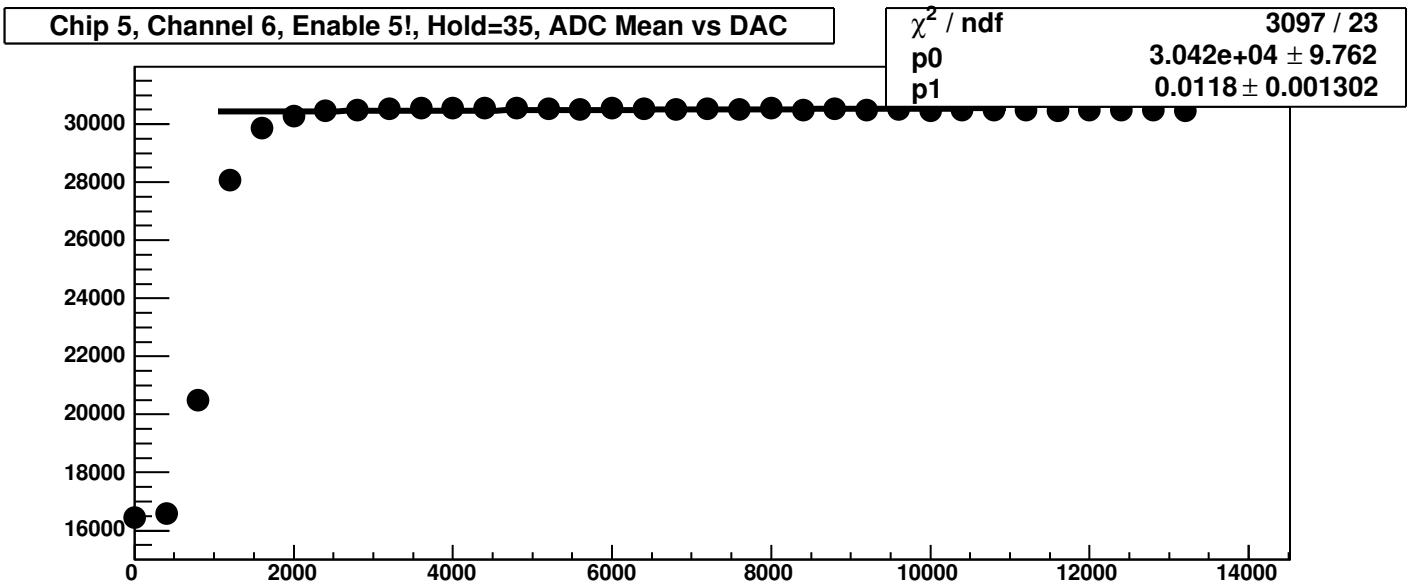


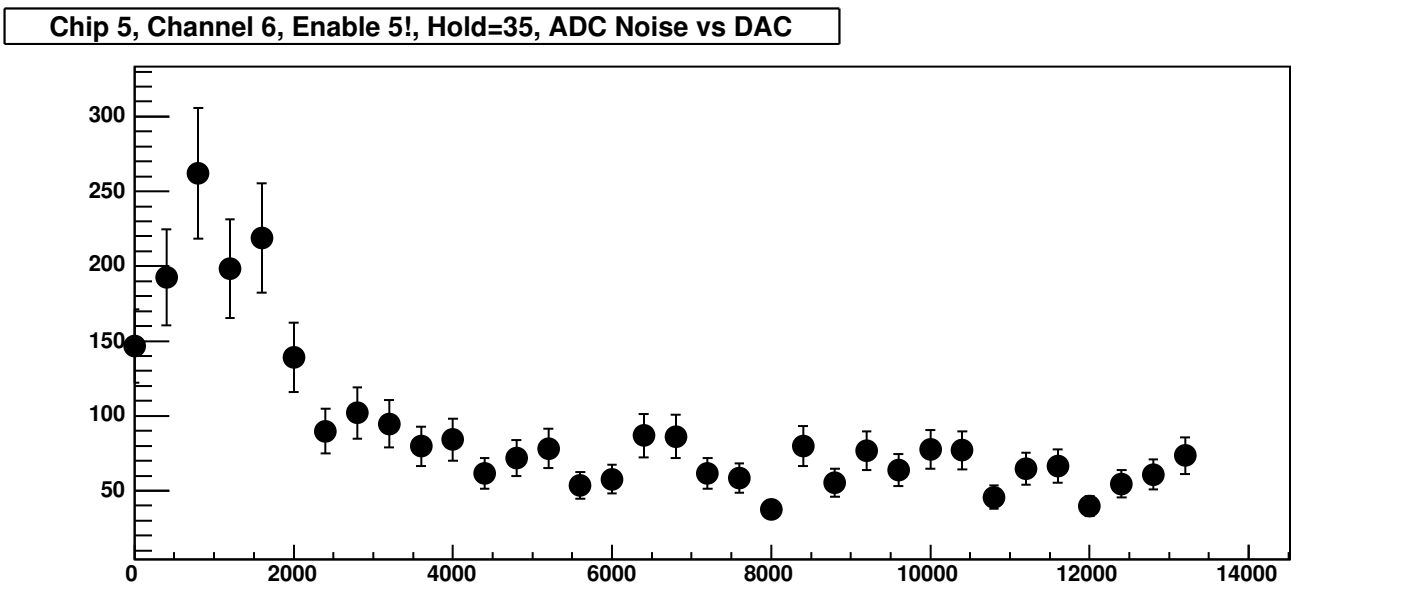
Chip 5, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

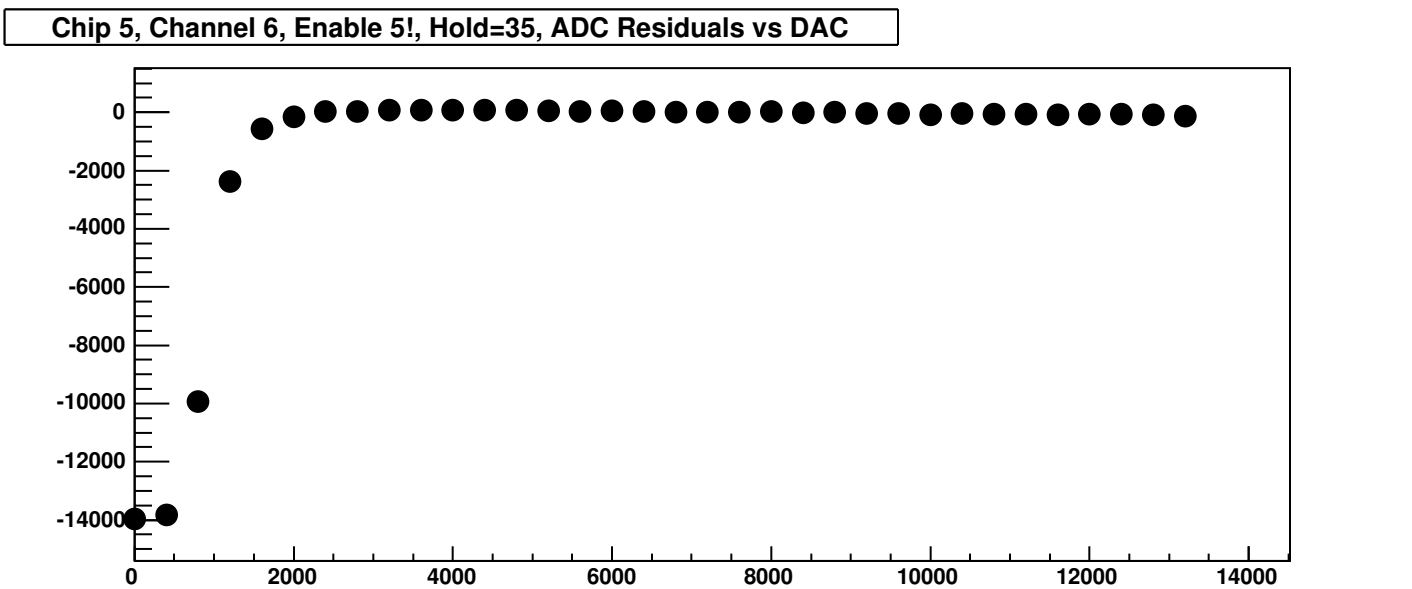


Chip 5, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

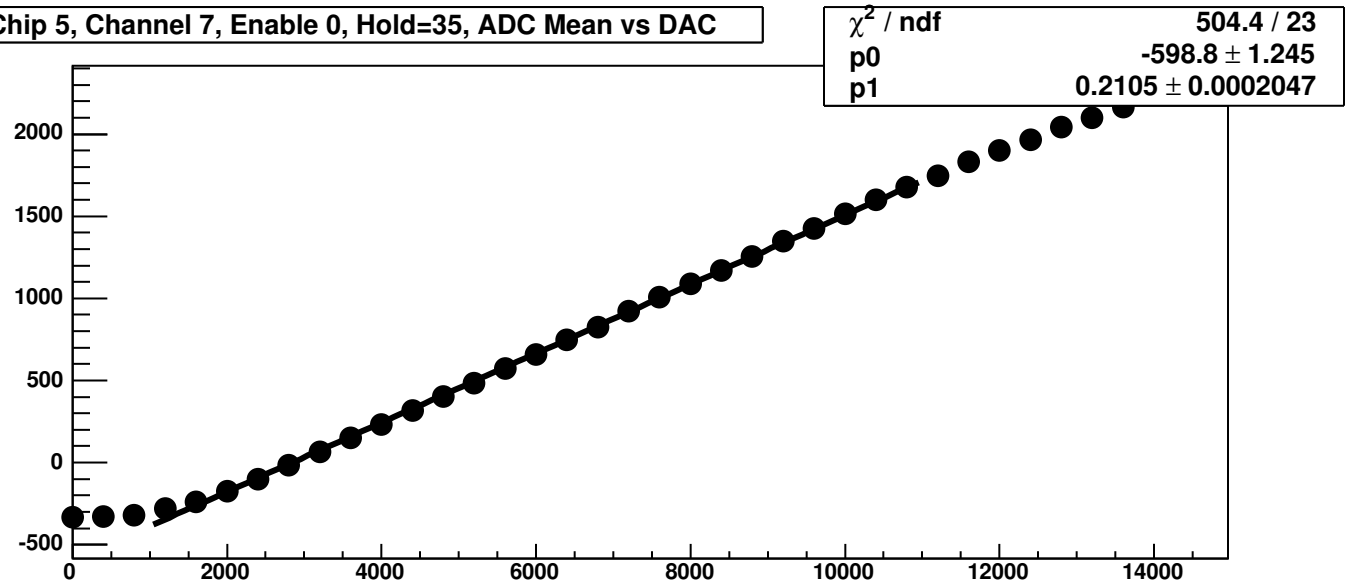



Chip 5, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3097 / 23
p0 3.042e+04 ± 9.762
p1 0.0118 ± 0.001302
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

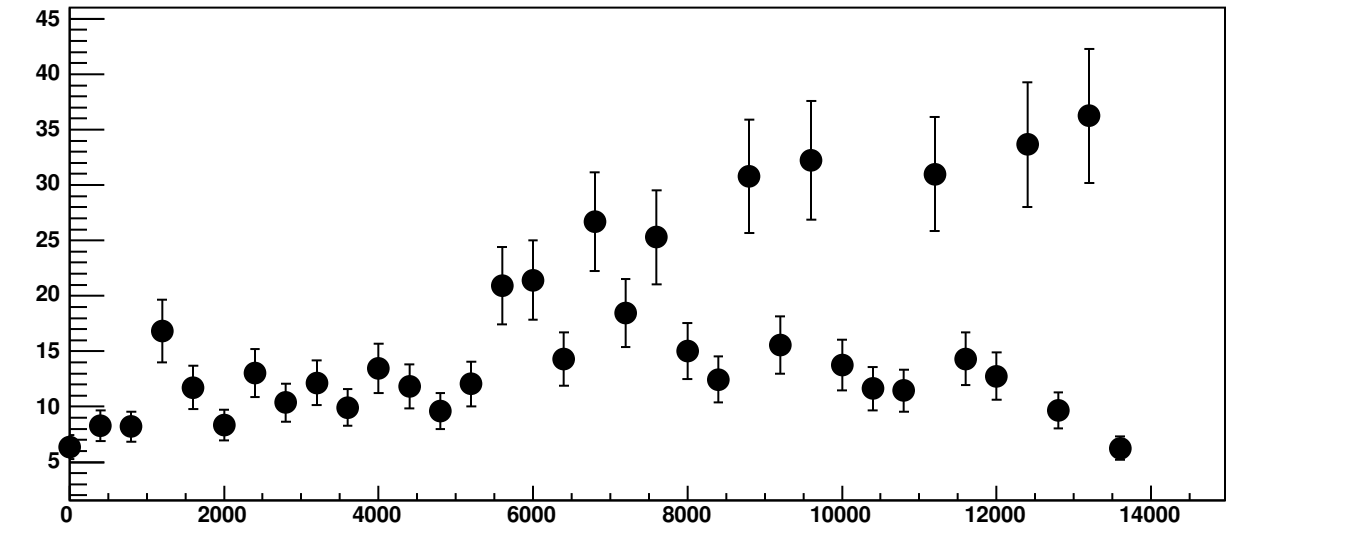

Chip 5, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

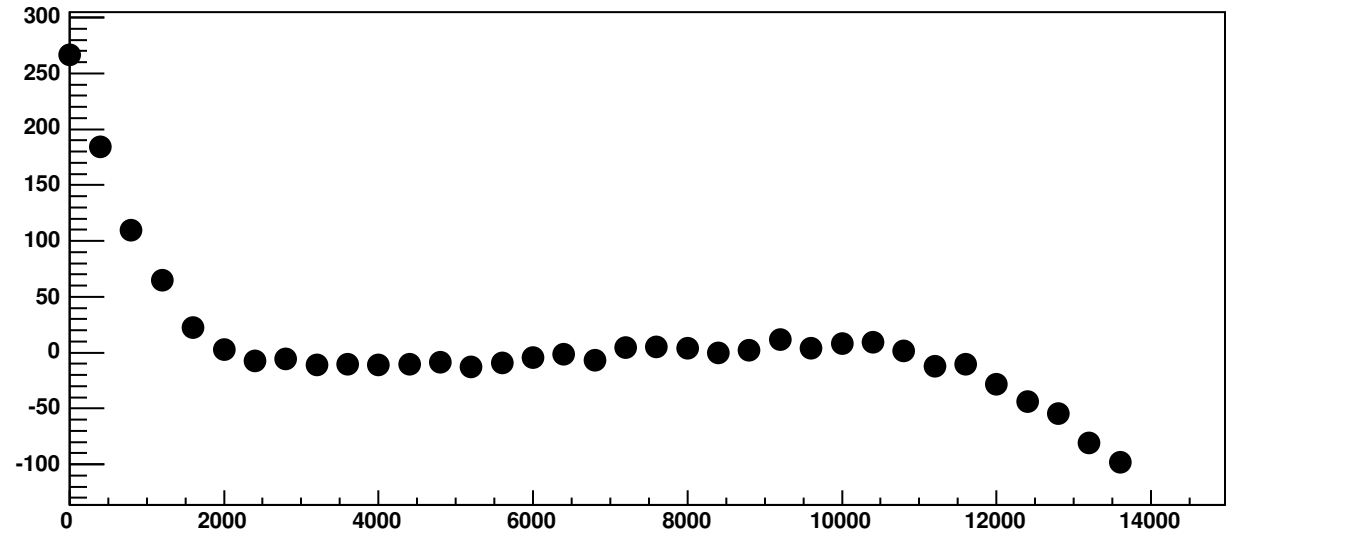
Chip 5, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

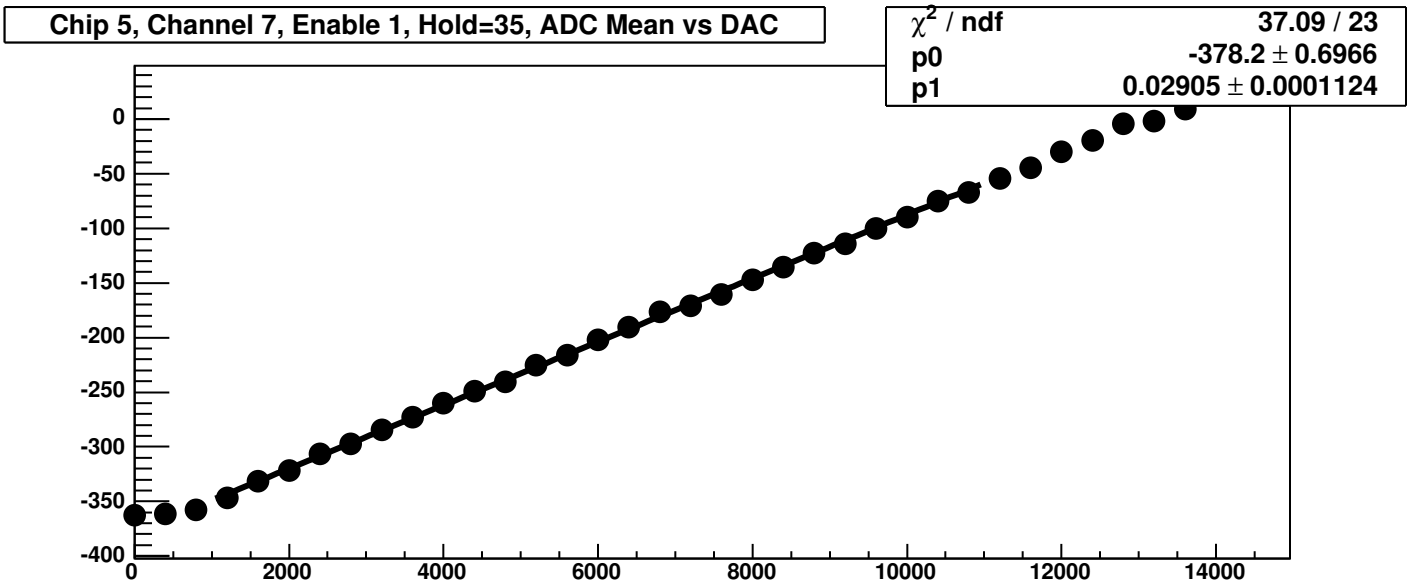


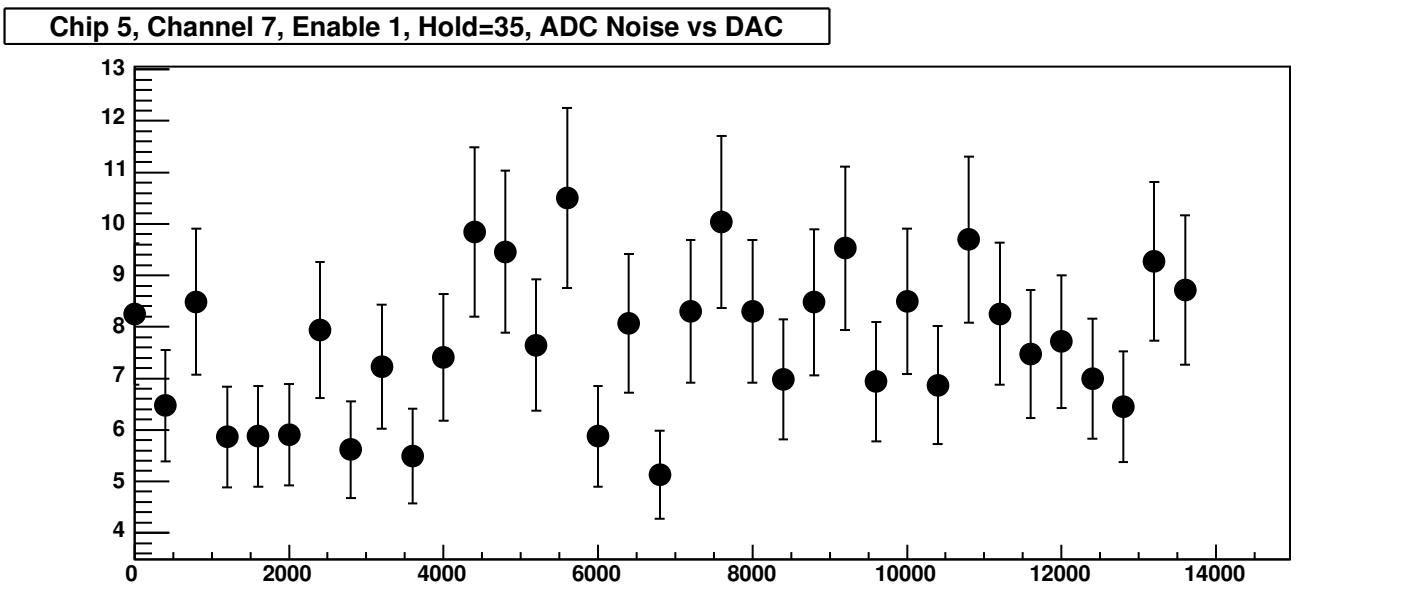
Chip 5, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC

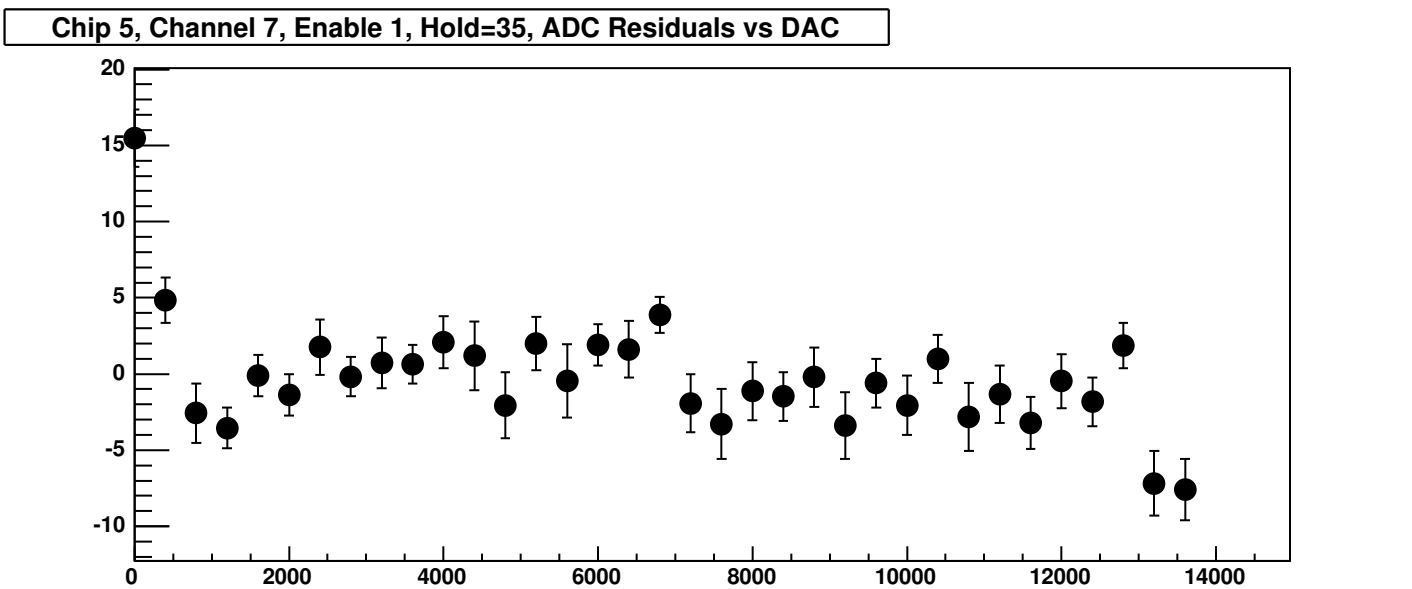


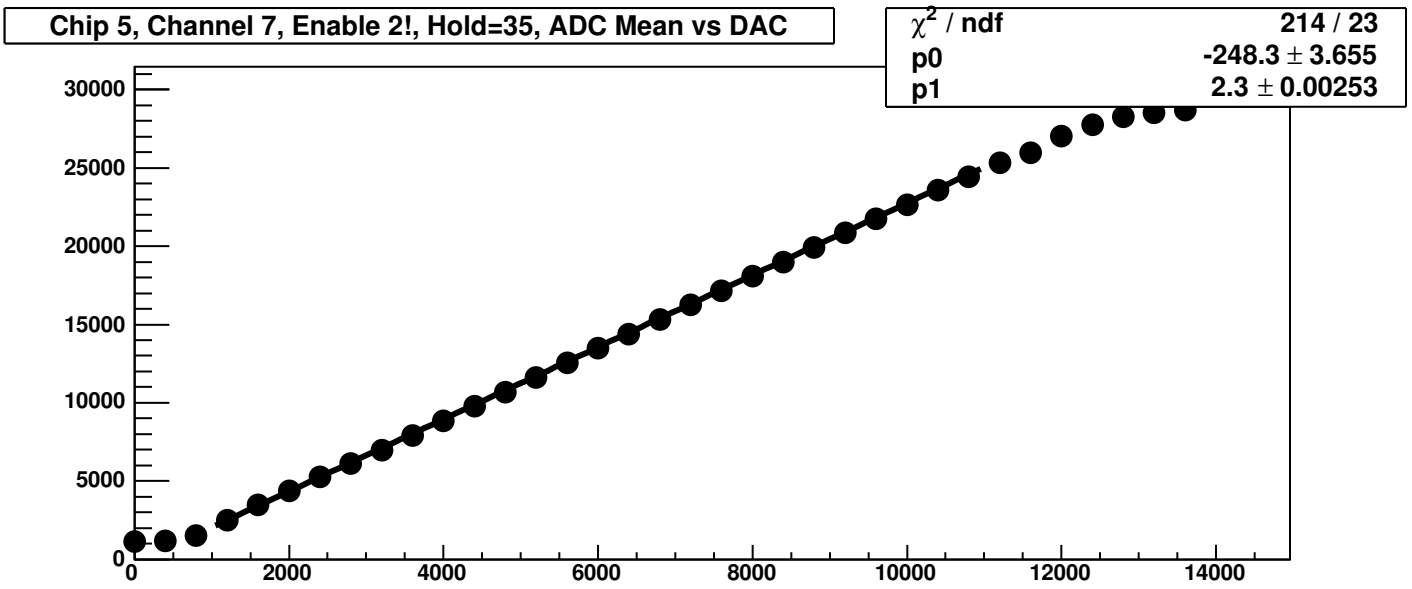
Chip 5, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

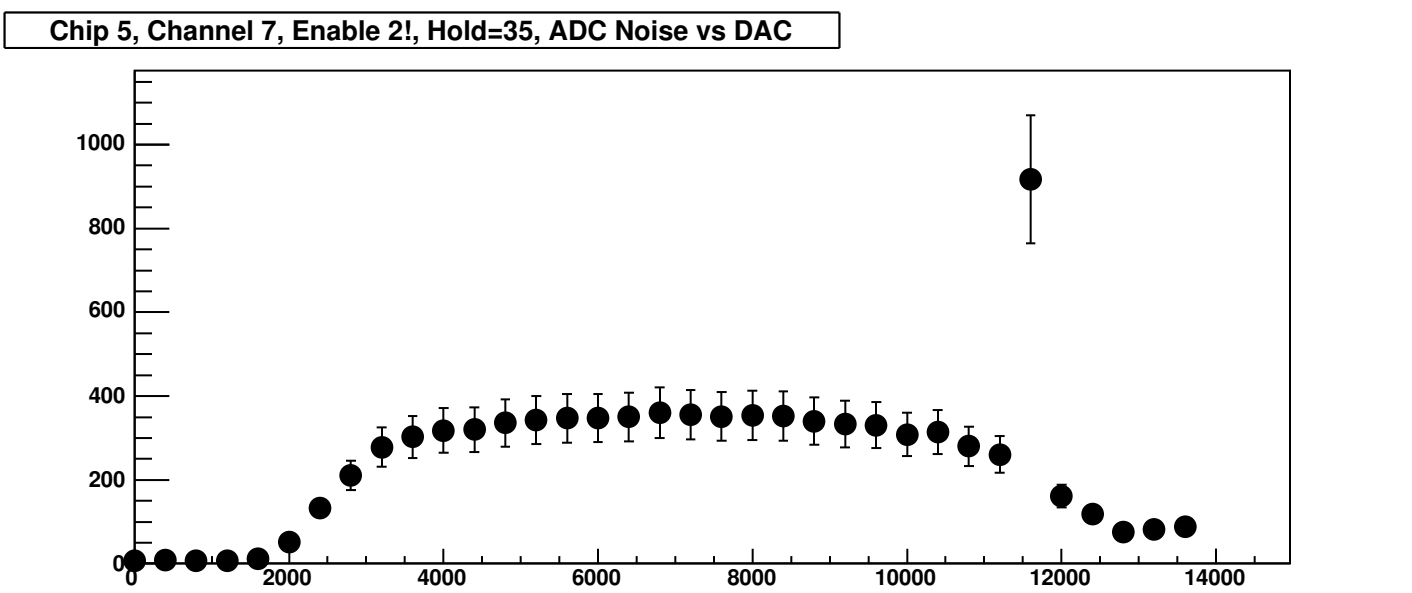


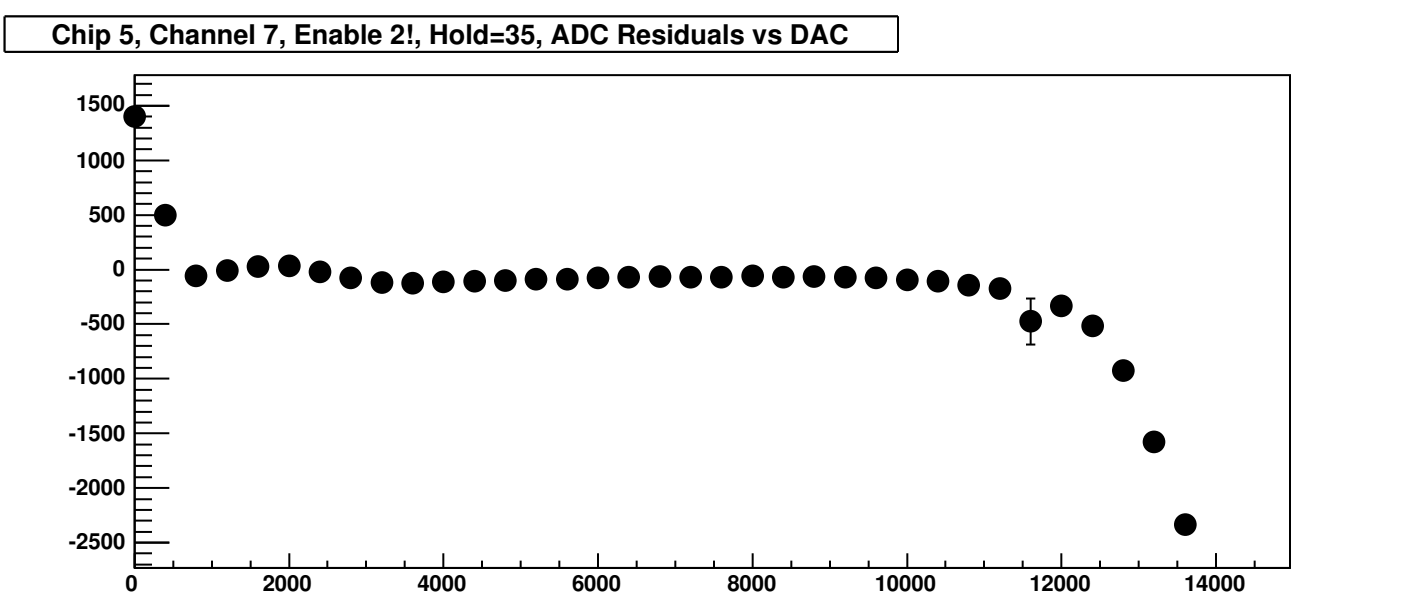

Chip 5, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 37.09 / 23
p0 -378.2 ± 0.6966
p1 0.02905 ± 0.0001124
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

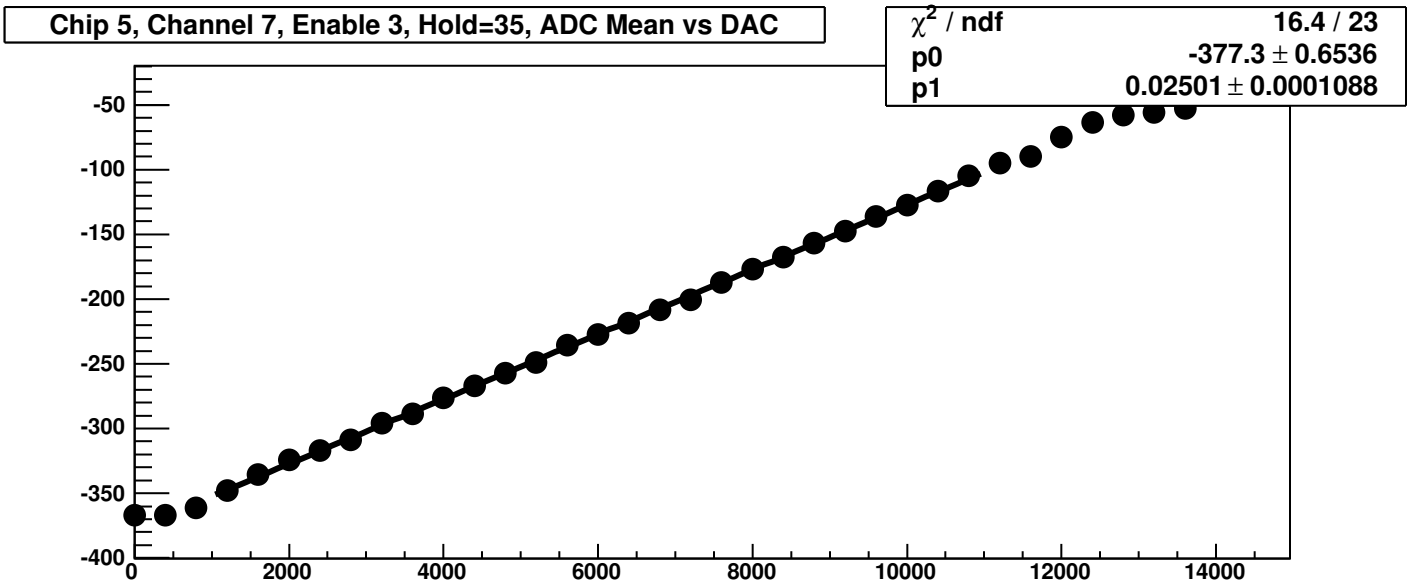

Chip 5, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

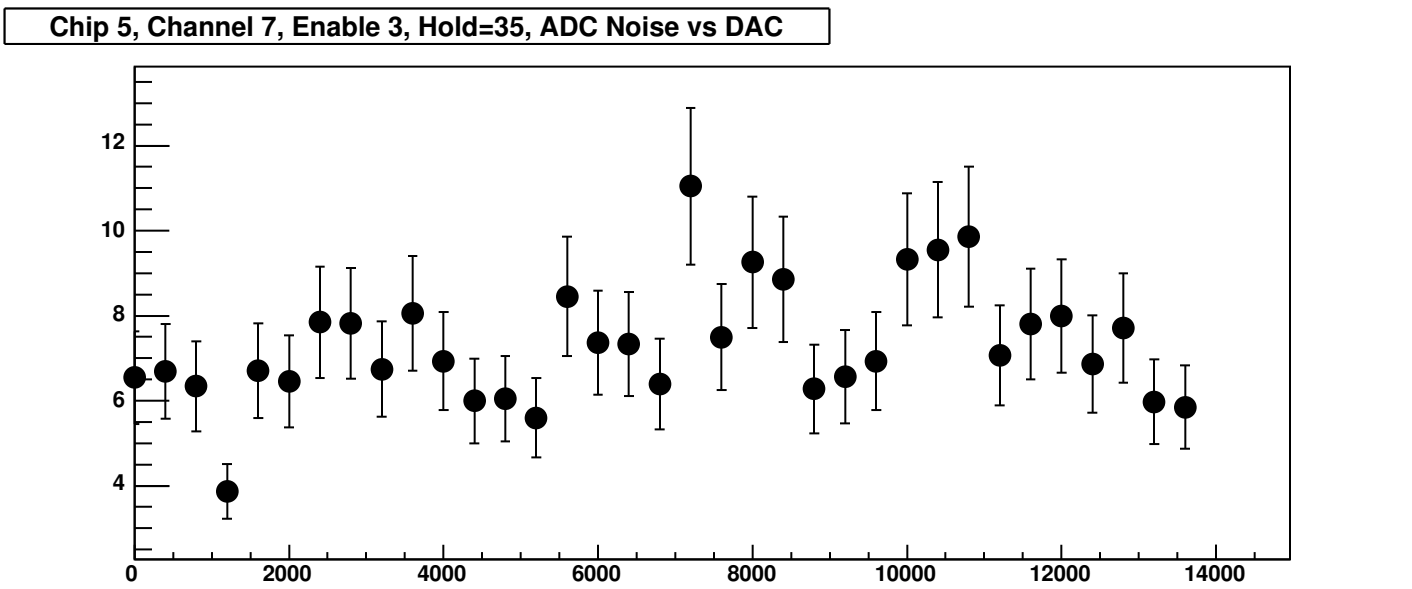

Chip 5, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

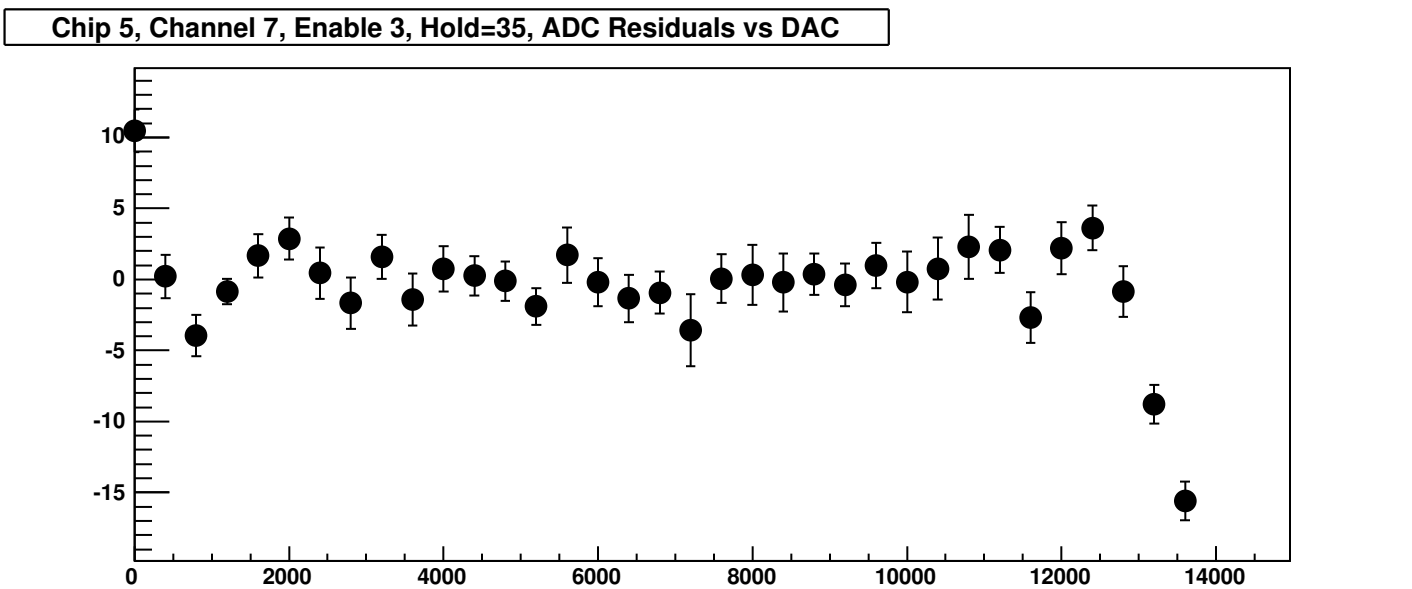

Chip 5, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 214 / 23
p0 -248.3 ± 3.655
p1 2.3 ± 0.00253
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

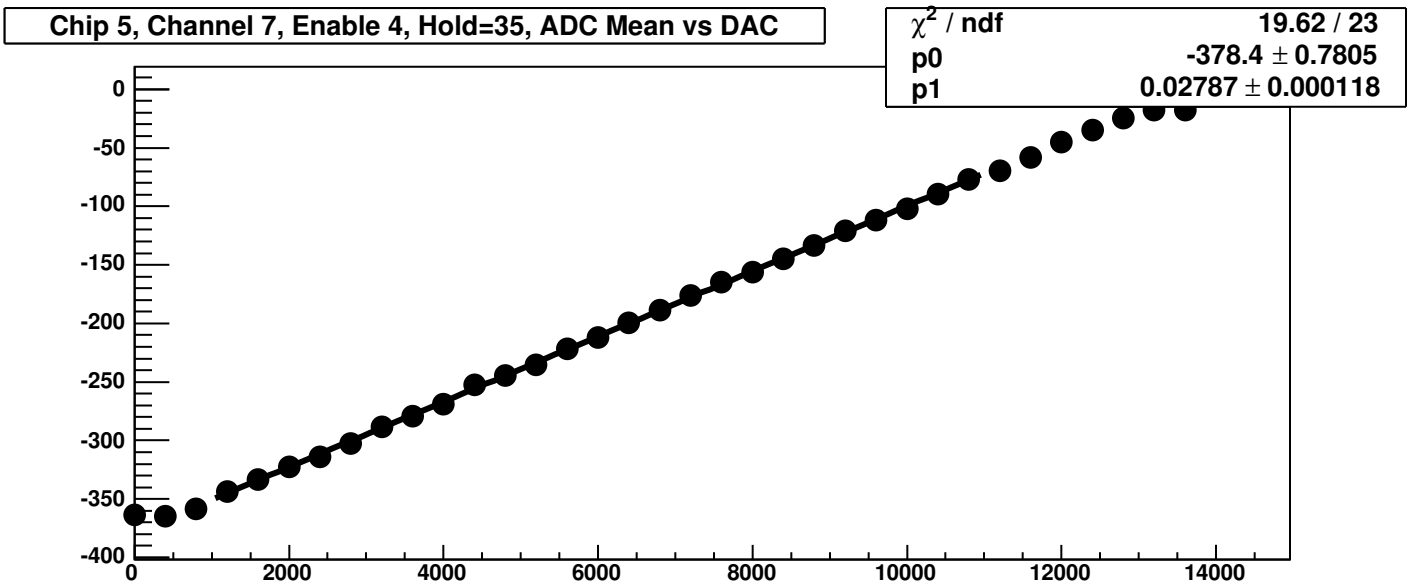

Chip 5, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

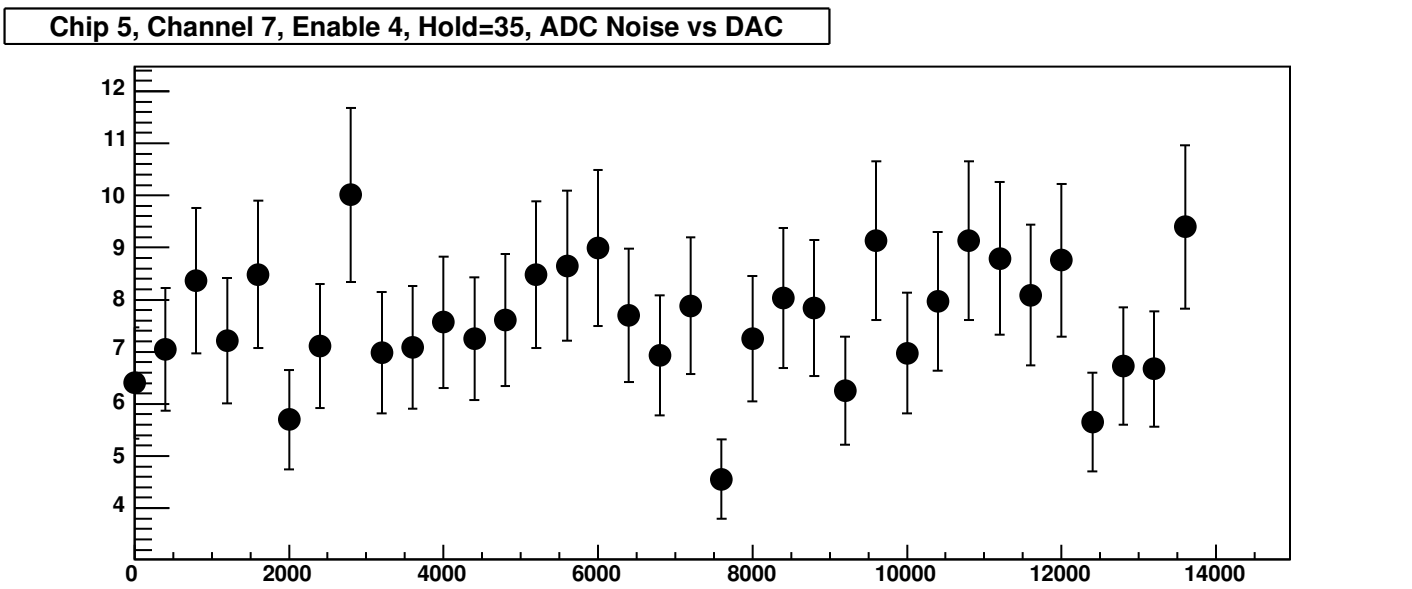

Chip 5, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

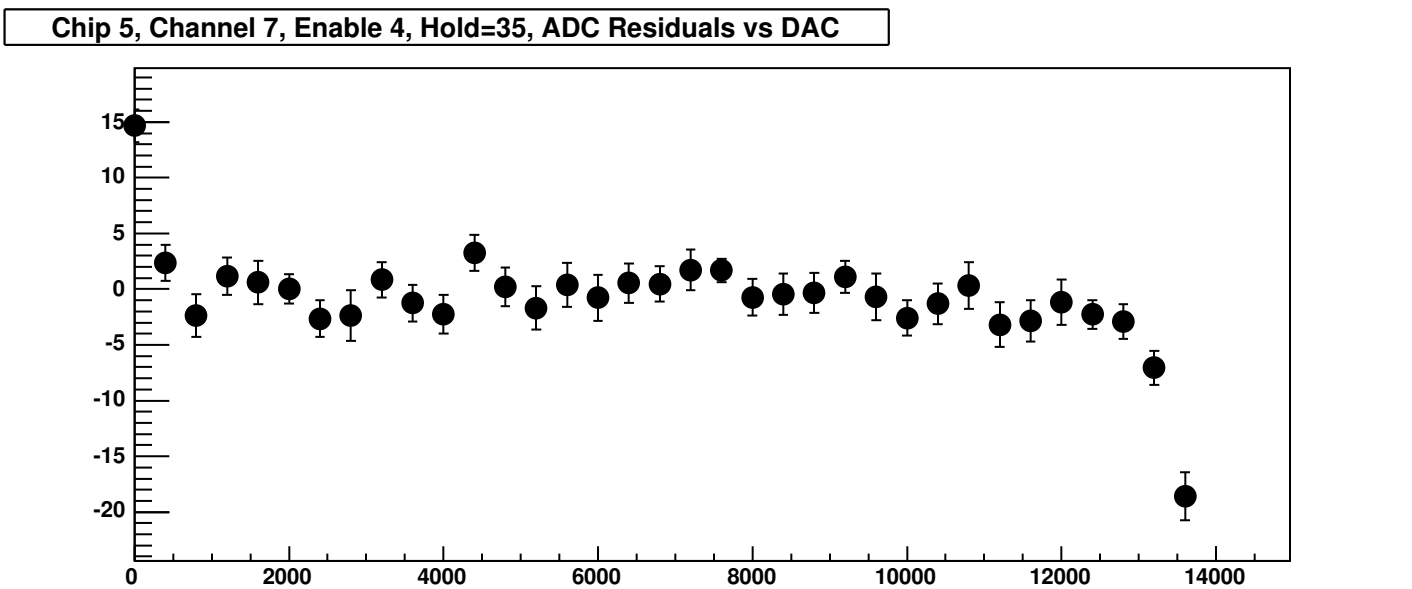

Chip 5, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 16.4 / 23
p0 -377.3 ± 0.6536
p1 0.02501 ± 0.0001088
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

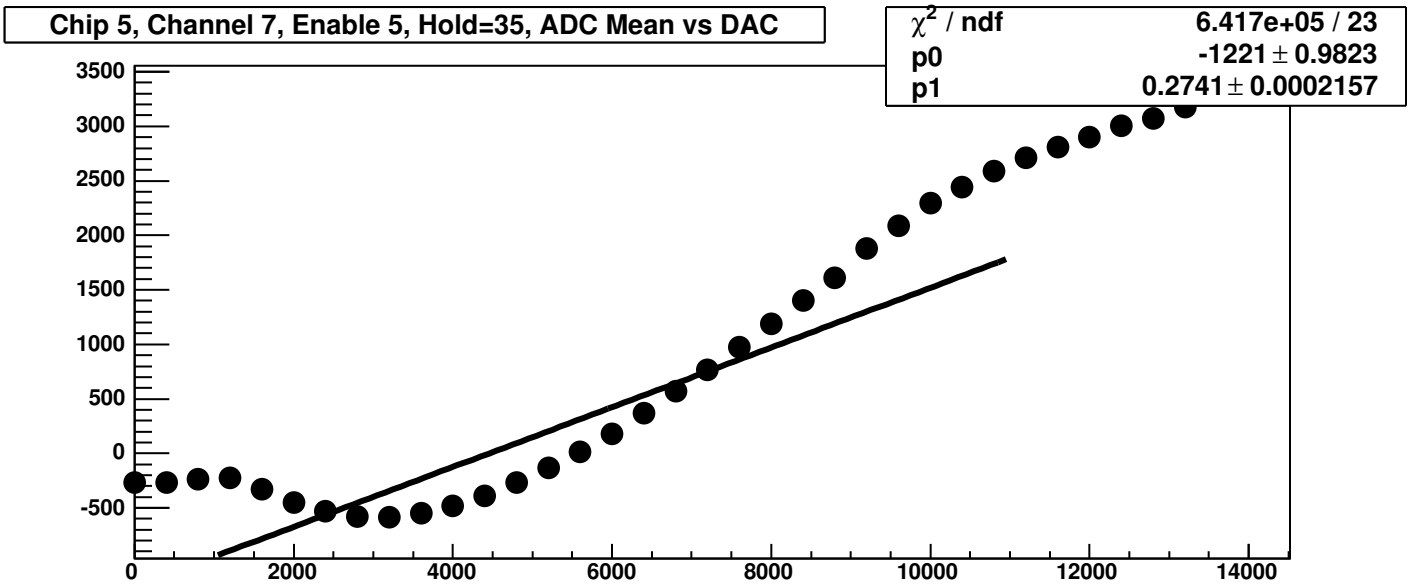

Chip 5, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

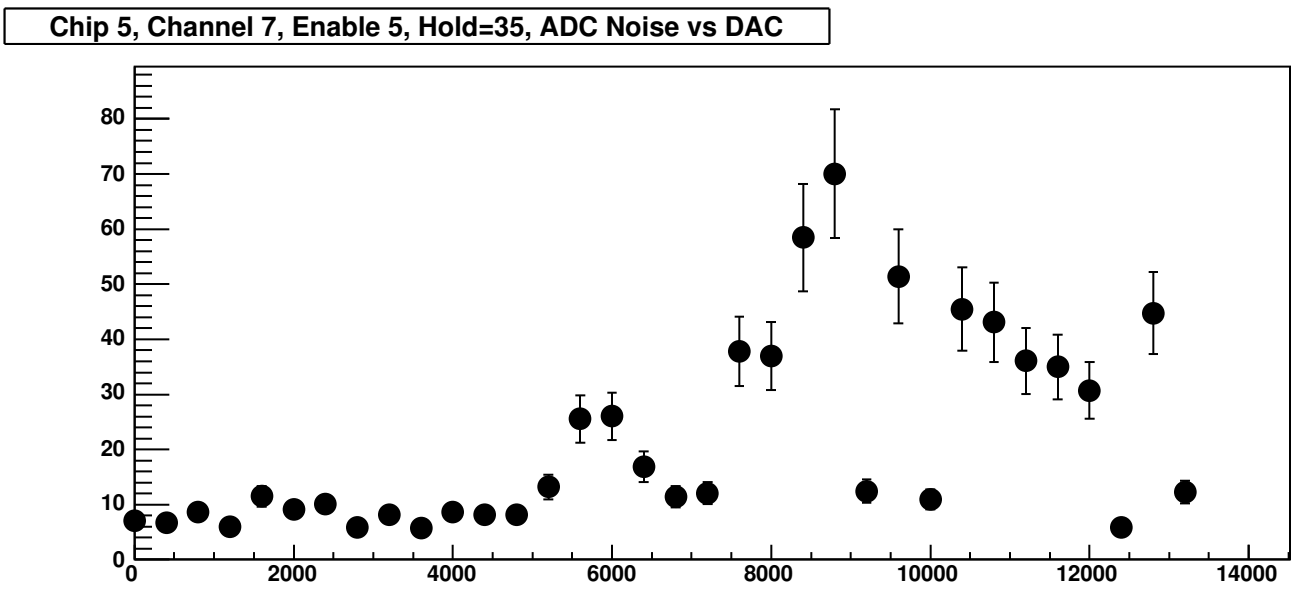

Chip 5, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

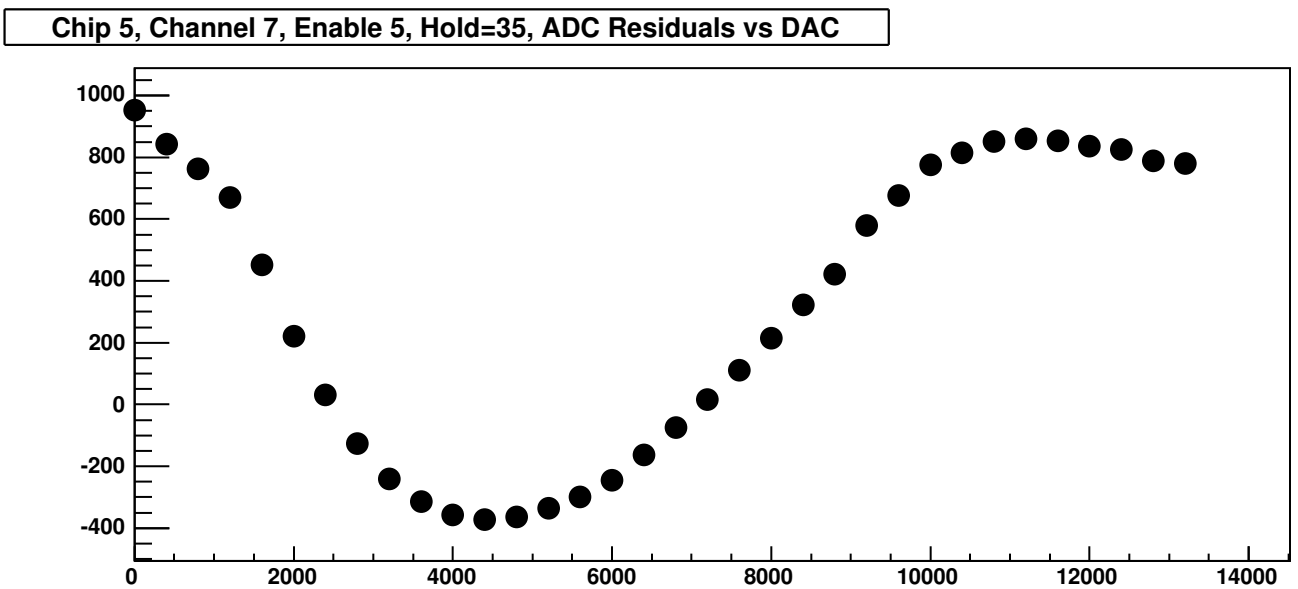

Chip 5, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 19.62 / 23
p0 -378.4 ± 0.7805
p1 0.02787 ± 0.000118
0
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

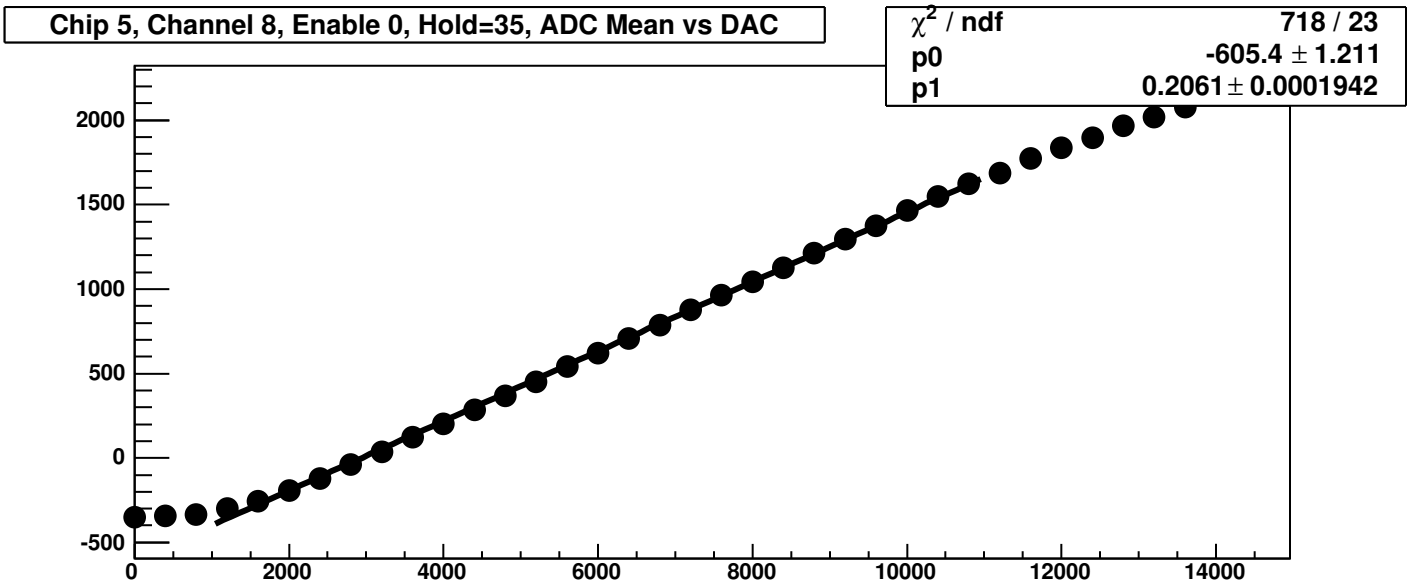

Chip 5, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

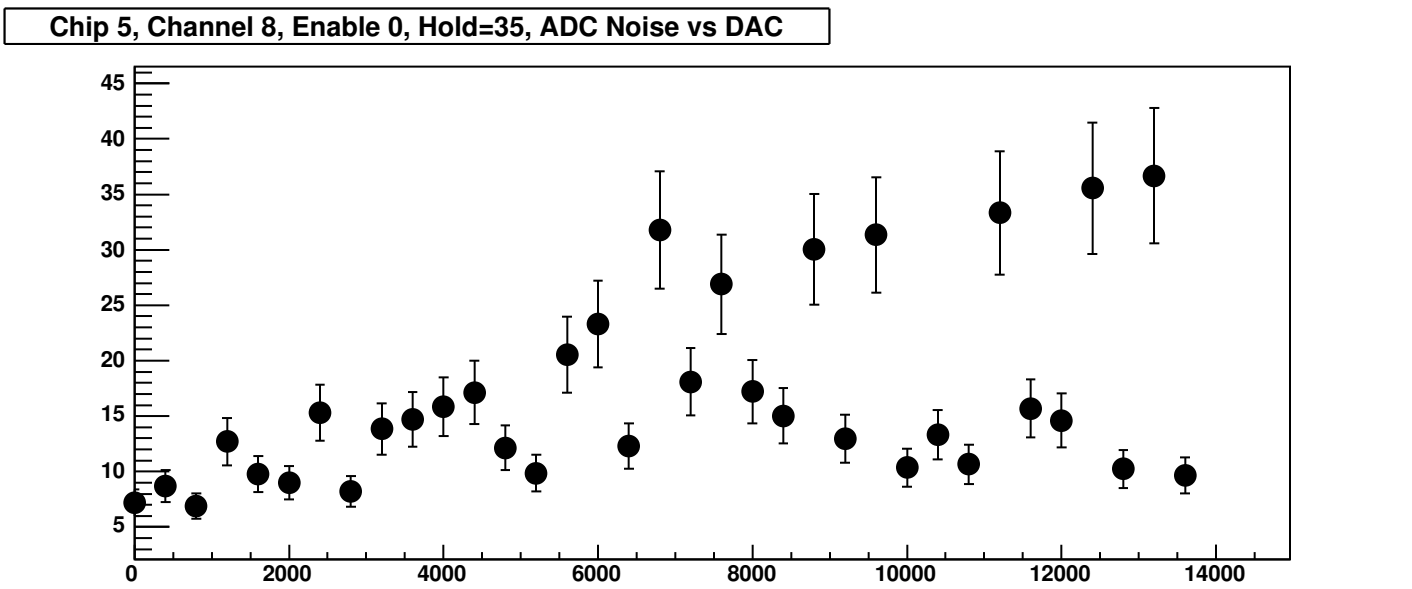

Chip 5, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

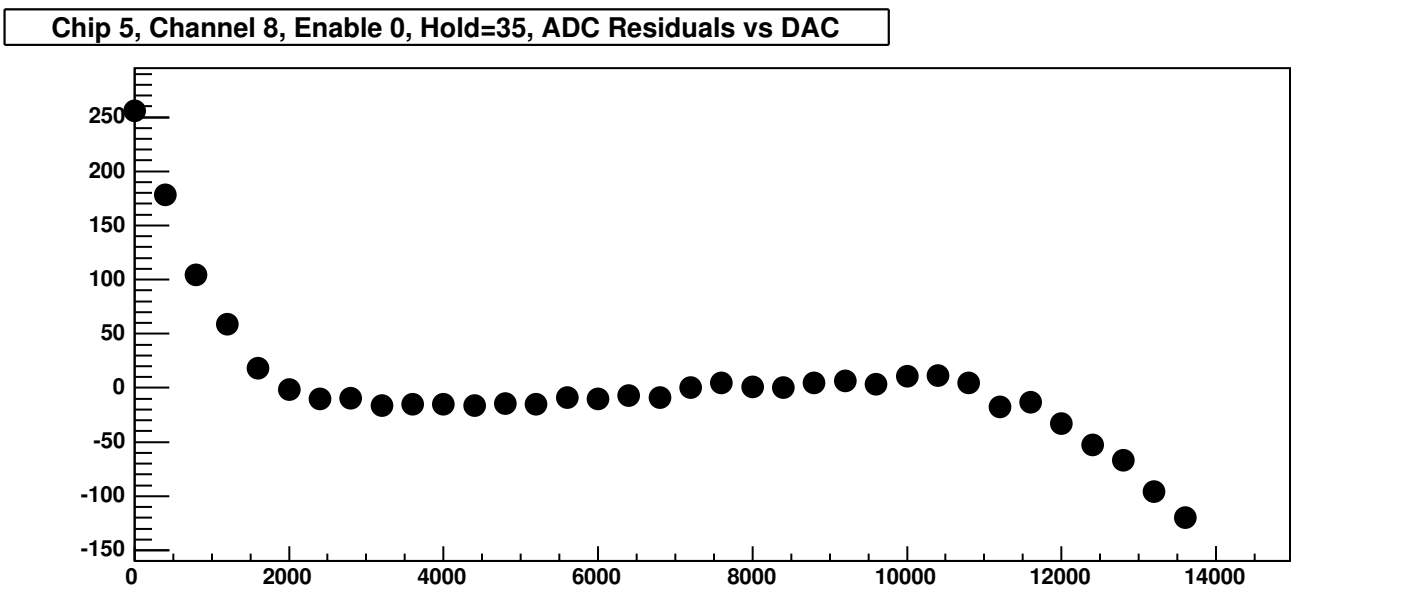

Chip 5, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 6.417e+05 / 23
p0 -1221 ± 0.9823
p1 0.2741 ± 0.0002157
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

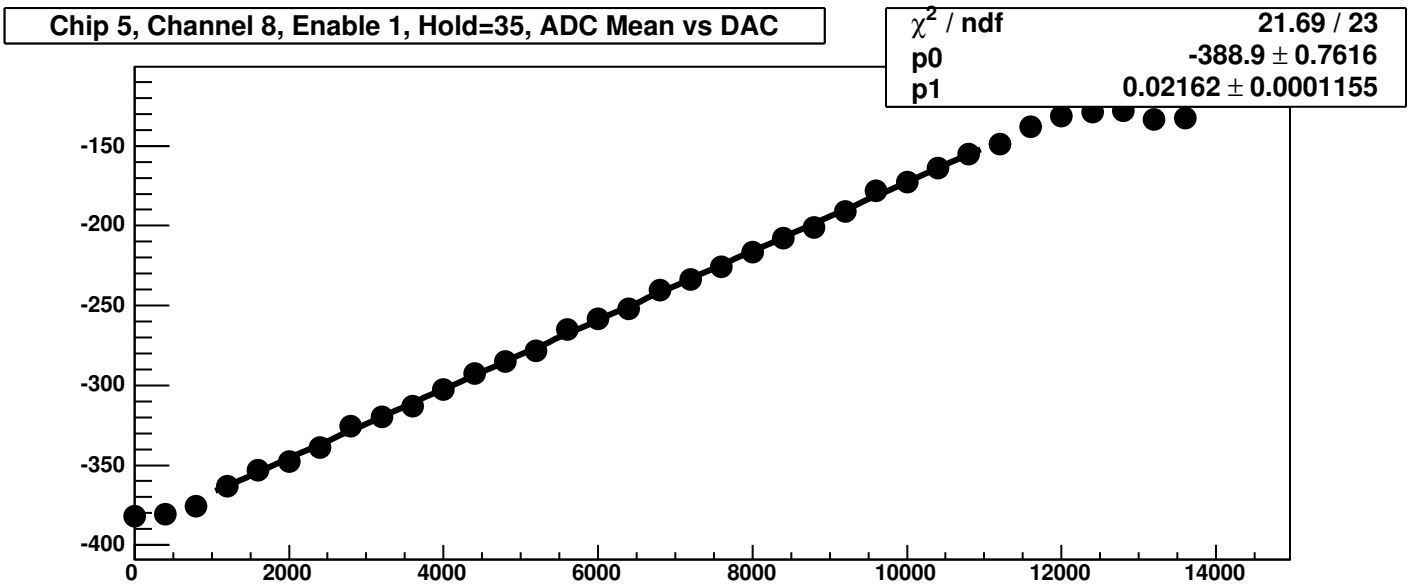

Chip 5, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

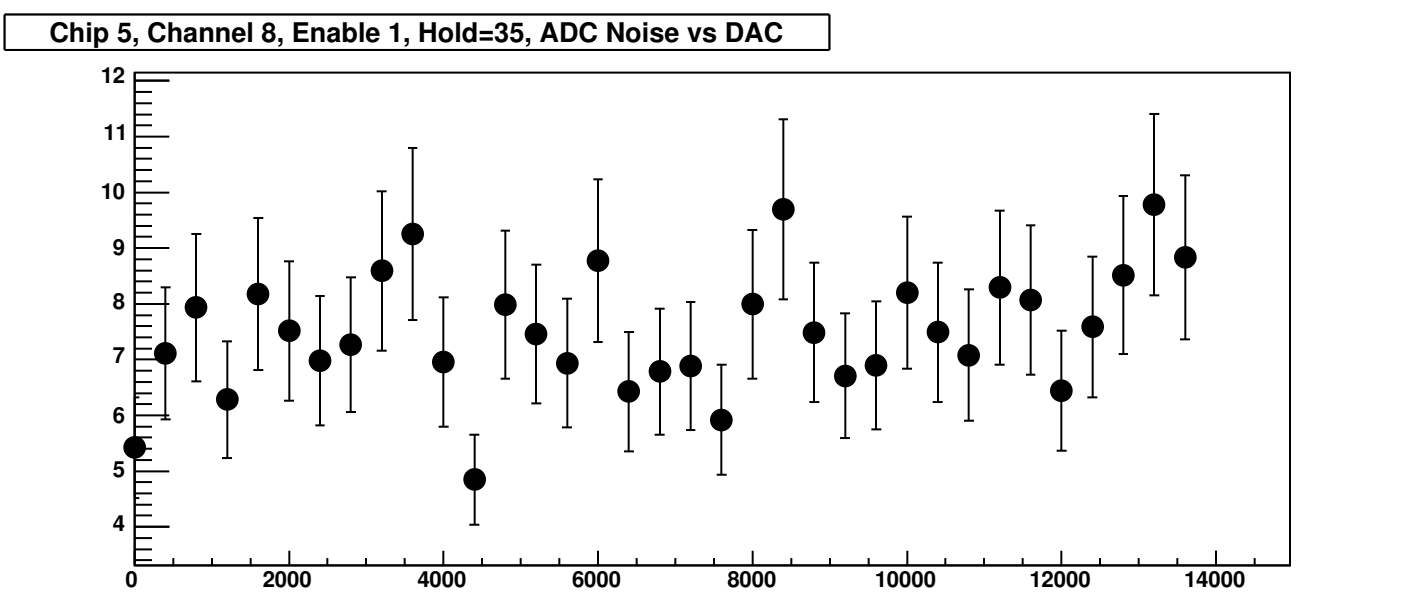

Chip 5, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

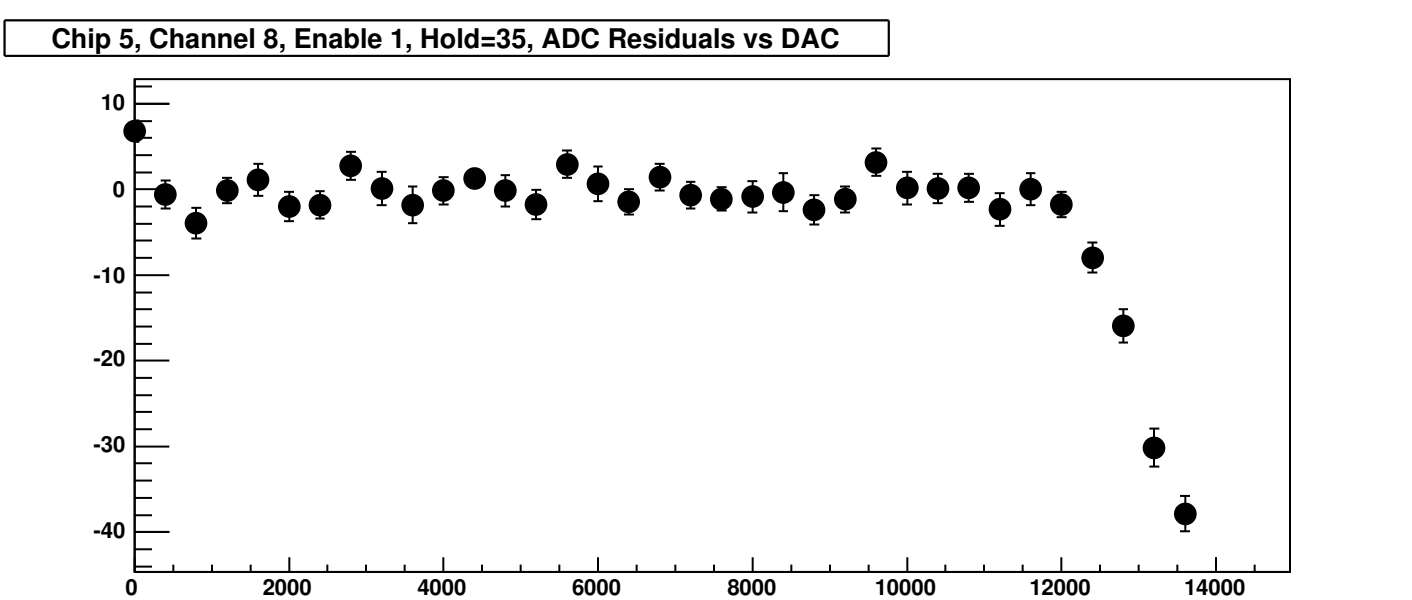

Chip 5, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 718 / 23
p0 -605.4 ± 1.211
p1 0.2061 ± 0.0001942
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

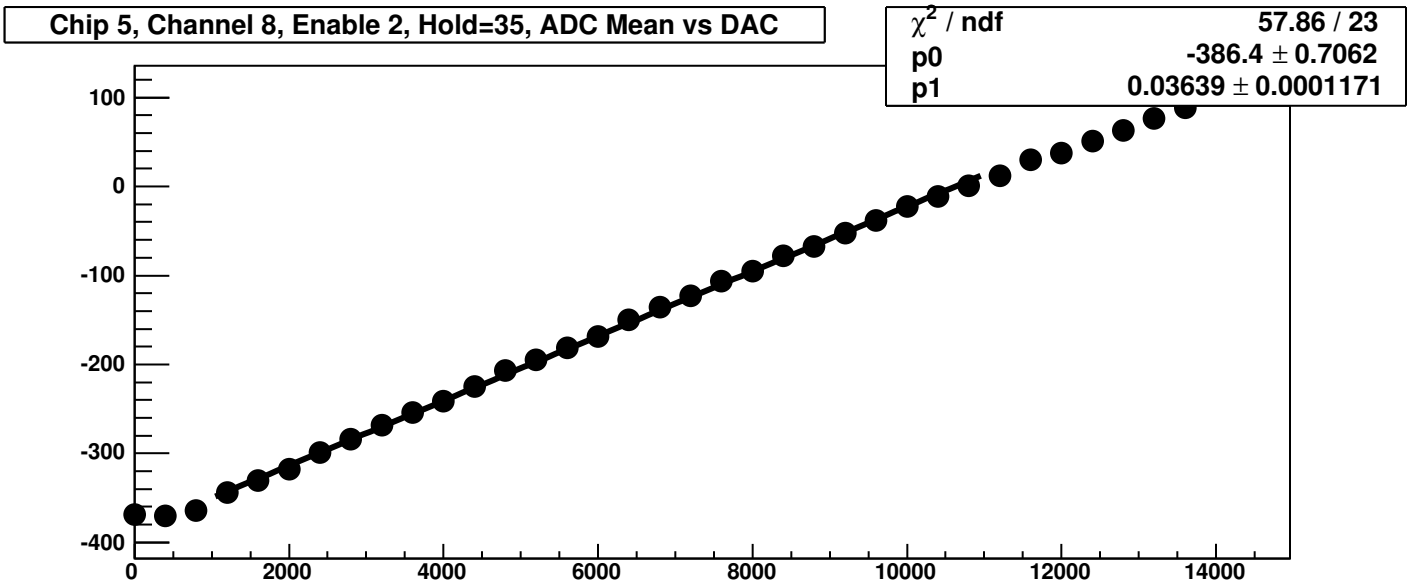

Chip 5, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

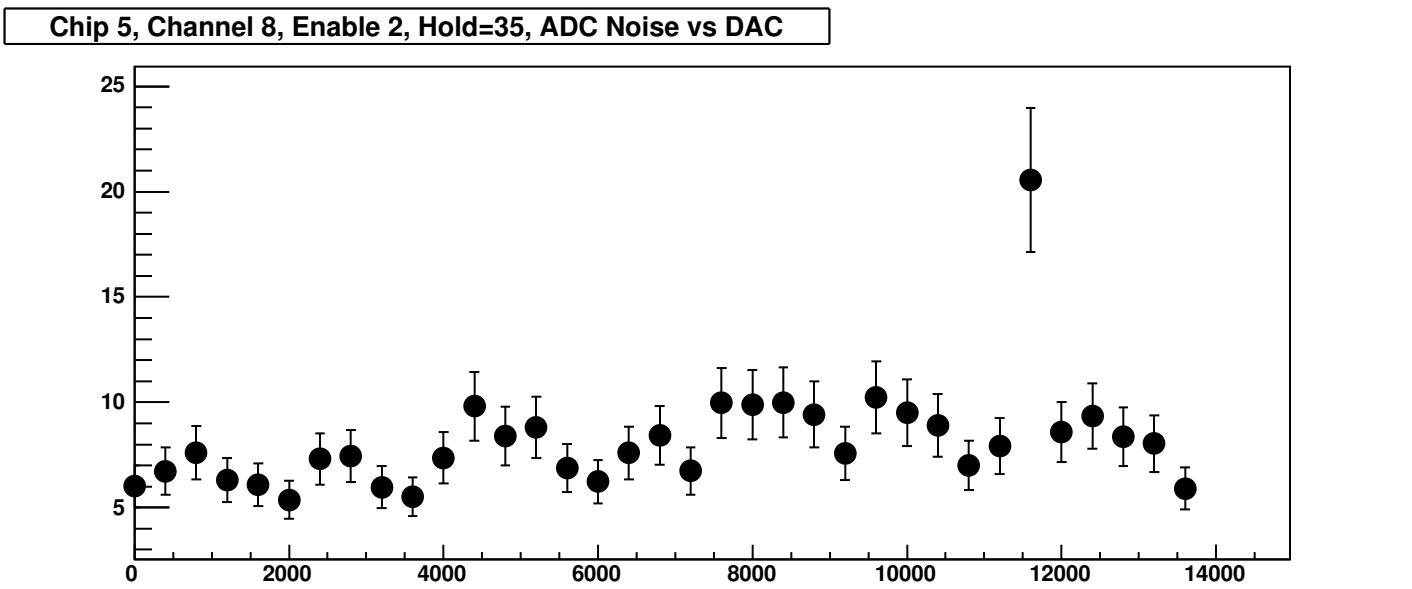

Chip 5, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

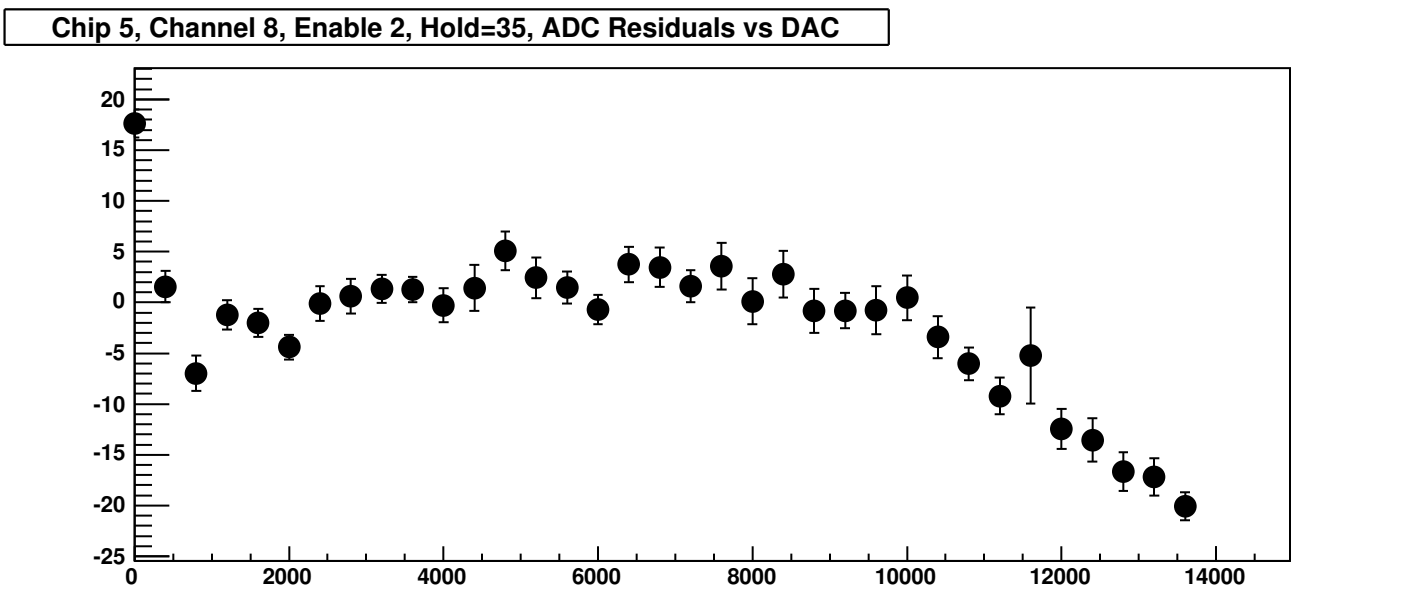

Chip 5, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 21.69 / 23
p0 -388.9 ± 0.7616
p1 0.02162 ± 0.0001155
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

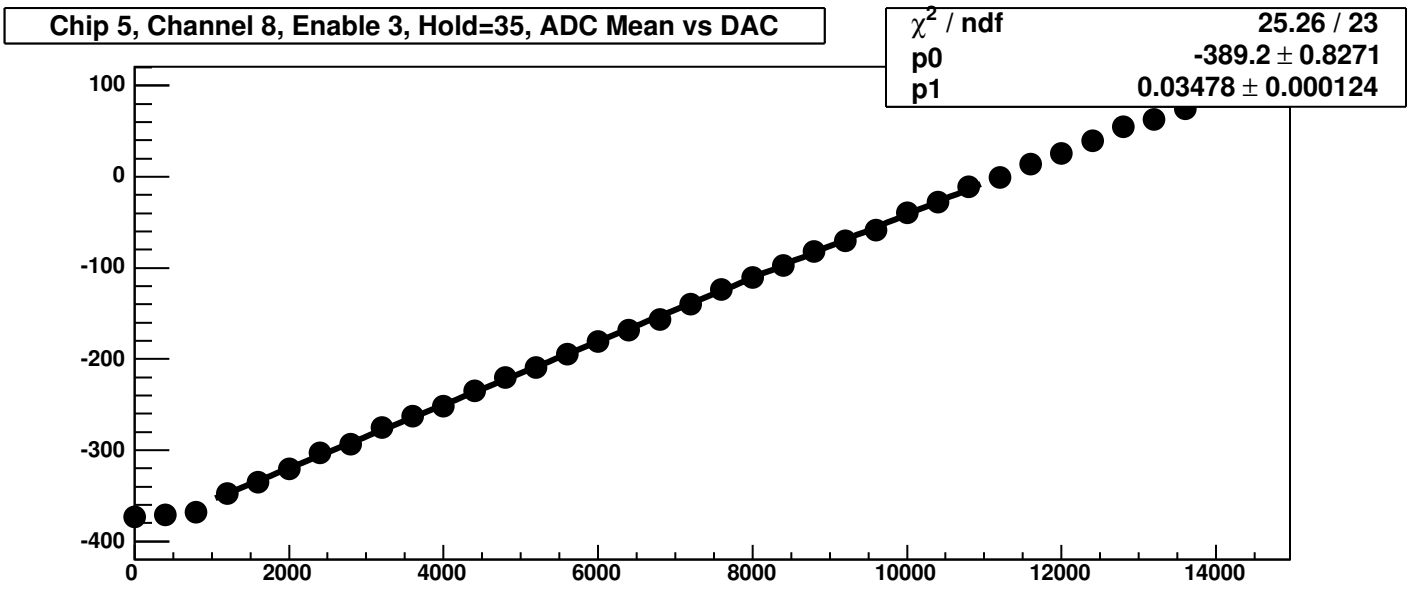

Chip 5, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

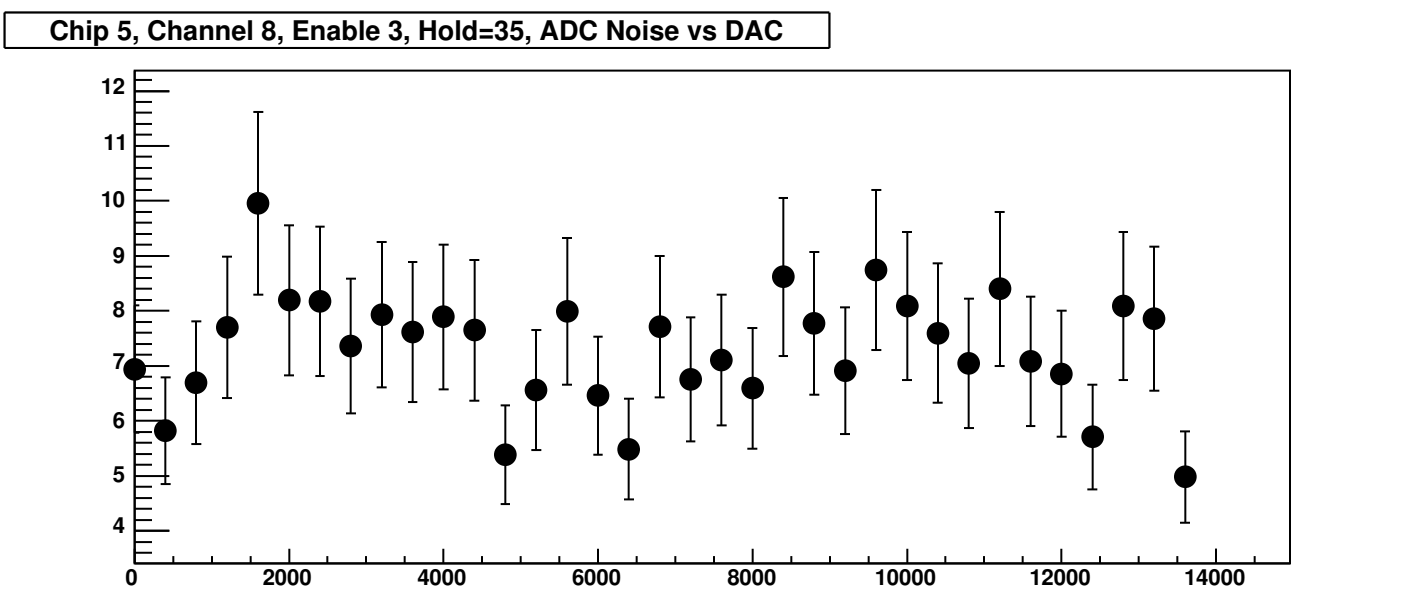

Chip 5, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

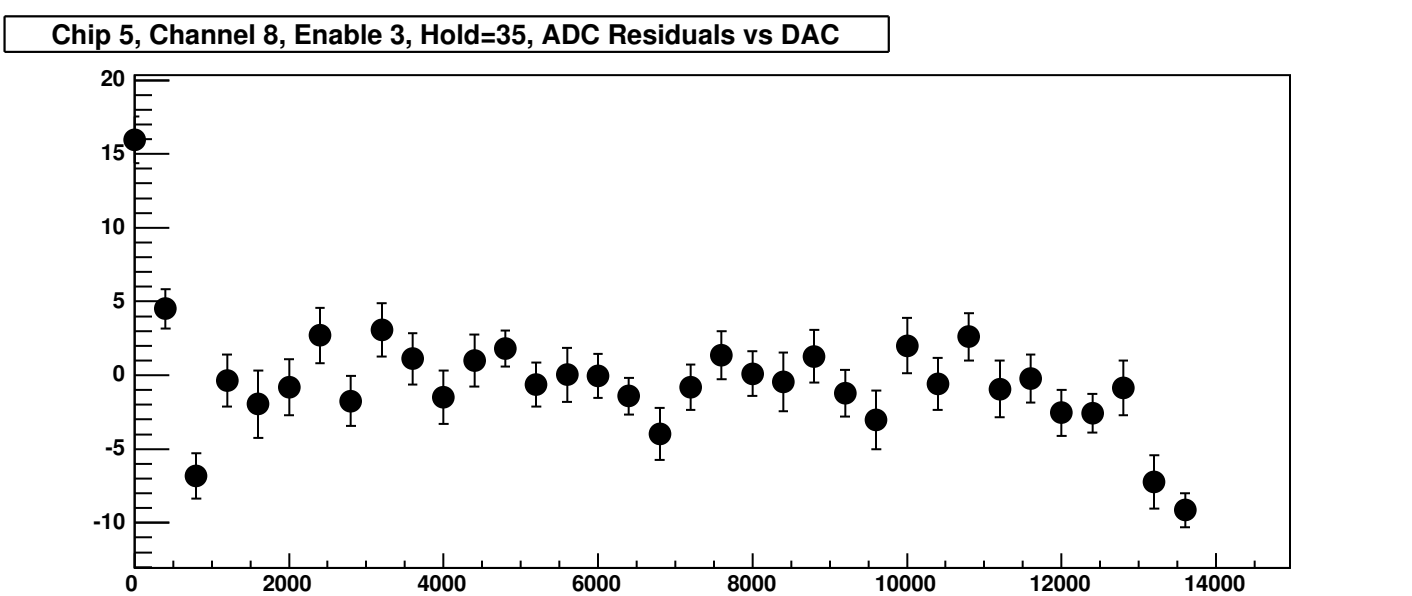

Chip 5, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 57.86 / 23
p0 -386.4 ± 0.7062
p1 0.03639 ± 0.0001171
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

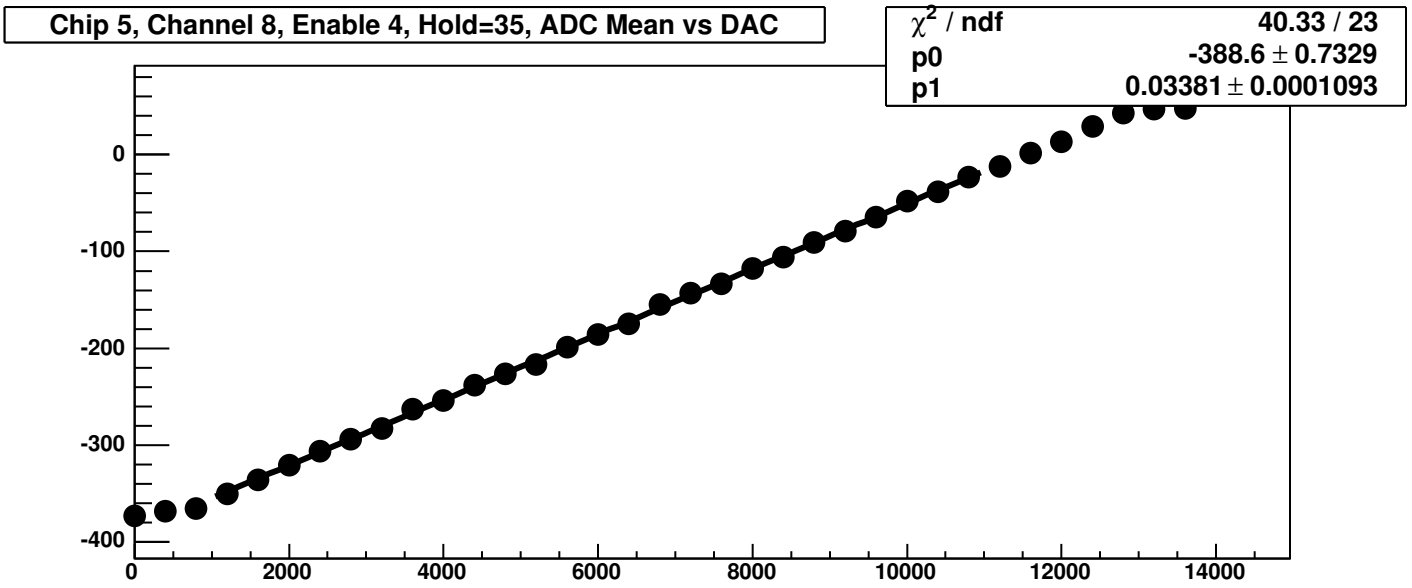

Chip 5, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

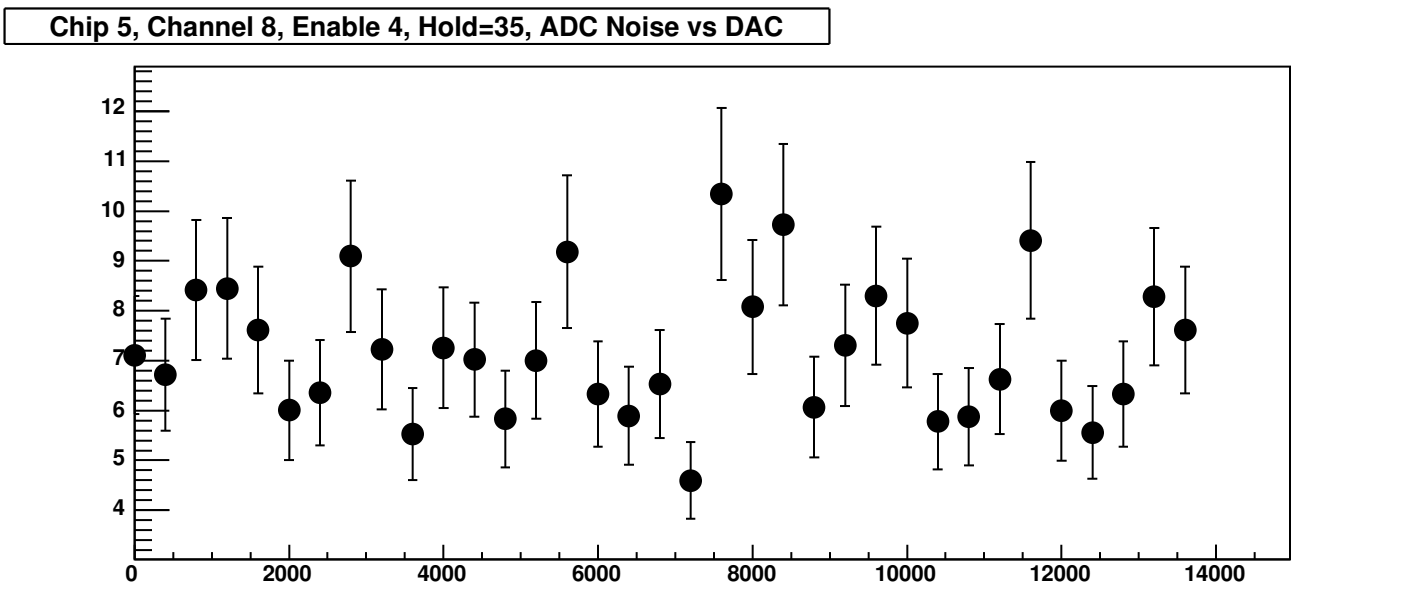

Chip 5, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

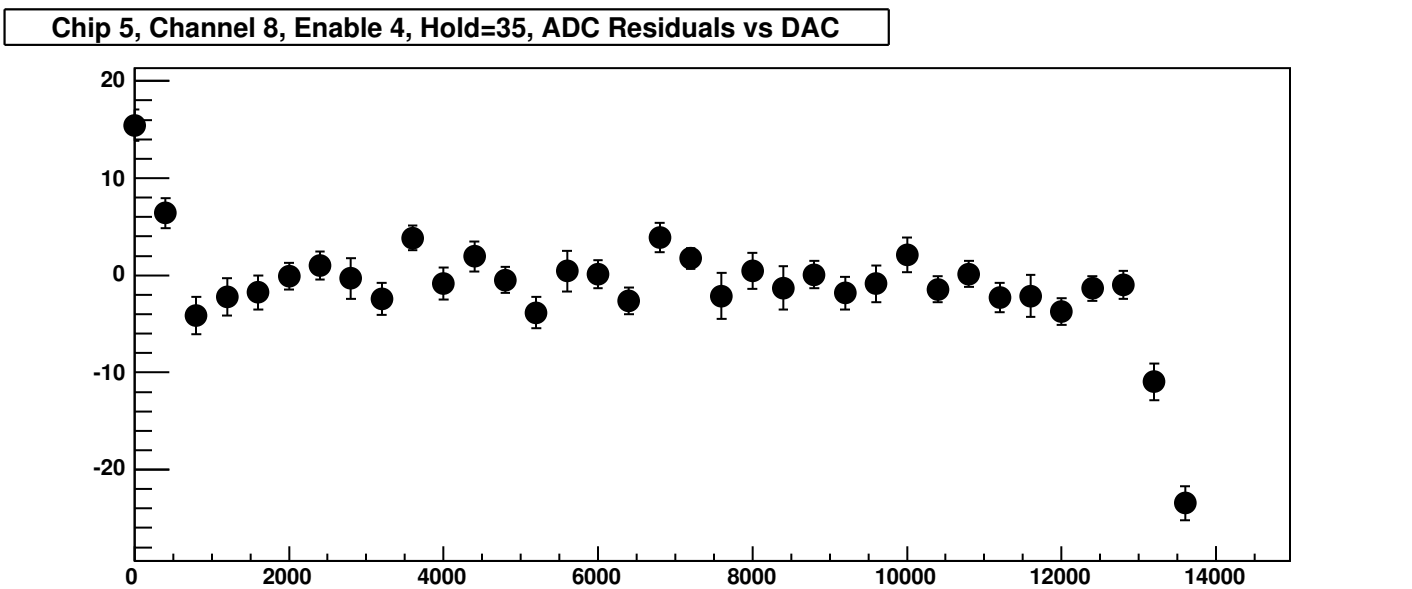

Chip 5, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.26 / 23
p0 -389.2 ± 0.8271
p1 0.03478 ± 0.000124
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

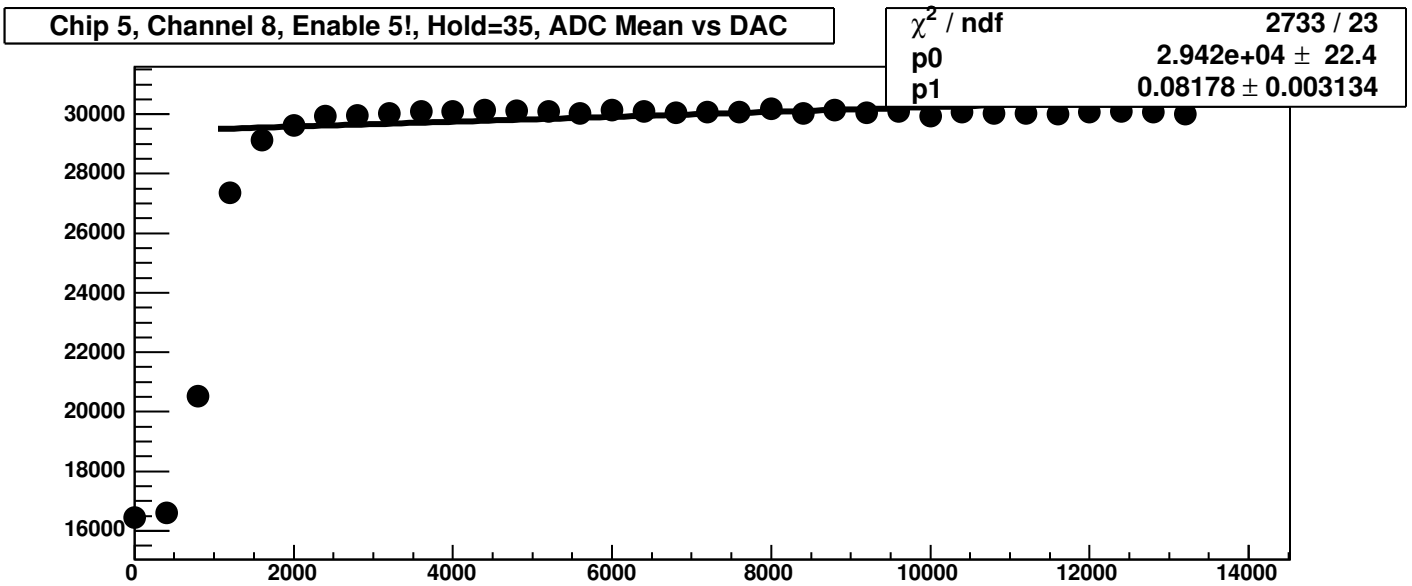

Chip 5, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

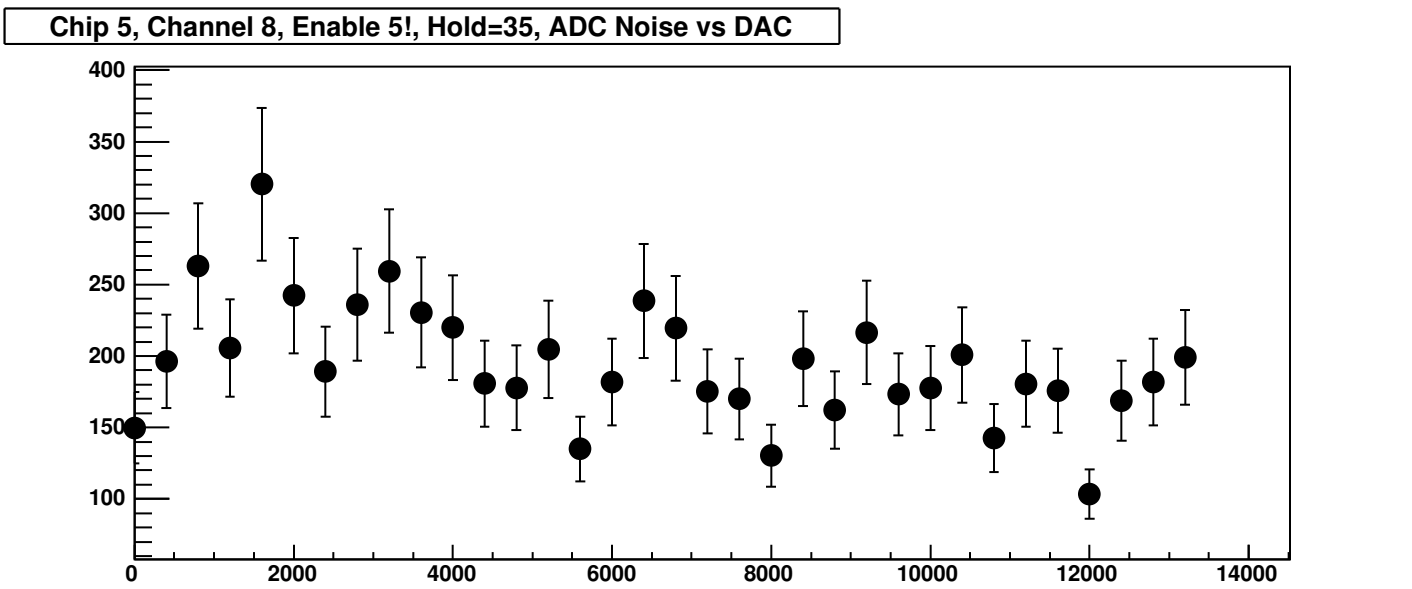

Chip 5, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

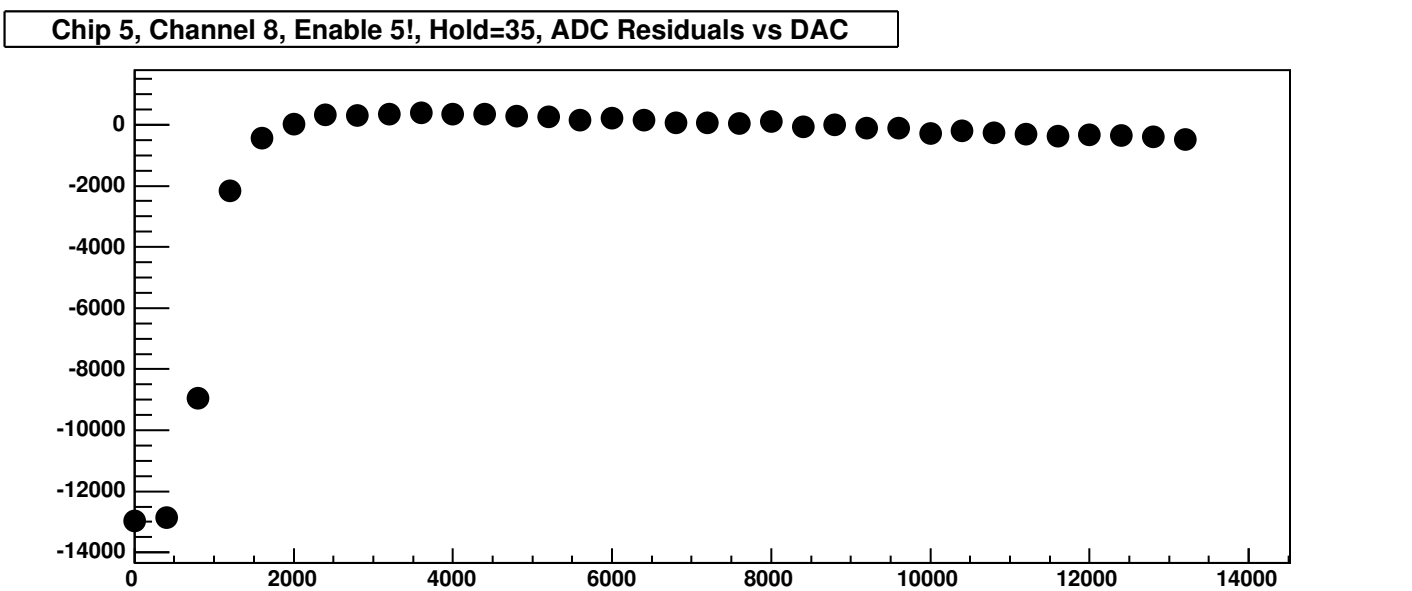

Chip 5, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 40.33 / 23
p0 -388.6 ± 0.7329
p1 0.03381 ± 0.0001093
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

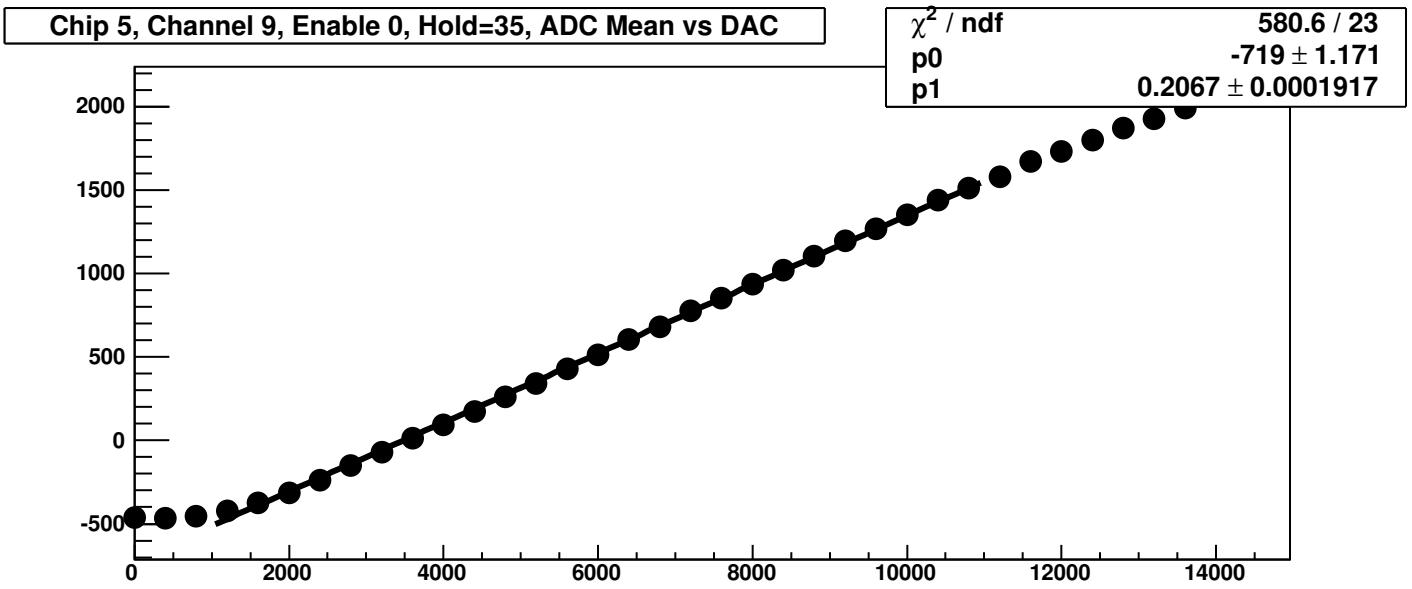

Chip 5, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

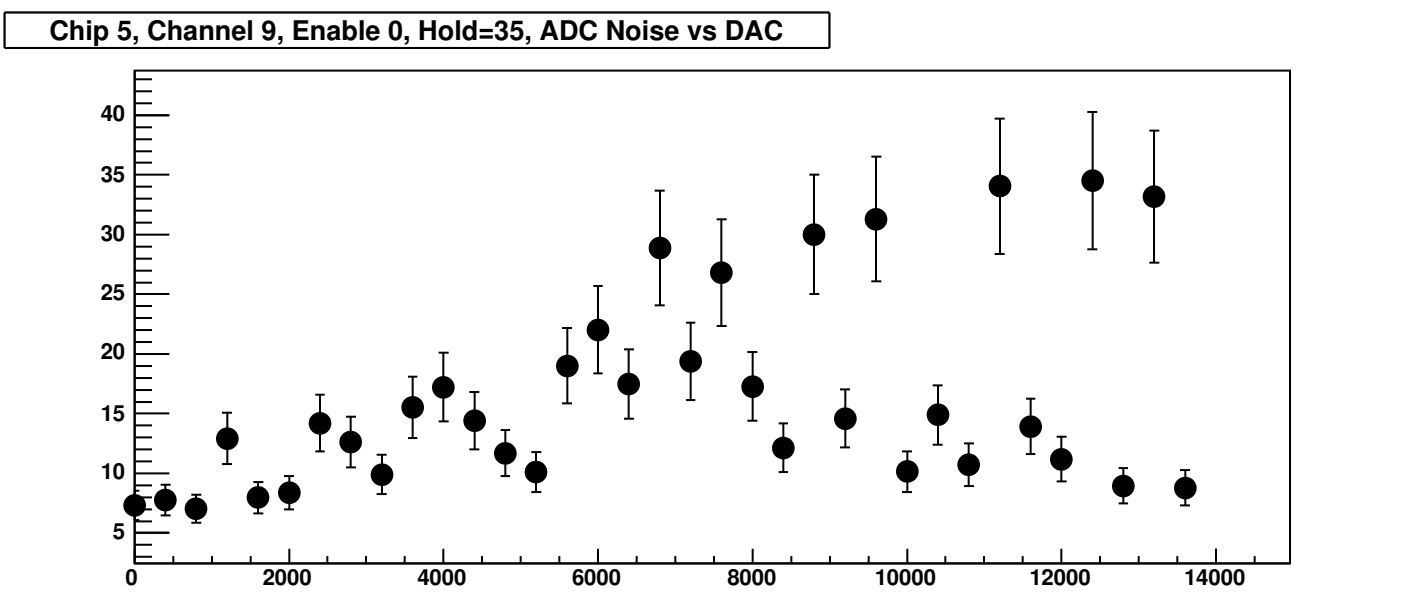

Chip 5, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

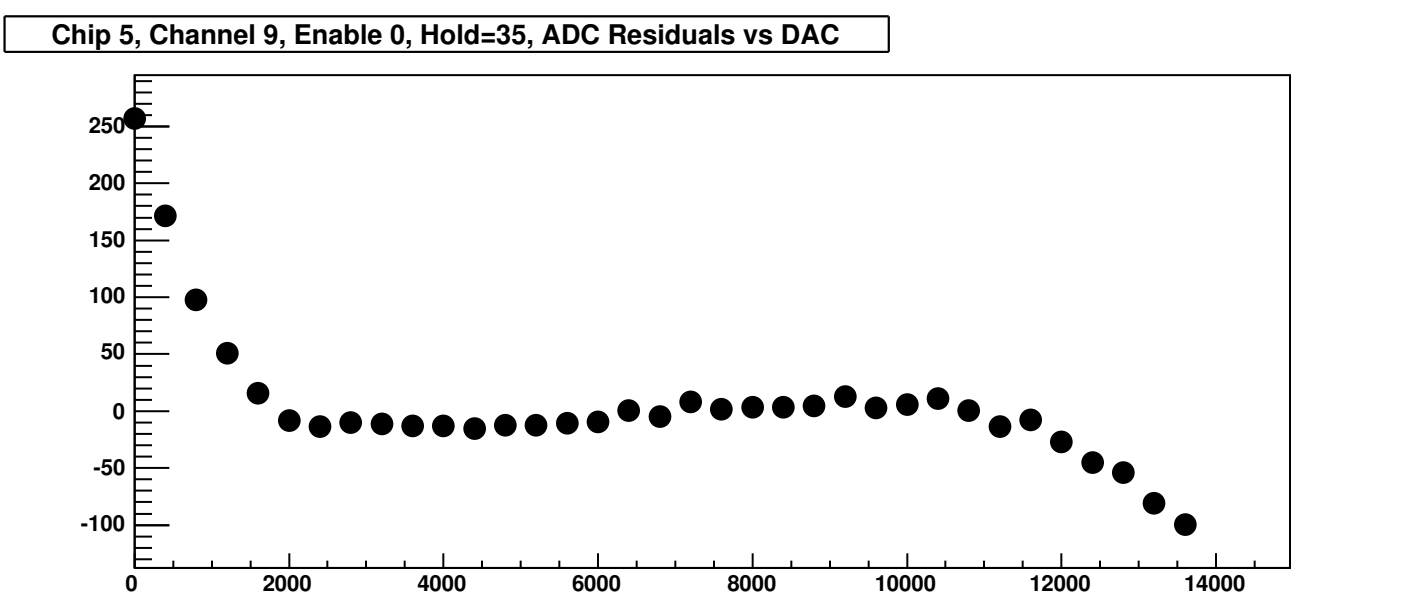

Chip 5, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2733 / 23
p0 2.942e+04 ± 22.4
p1 0.08178 ± 0.003134
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

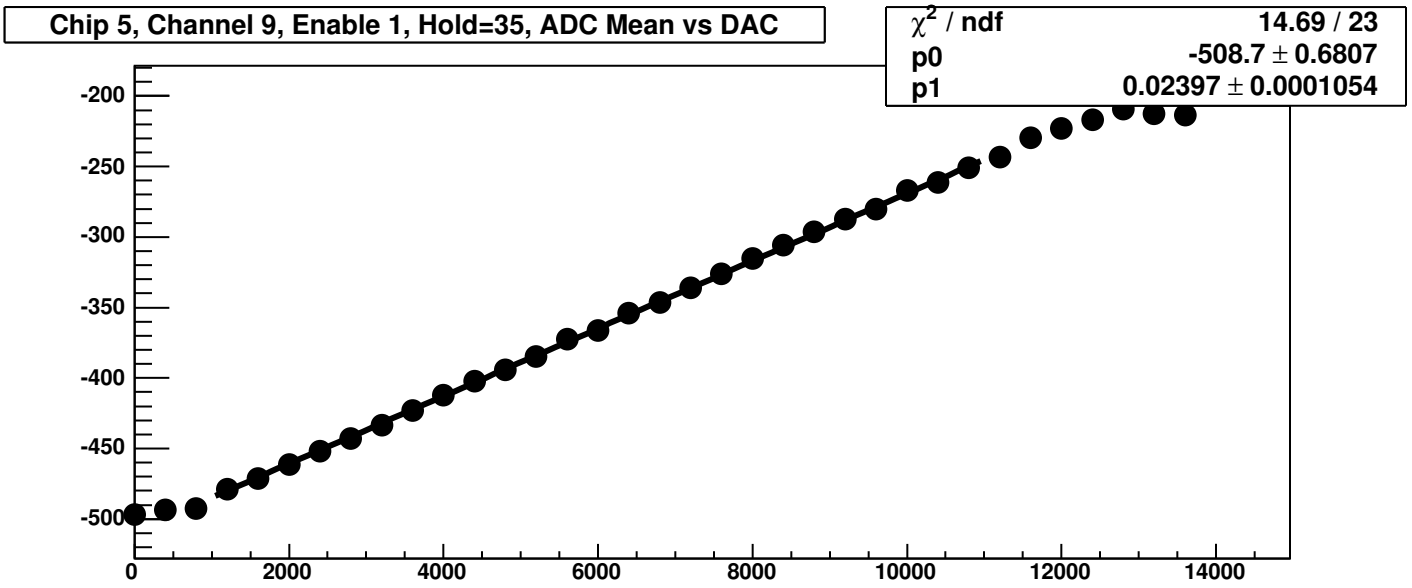

Chip 5, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

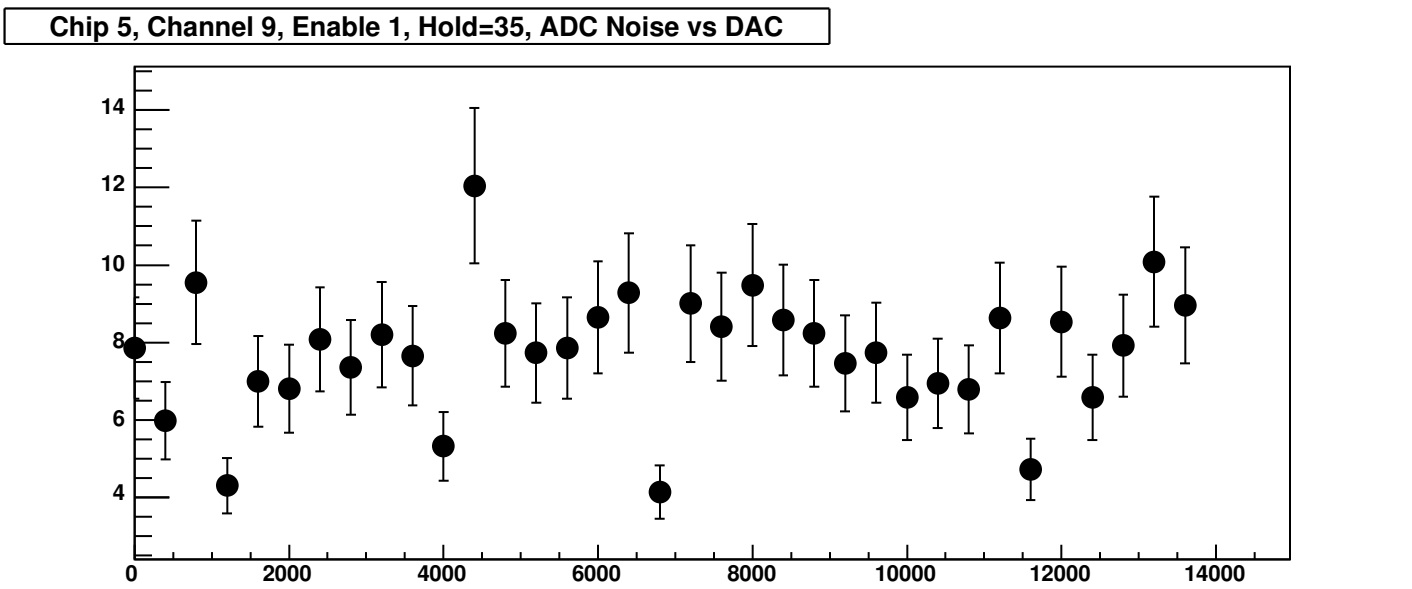

Chip 5, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

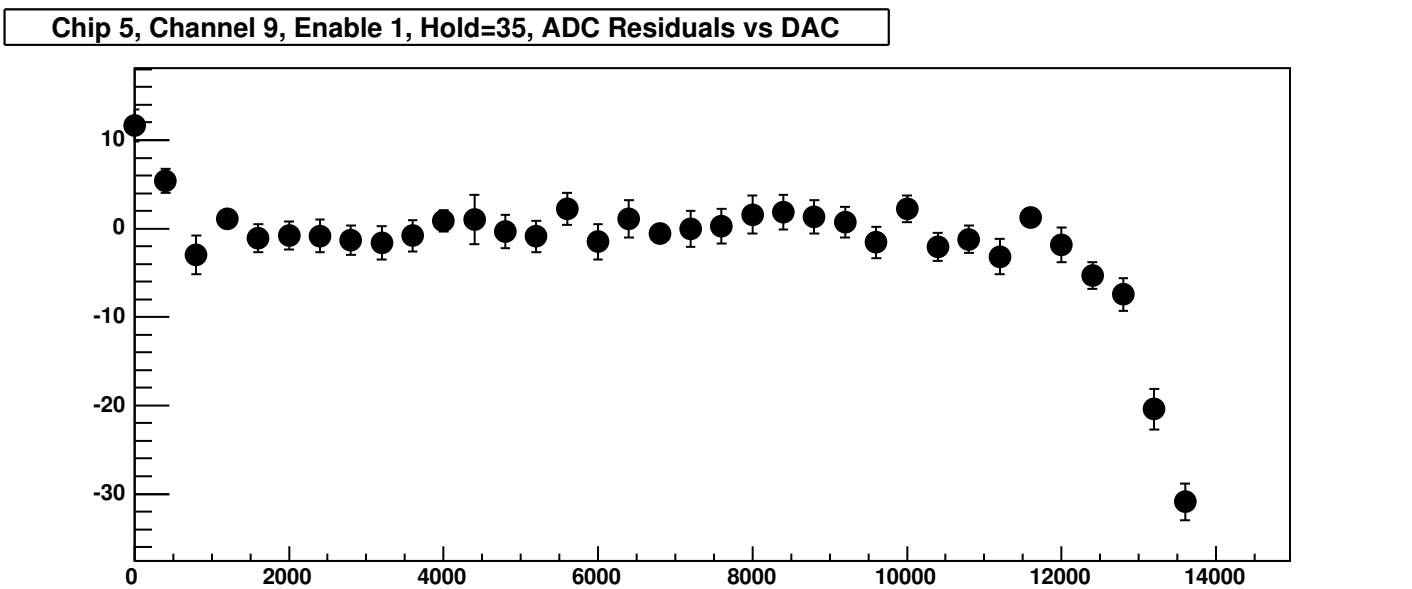

Chip 5, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 580.6 / 23
p0 -719 ± 1.171
p1 0.2067 ± 0.0001917
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

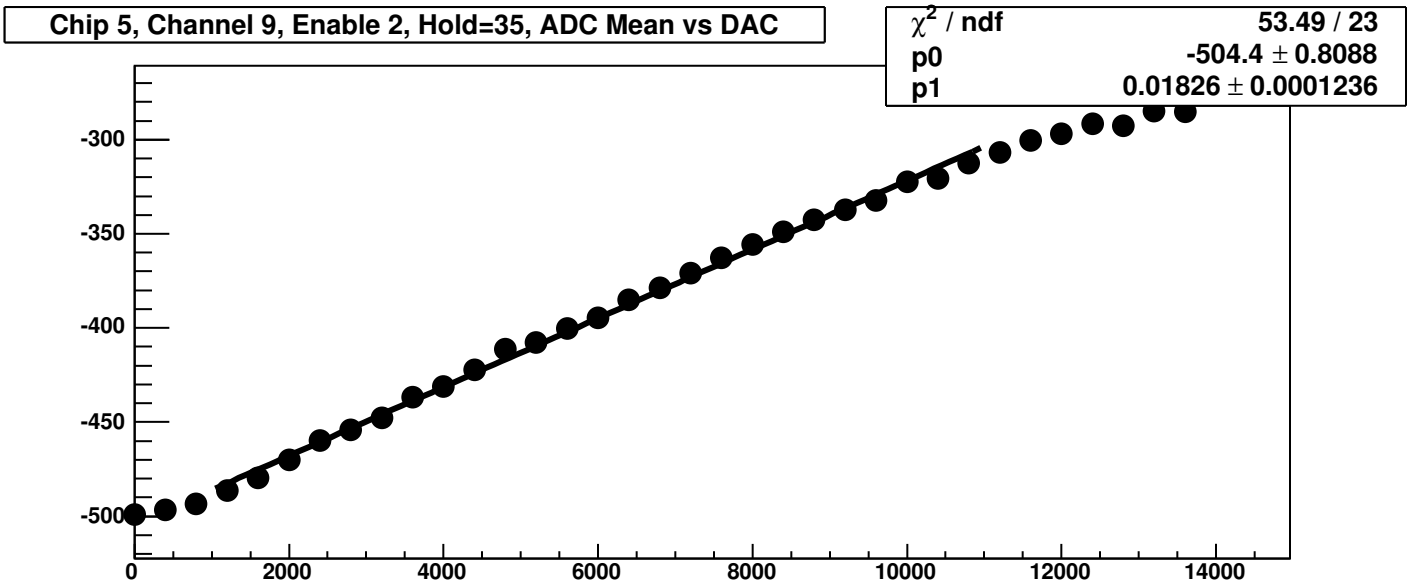

Chip 5, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

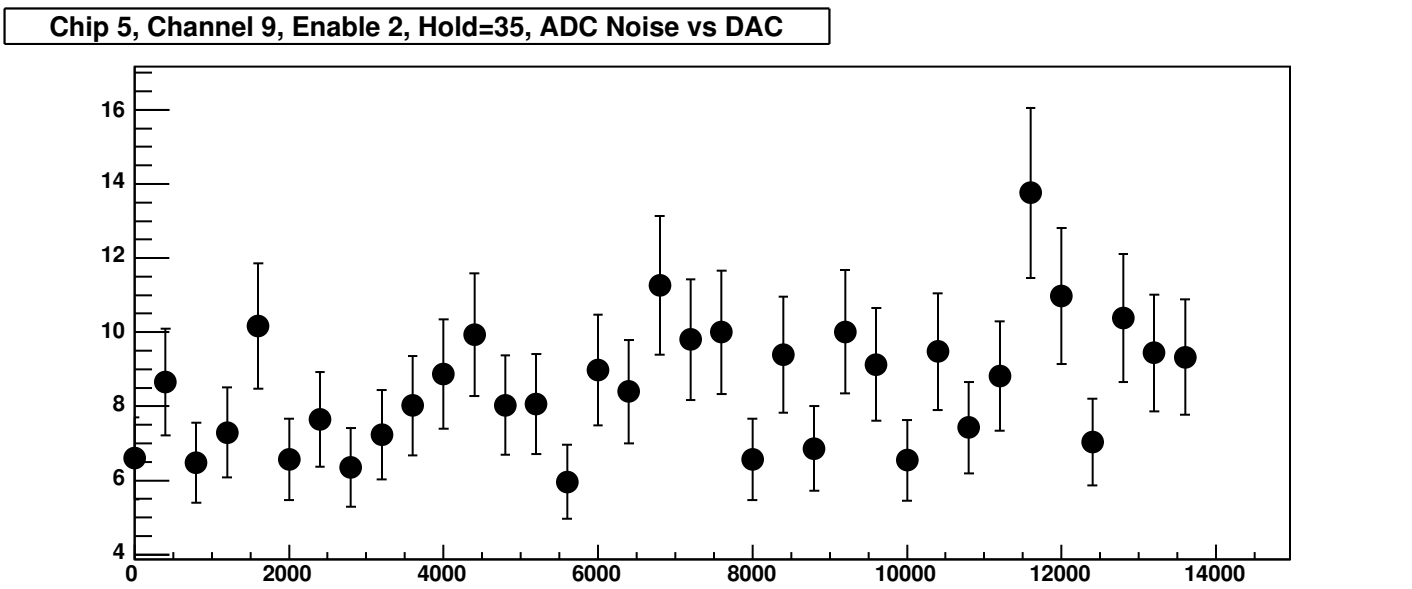

Chip 5, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

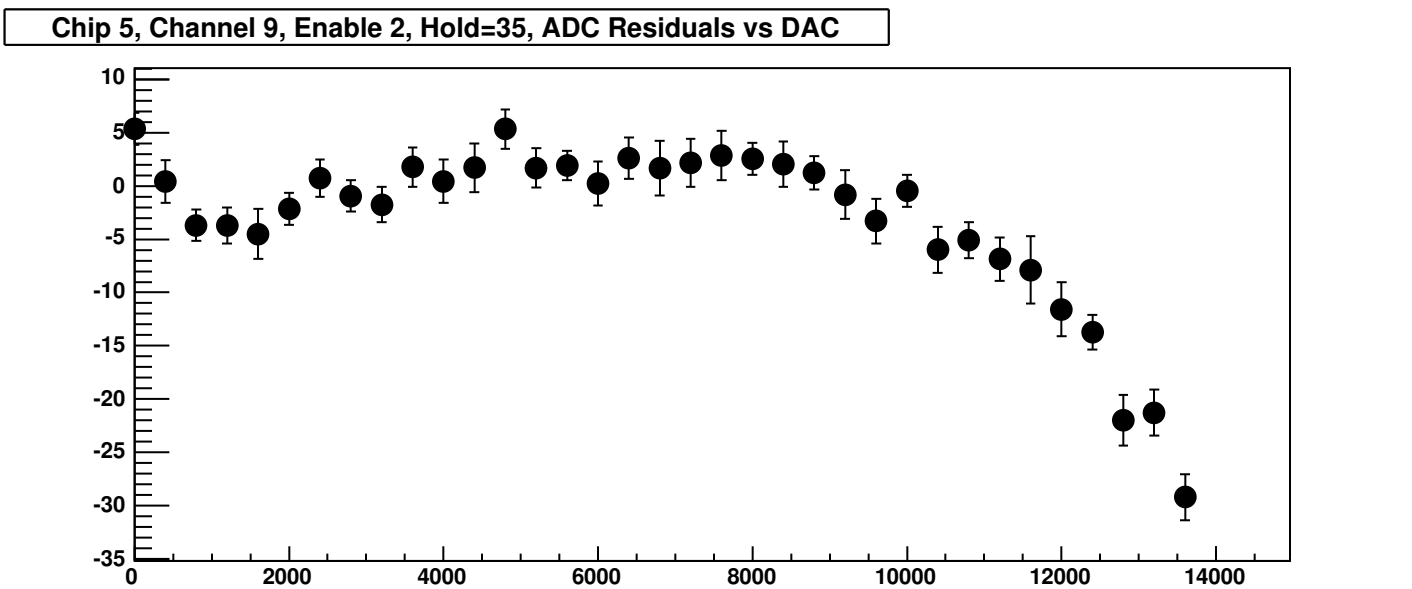

Chip 5, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 14.69 / 23
p0 -508.7 ± 0.6807
p1 0.02397 ± 0.0001054
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

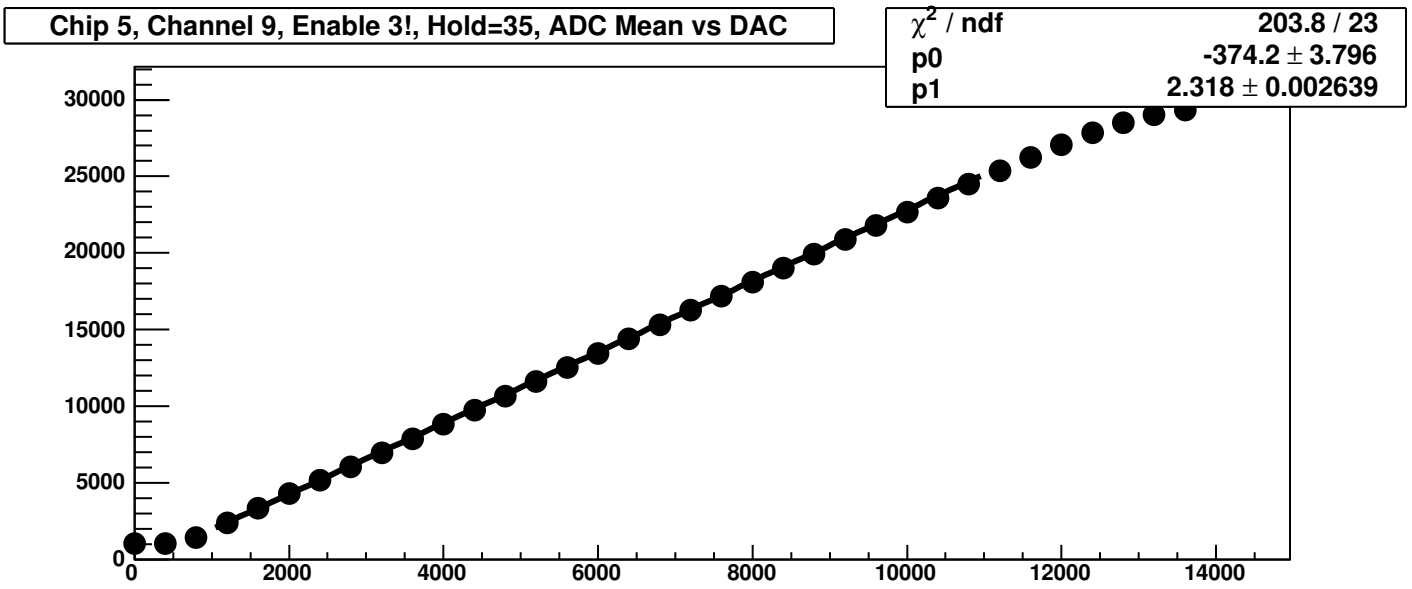

Chip 5, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

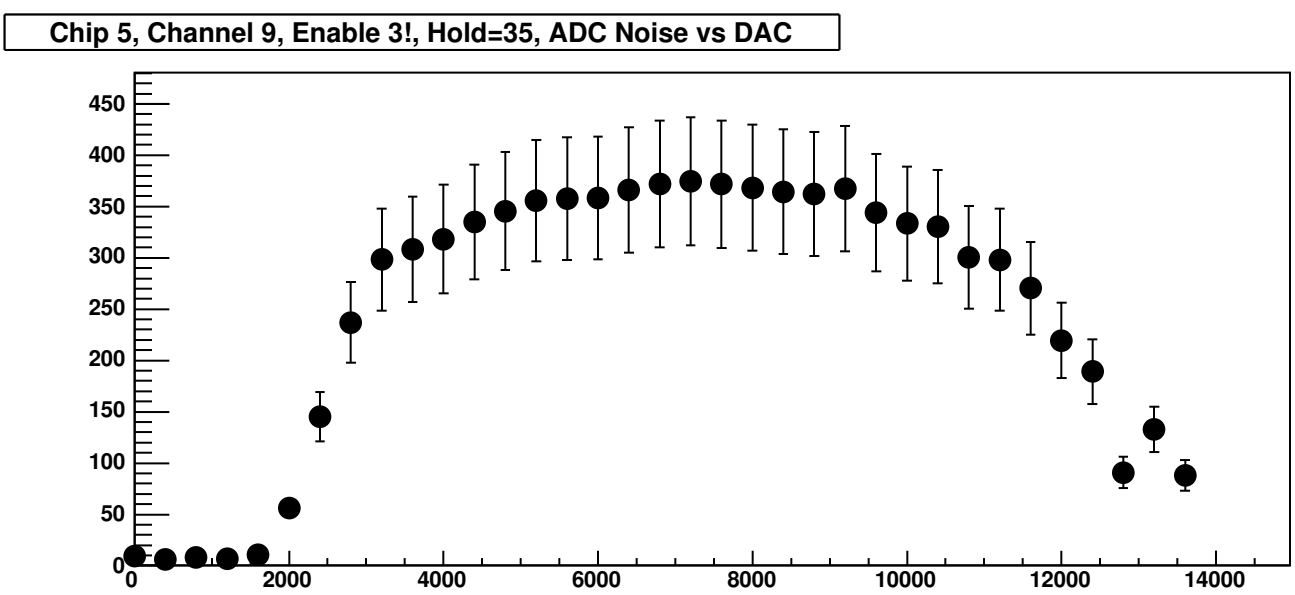

Chip 5, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

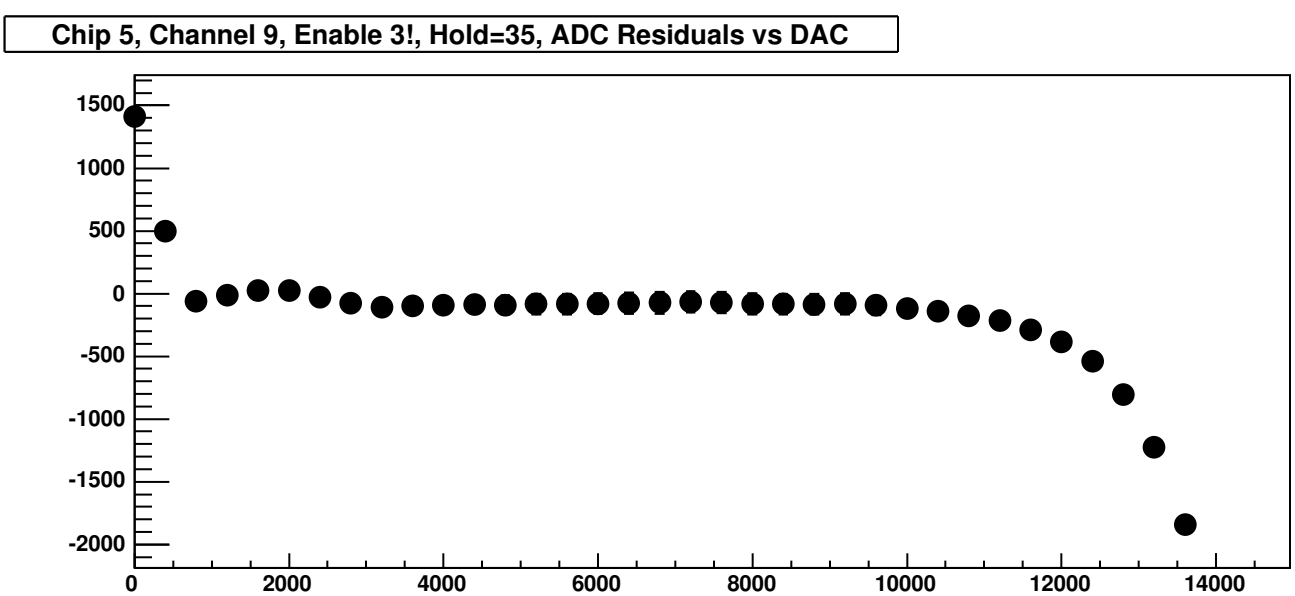

Chip 5, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 53.49 / 23
p0 -504.4 ± 0.8088
p1 0.01826 ± 0.0001236
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

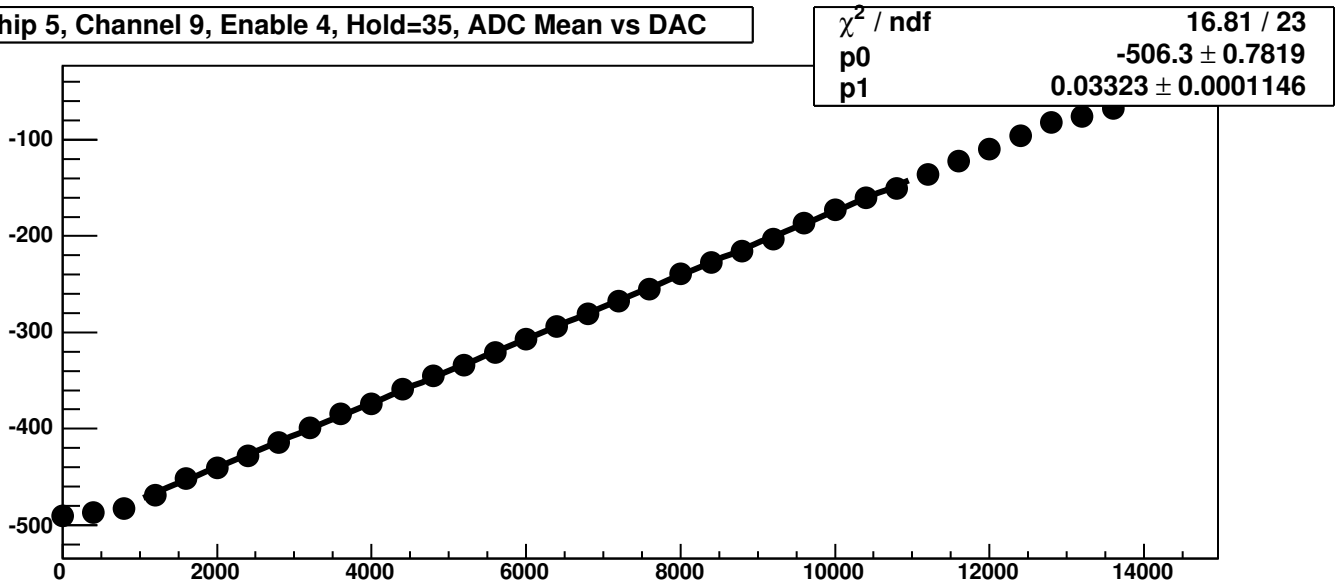

Chip 5, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
-35
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 203.8 / 23
p0 -374.2 ± 3.796
p1 2.318 ± 0.002639
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

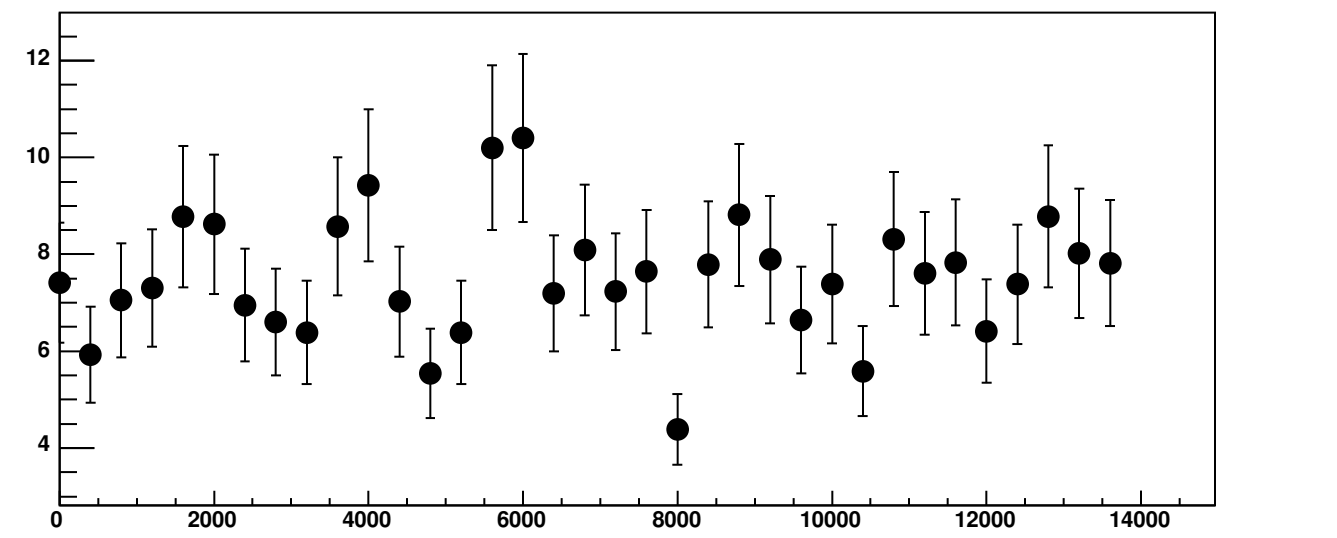

Chip 5, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

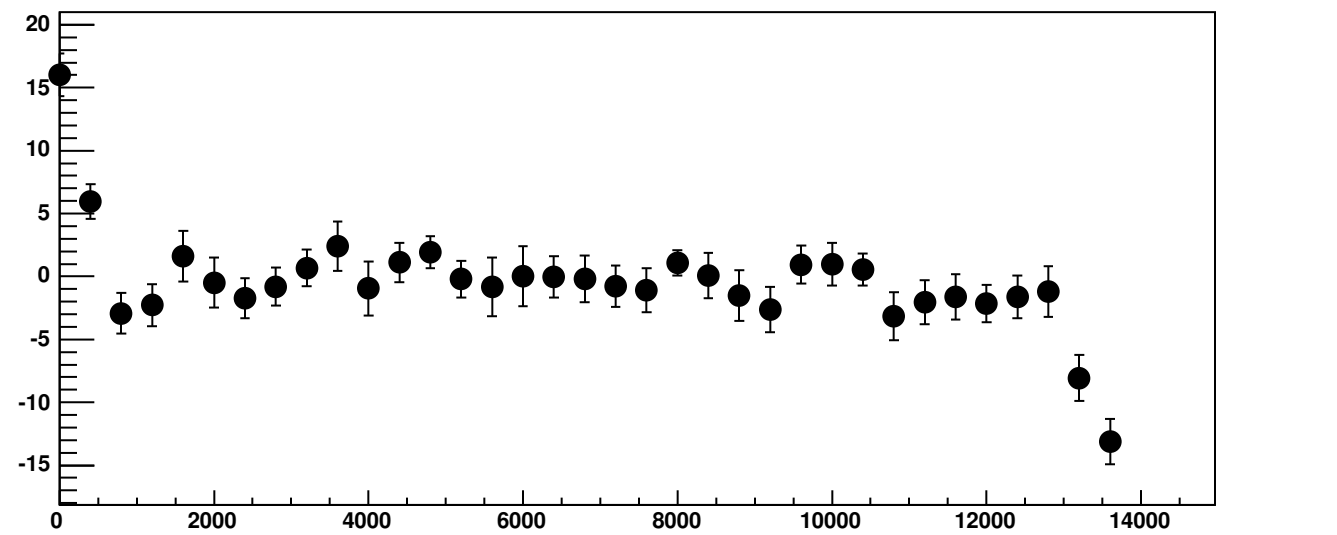
Chip 5, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



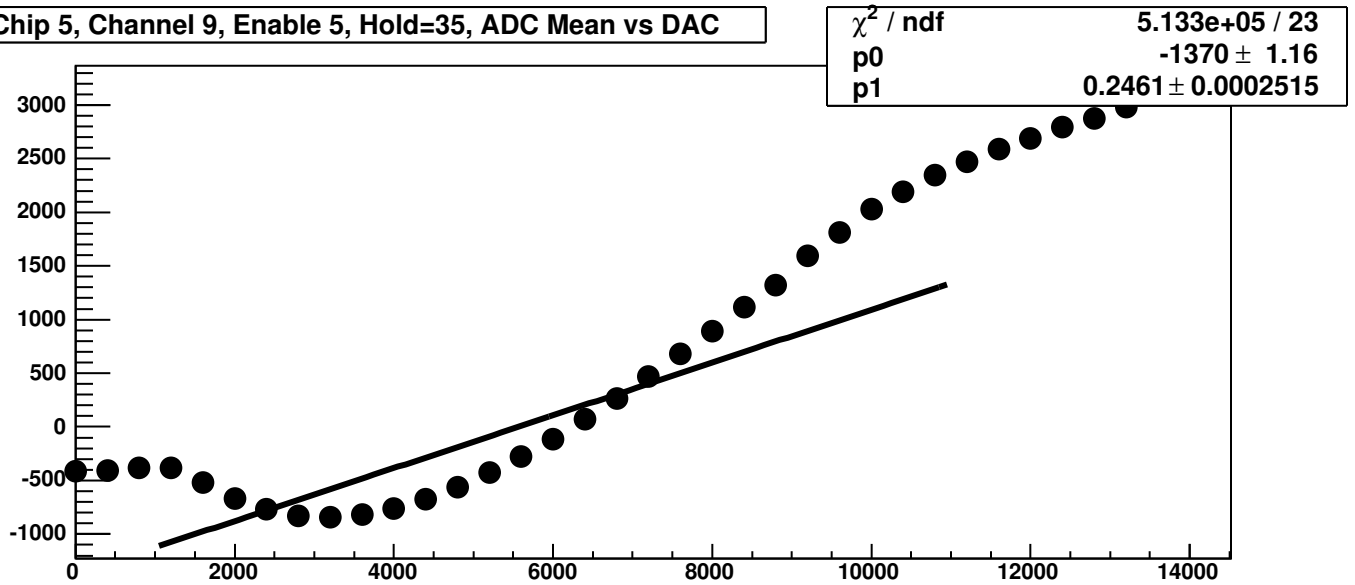
Chip 5, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



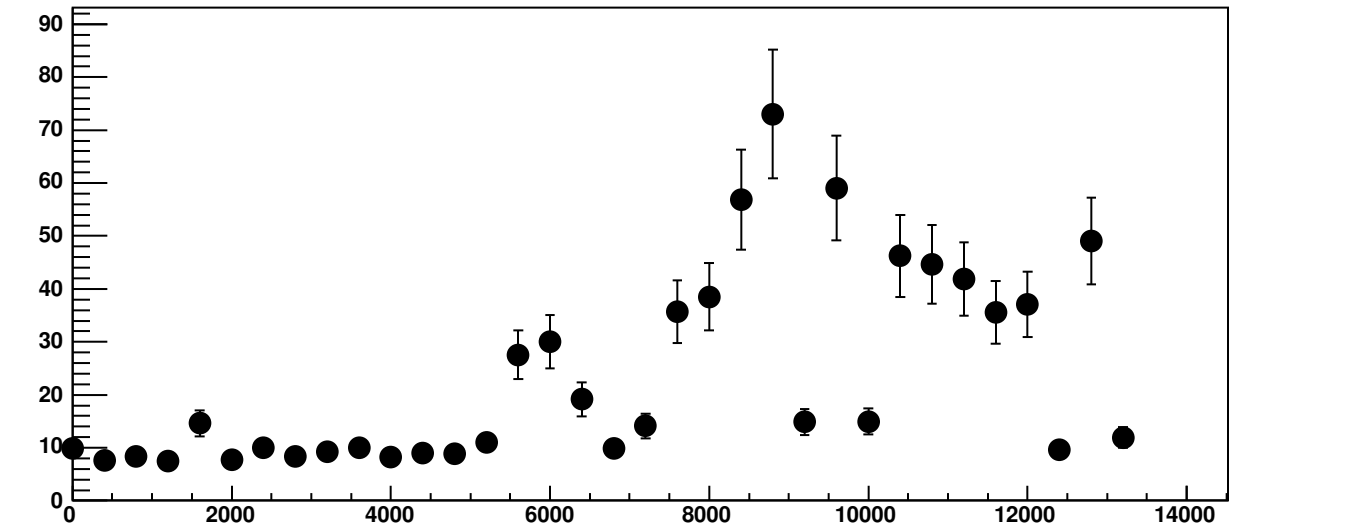
Chip 5, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



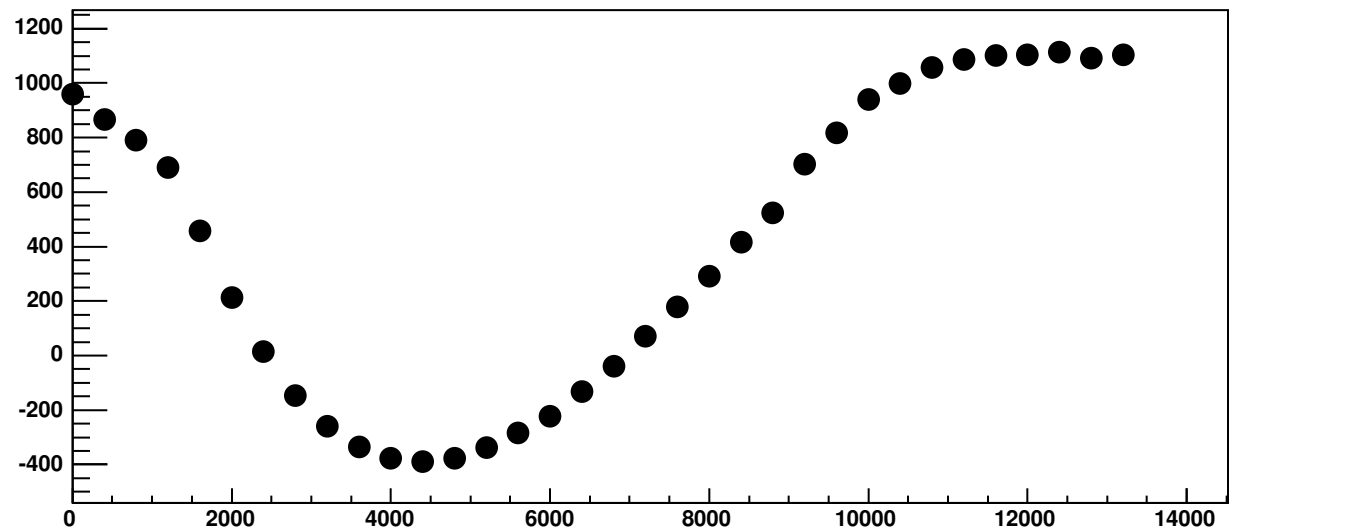
Chip 5, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

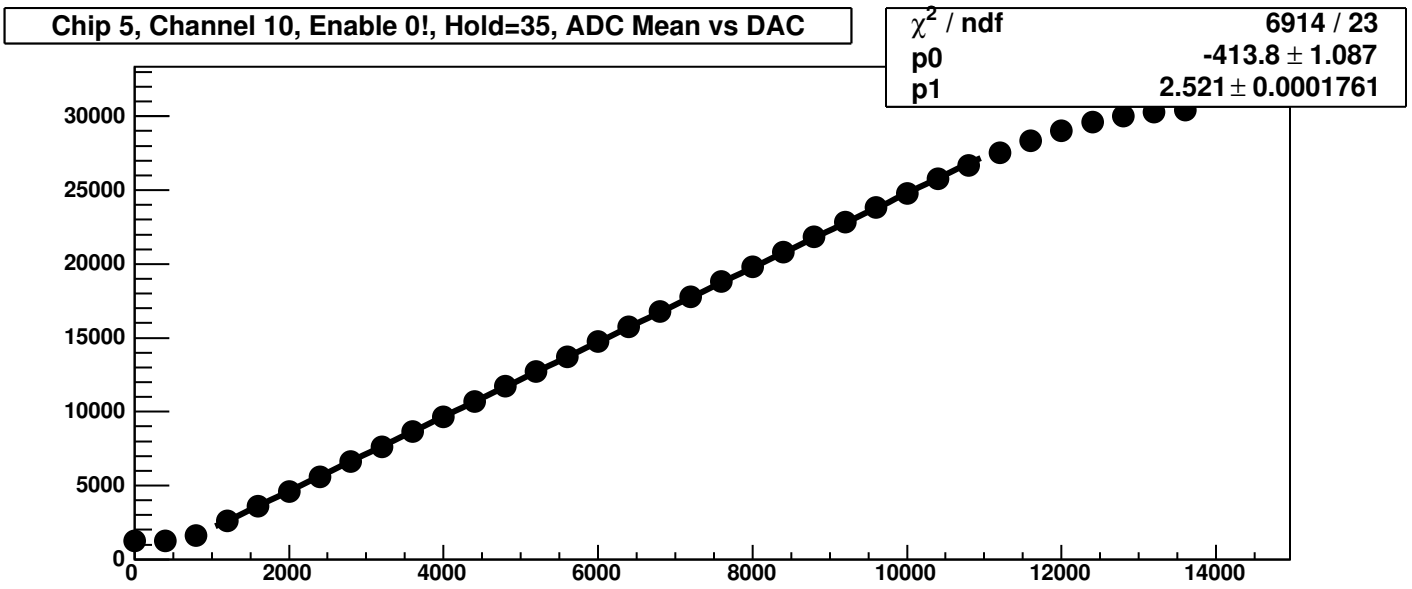


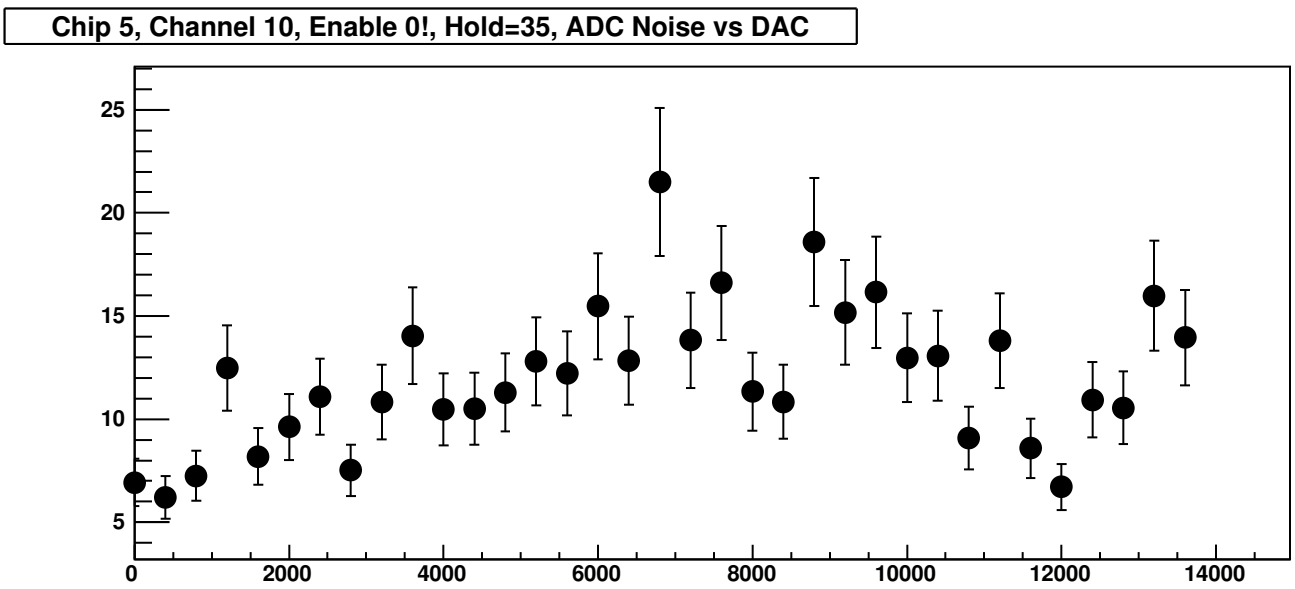
Chip 5, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

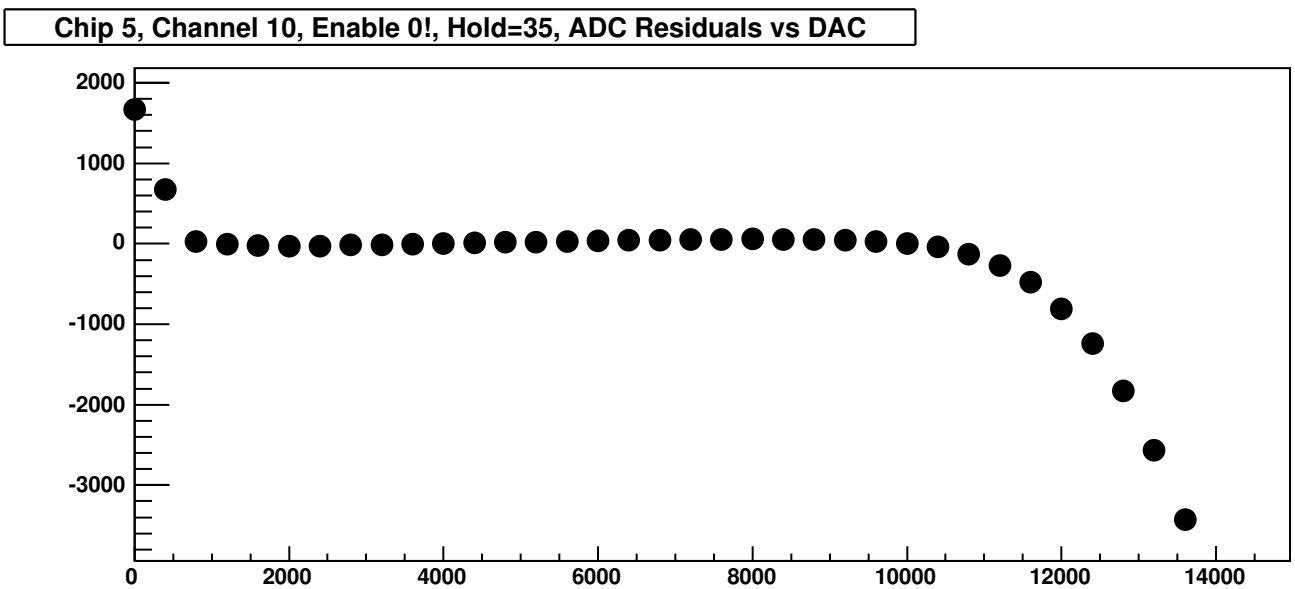


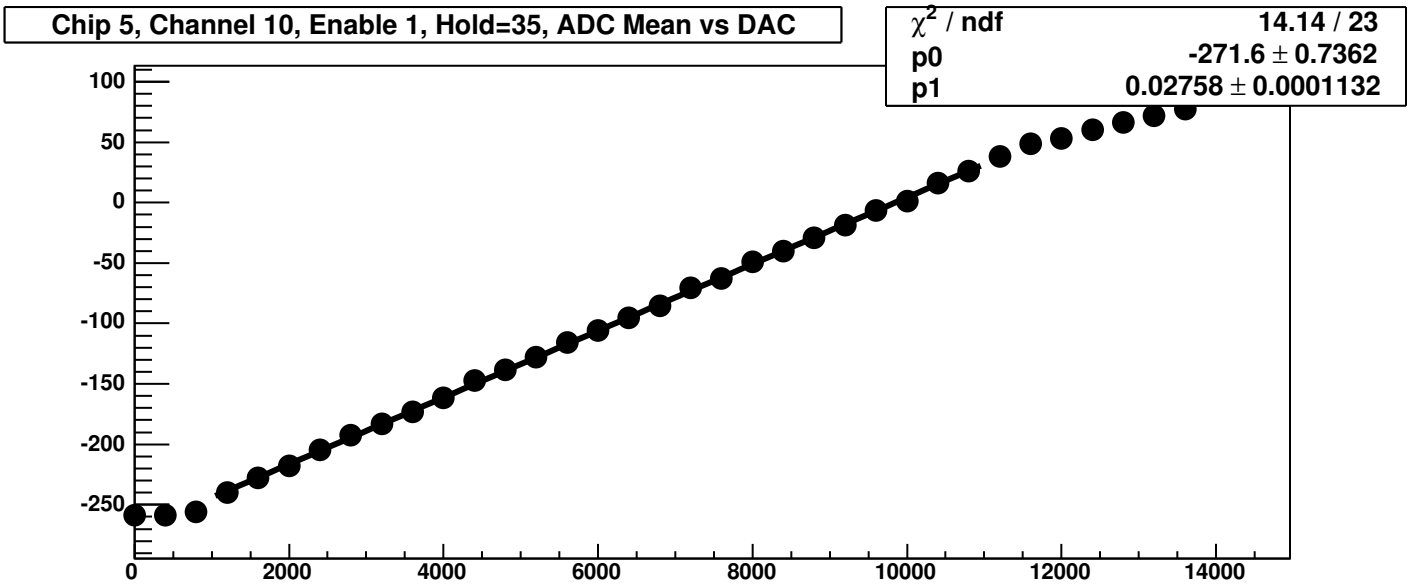
Chip 5, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

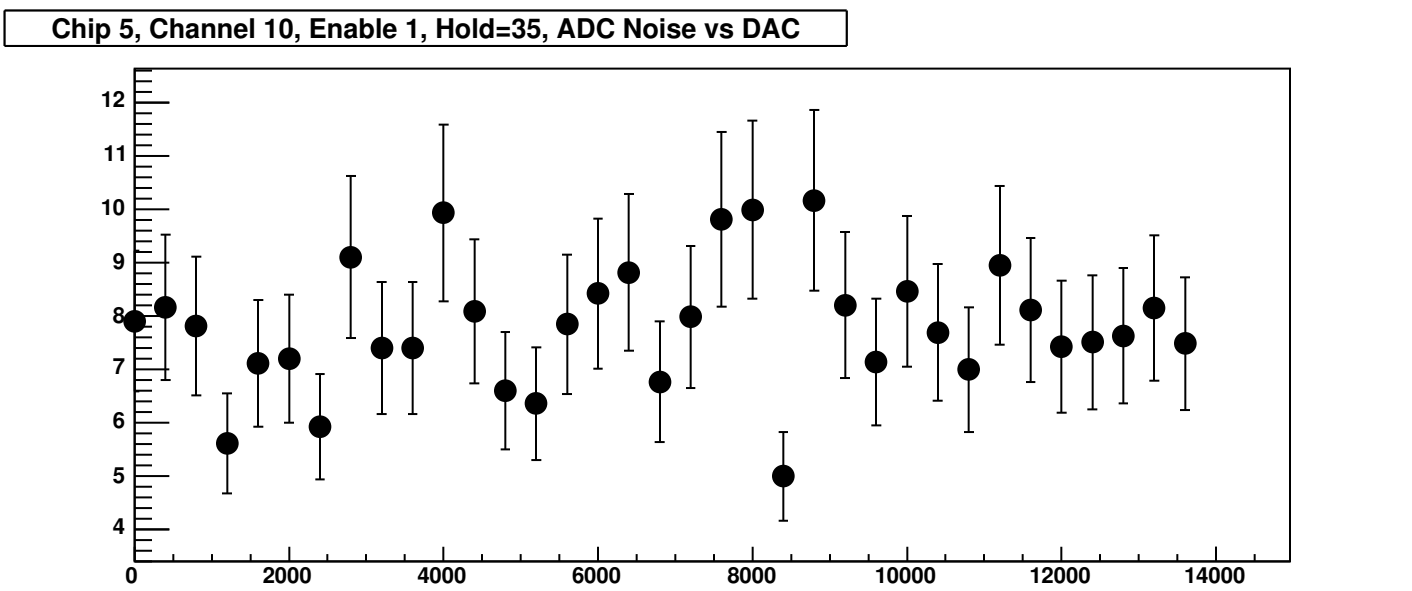


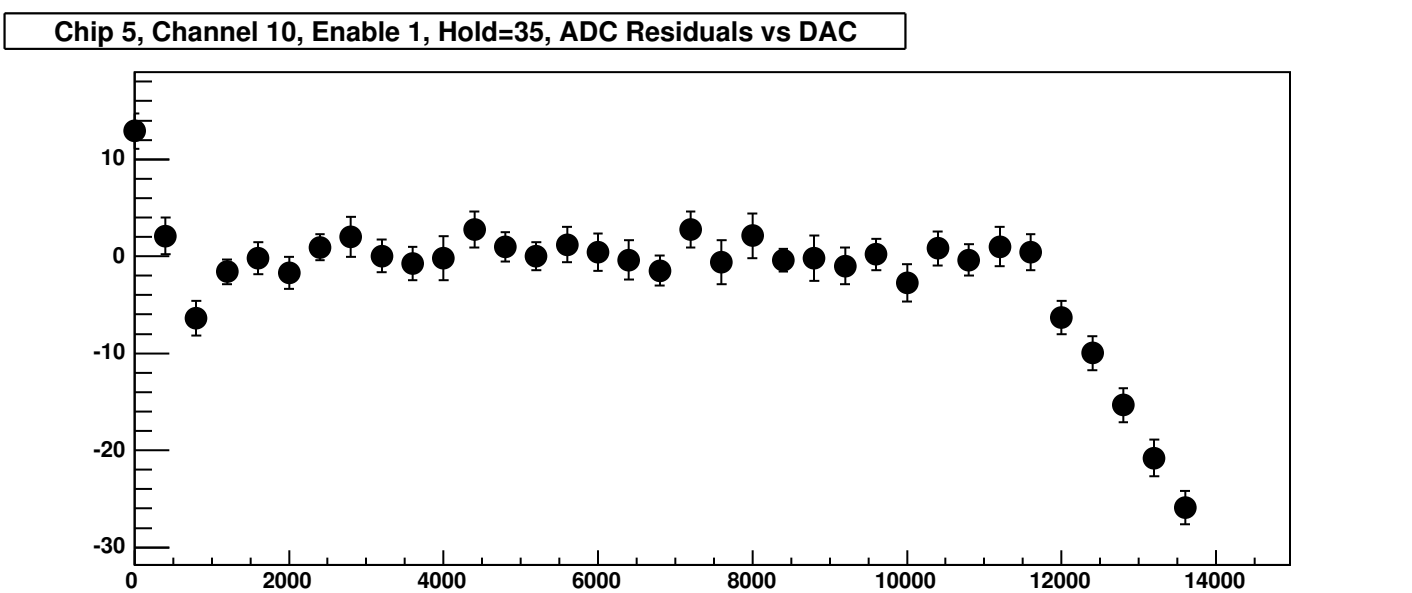

Chip 5, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 6914 / 23
p0 -413.8 ± 1.087
p1 2.521 ± 0.0001761
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

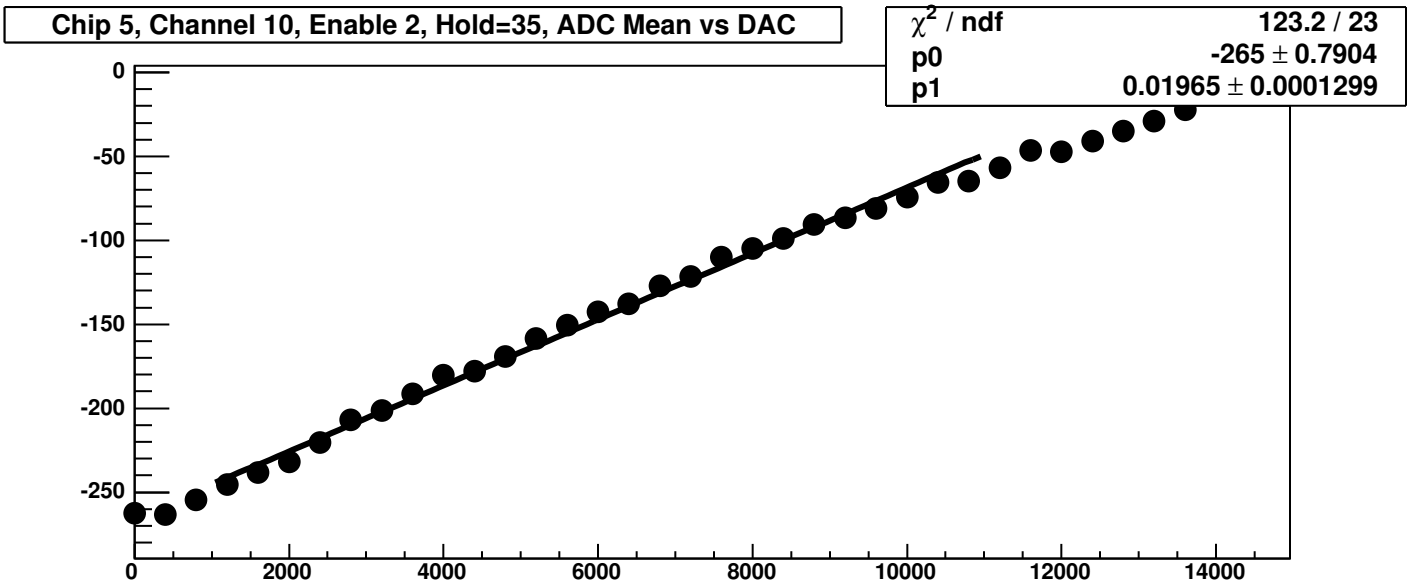

Chip 5, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

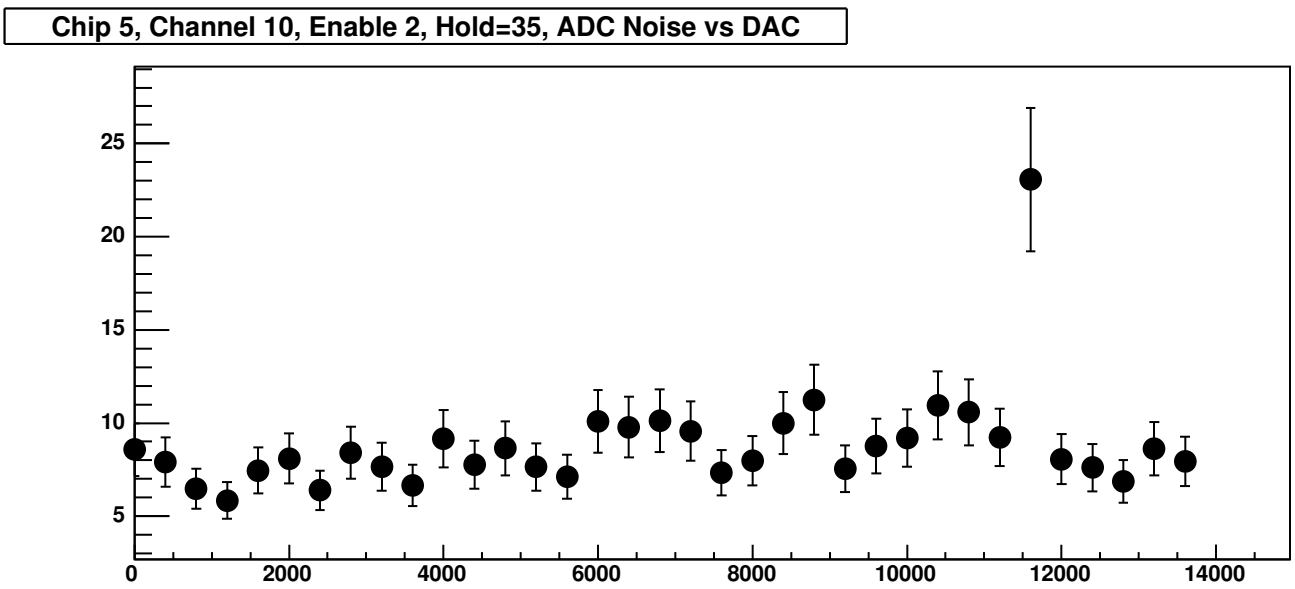

Chip 5, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

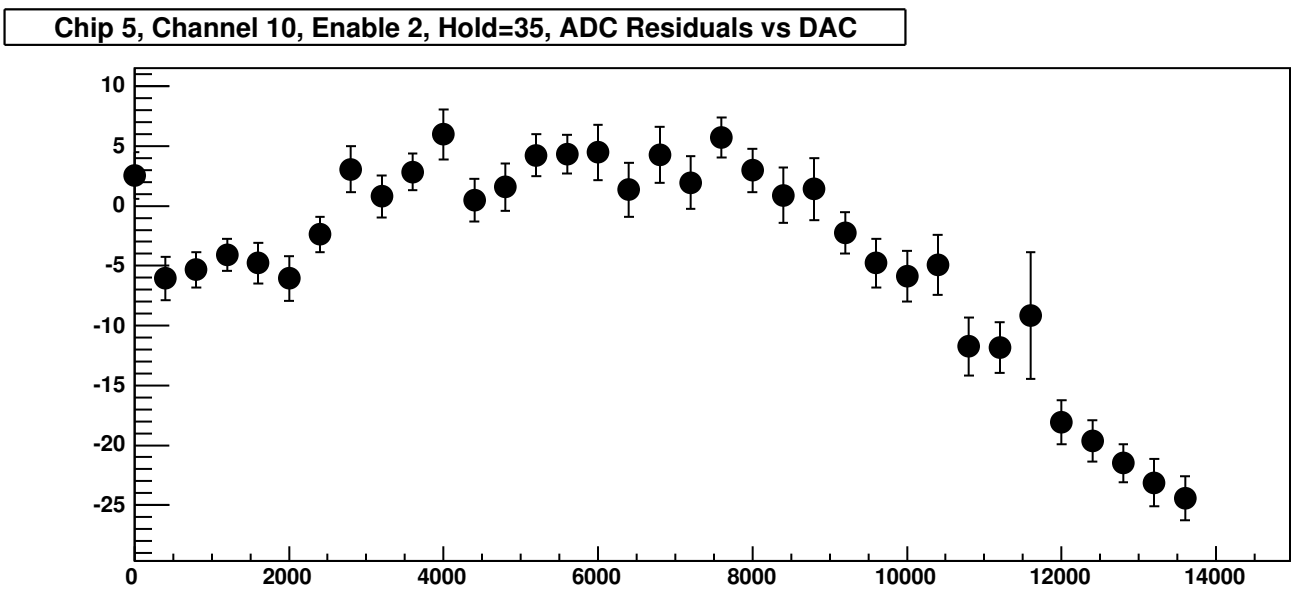

Chip 5, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 14.14 / 23
p0 -271.6 ± 0.7362
p1 0.02758 ± 0.0001132
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

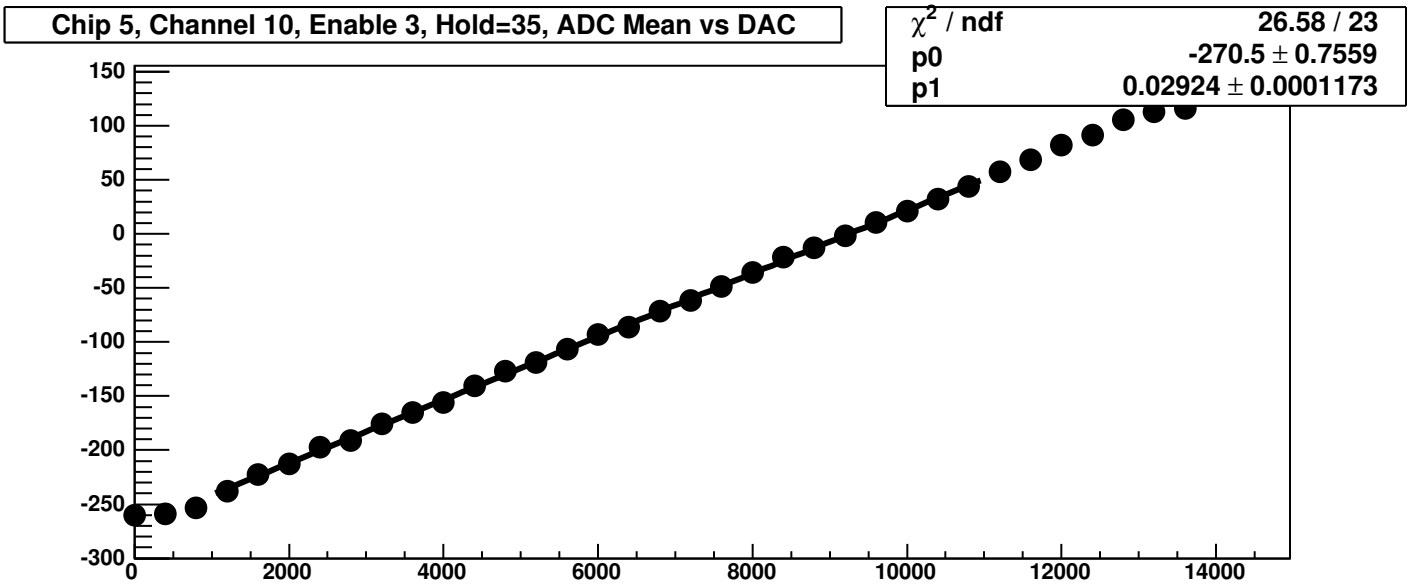

Chip 5, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

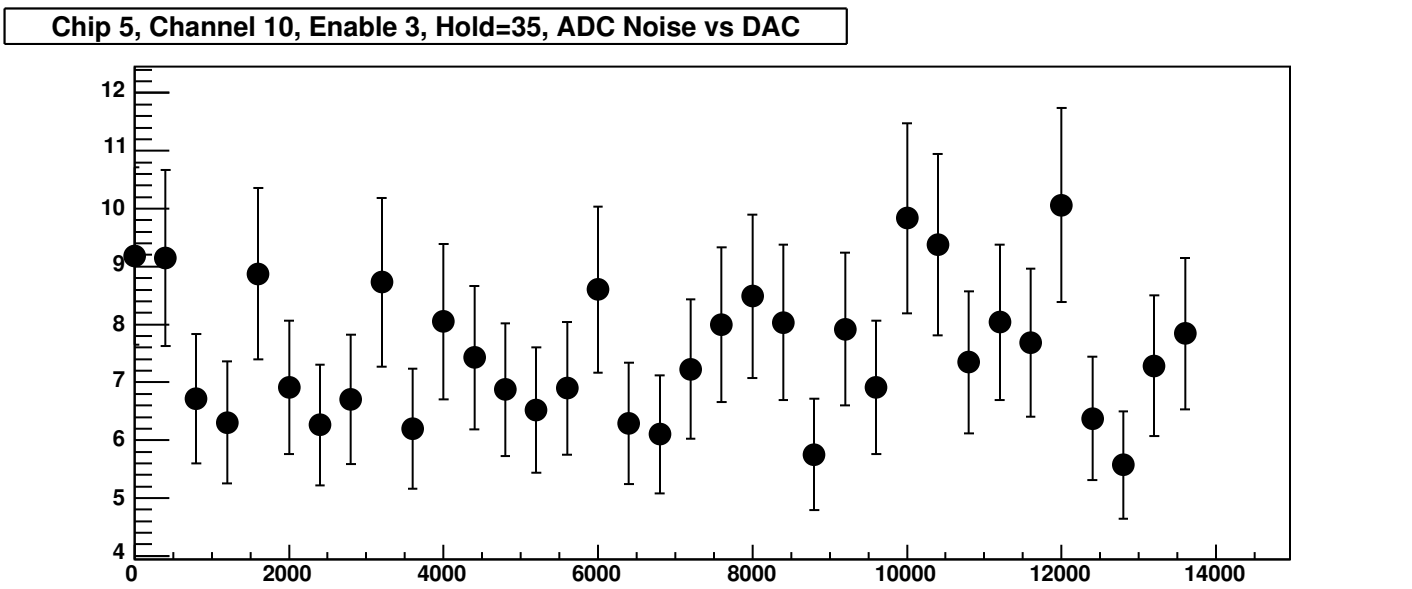

Chip 5, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

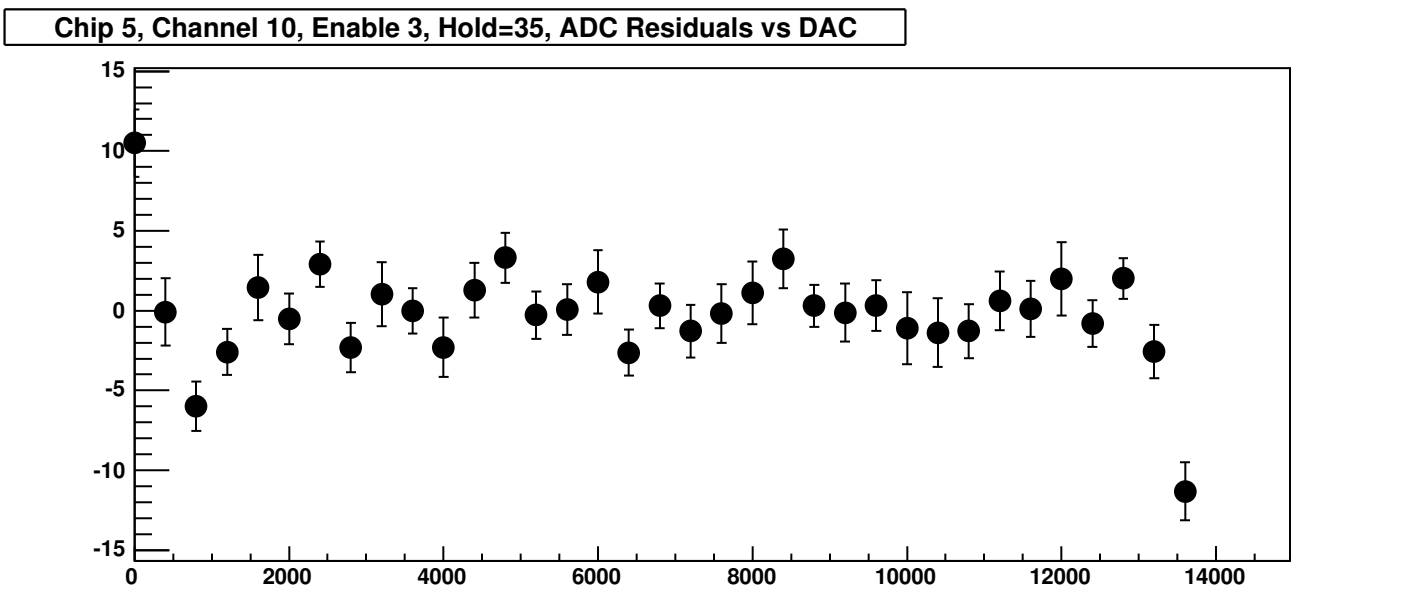

Chip 5, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 123.2 / 23
p0 -265 ± 0.7904
p1 0.01965 ± 0.0001299
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

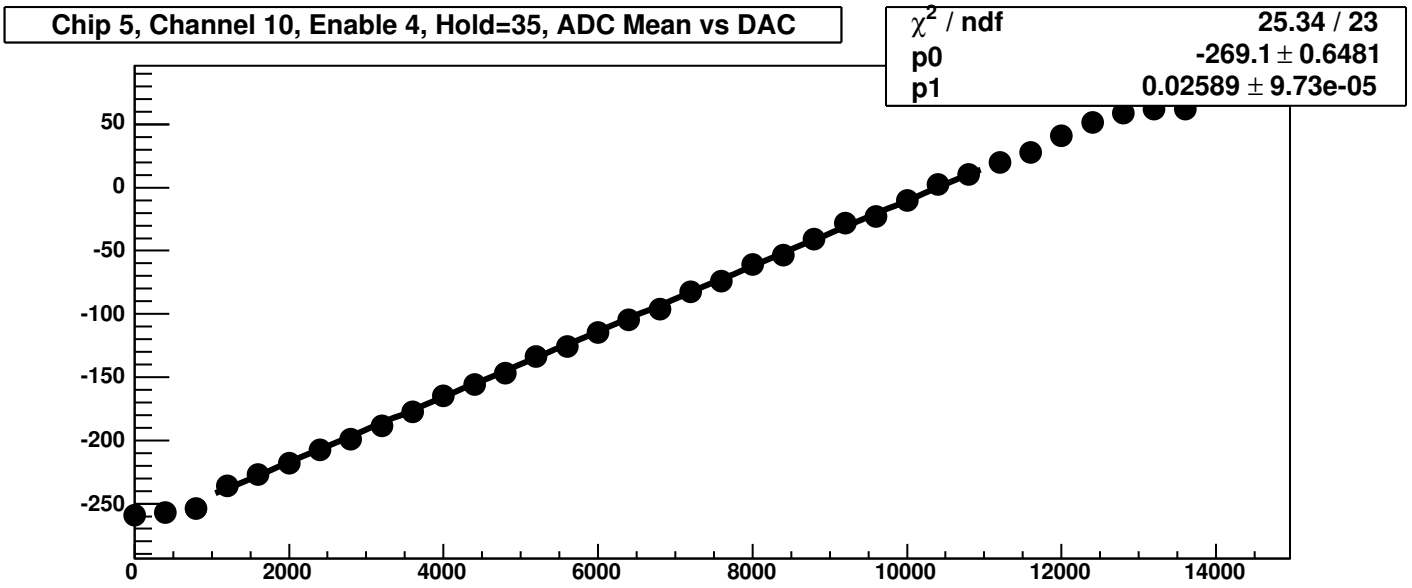

Chip 5, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

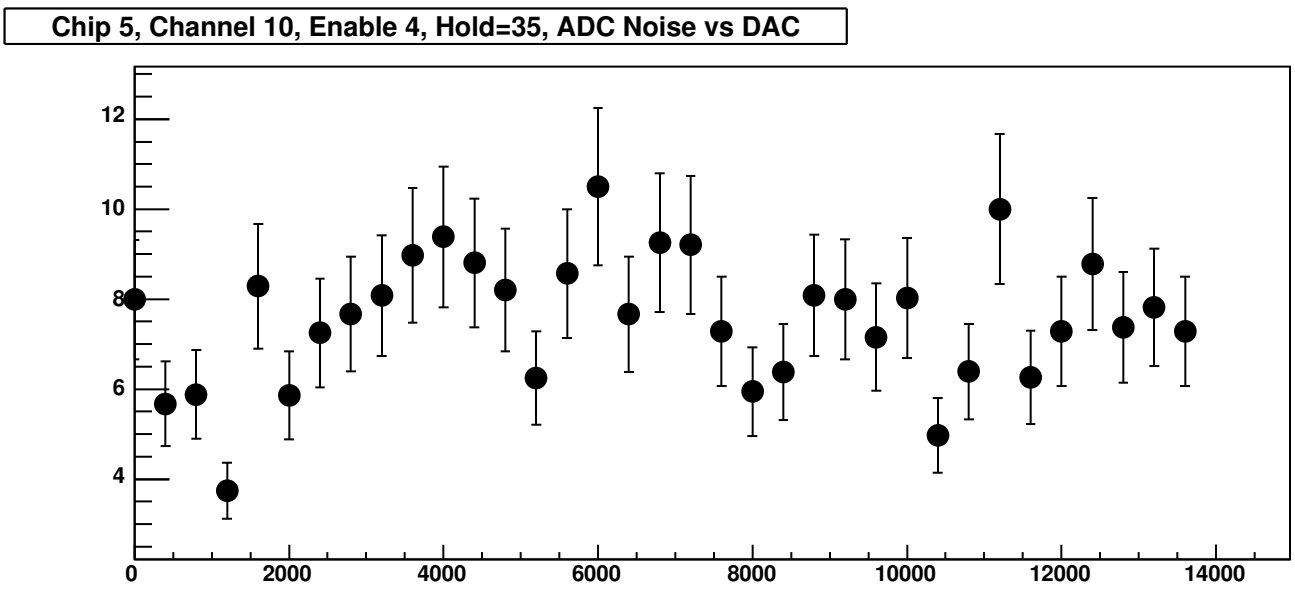

Chip 5, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

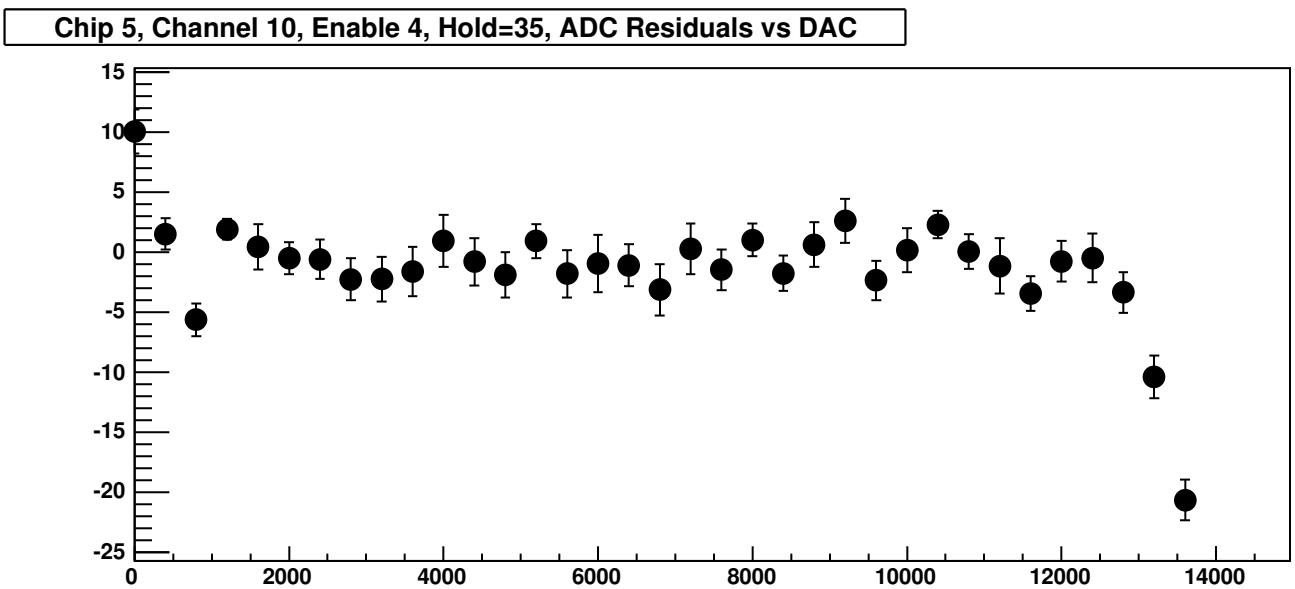

Chip 5, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.58 / 23
p0 -270.5 ± 0.7559
p1 0.02924 ± 0.0001173
150
100
50
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

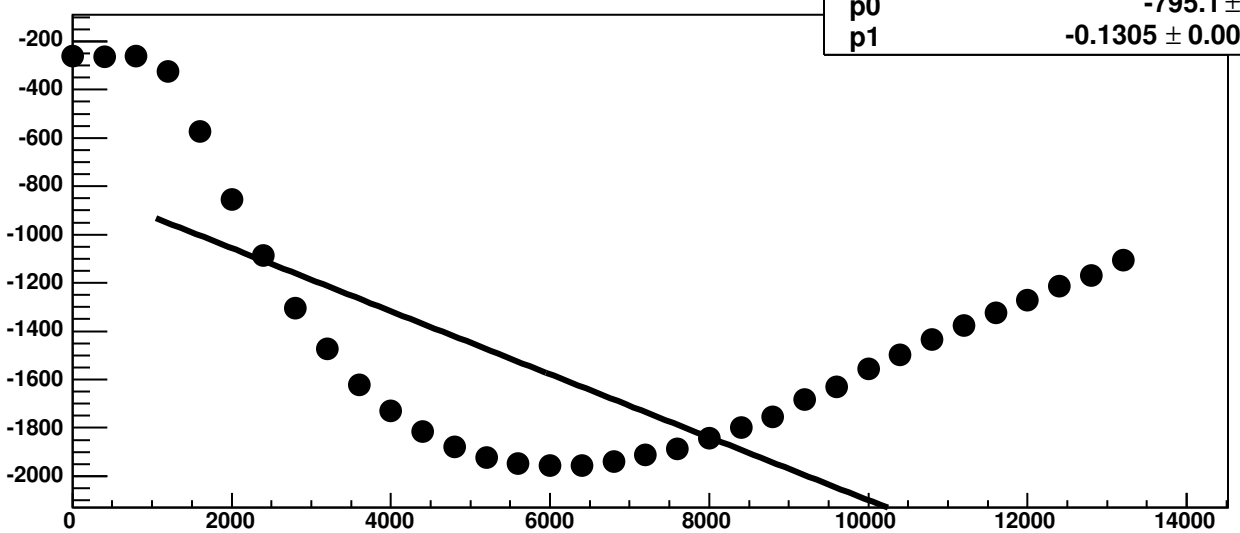

Chip 5, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 25.34 / 23
p0 -269.1 ± 0.6481
p1 0.02589 ± 9.73e-05
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

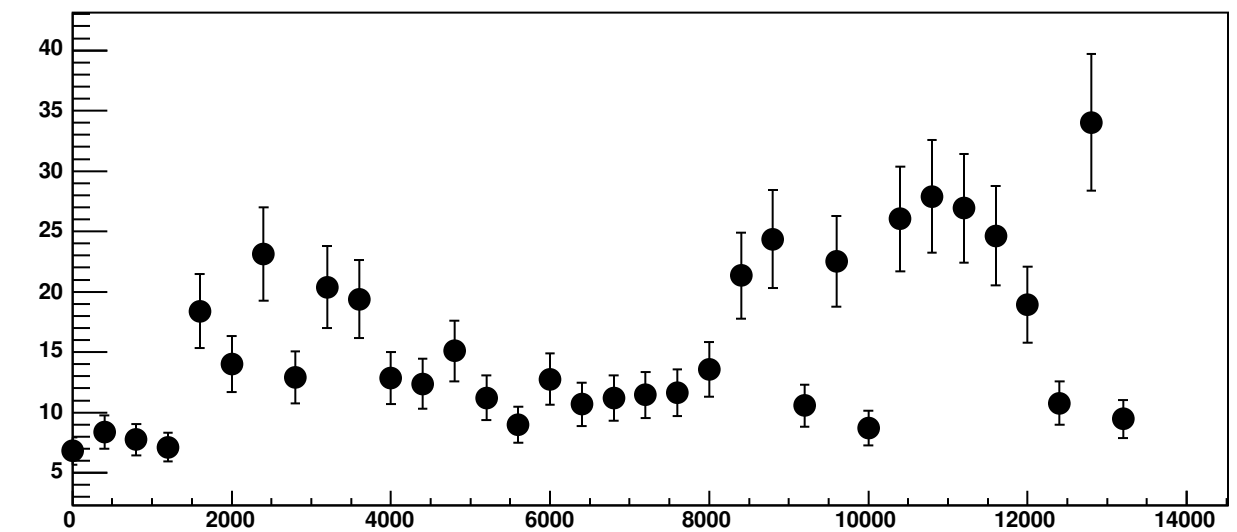

Chip 5, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

Chip 5, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

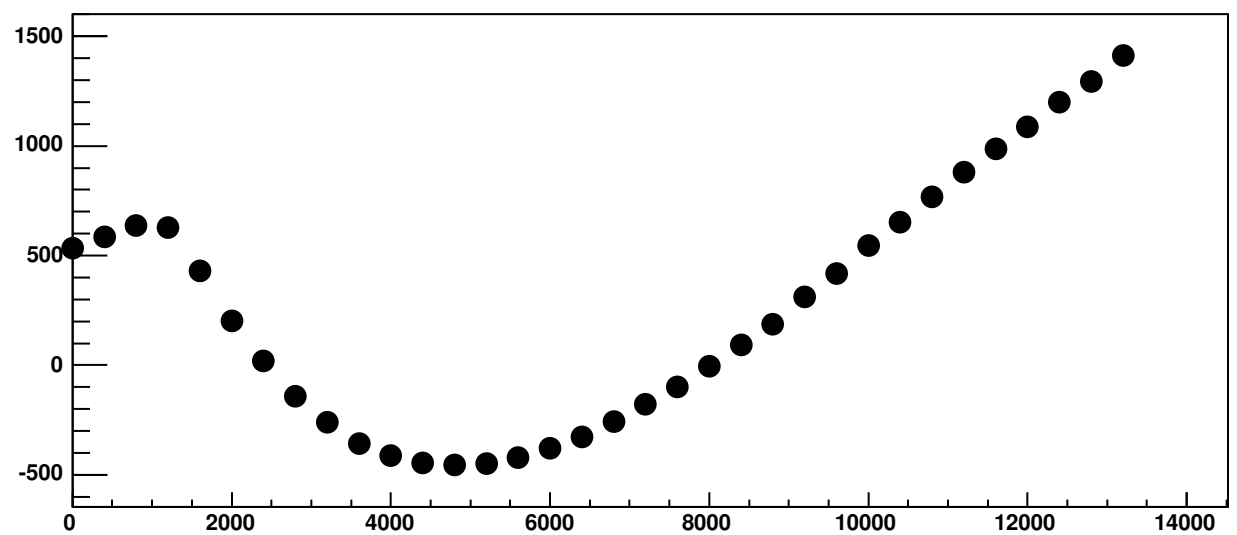
χ^2 / ndf	4.838e+05 / 23
p0	-795.1 ± 1.313
p1	-0.1305 ± 0.0002078



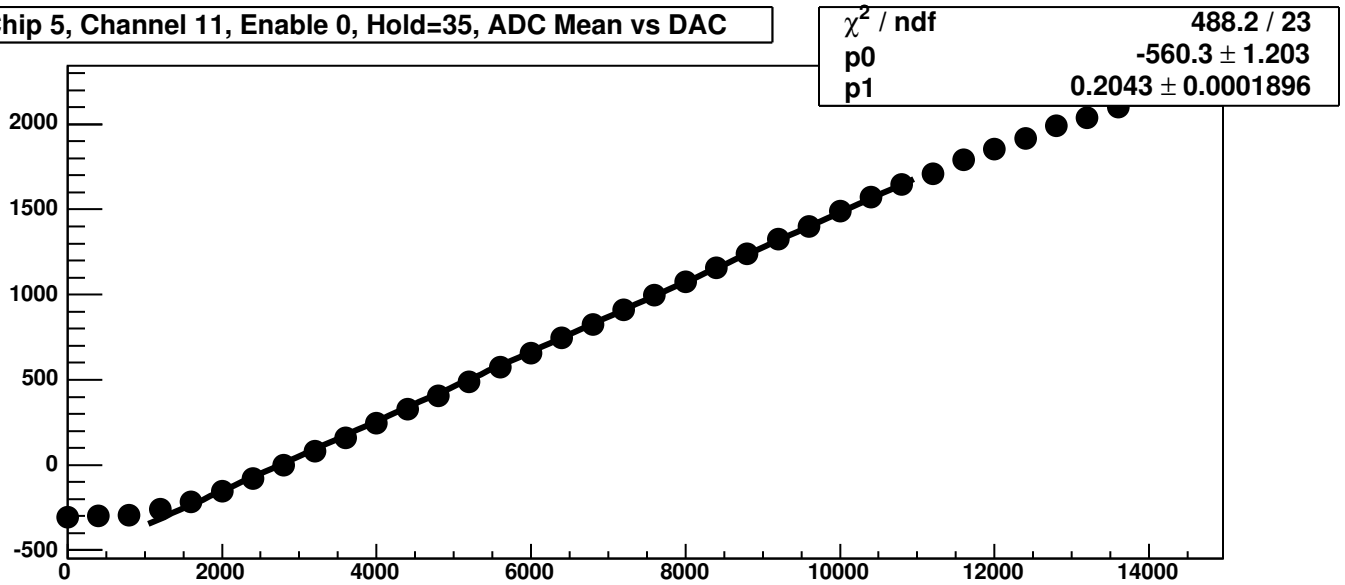
Chip 5, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



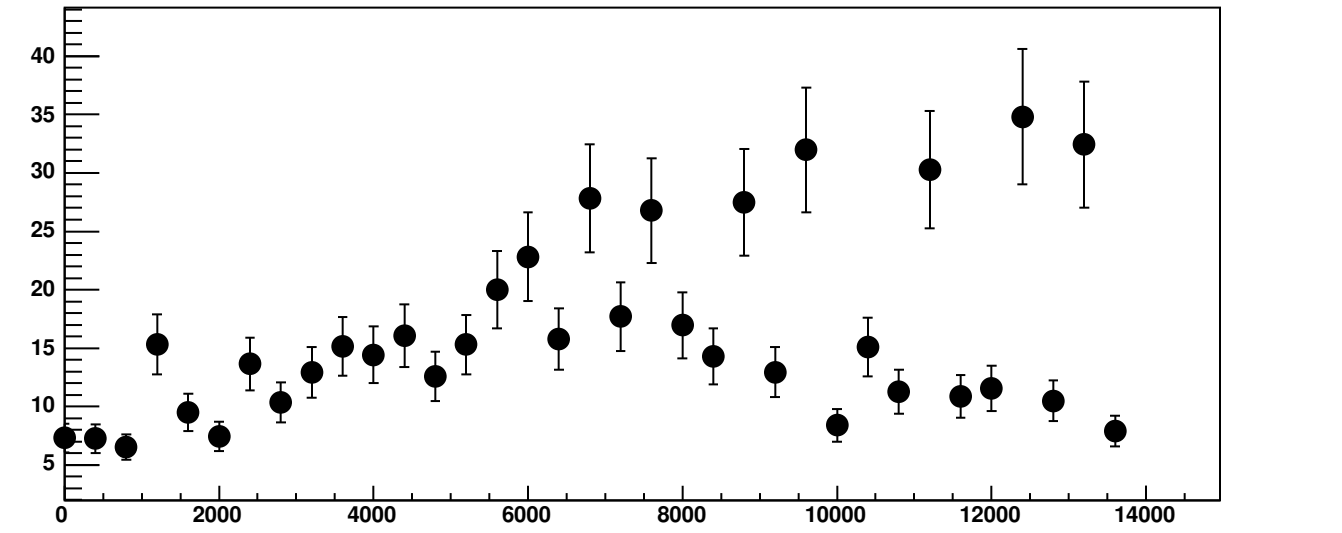
Chip 5, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



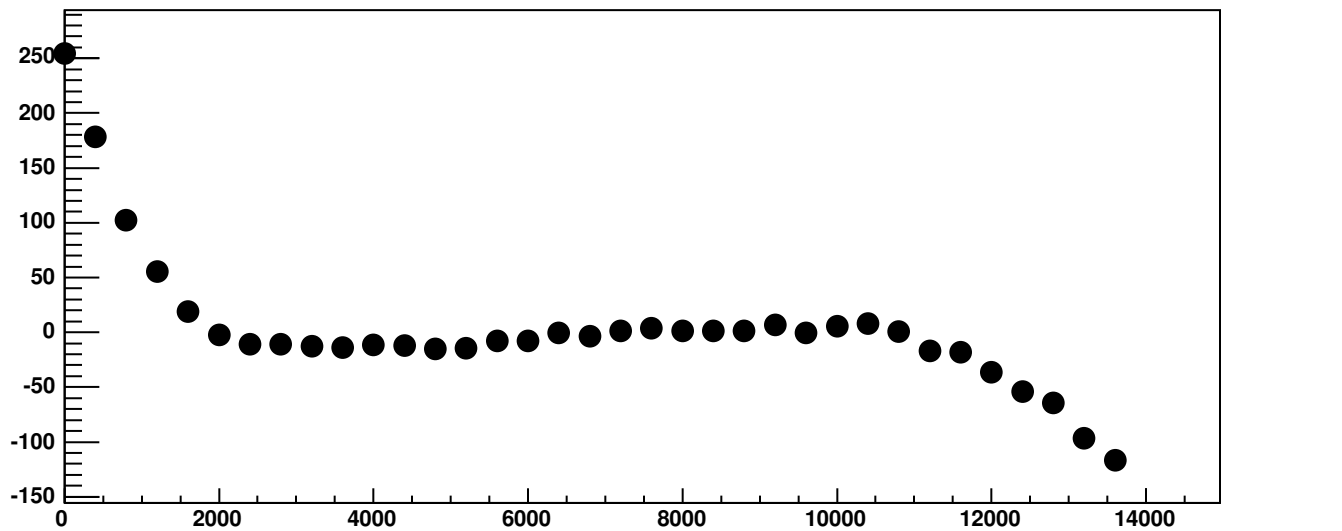
Chip 5, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC

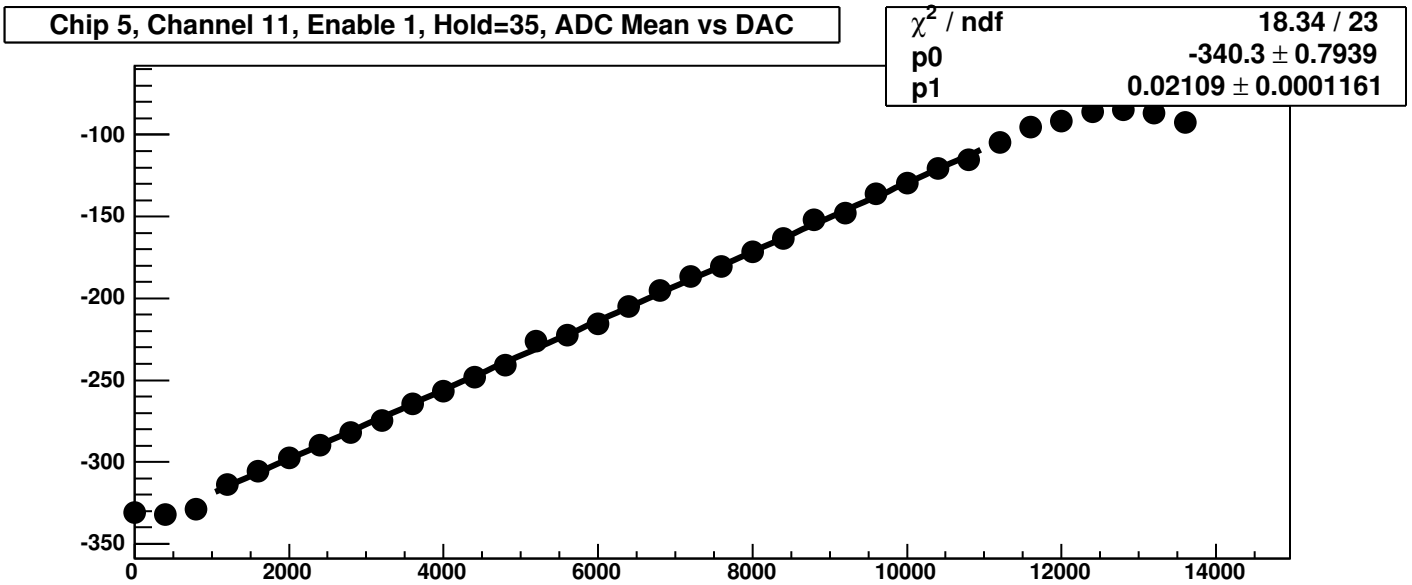


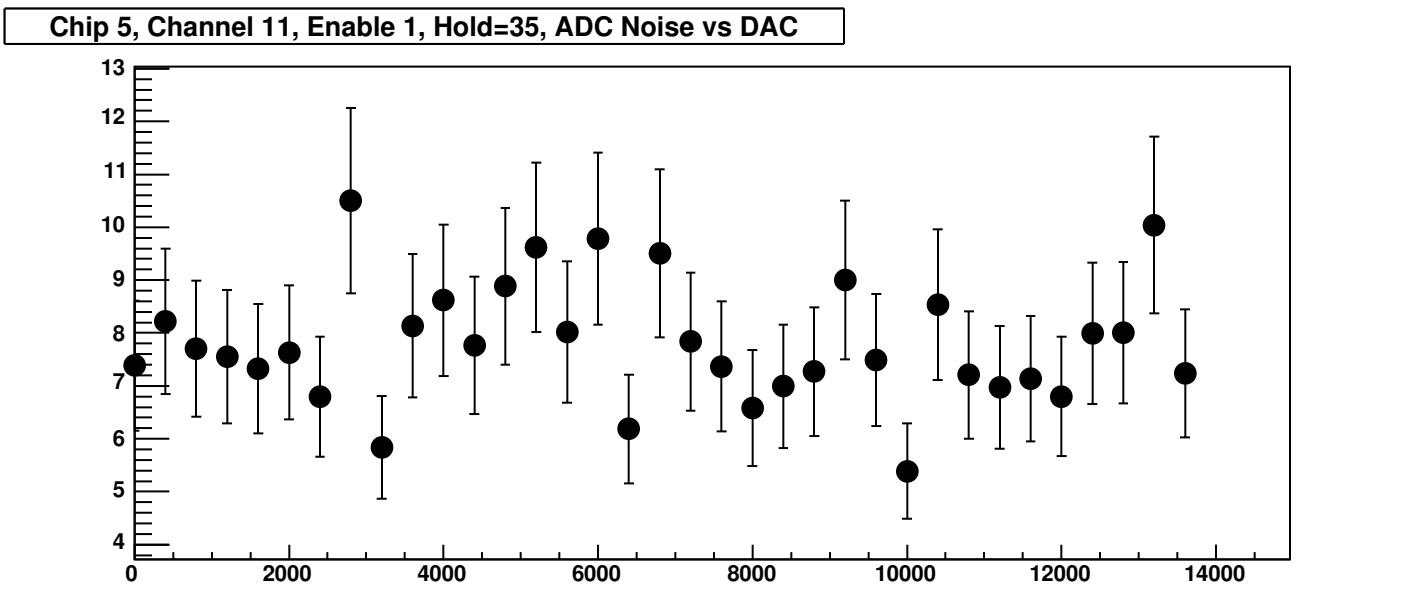
Chip 5, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC

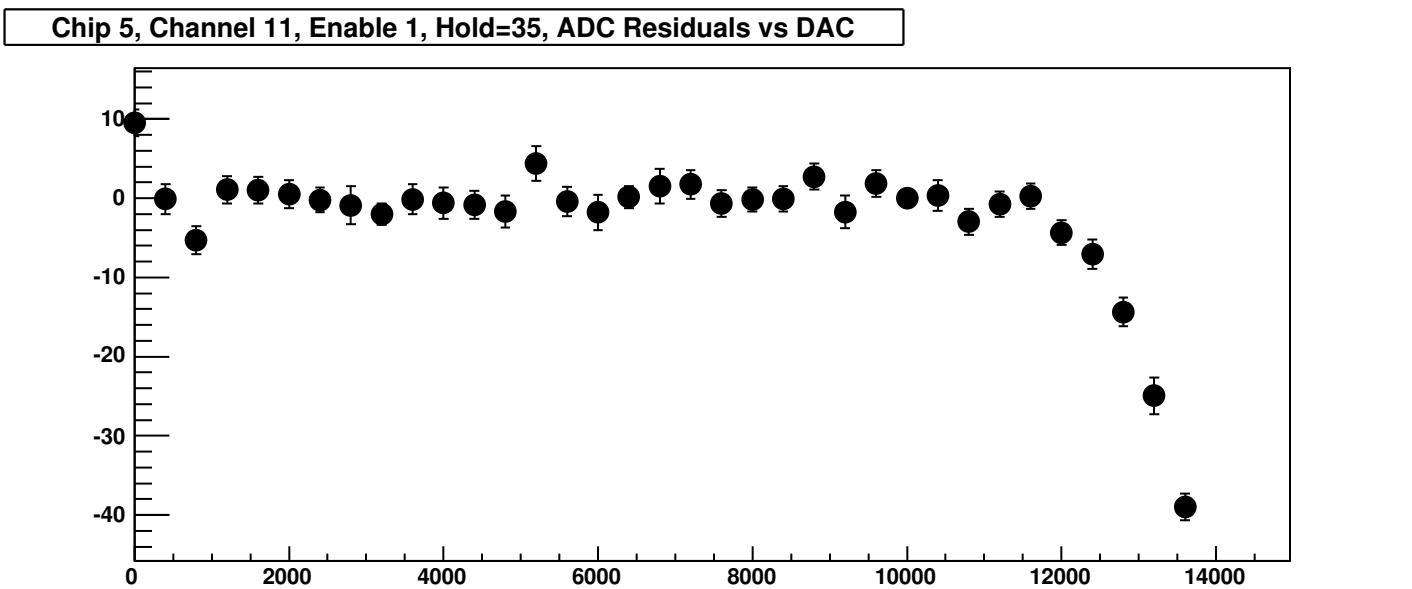


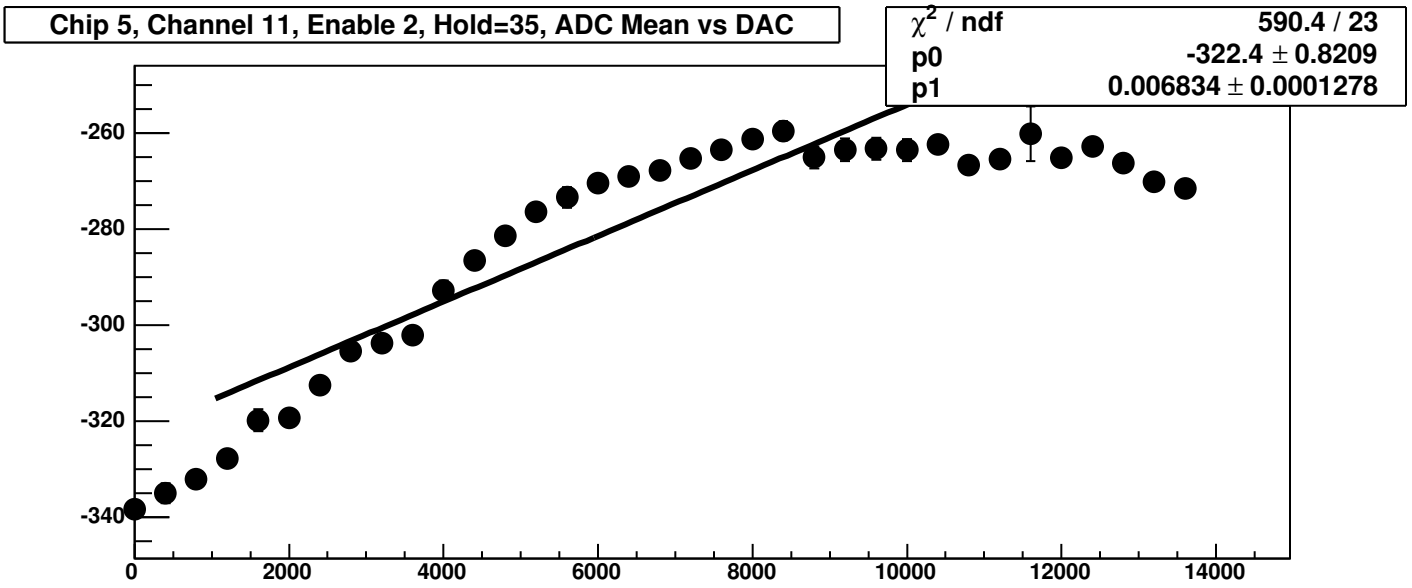
Chip 5, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC

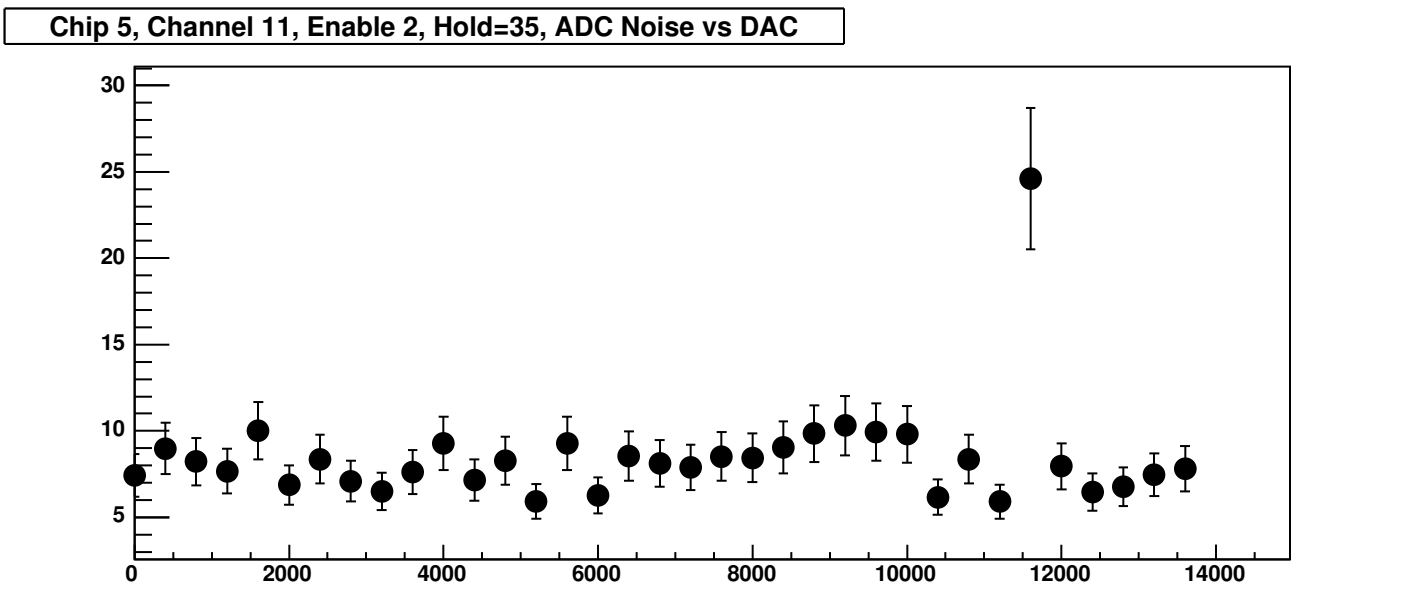


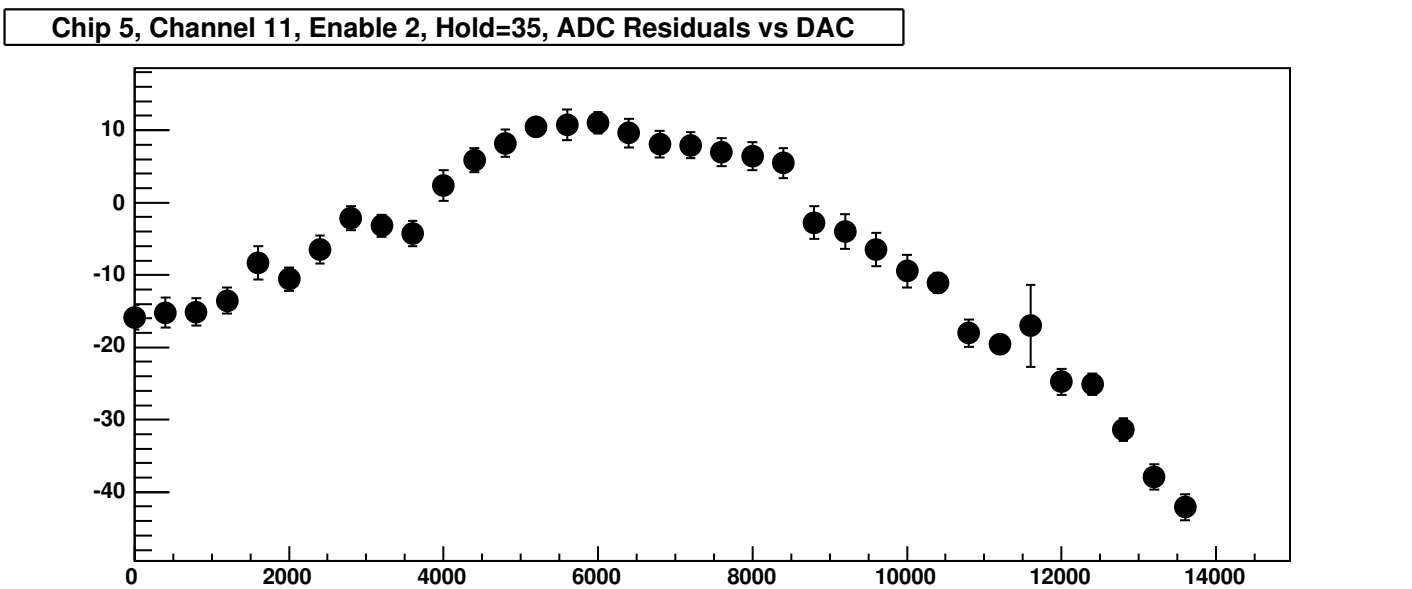

Chip 5, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 18.34 / 23
p0 -340.3 ± 0.7939
p1 0.02109 ± 0.0001161
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

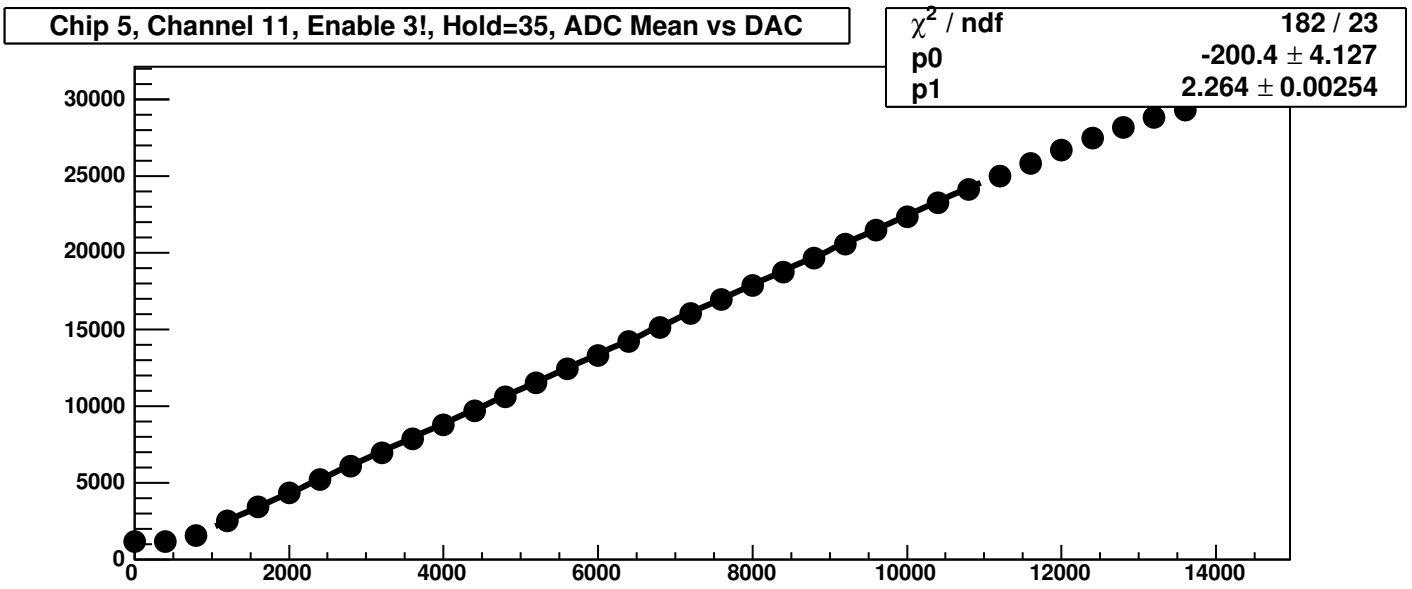

Chip 5, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

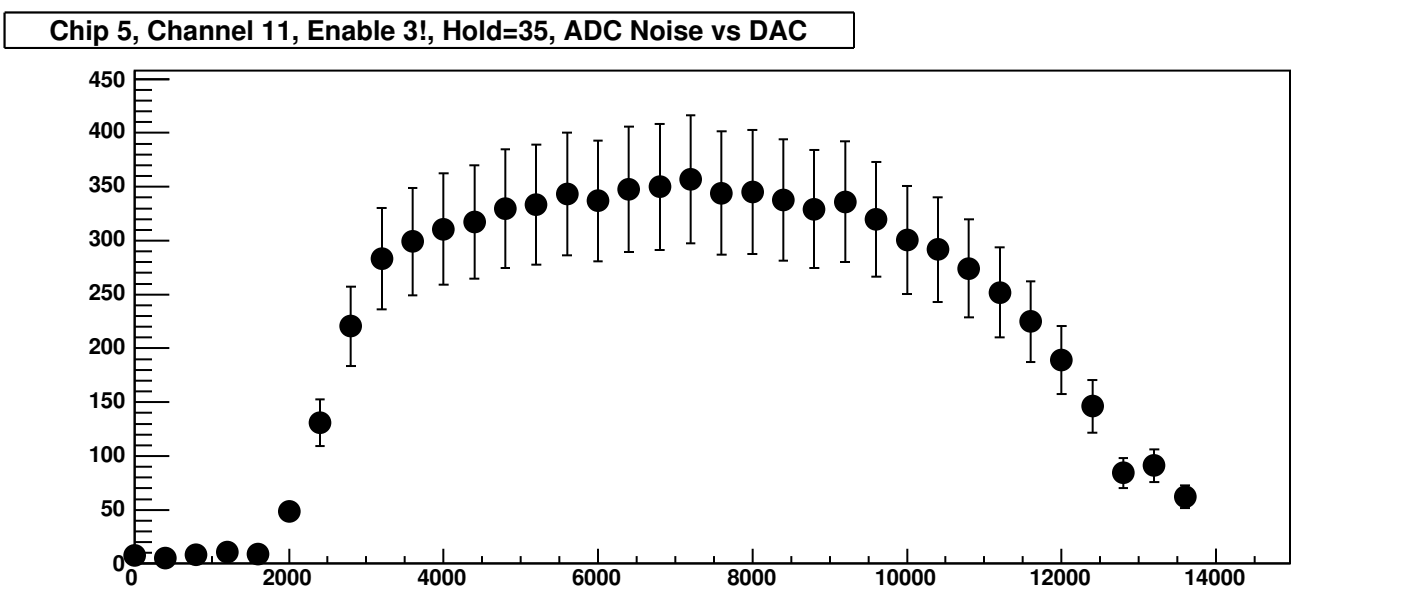

Chip 5, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

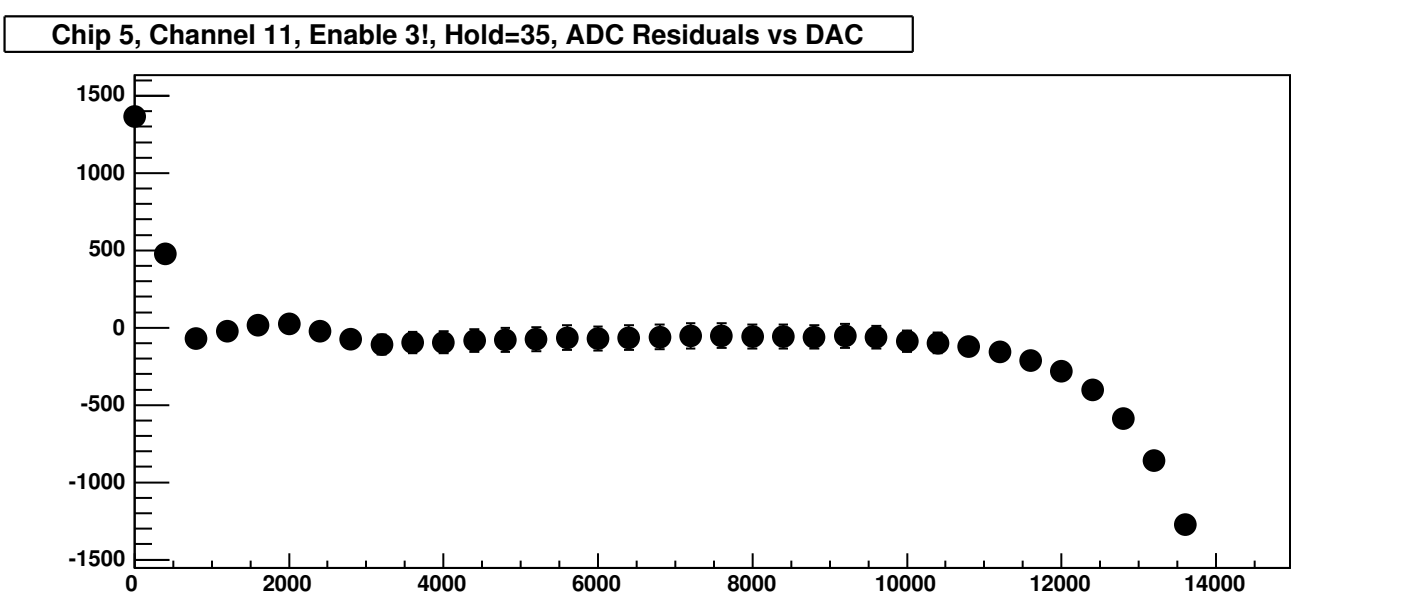

Chip 5, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 590.4 / 23
p0 -322.4 ± 0.8209
p1 0.006834 ± 0.0001278
-260
-280
-300
-320
-340
0
2000
4000
6000
8000
10000
12000
14000

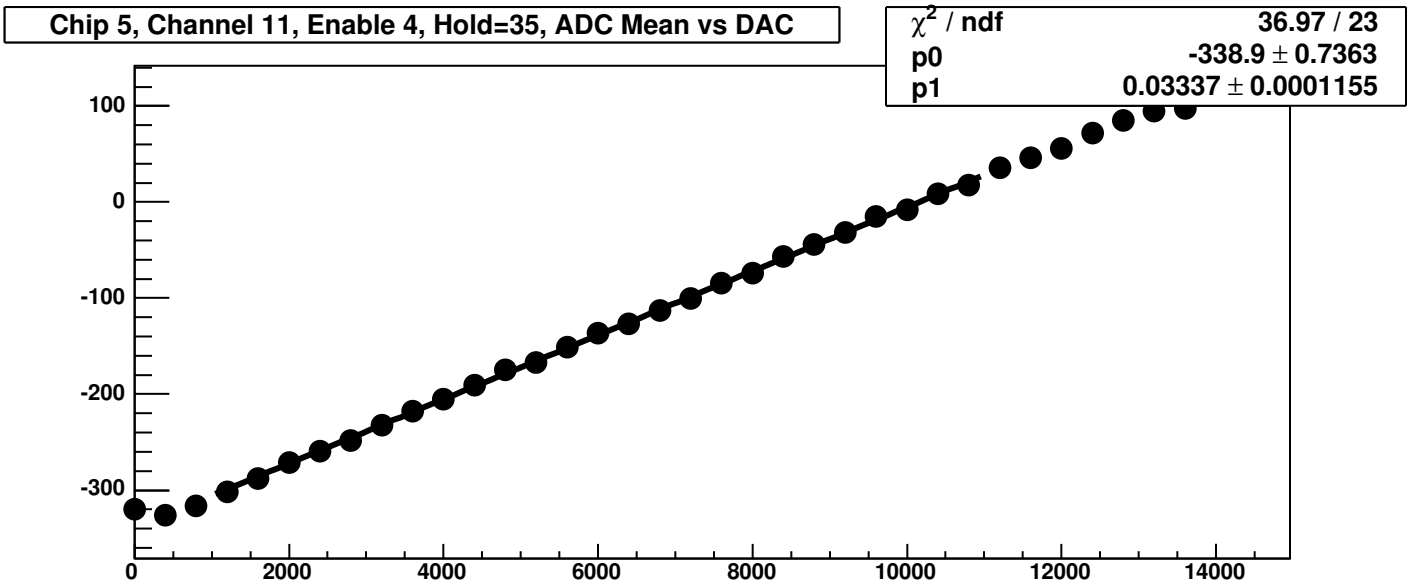

Chip 5, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

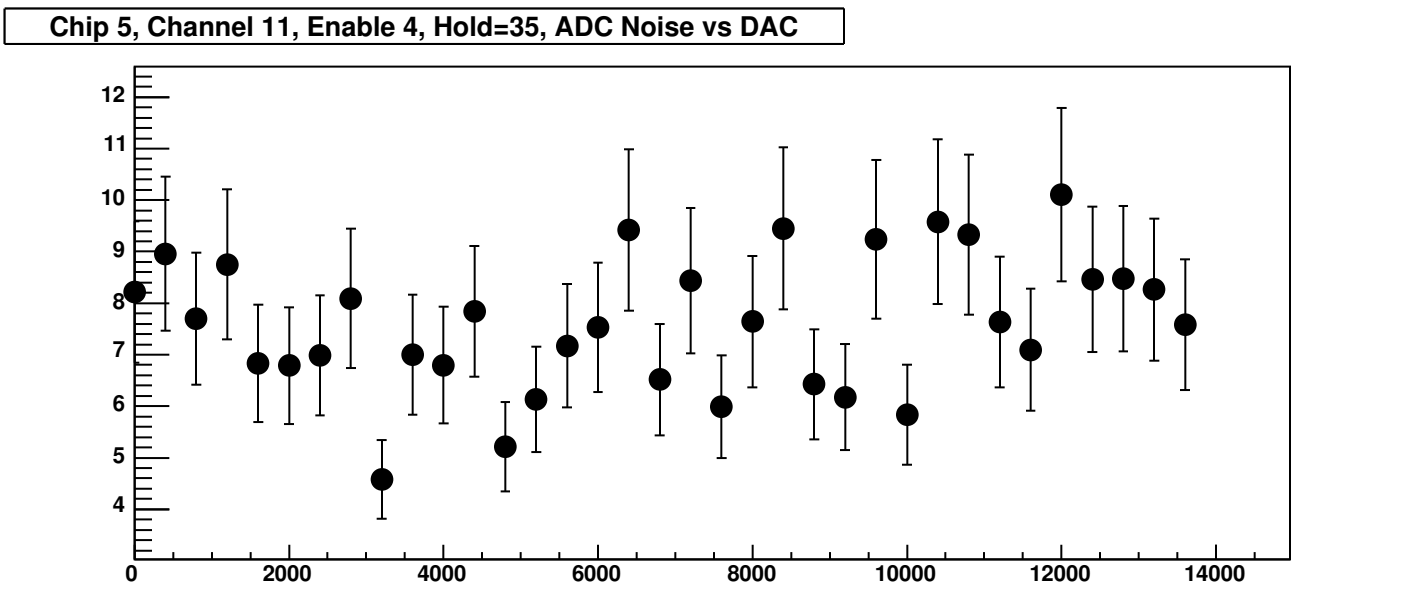

Chip 5, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

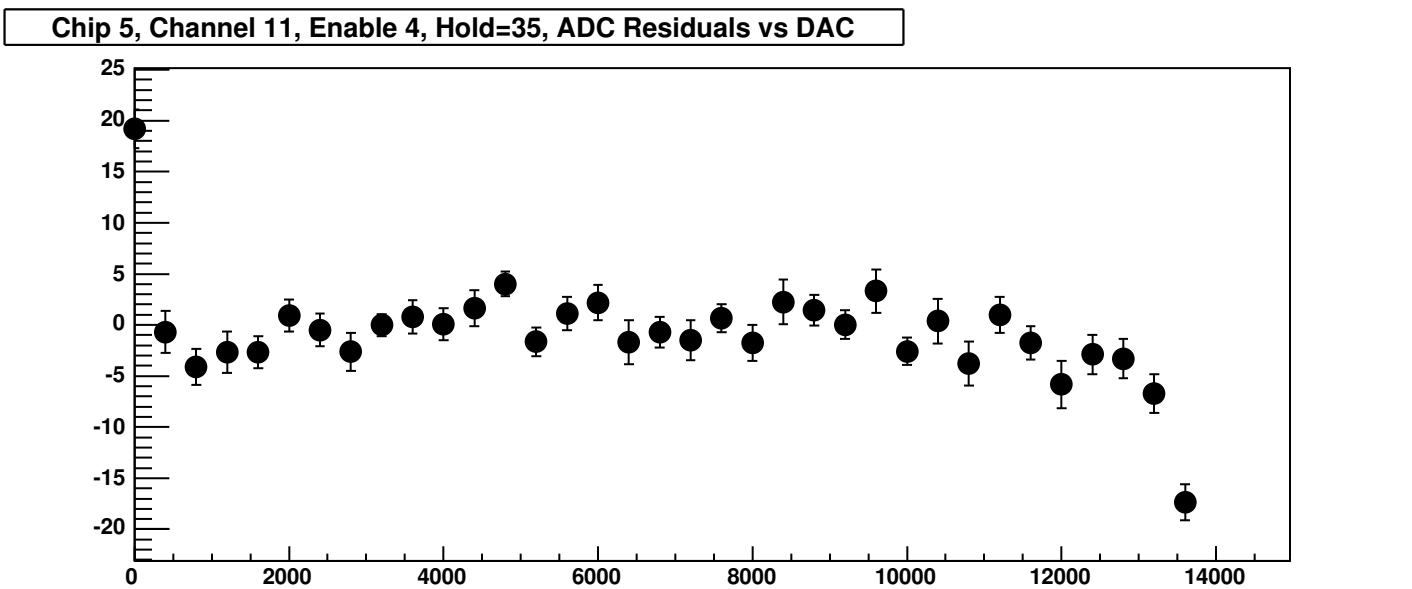

Chip 5, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 182 / 23
p0 -200.4 ± 4.127
p1 2.264 ± 0.00254
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

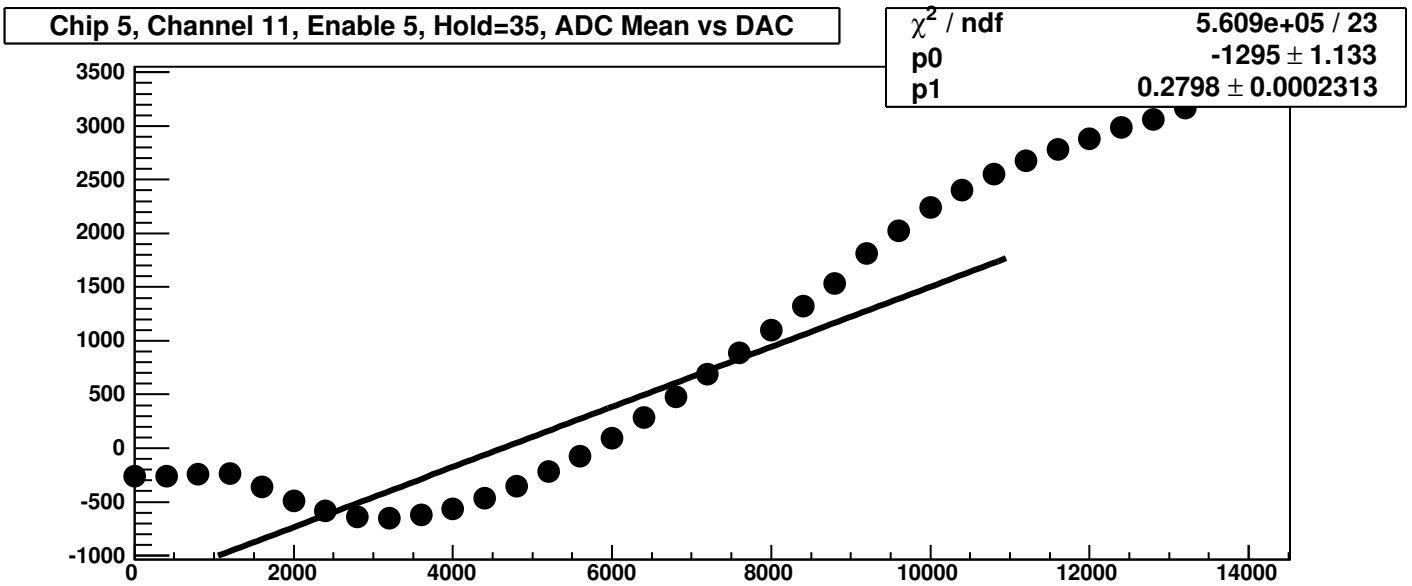

Chip 5, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

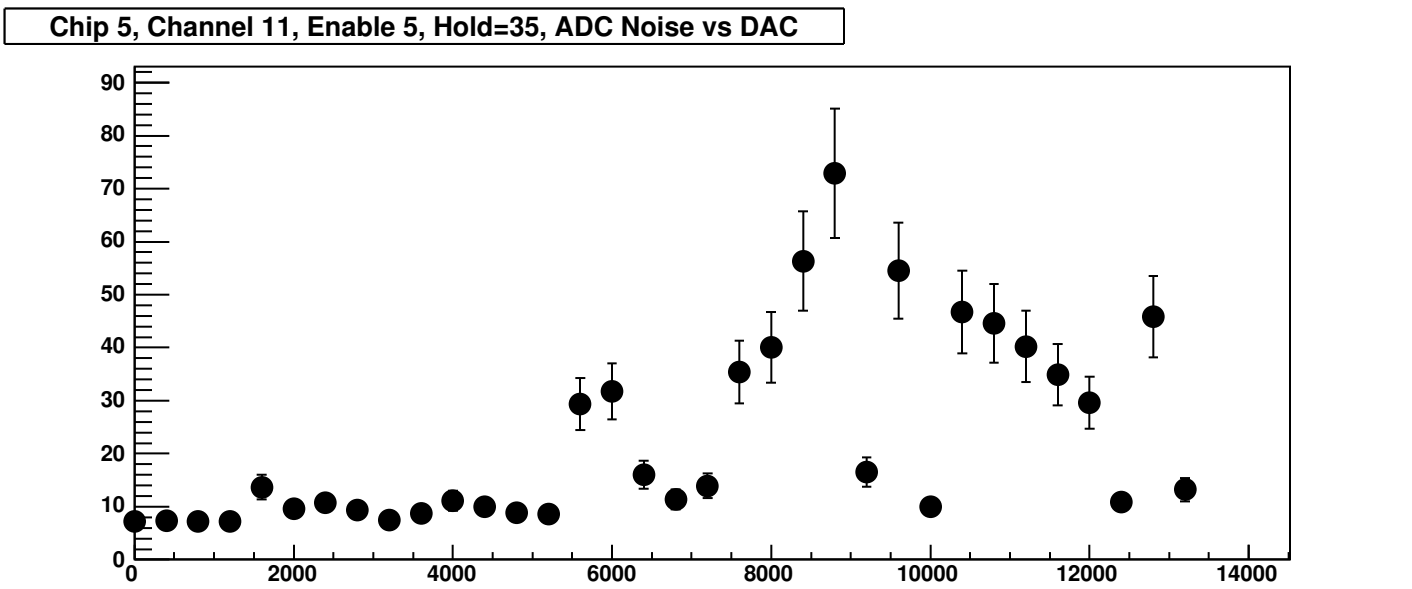

Chip 5, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

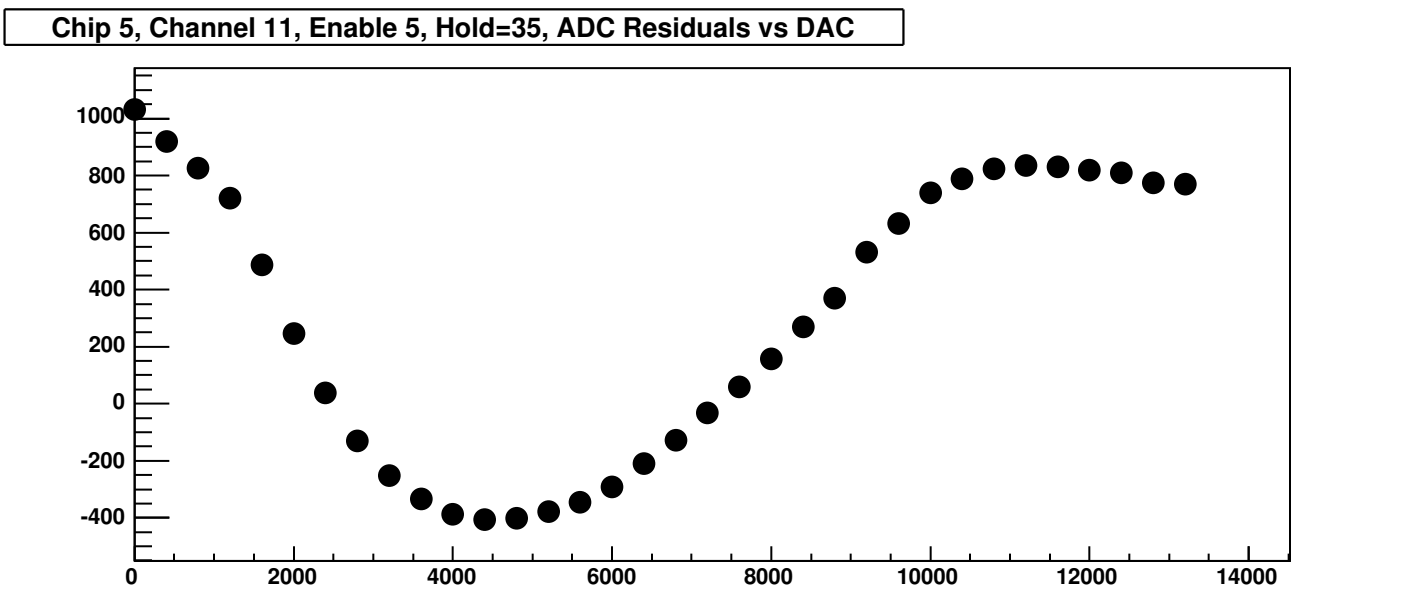

Chip 5, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 36.97 / 23
p0 -338.9 ± 0.7363
p1 0.03337 ± 0.0001155
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

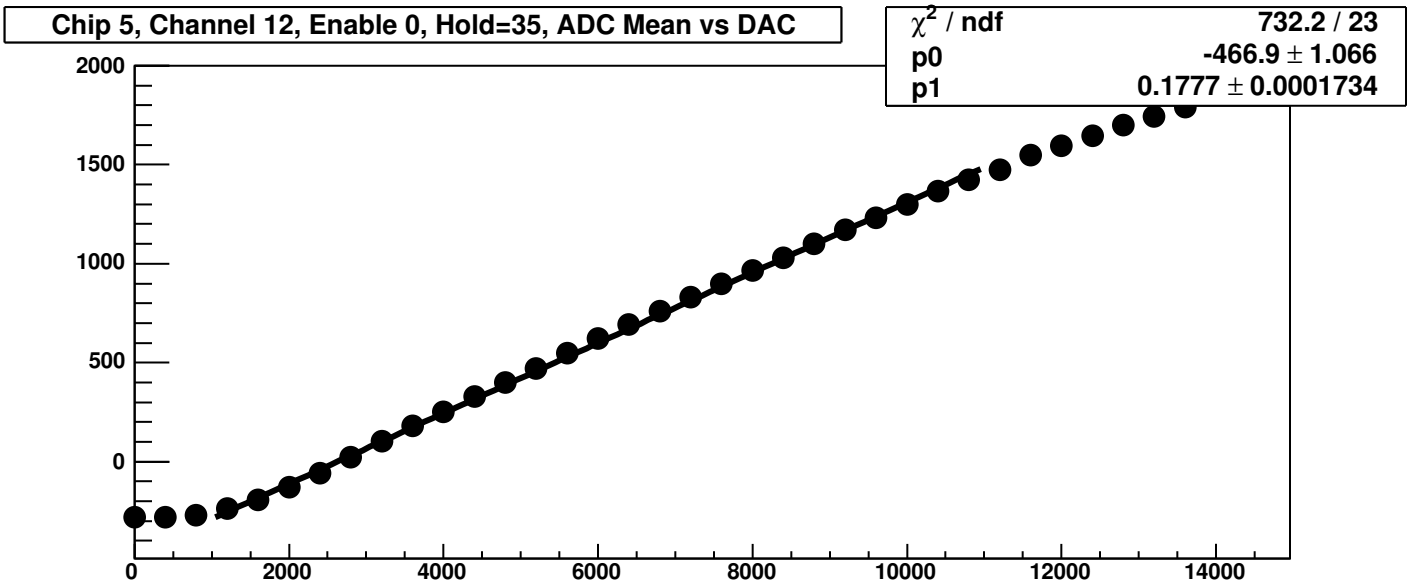

Chip 5, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

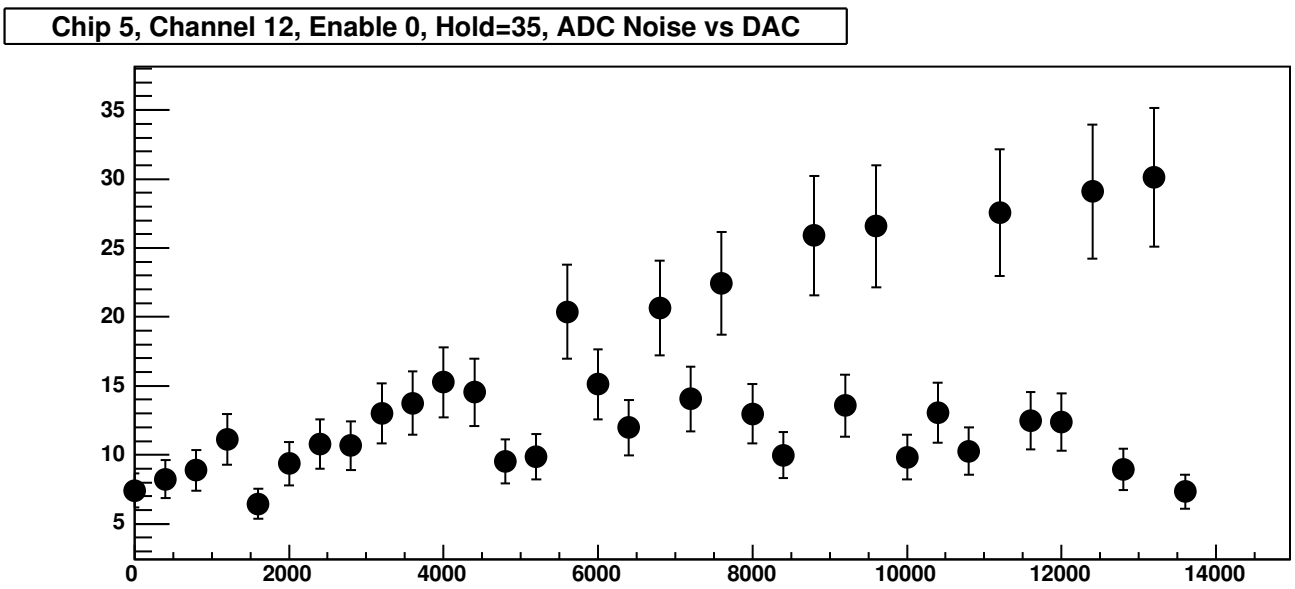

Chip 5, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

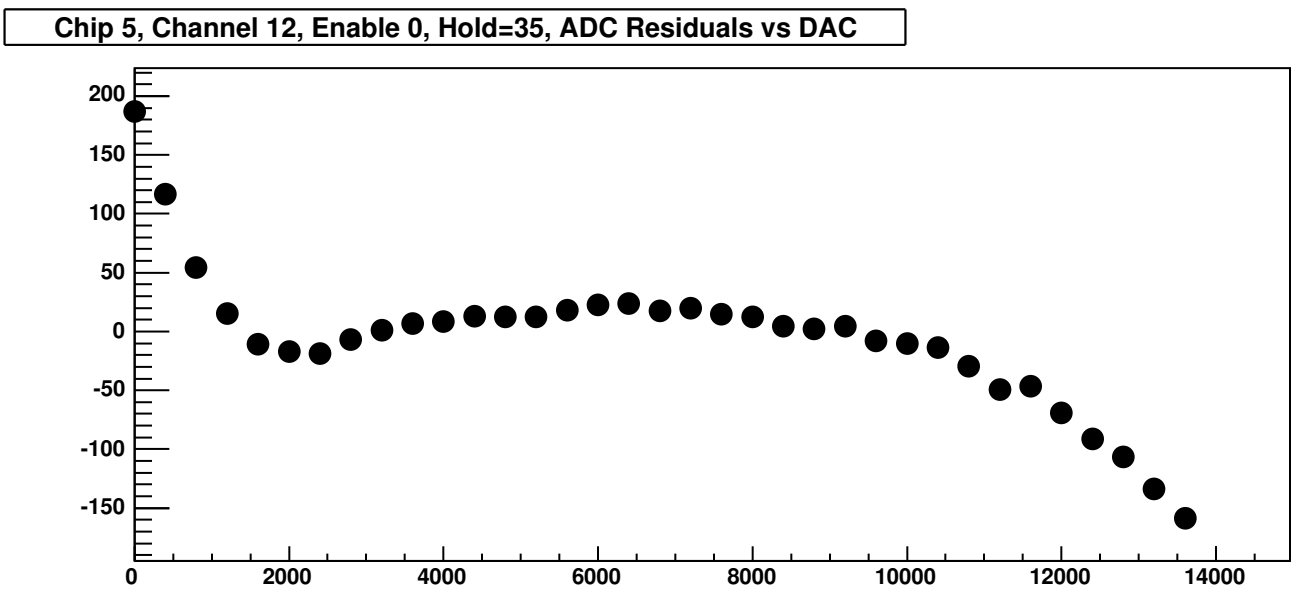

Chip 5, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5.609e+05 / 23
p0 -1295 ± 1.133
p1 0.2798 ± 0.0002313
3500
3000
2500
2000
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

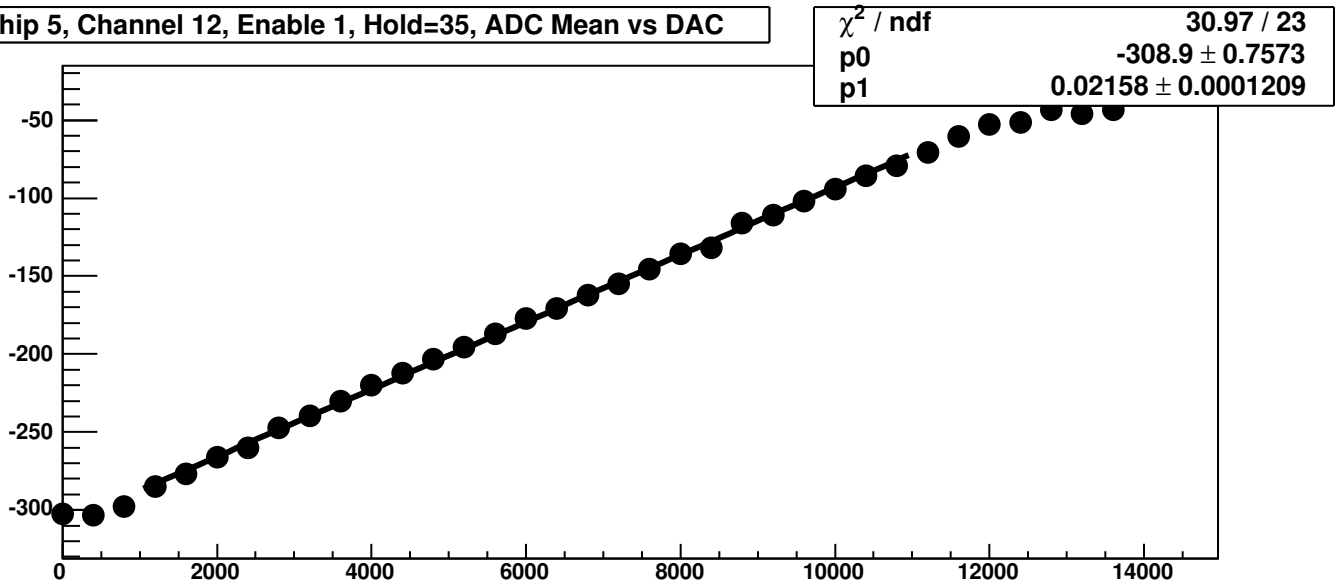

Chip 5, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 732.2 / 23
p0 -466.9 ± 1.066
p1 0.1777 ± 0.0001734
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

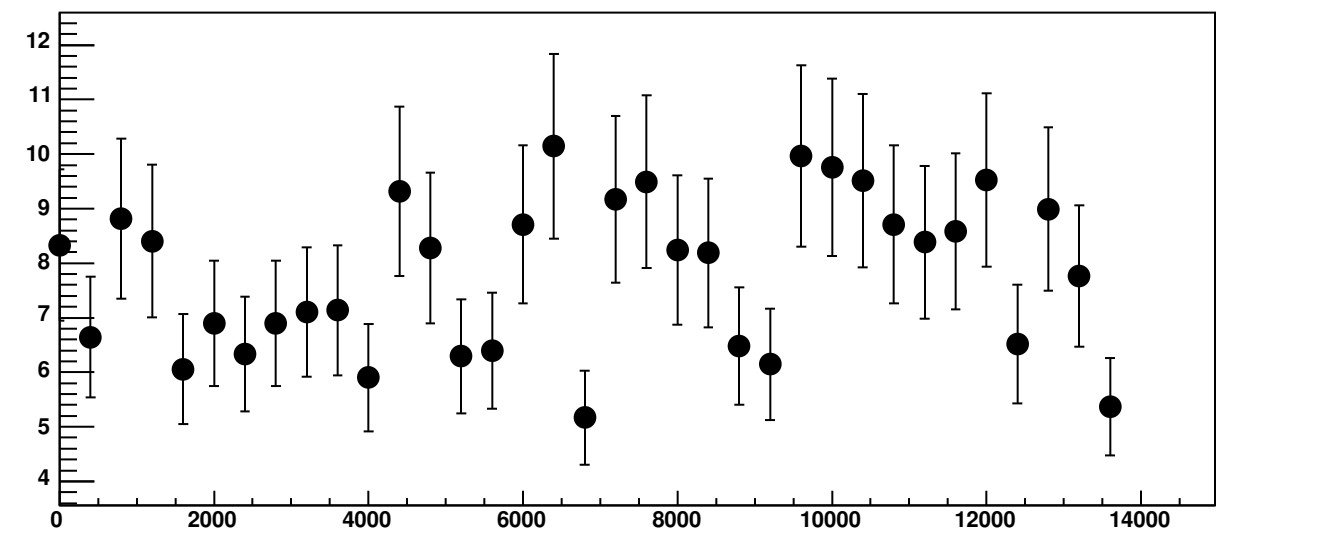

Chip 5, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

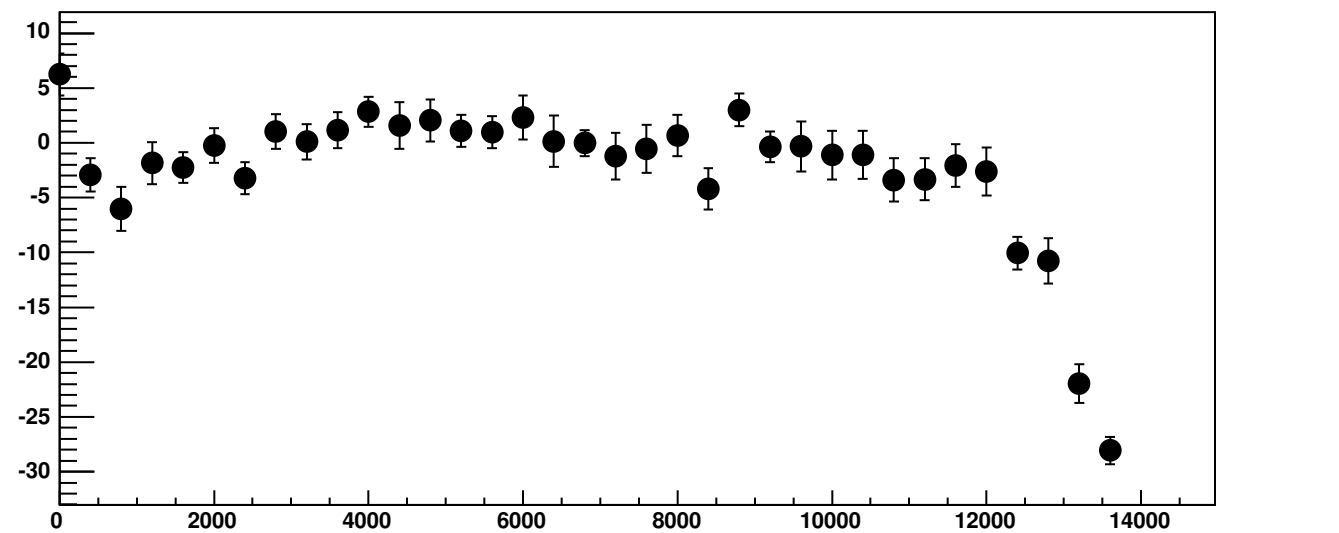
Chip 5, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC

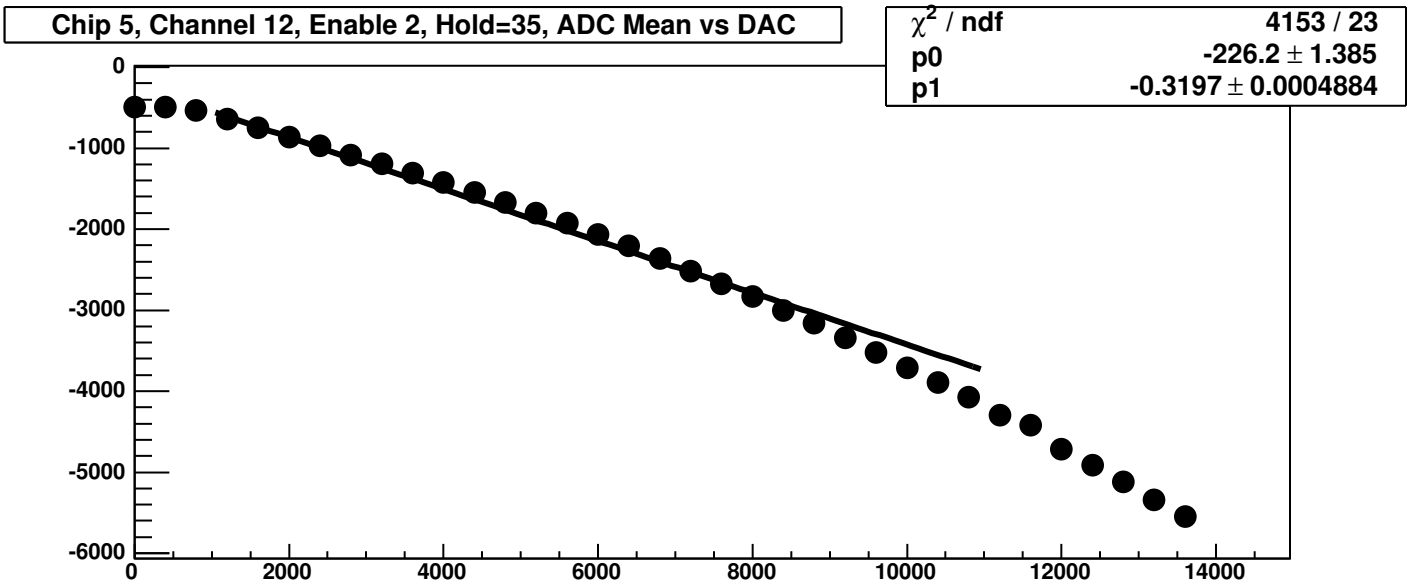


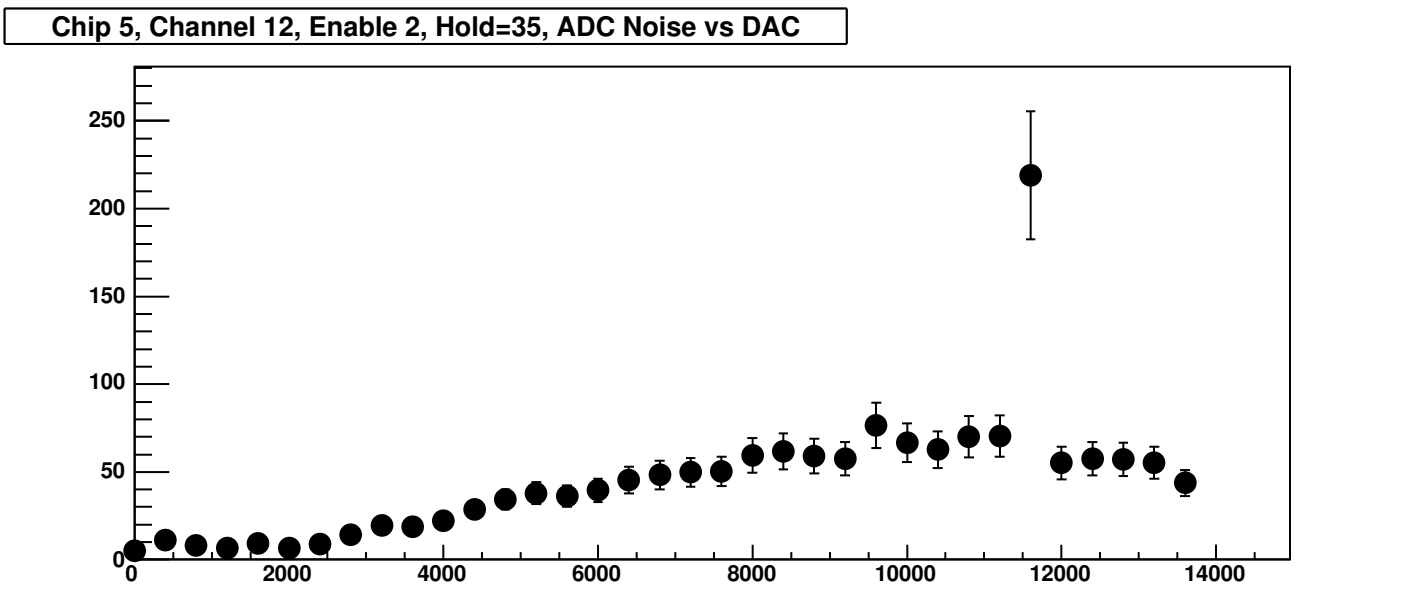
Chip 5, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC

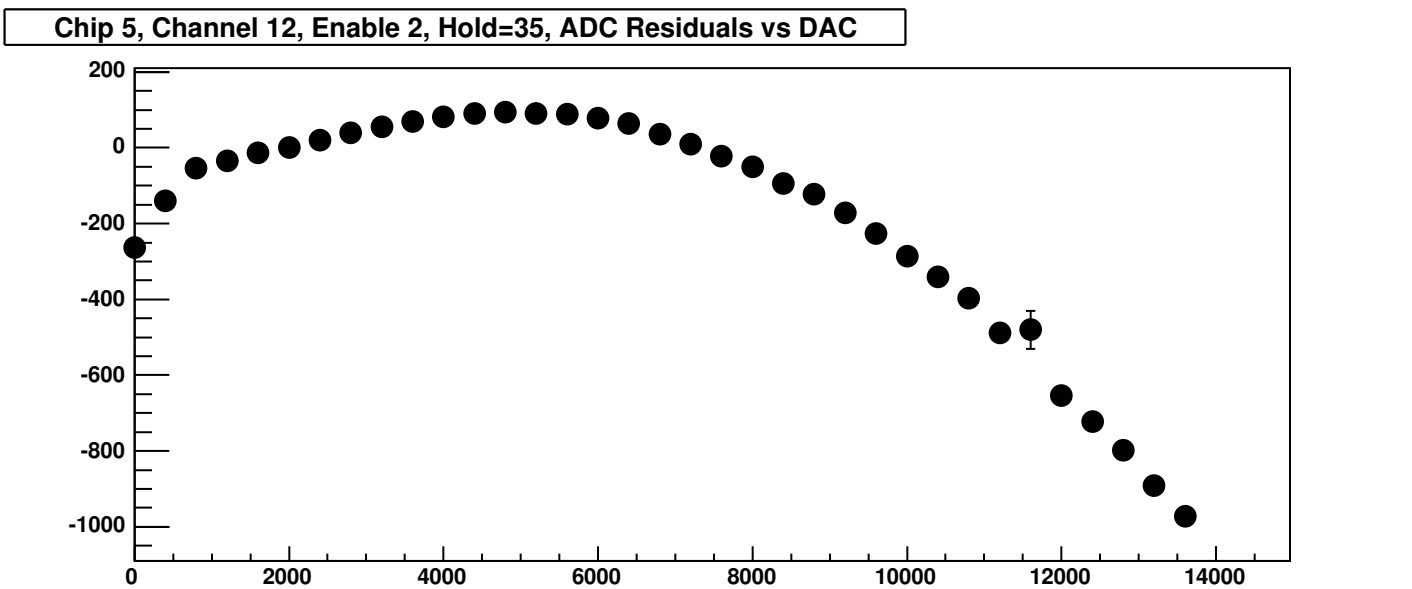


Chip 5, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC

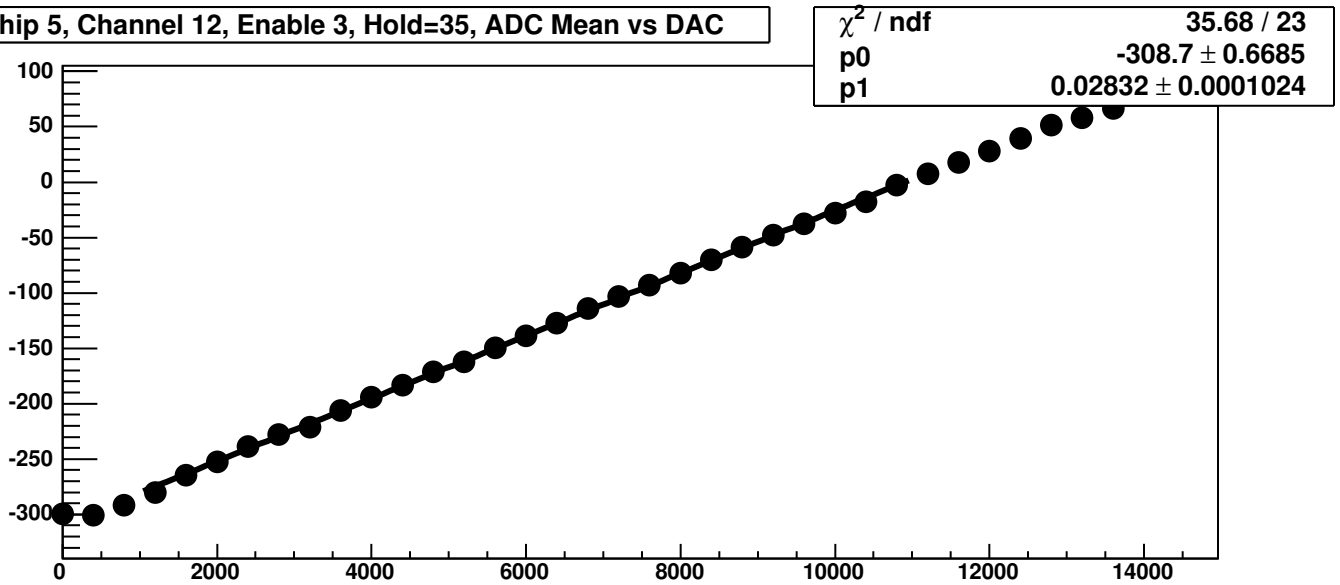



Chip 5, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 4153 / 23
p0 -226.2 ± 1.385
p1 -0.3197 ± 0.0004884
0
-1000
-2000
-3000
-4000
-5000
-6000
0
2000
4000
6000
8000
10000
12000
14000

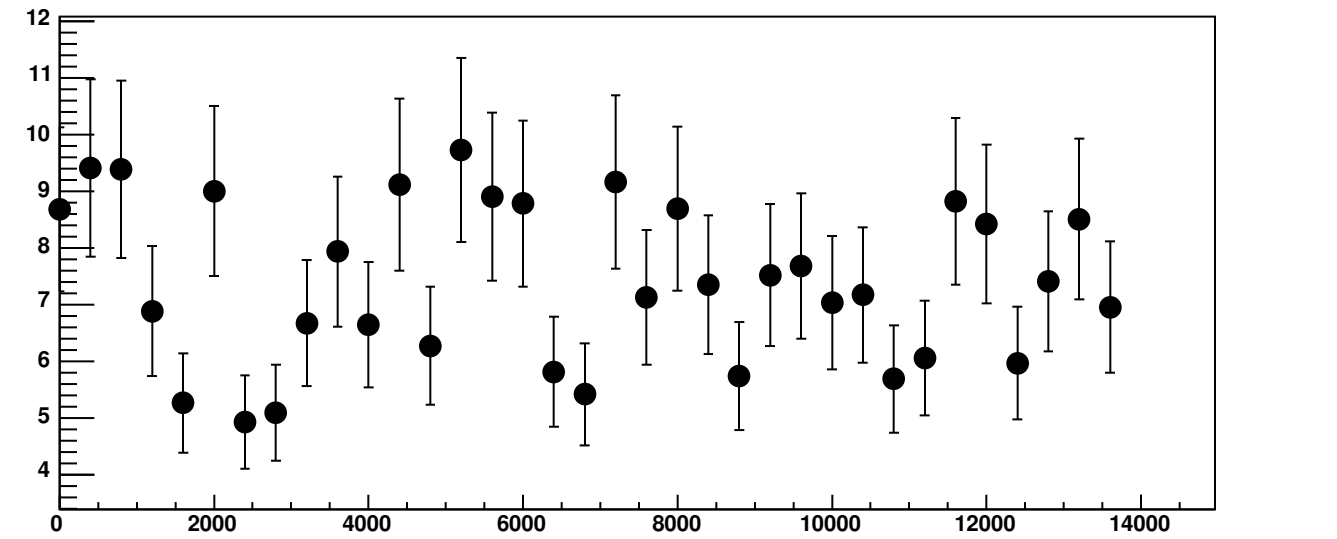

Chip 5, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

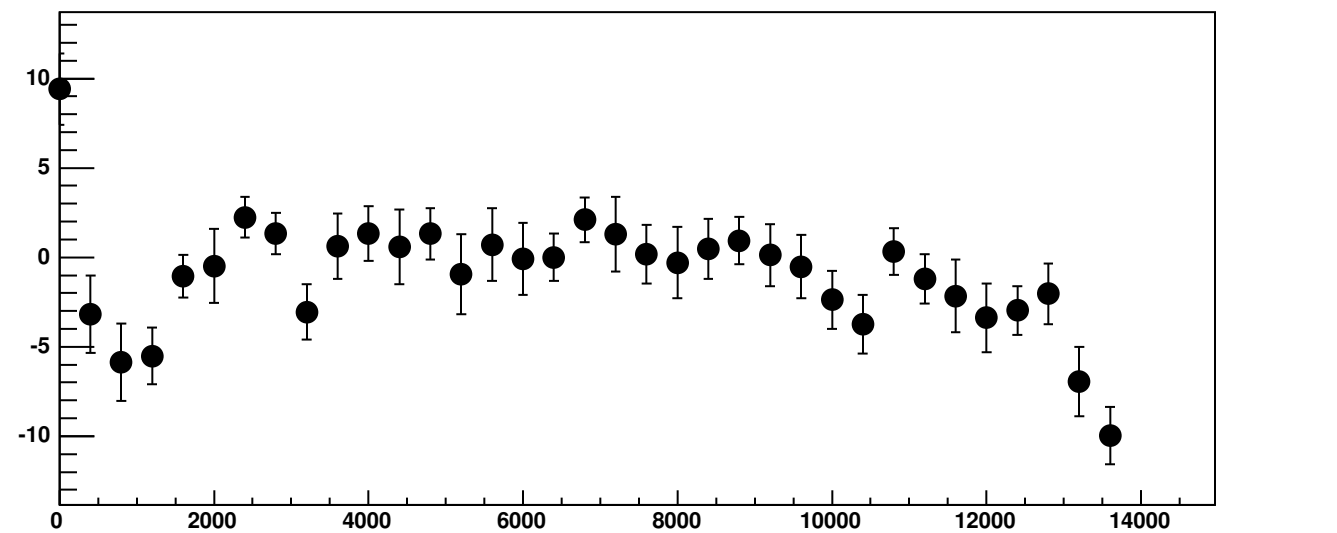
Chip 5, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

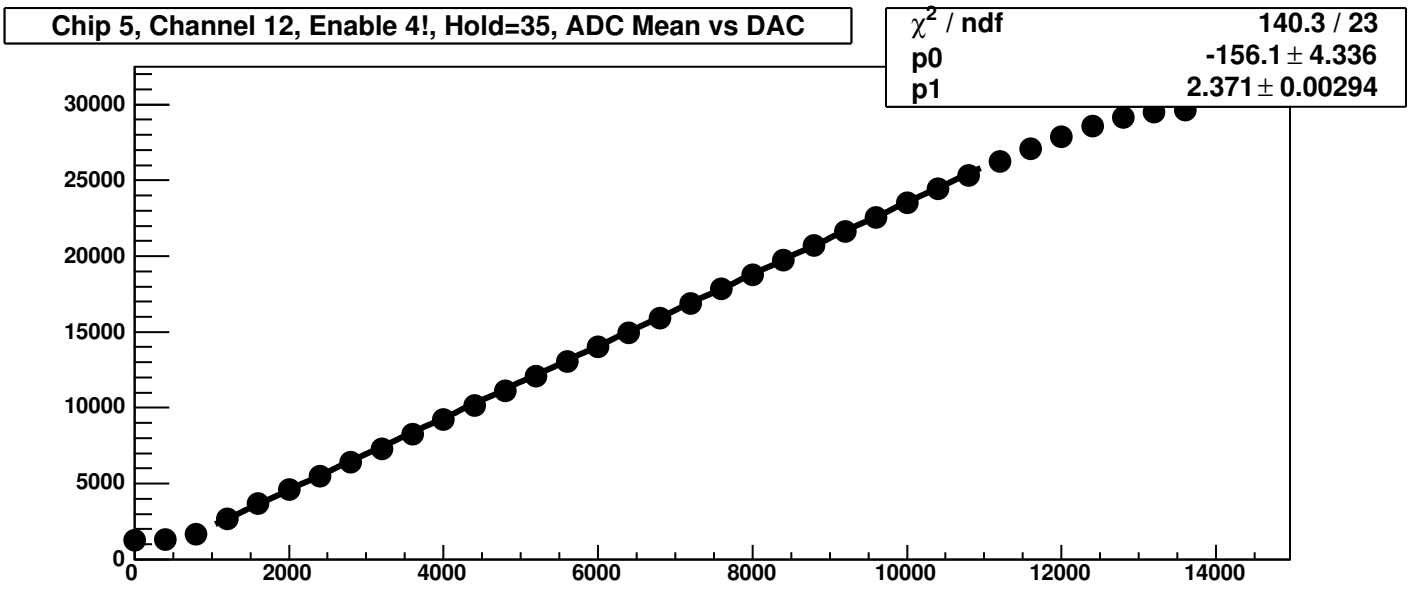


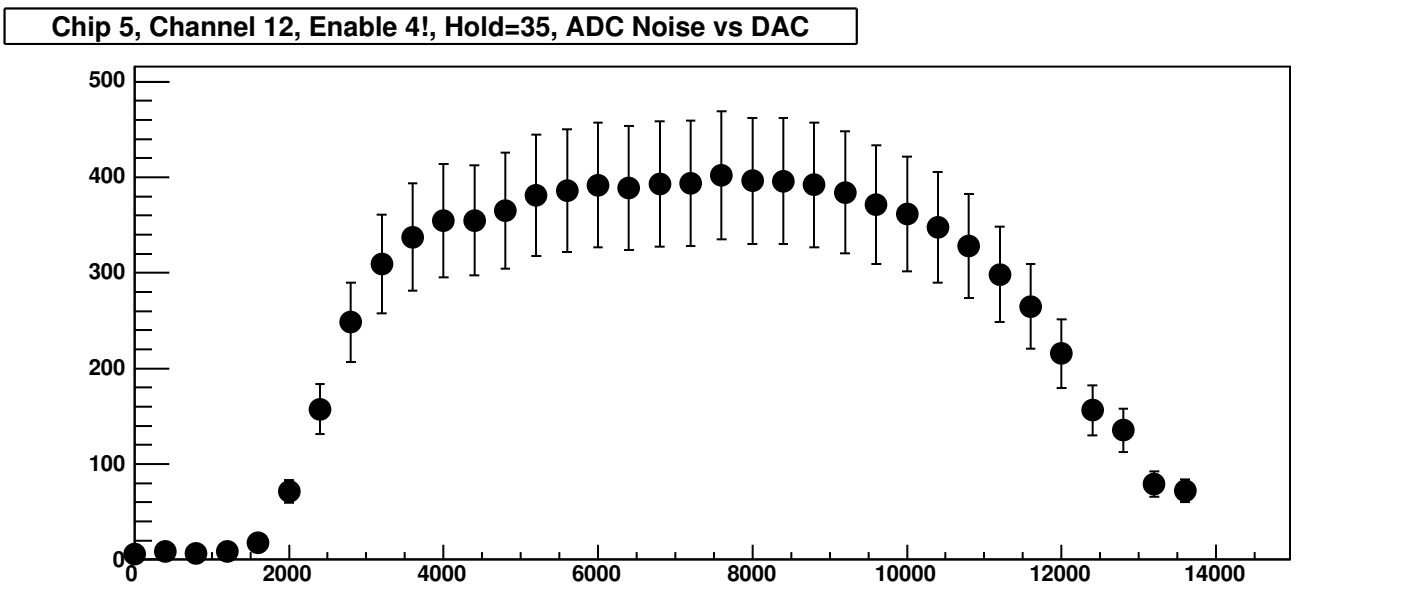
Chip 5, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC

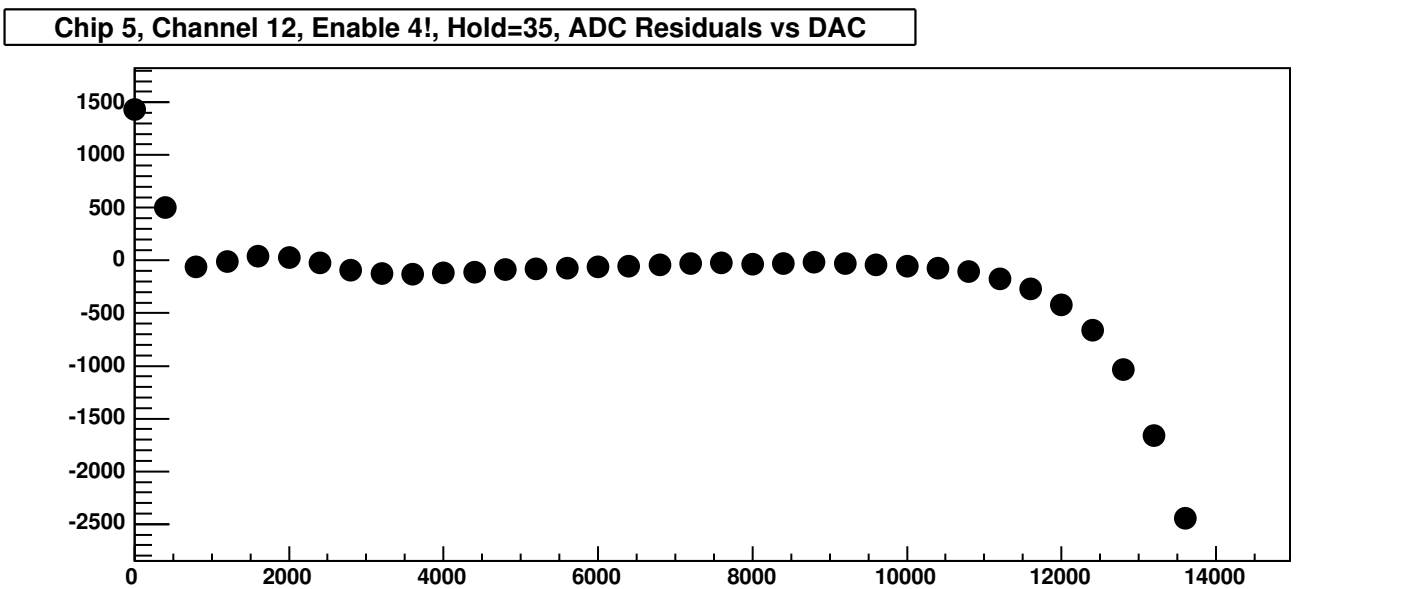


Chip 5, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

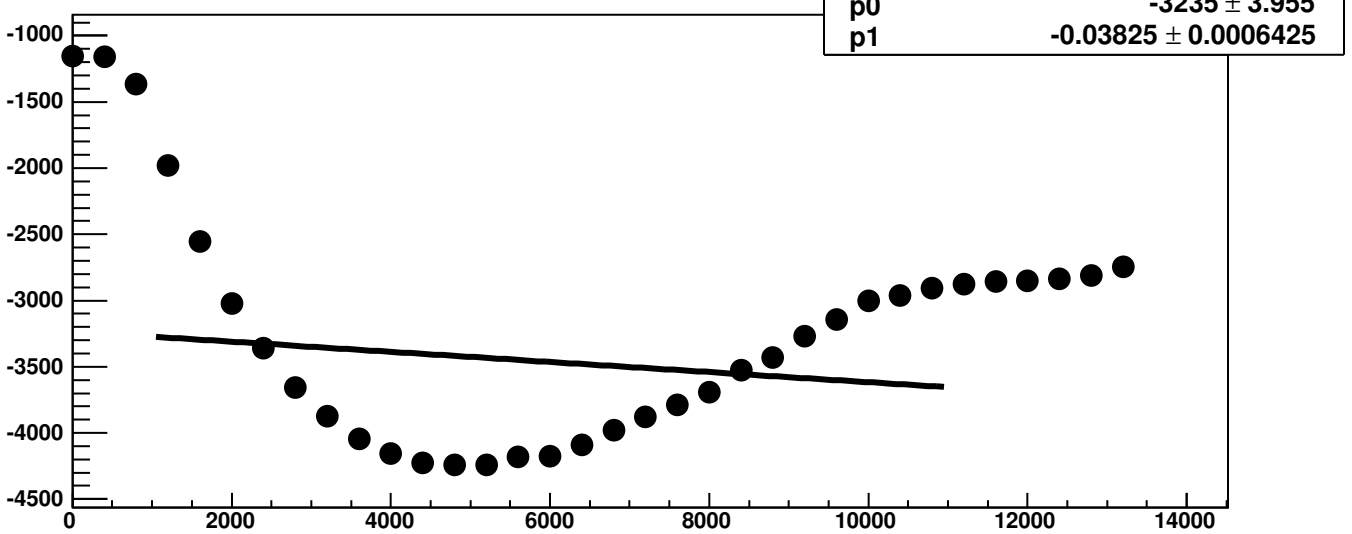



Chip 5, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 140.3 / 23
p0 -156.1 ± 4.336
p1 2.371 ± 0.00294
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

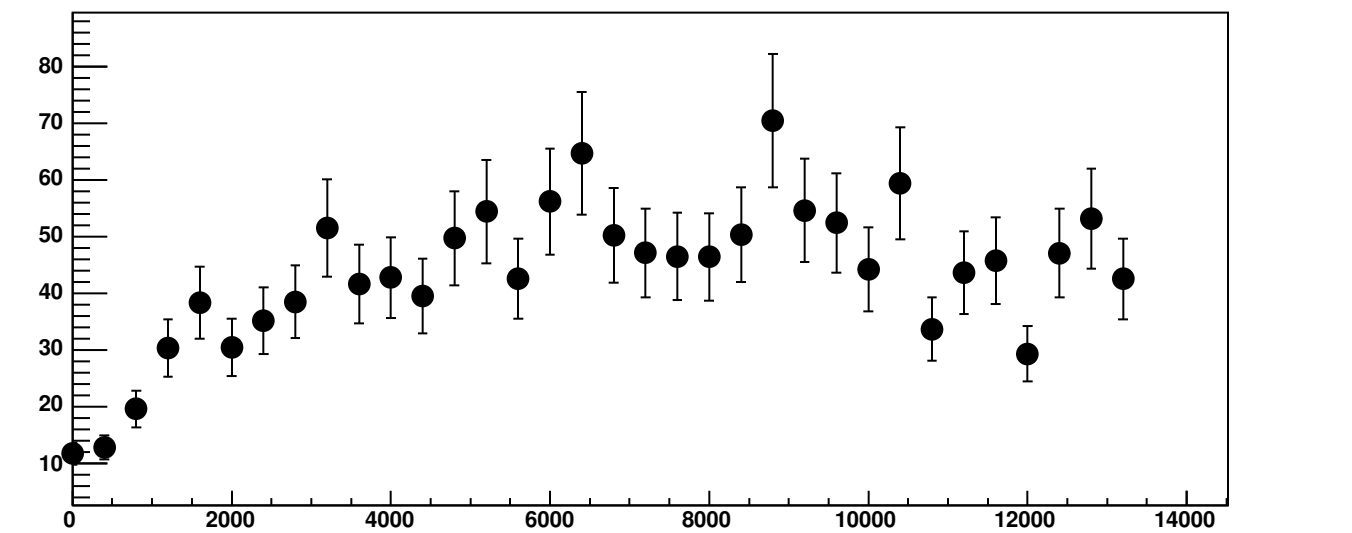

Chip 5, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

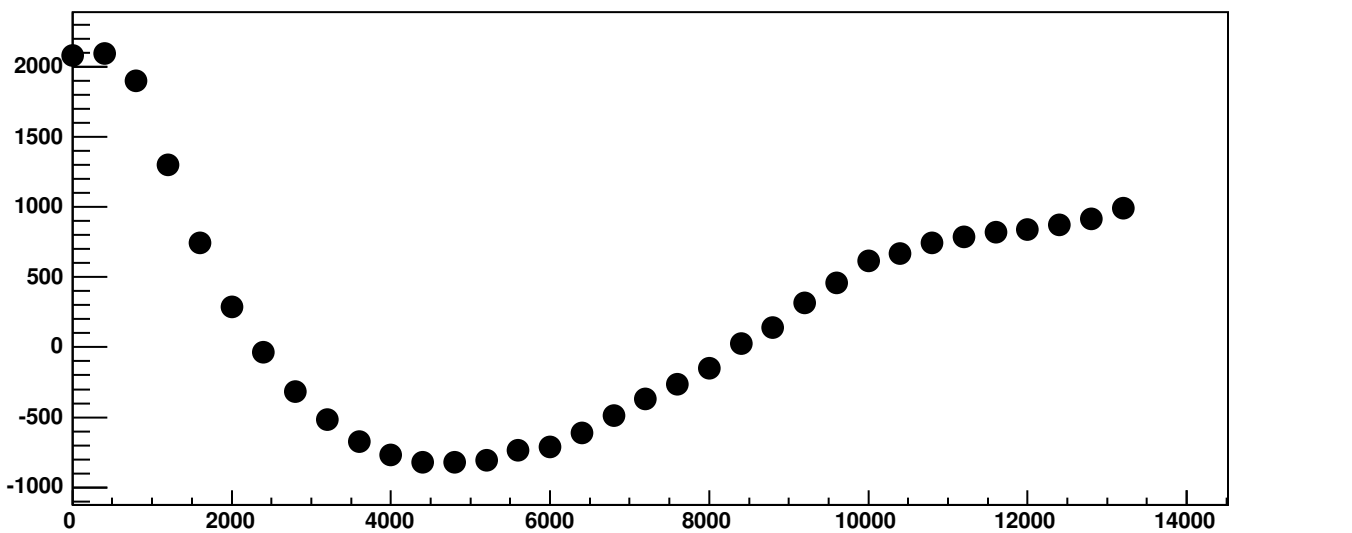
Chip 5, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

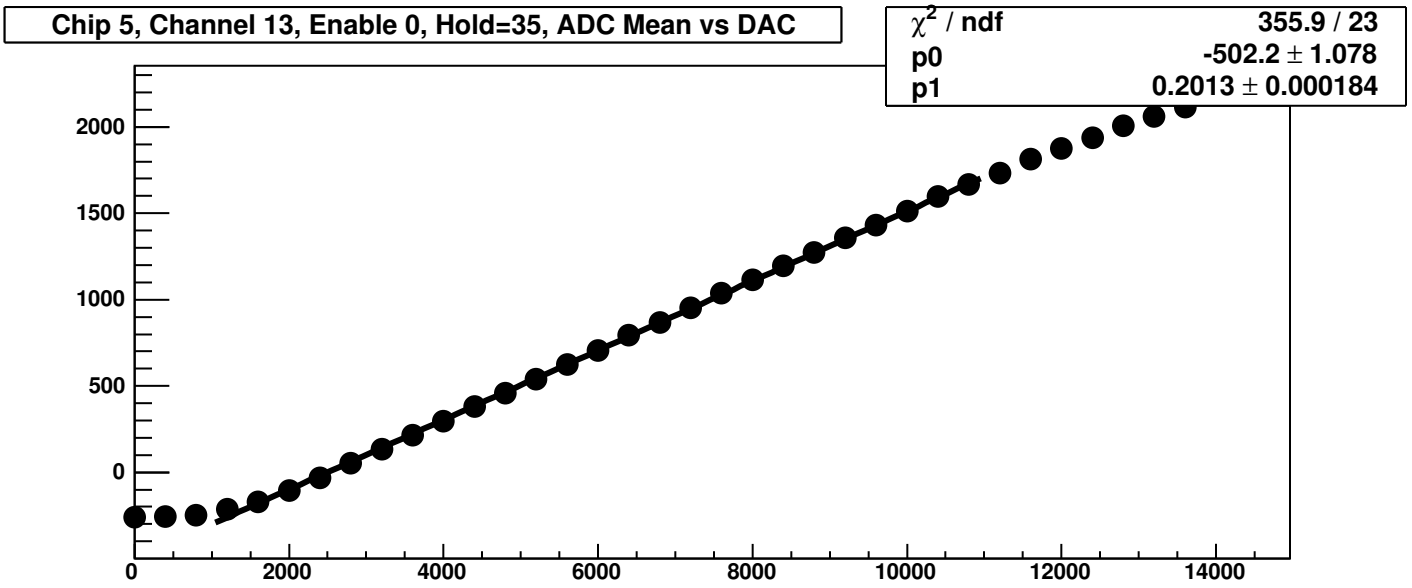


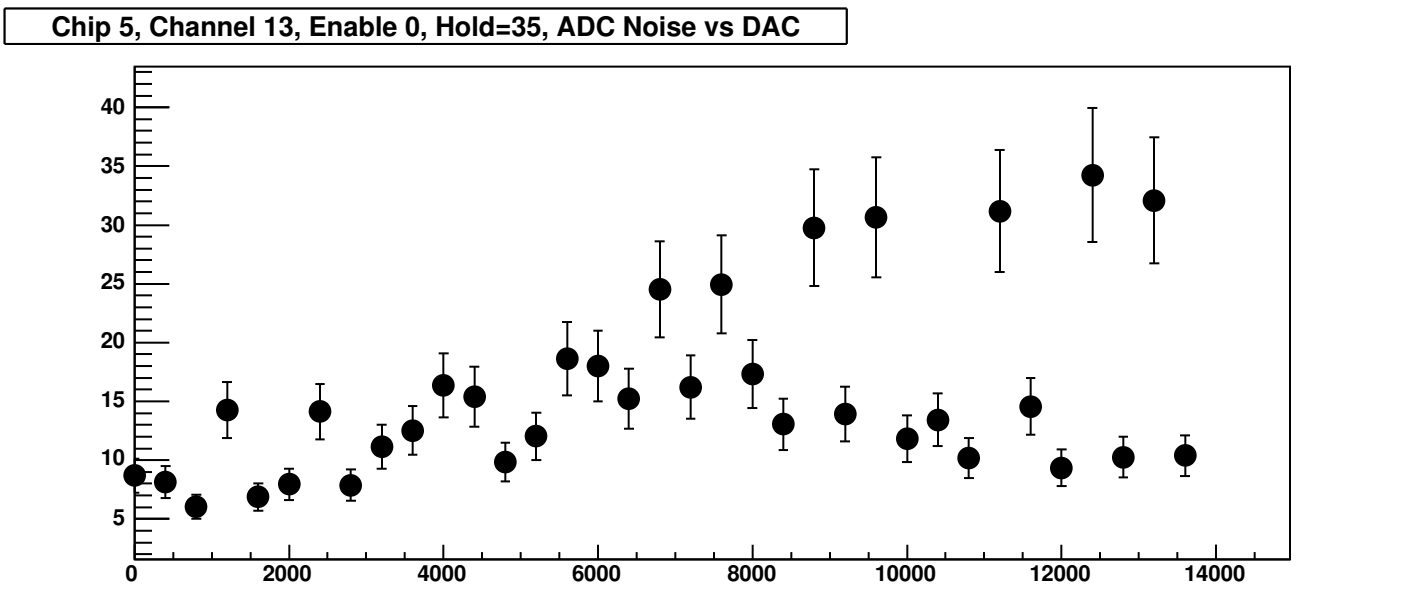
Chip 5, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

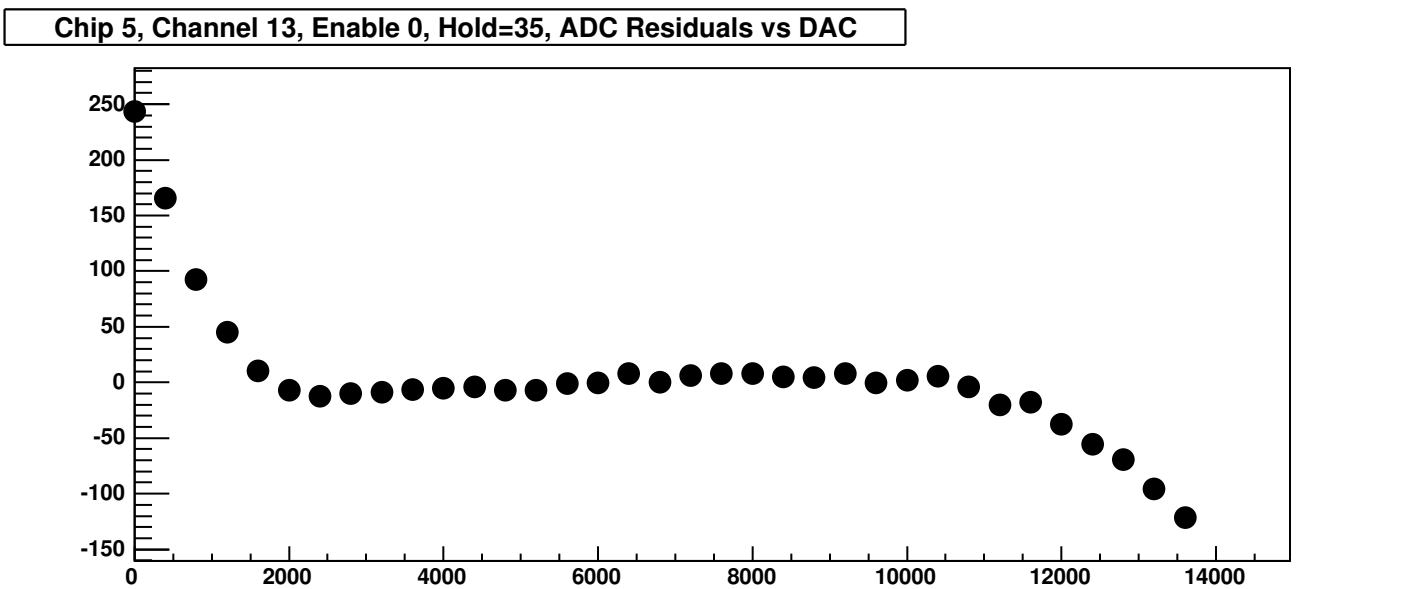


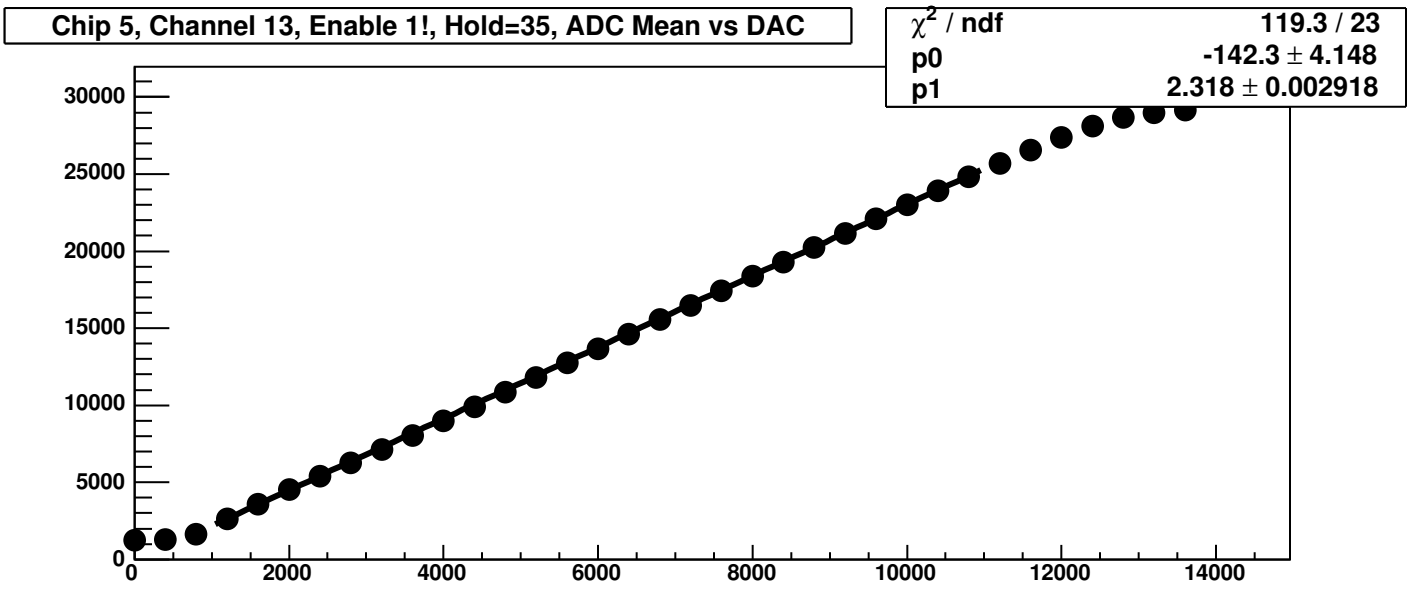
Chip 5, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

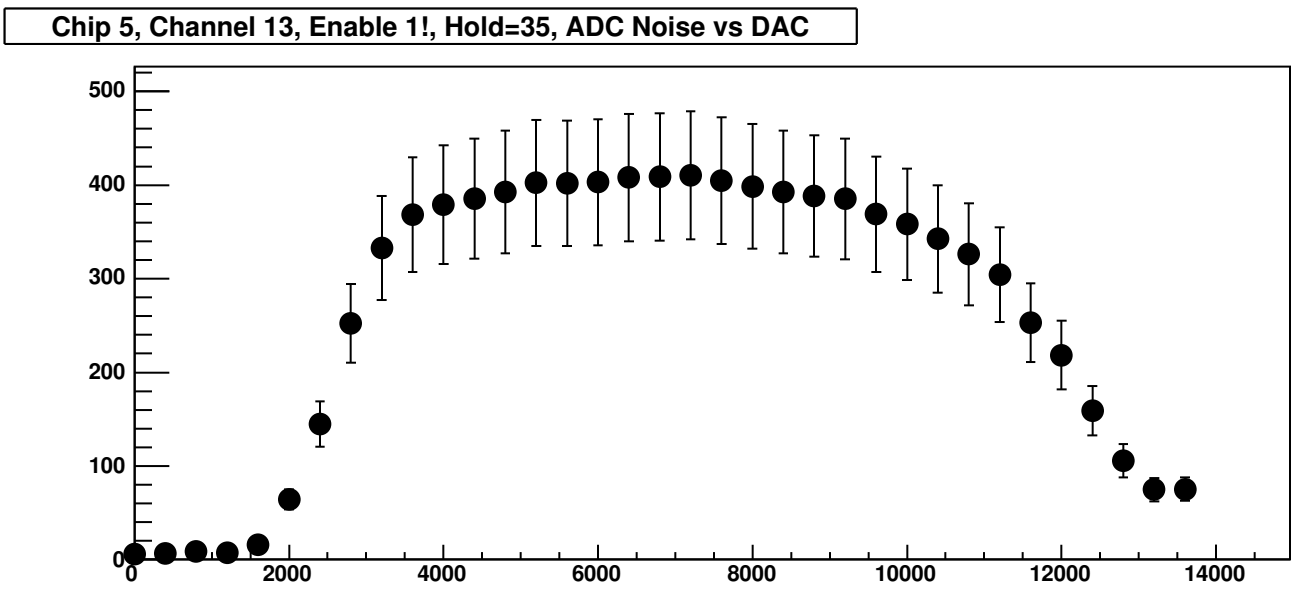


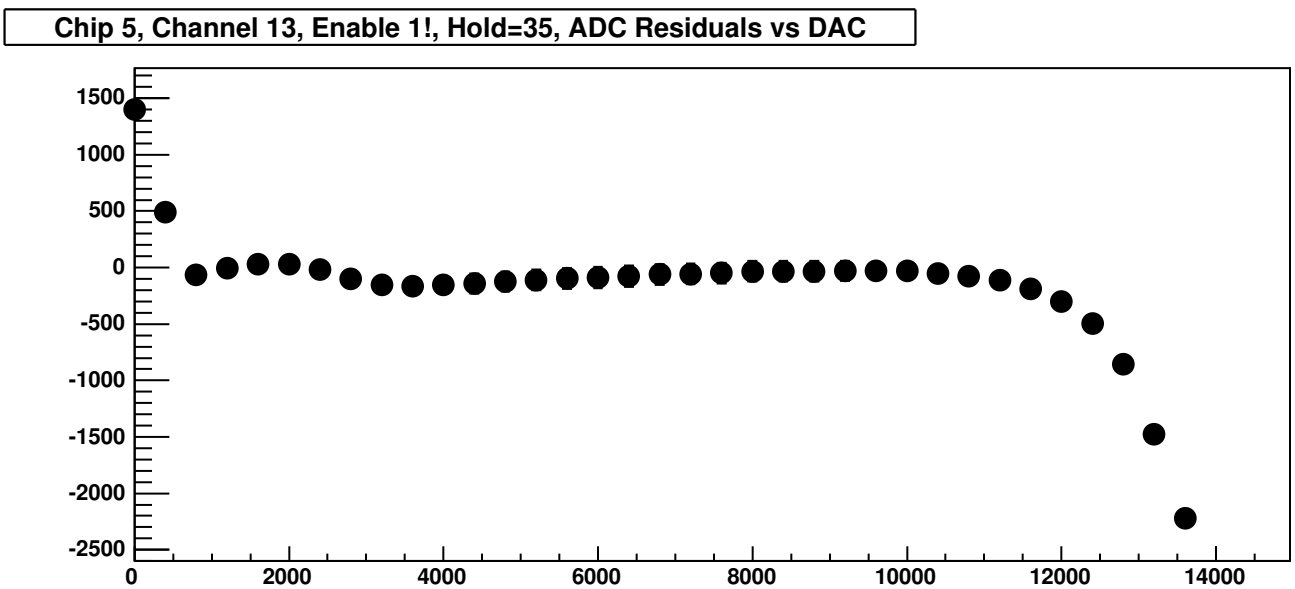

Chip 5, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 355.9 / 23
p0 -502.2 ± 1.078
p1 0.2013 ± 0.000184
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

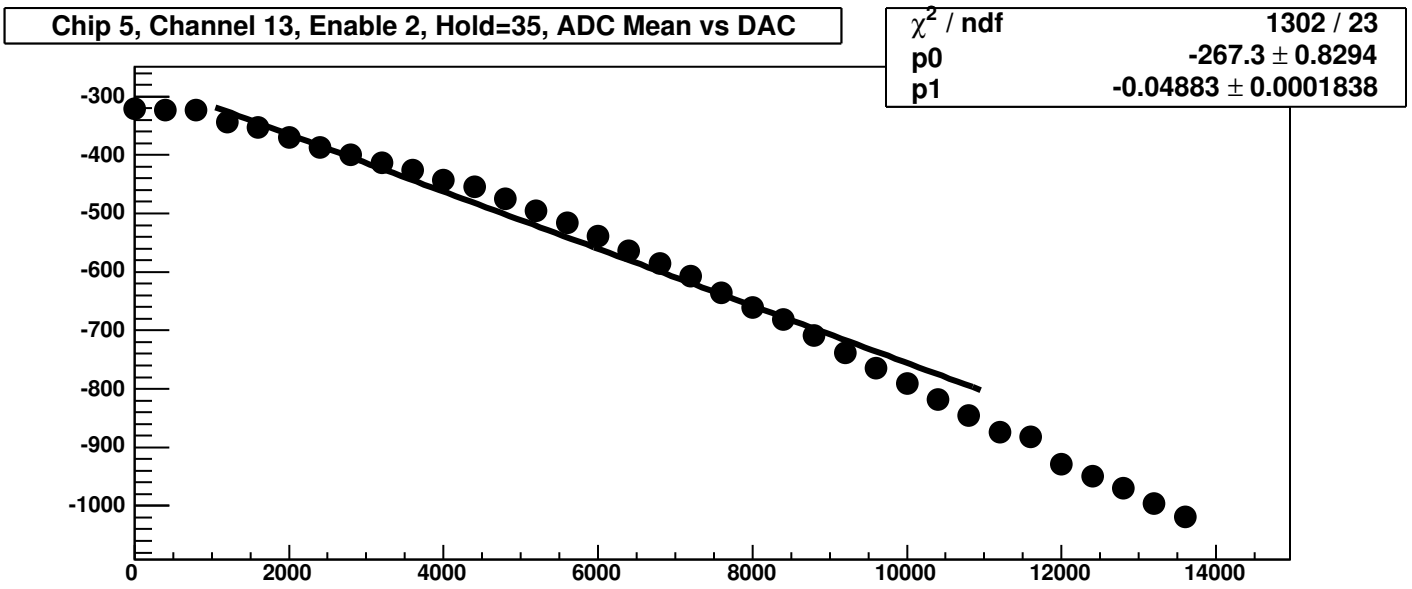

Chip 5, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

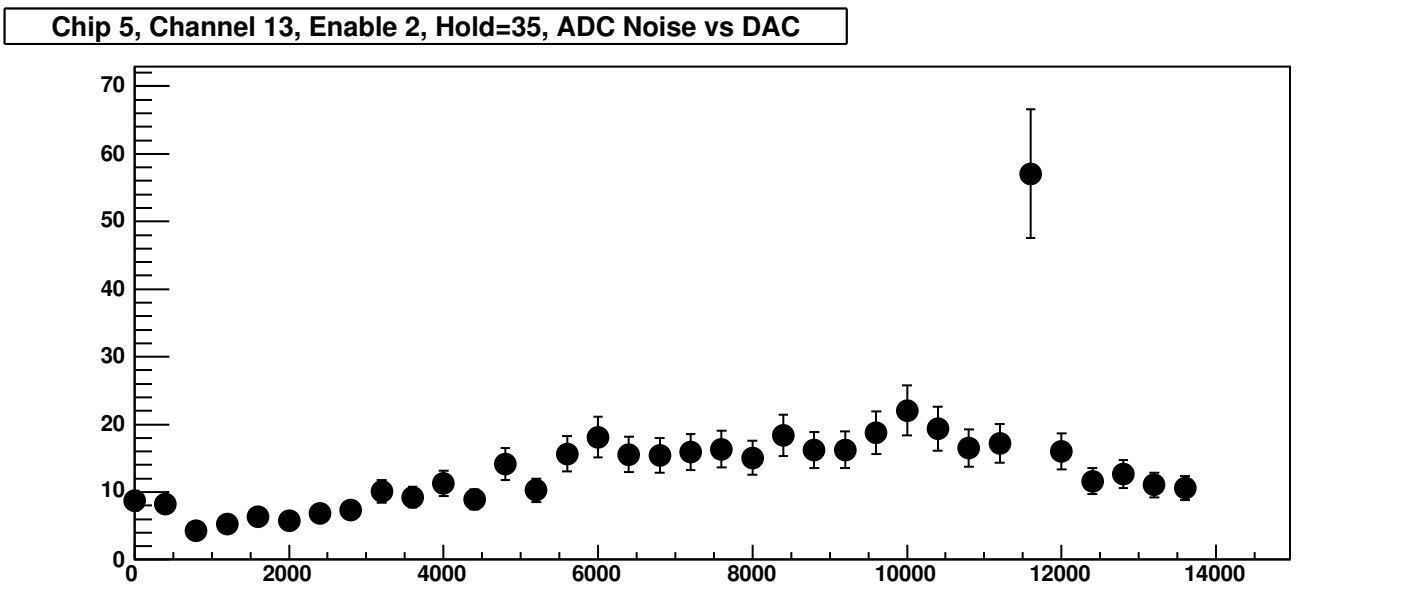

Chip 5, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

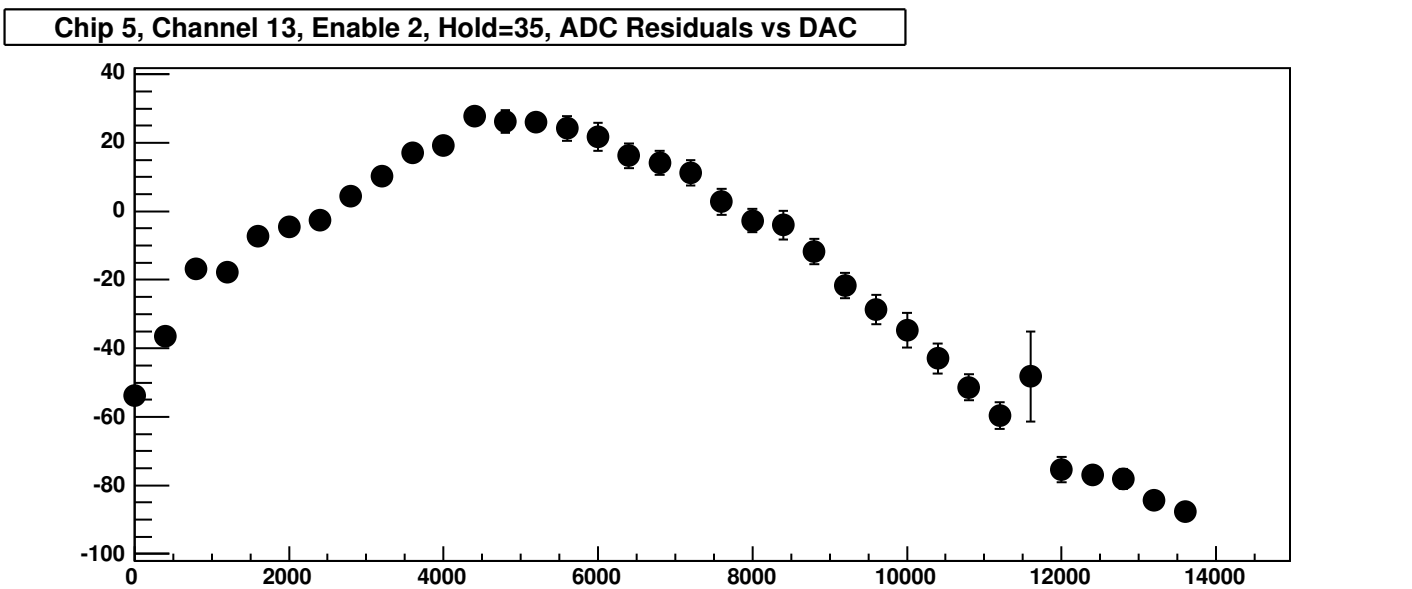

Chip 5, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 119.3 / 23
p0 -142.3 ± 4.148
p1 2.318 ± 0.002918
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

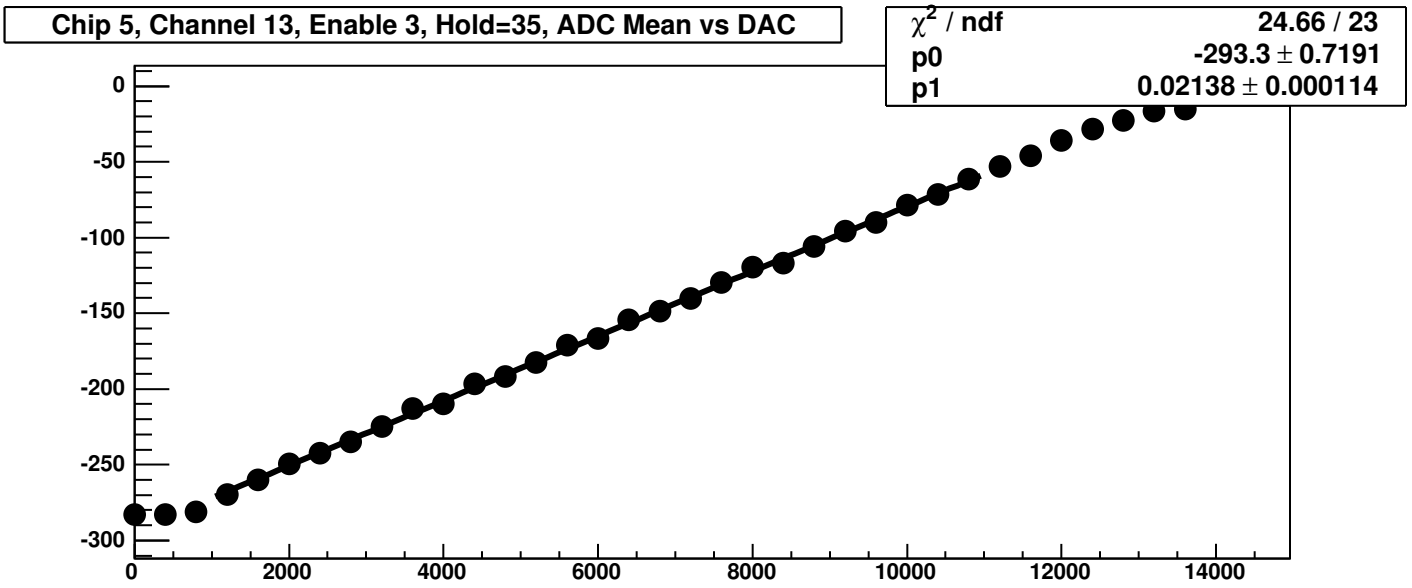

Chip 5, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

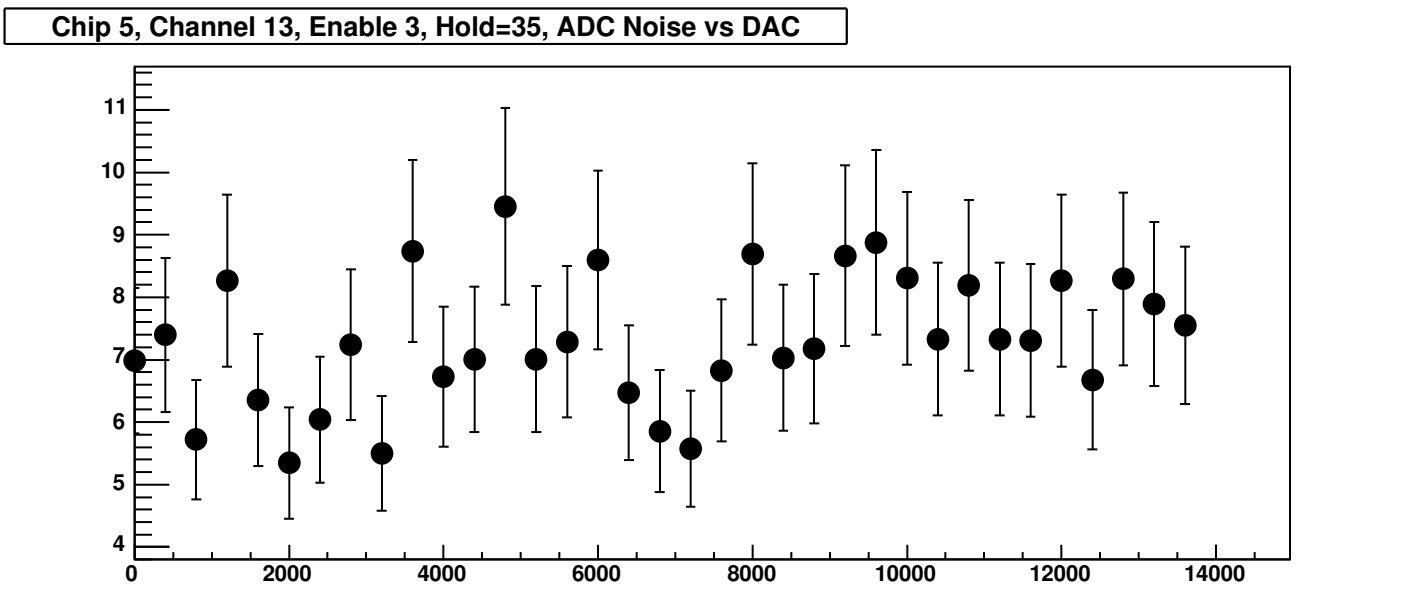

Chip 5, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

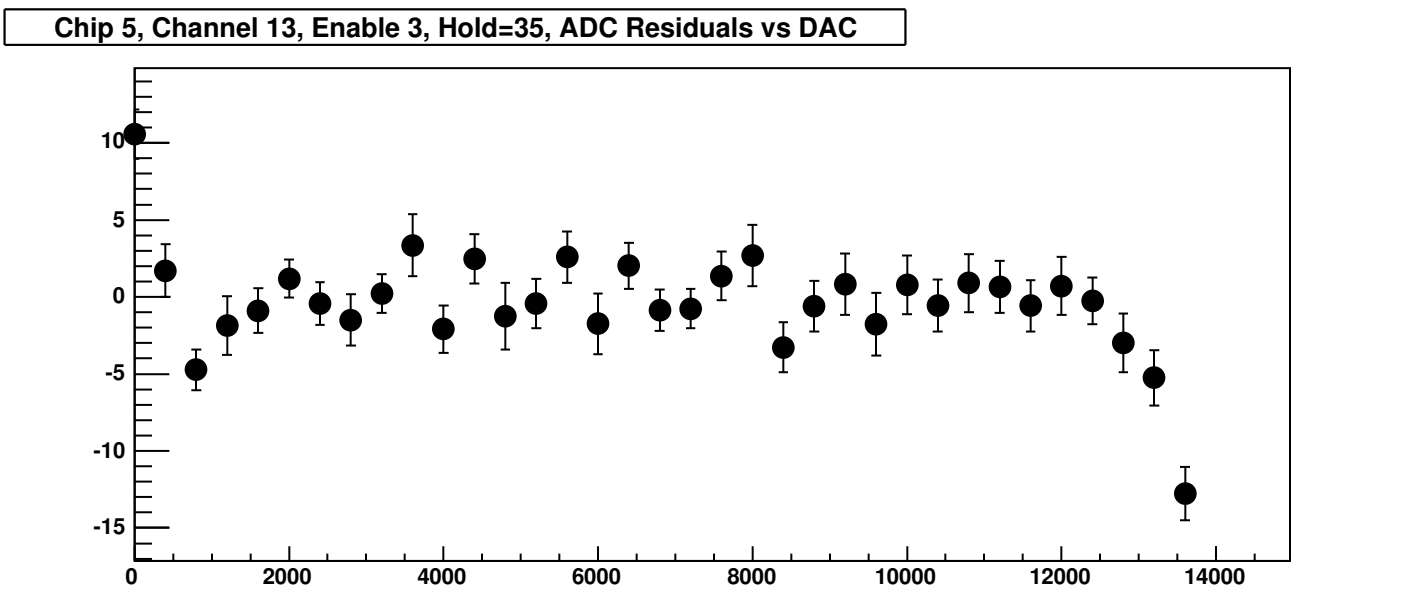

Chip 5, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1302 / 23
p0 -267.3 ± 0.8294
p1 -0.04883 ± 0.0001838
-300
-400
-500
-600
-700
-800
-900
-1000
0
2000
4000
6000
8000
10000
12000
14000

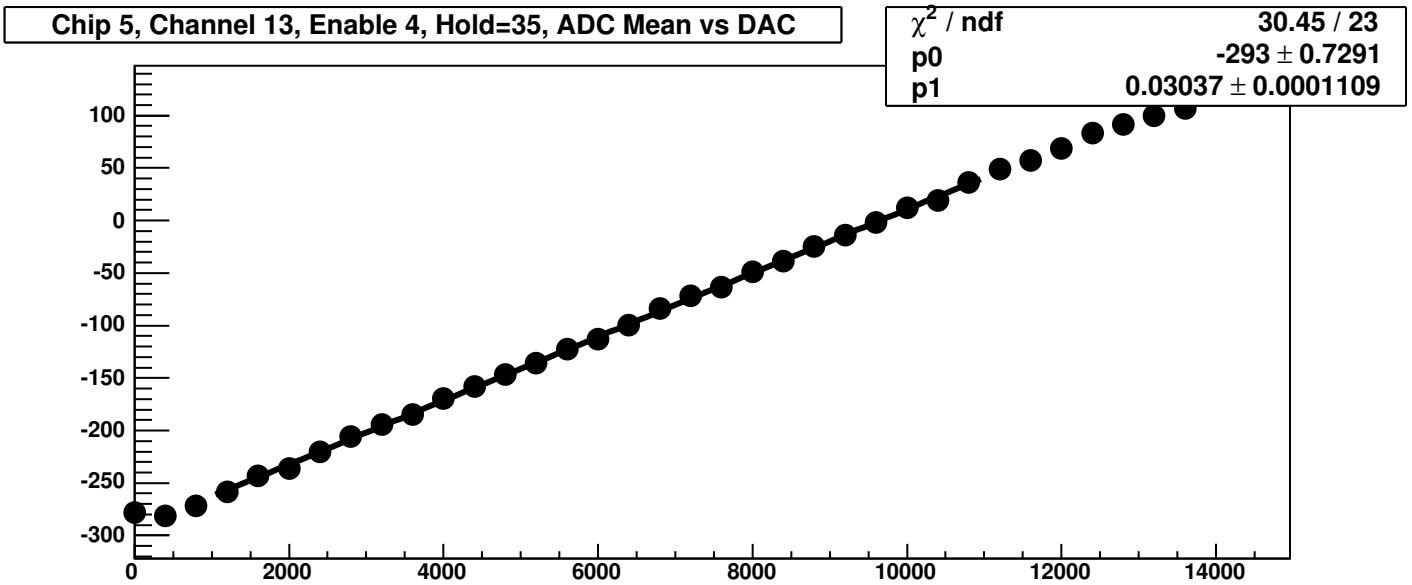

Chip 5, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

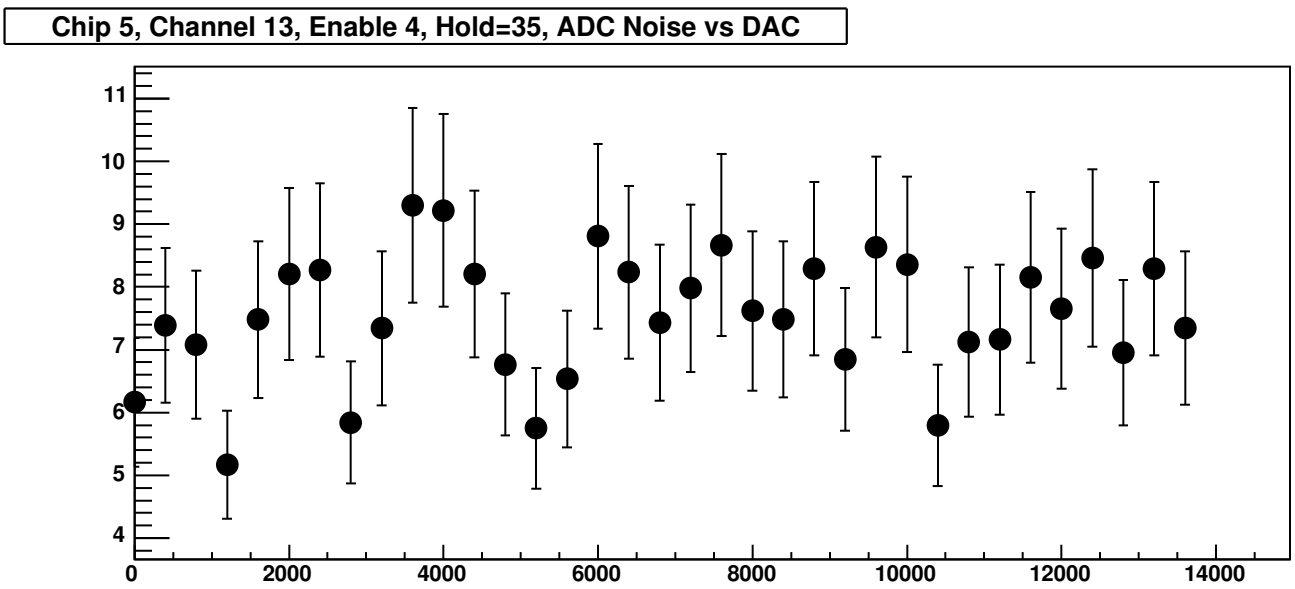

Chip 5, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
40
20
0
-20
-40
-60
-80
-100
0
2000
4000
6000
8000
10000
12000
14000

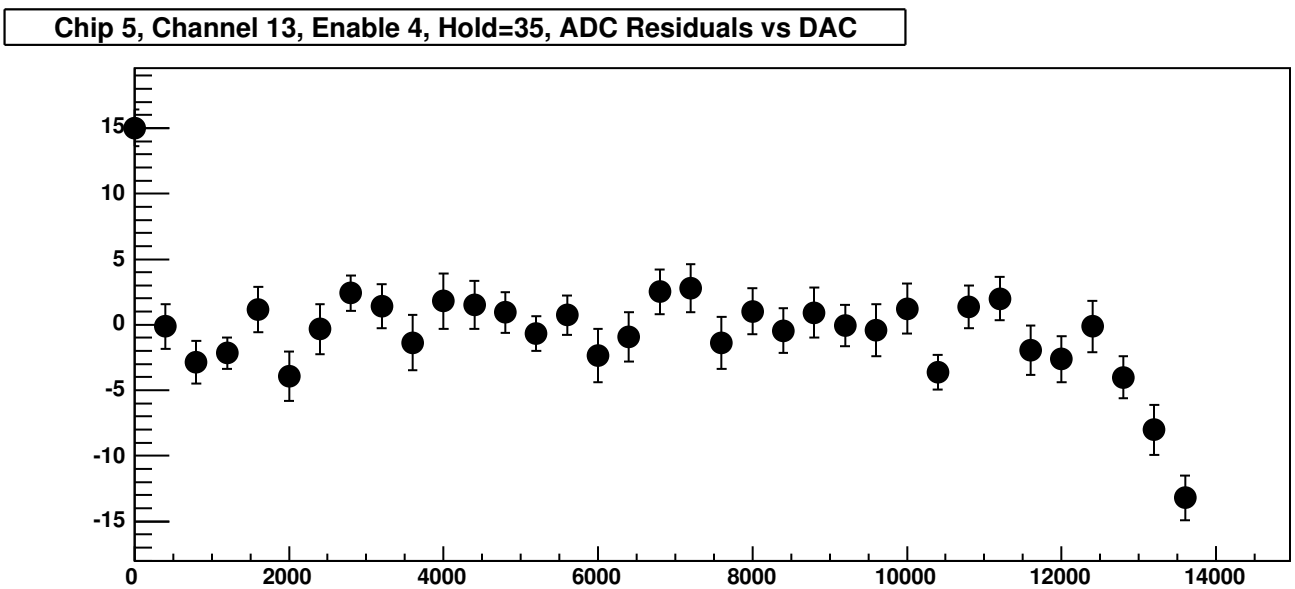

Chip 5, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.66 / 23
p0 -293.3 ± 0.7191
p1 0.02138 ± 0.000114
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

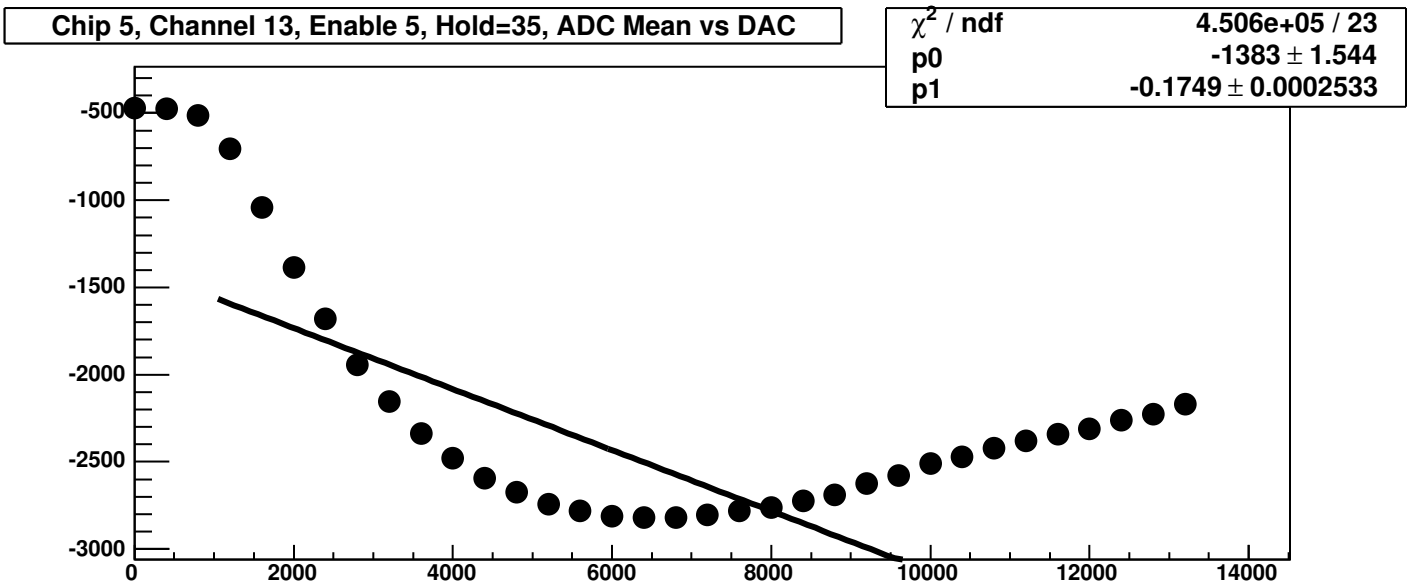

Chip 5, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

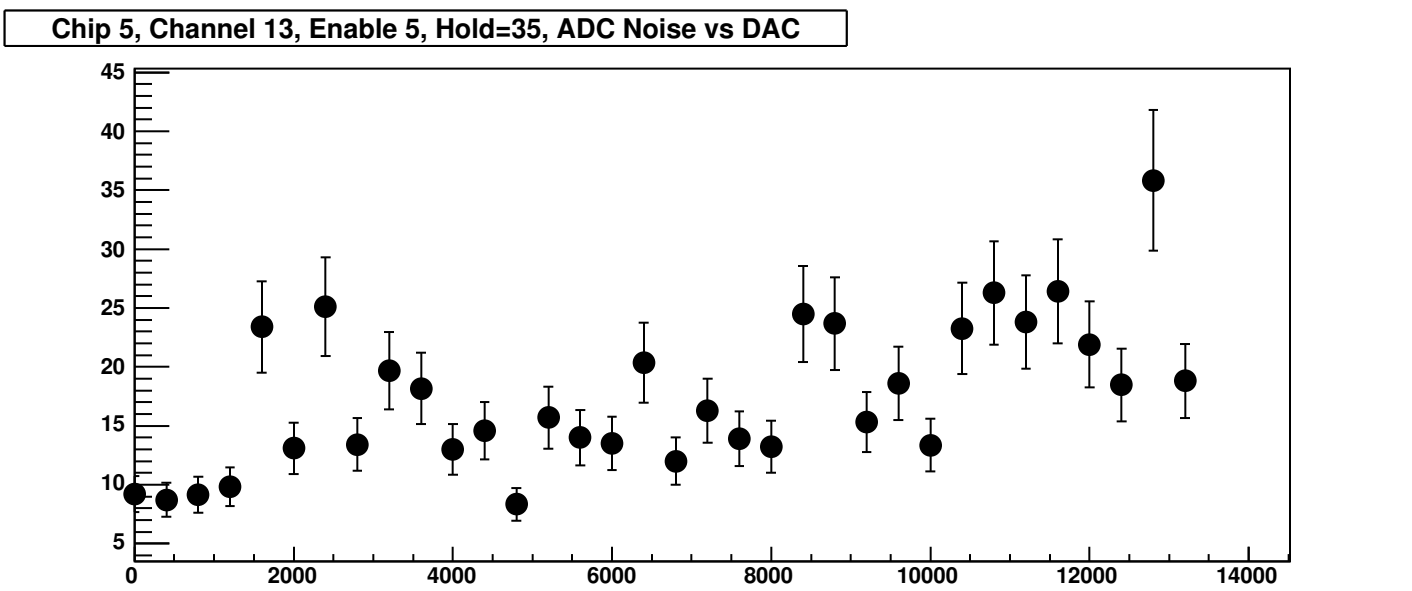

Chip 5, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

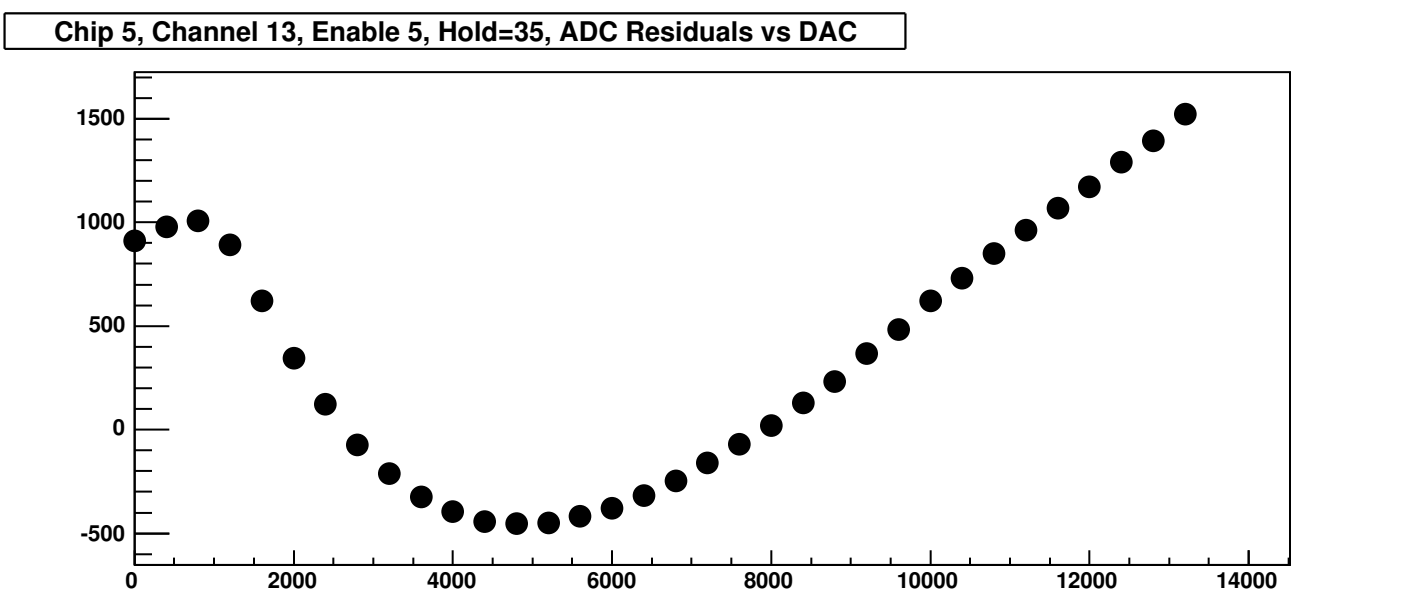

Chip 5, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.45 / 23
p0 -293 ± 0.7291
p1 0.03037 ± 0.0001109
100
50
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

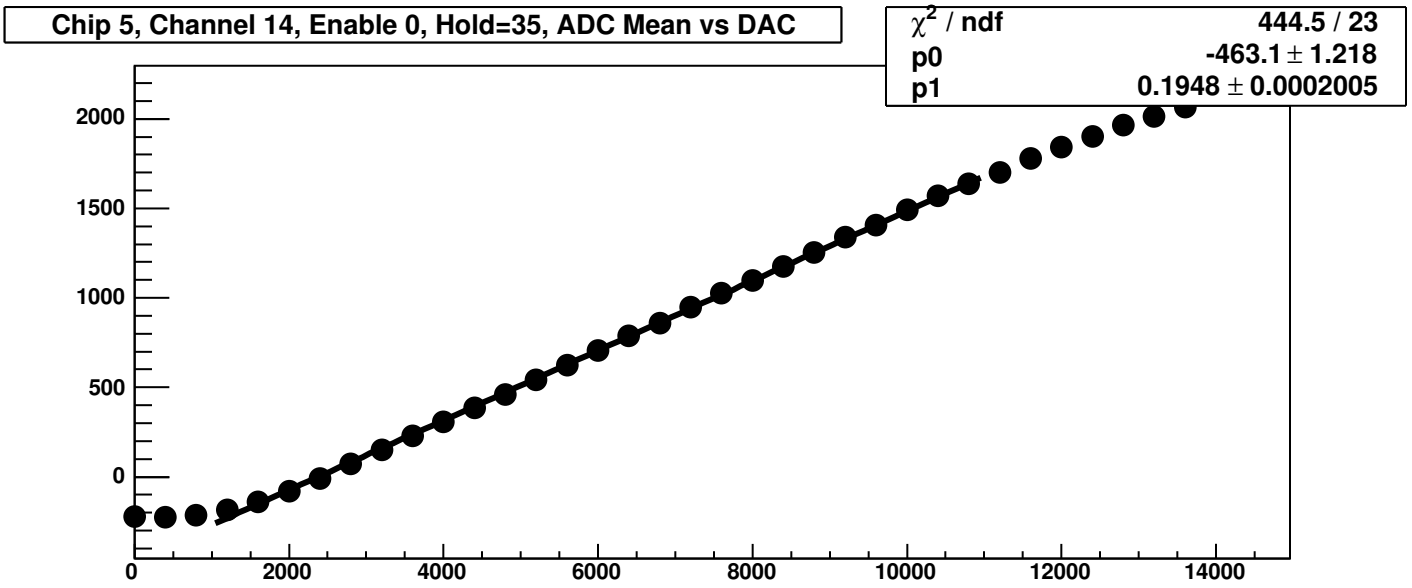

Chip 5, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

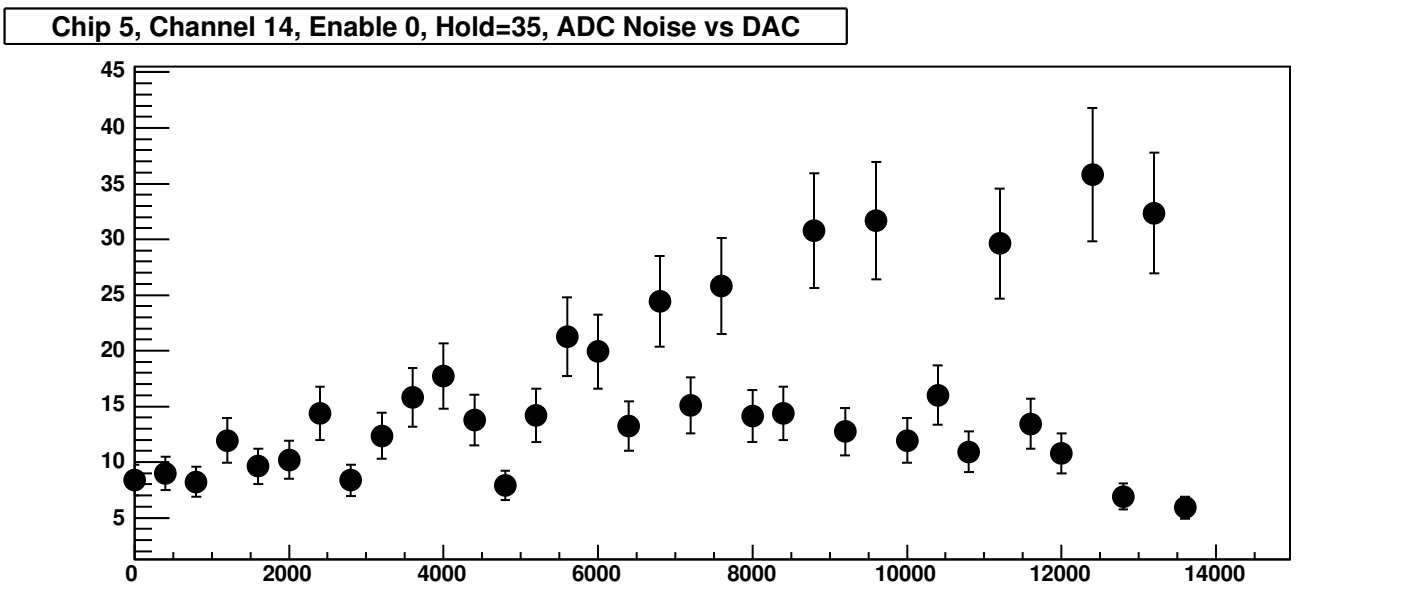

Chip 5, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

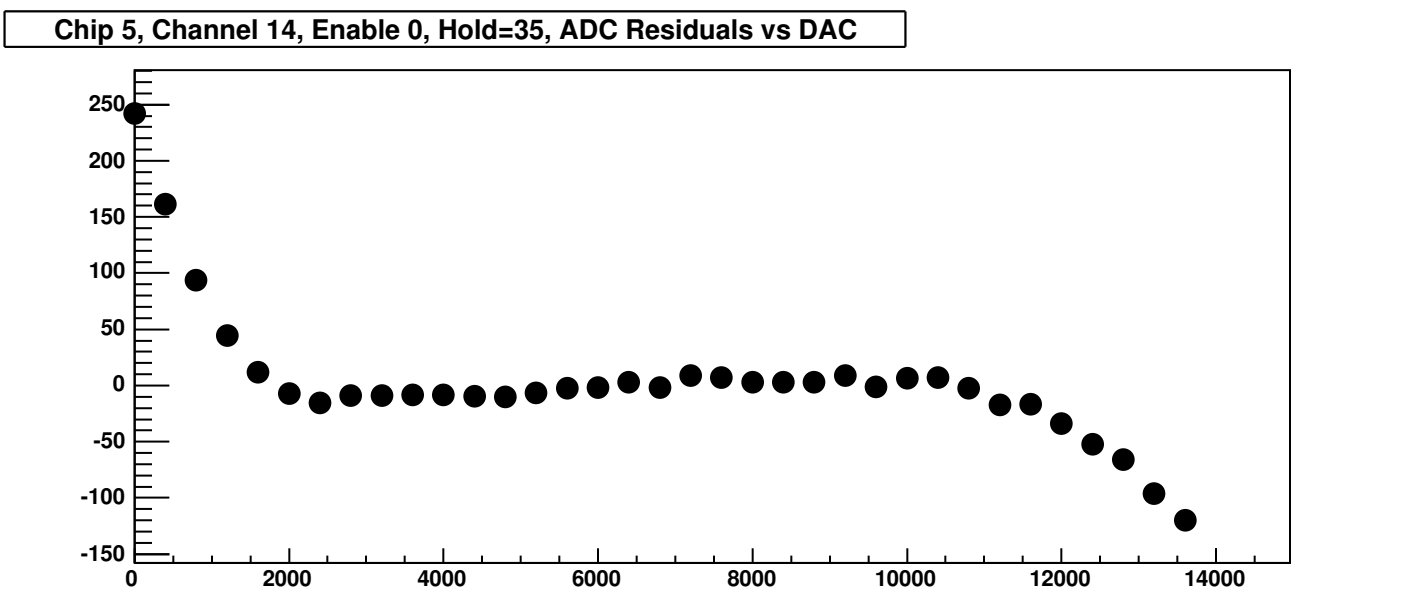

Chip 5, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4.506e+05 / 23
p0 -1383 ± 1.544
p1 -0.1749 ± 0.0002533
-500
-1000
-1500
-2000
-2500
-3000
0
2000
4000
6000
8000
10000
12000
14000

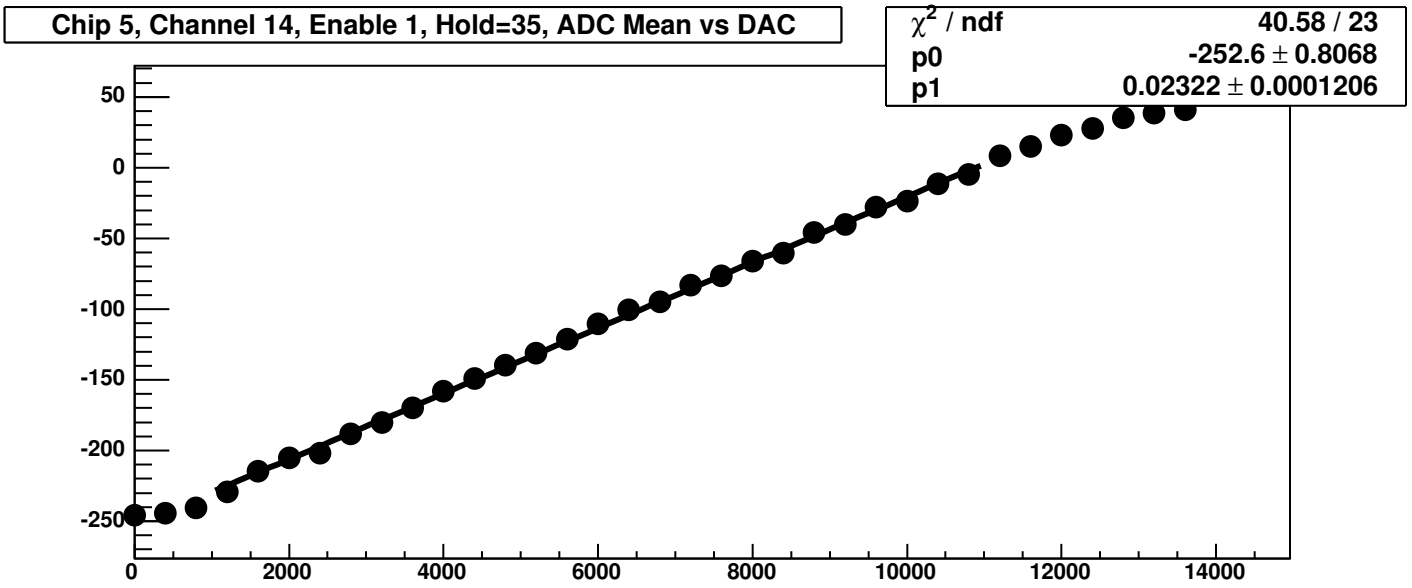

Chip 5, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

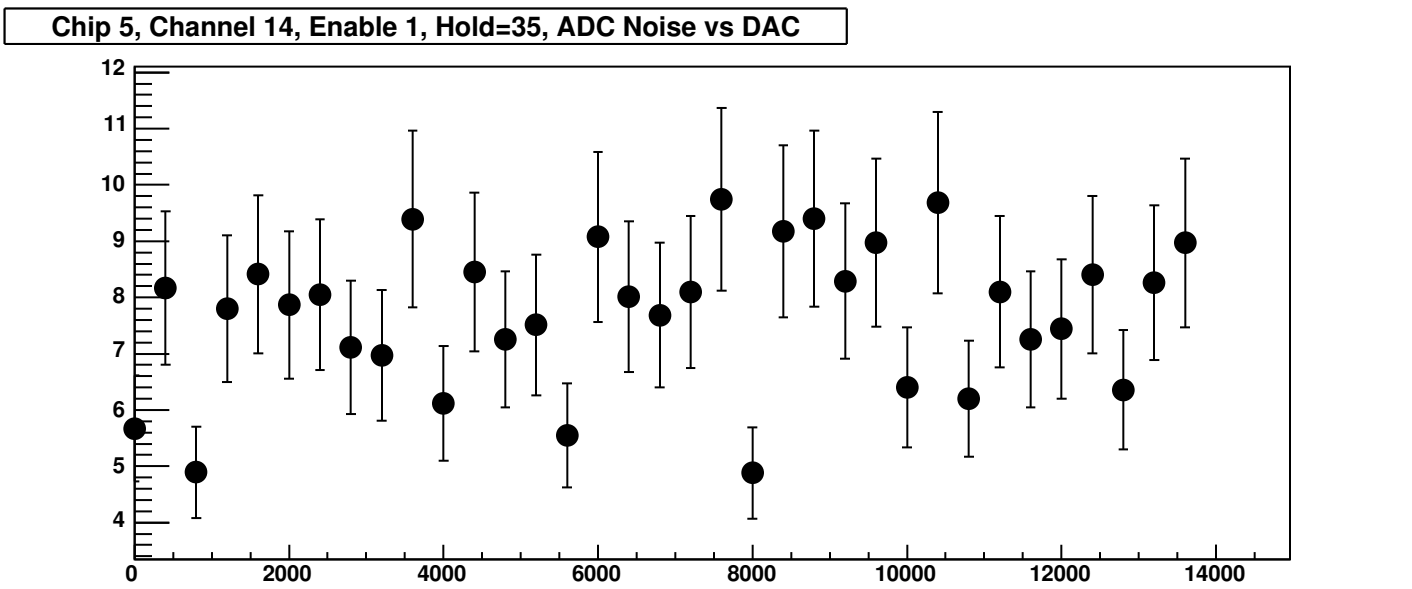

Chip 5, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

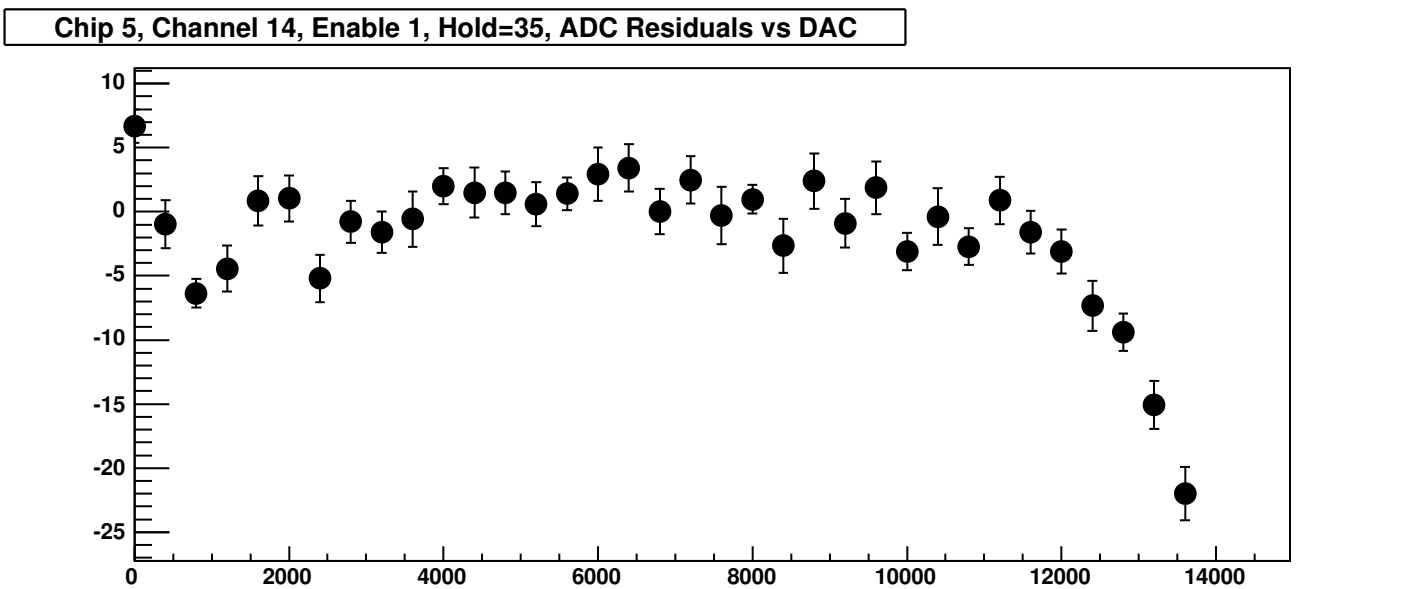

Chip 5, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 444.5 / 23
p0 -463.1 ± 1.218
p1 0.1948 ± 0.0002005
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

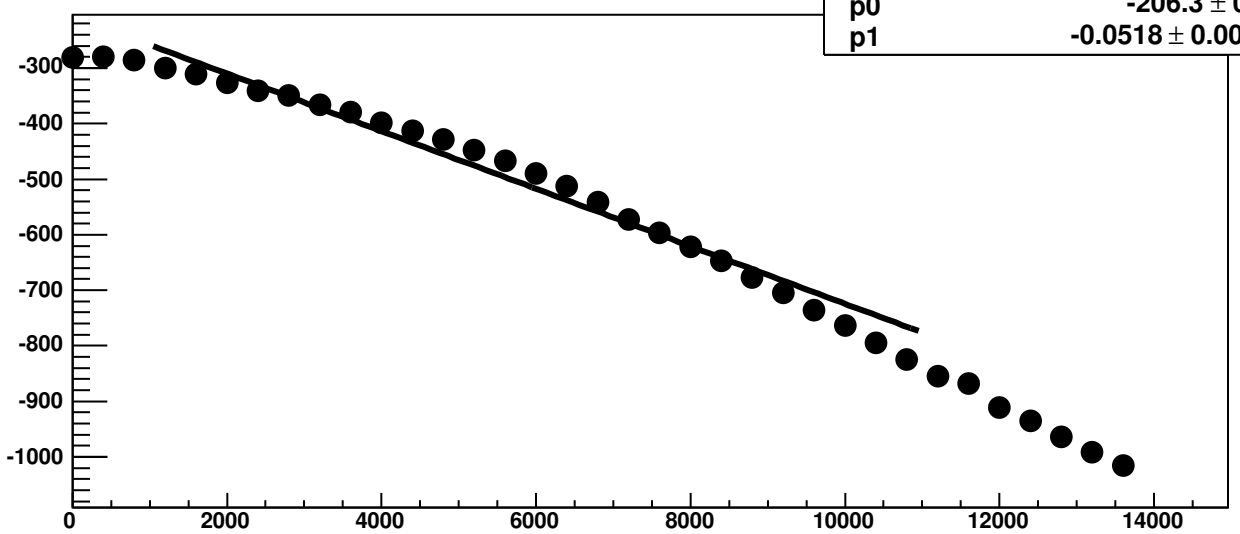

Chip 5, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 40.58 / 23
p0 -252.6 ± 0.8068
p1 0.02322 ± 0.0001206
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

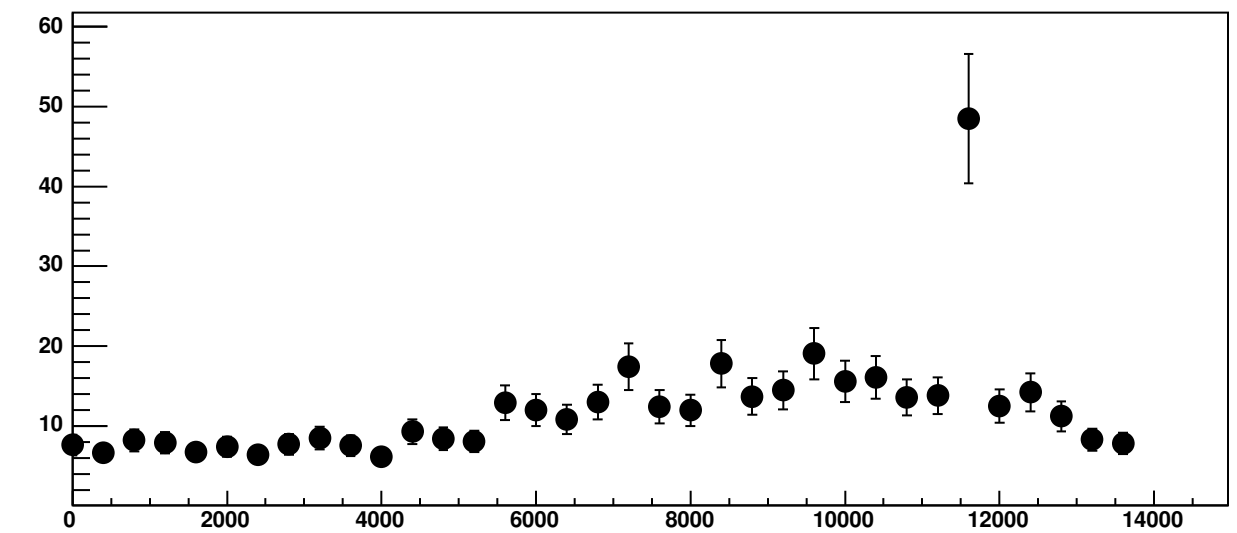

Chip 5, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

Chip 5, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC

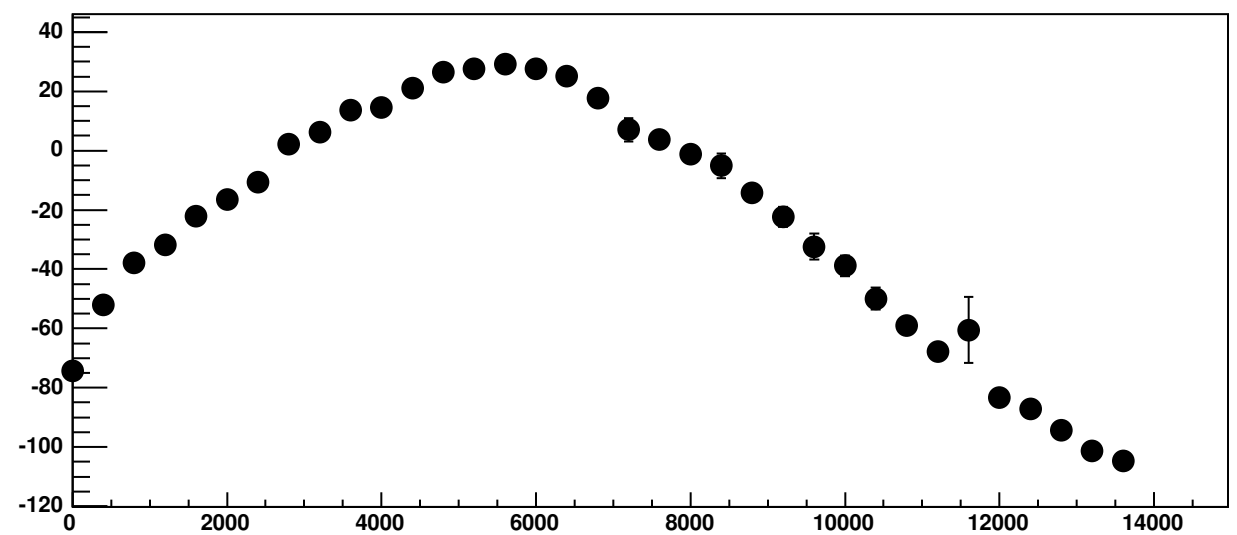
χ^2 / ndf	2468 / 23
p0	-206.3 ± 0.8867
p1	-0.0518 ± 0.0001741

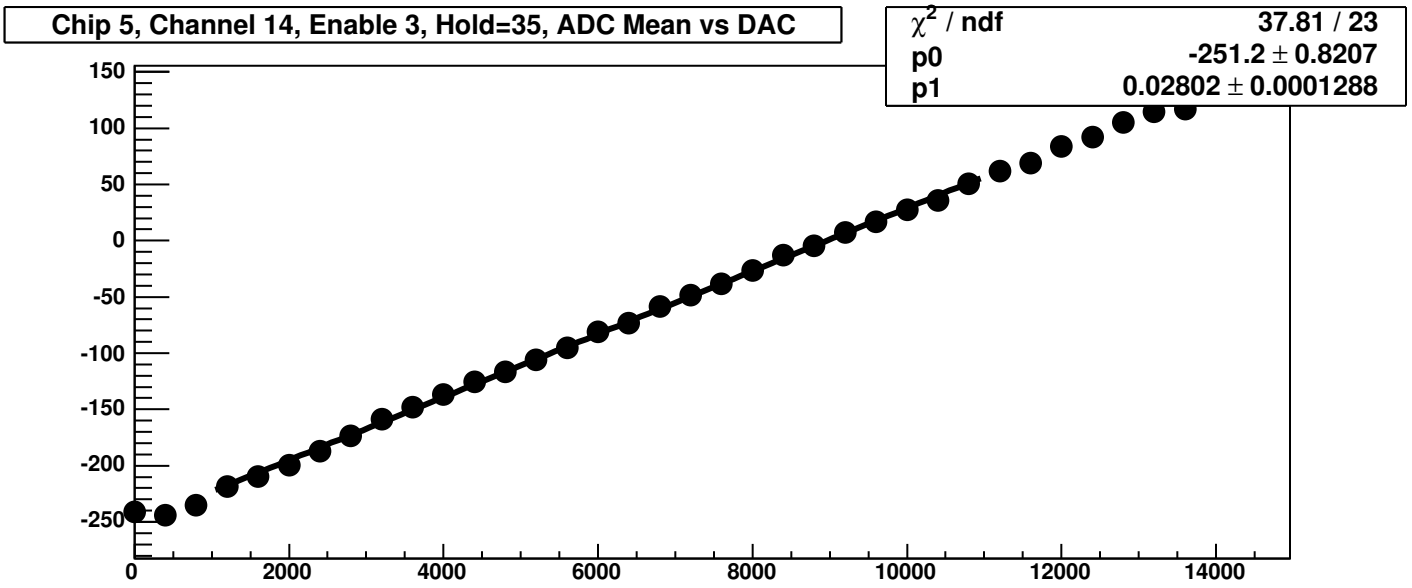


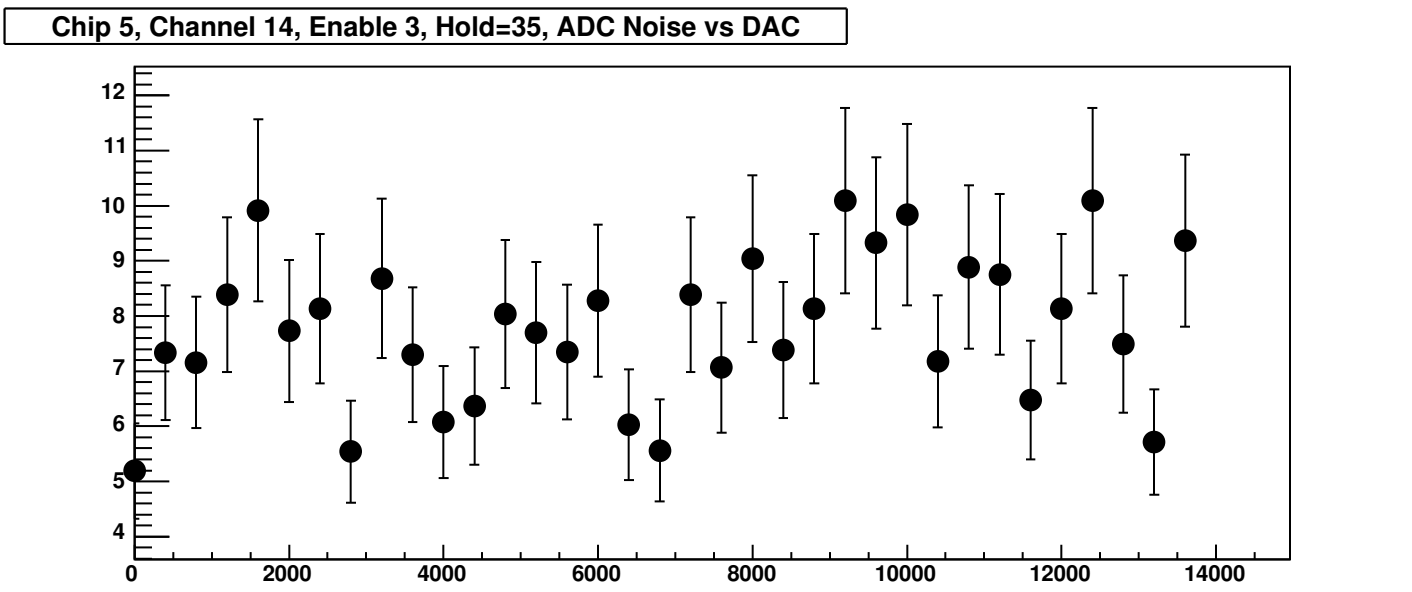
Chip 5, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC

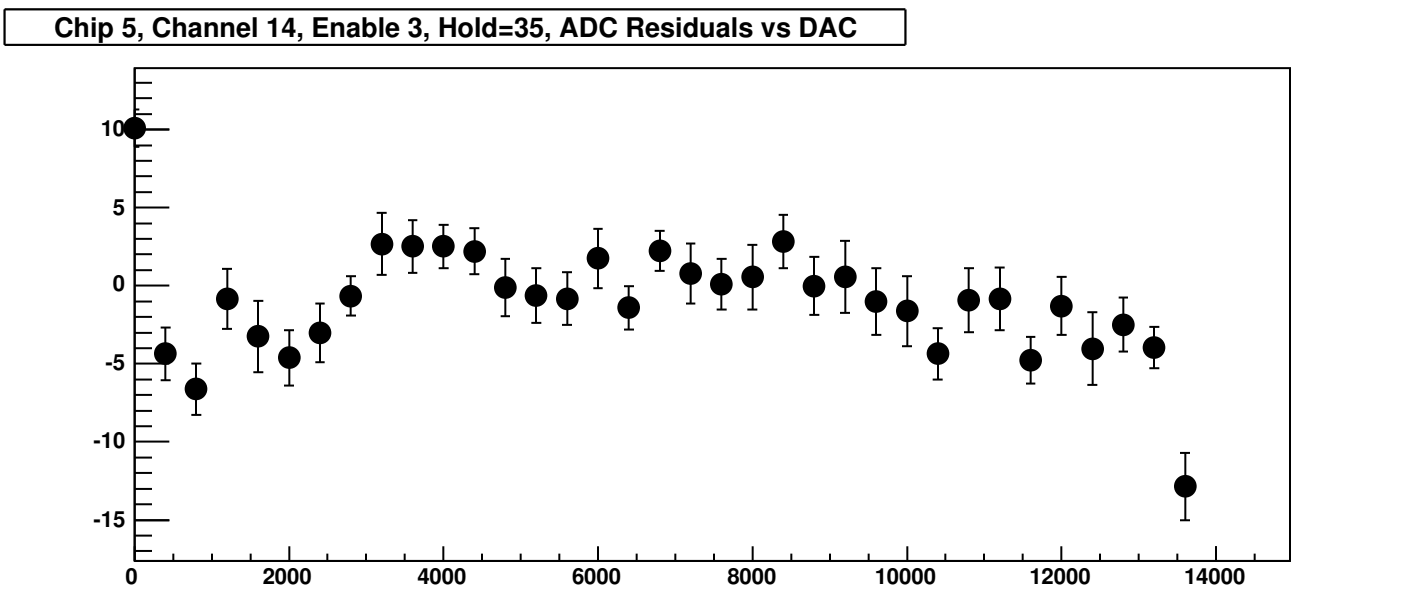


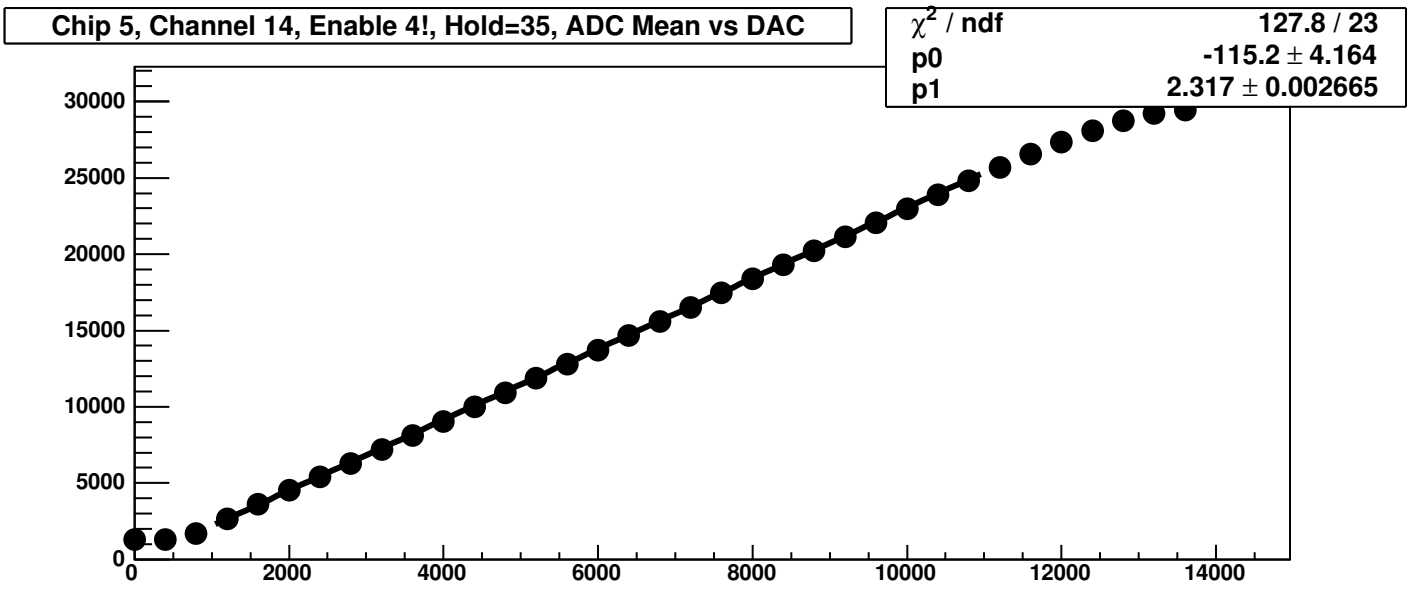
Chip 5, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC

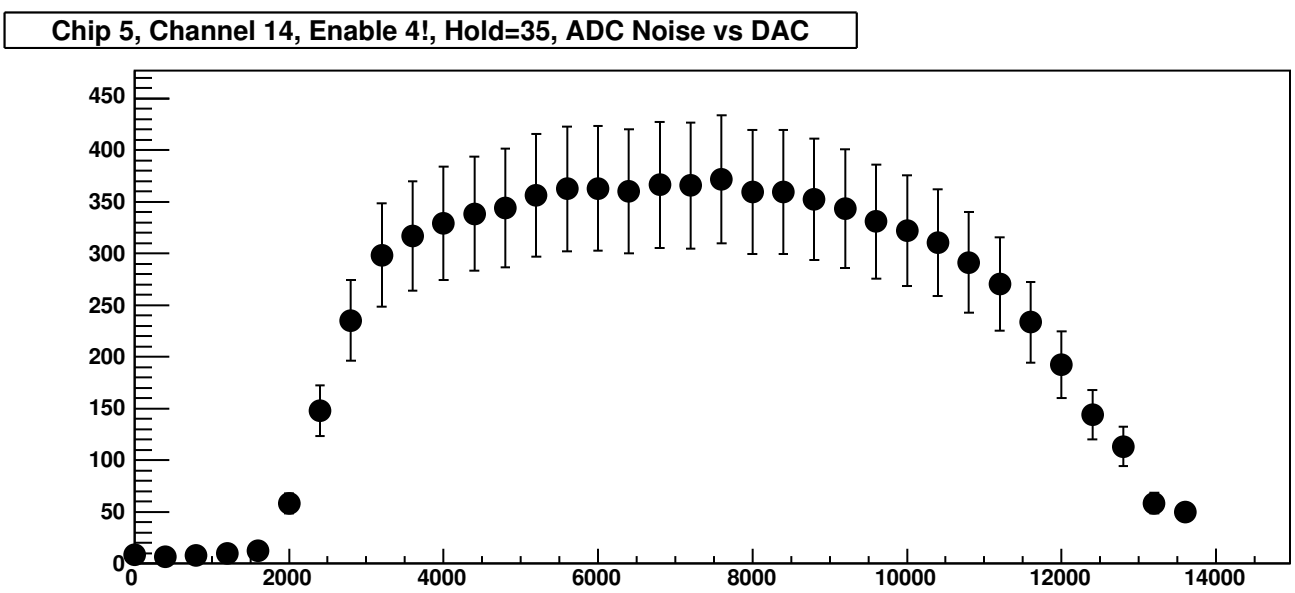


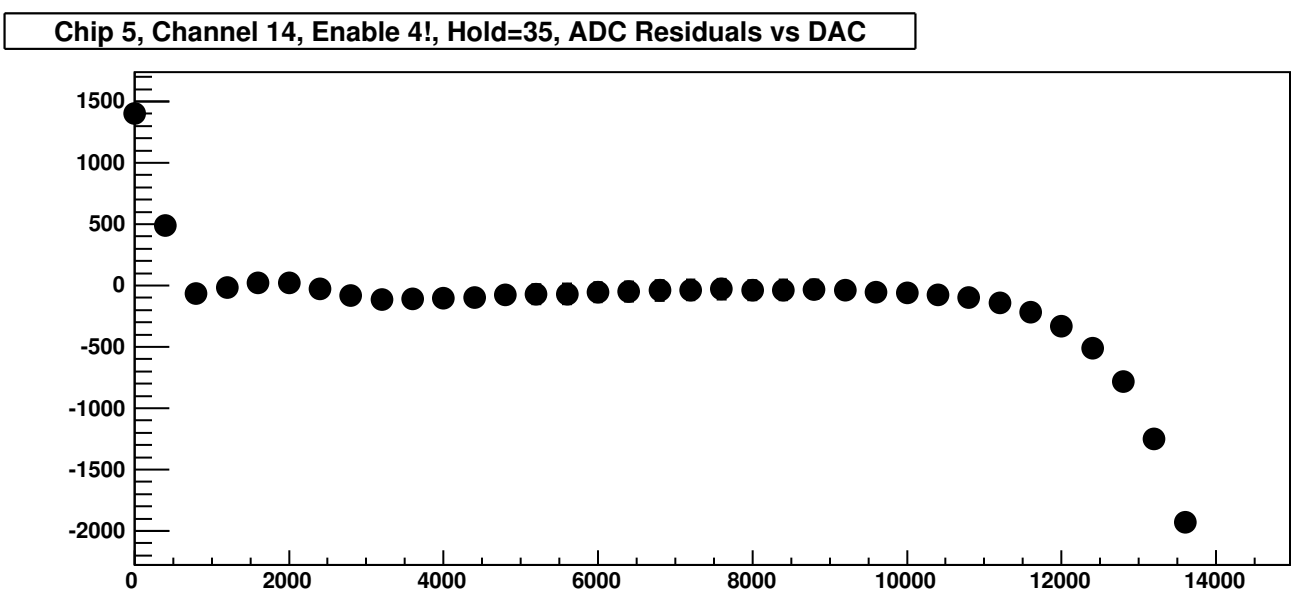

Chip 5, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 37.81 / 23
p0 -251.2 ± 0.8207
p1 0.02802 ± 0.0001288
150
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

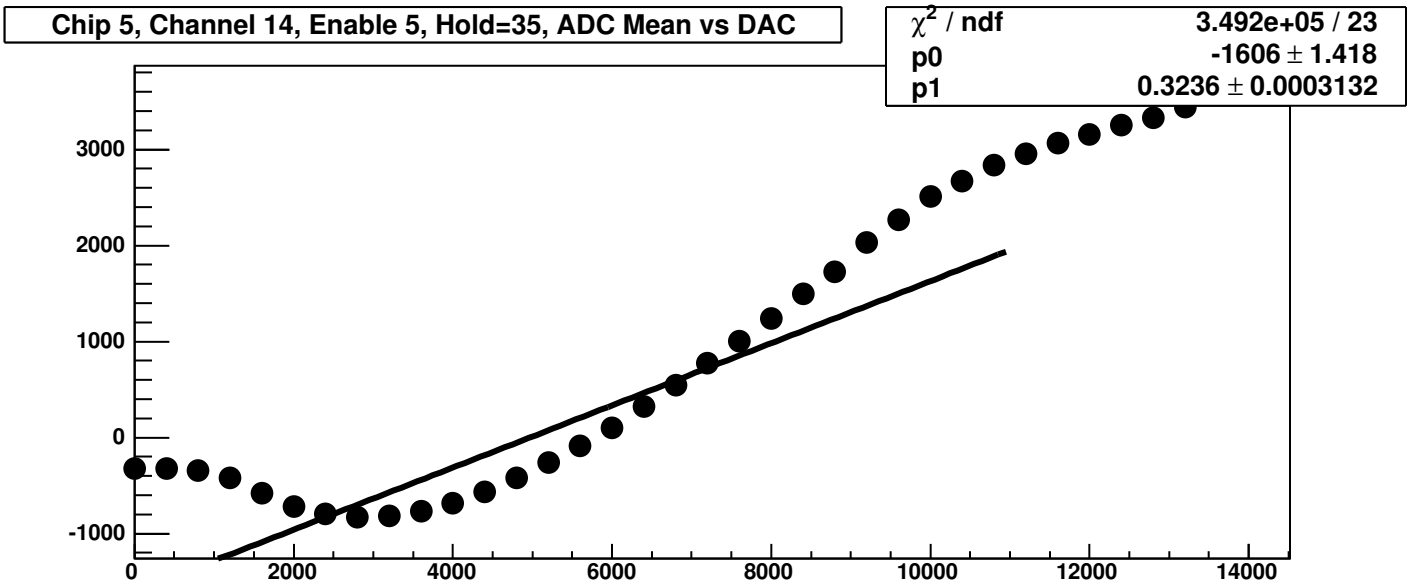

Chip 5, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

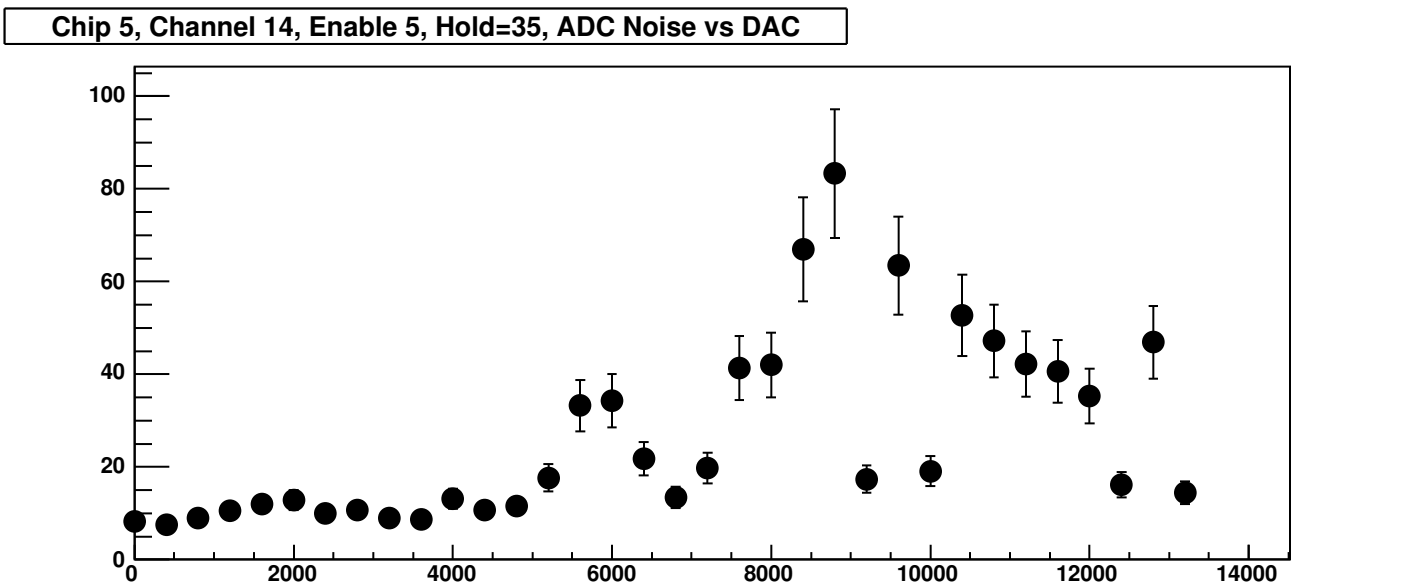

Chip 5, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

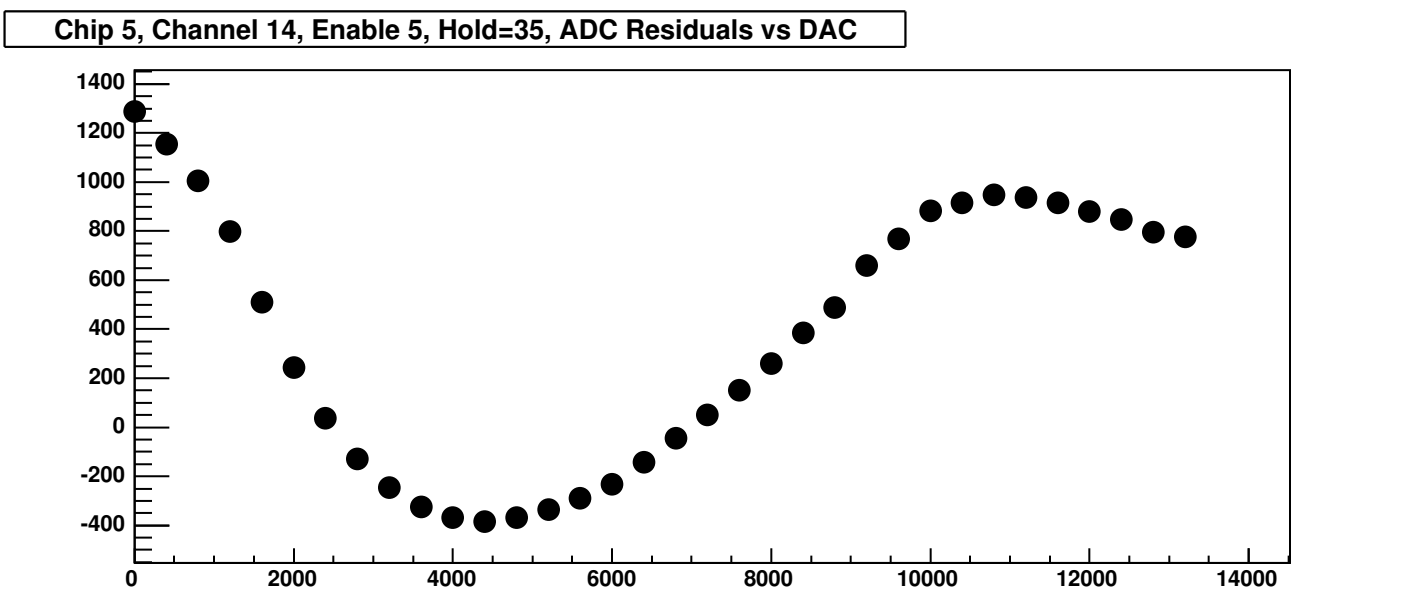

Chip 5, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 127.8 / 23
p0 -115.2 ± 4.164
p1 2.317 ± 0.002665
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

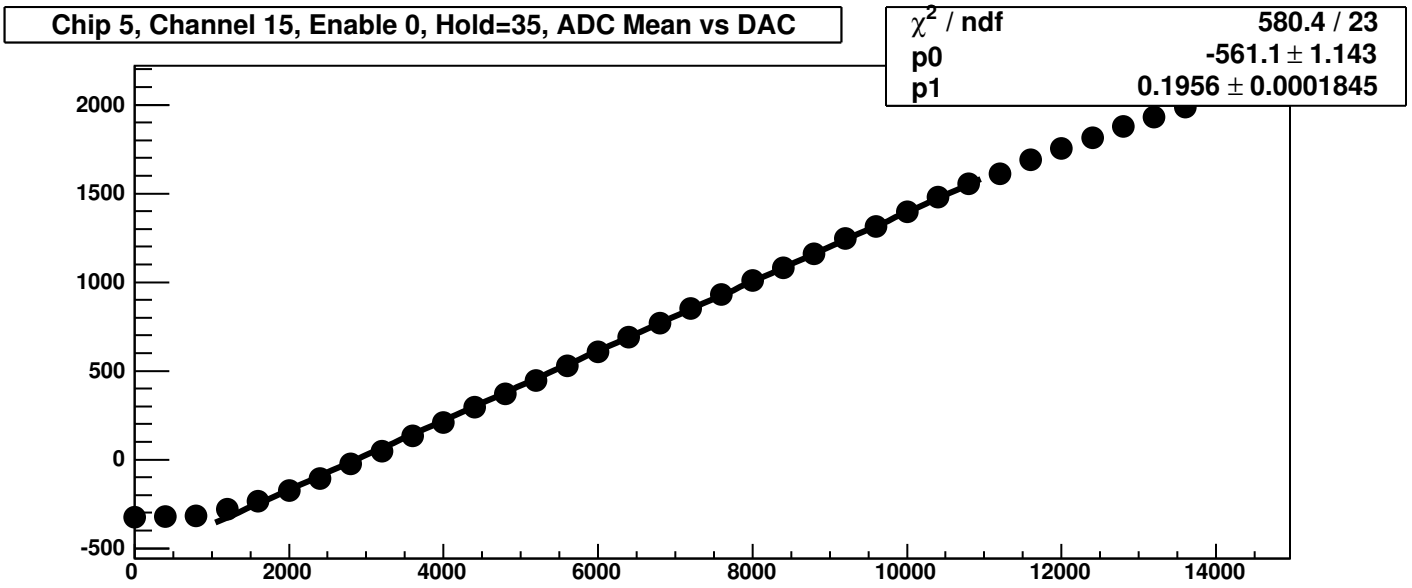

Chip 5, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

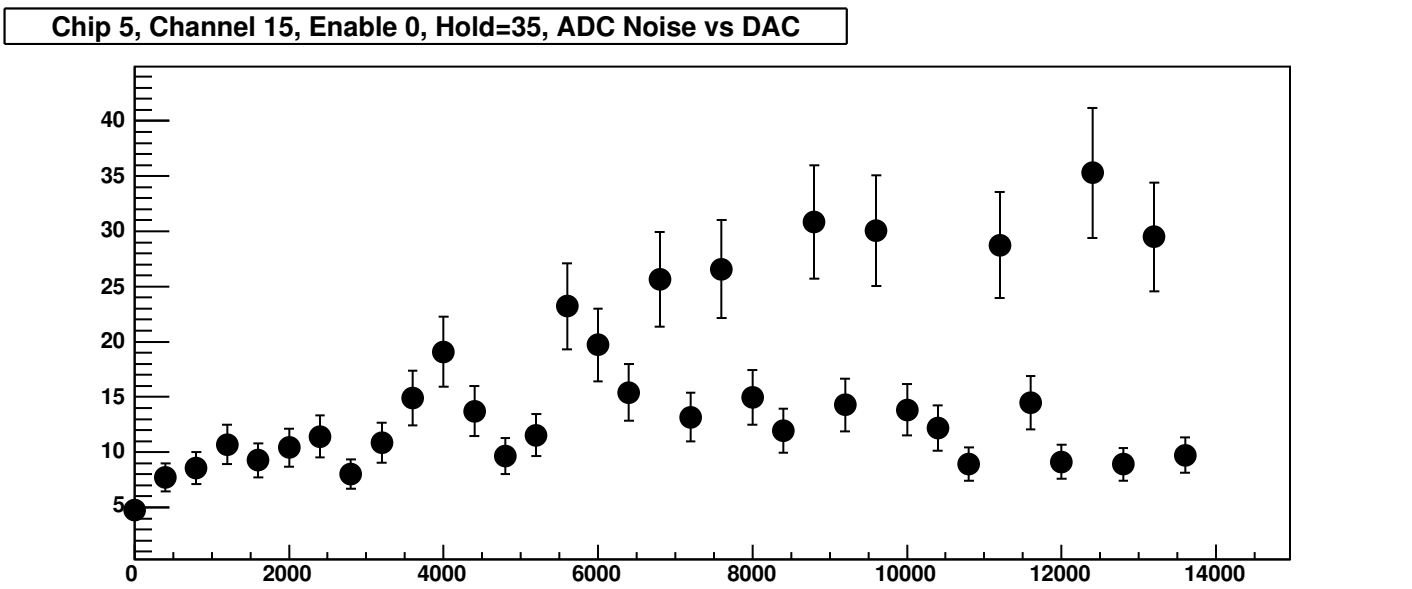

Chip 5, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

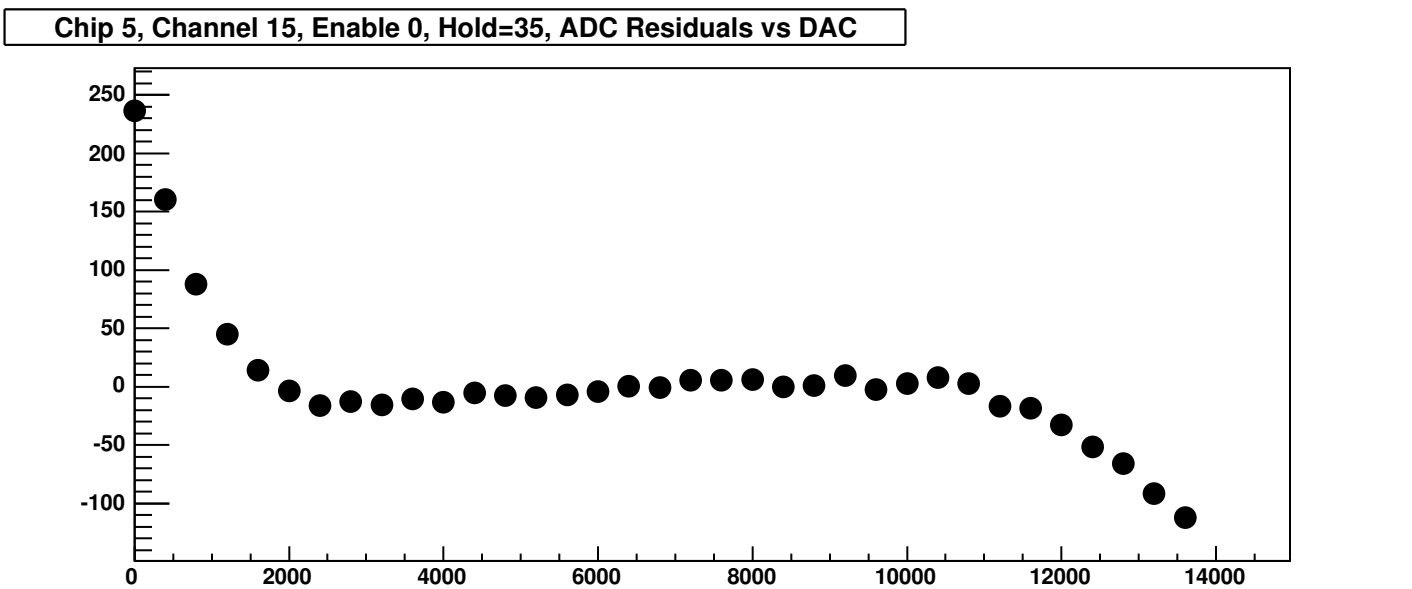

Chip 5, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.492e+05 / 23
p0 -1606 ± 1.418
p1 0.3236 ± 0.0003132
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

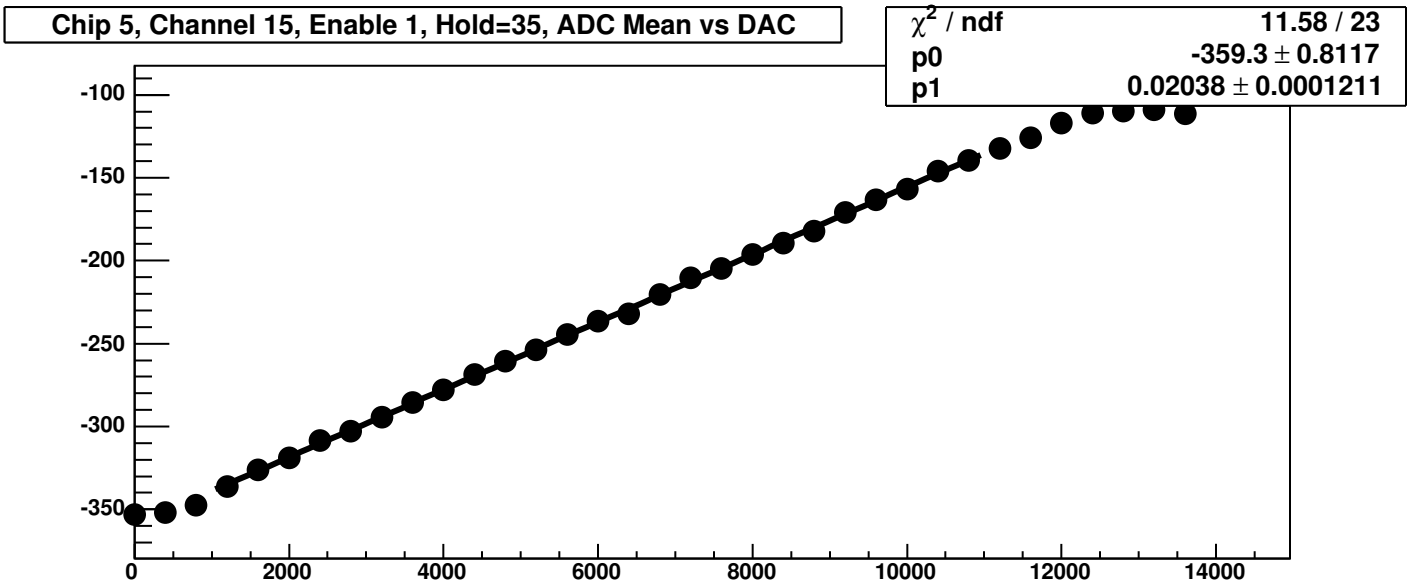

Chip 5, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

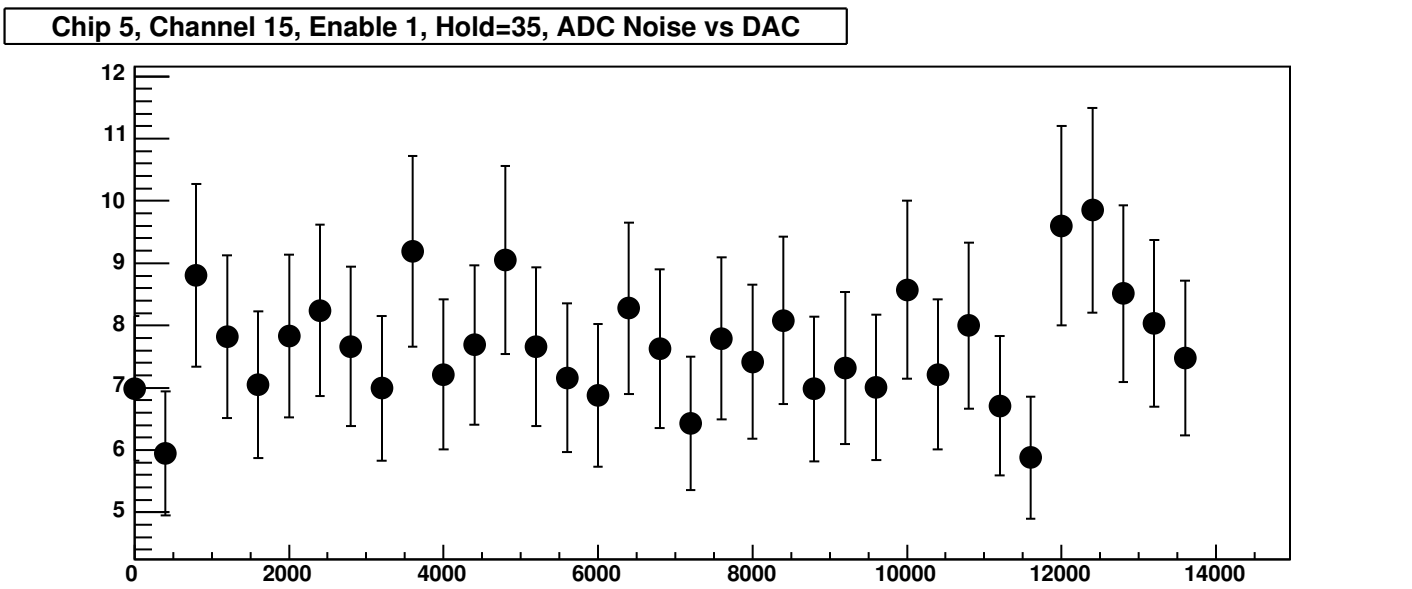

Chip 5, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

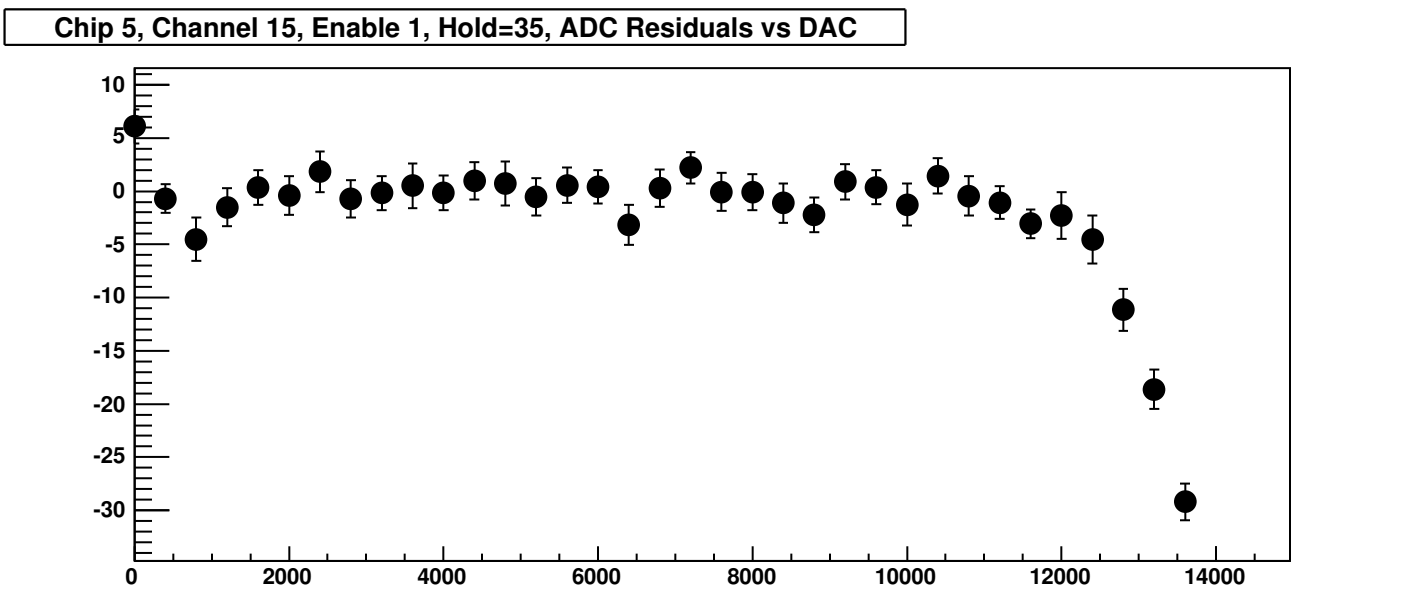

Chip 5, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 580.4 / 23
p0 -561.1 ± 1.143
p1 0.1956 ± 0.0001845
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

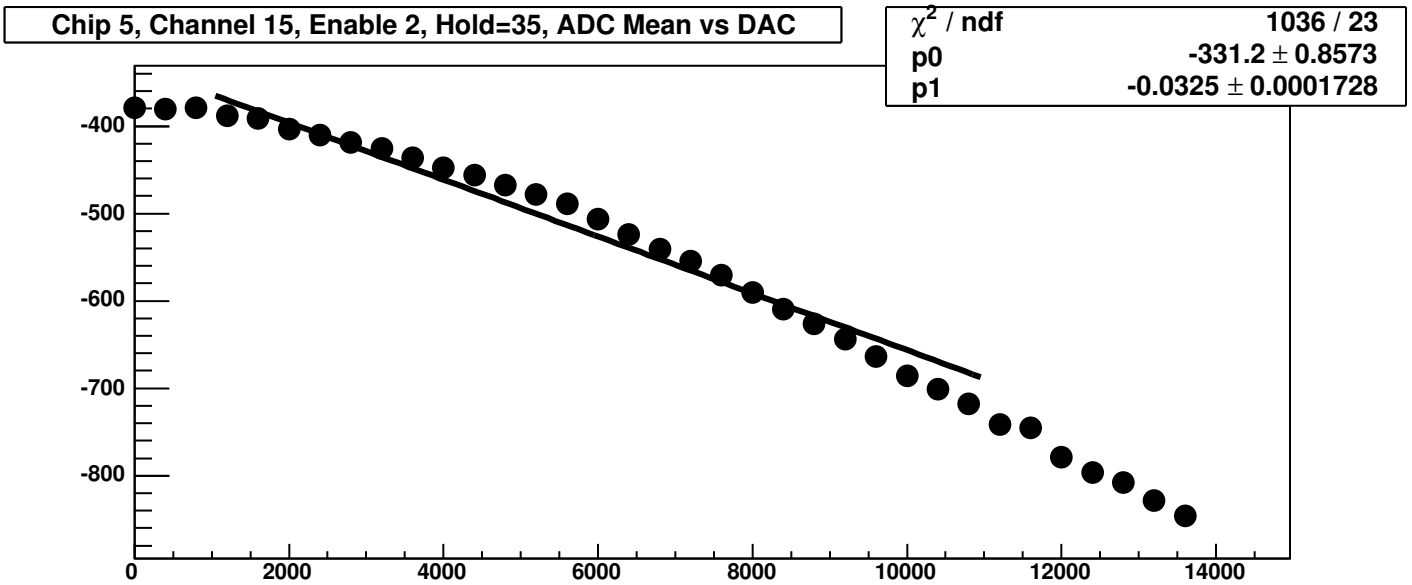

Chip 5, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

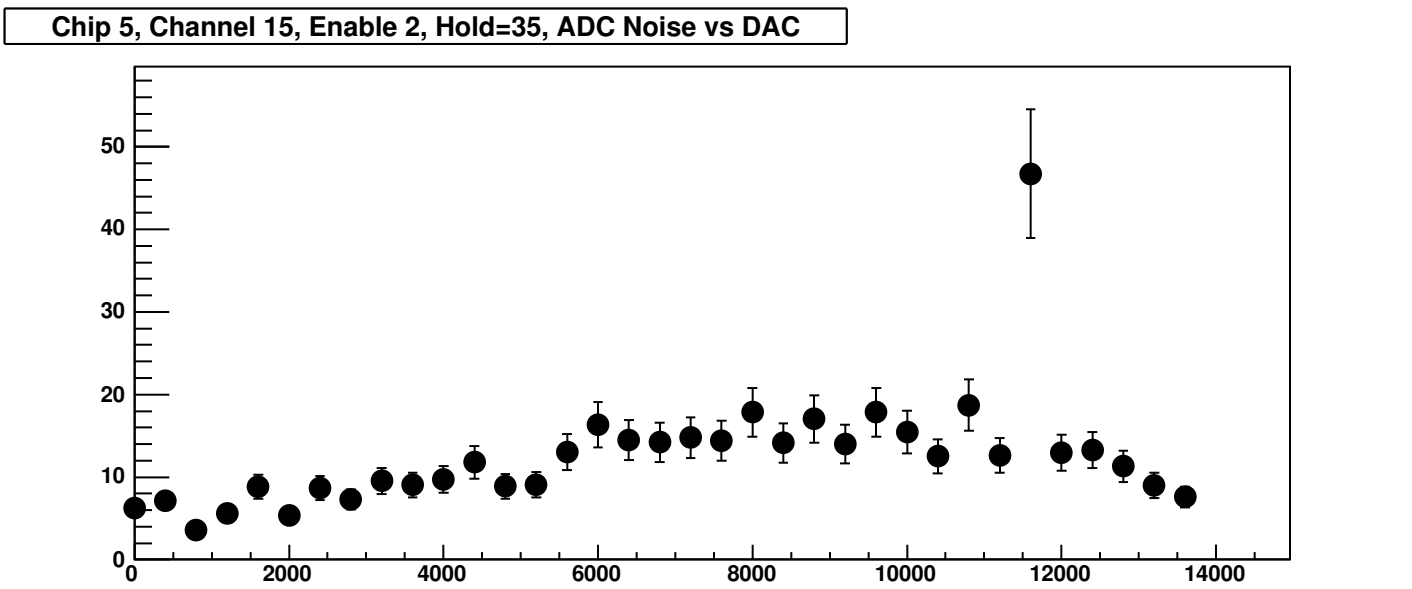

Chip 5, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

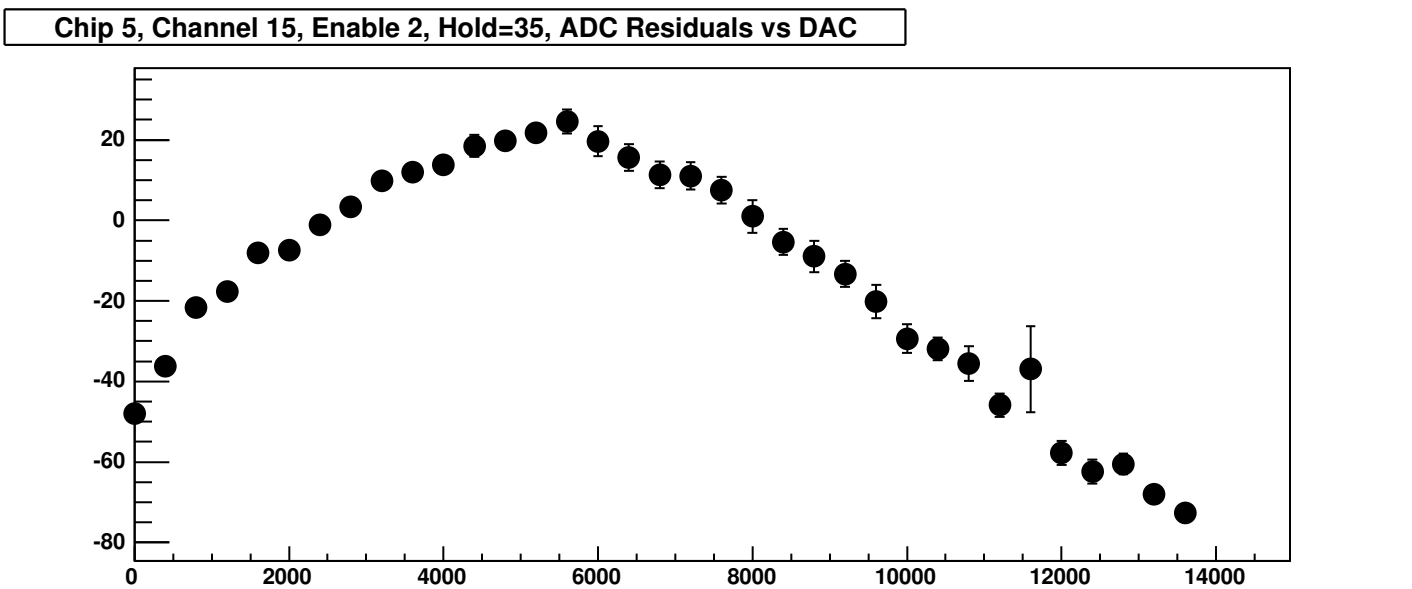

Chip 5, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 11.58 / 23
p0 -359.3 ± 0.8117
p1 0.02038 ± 0.0001211
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

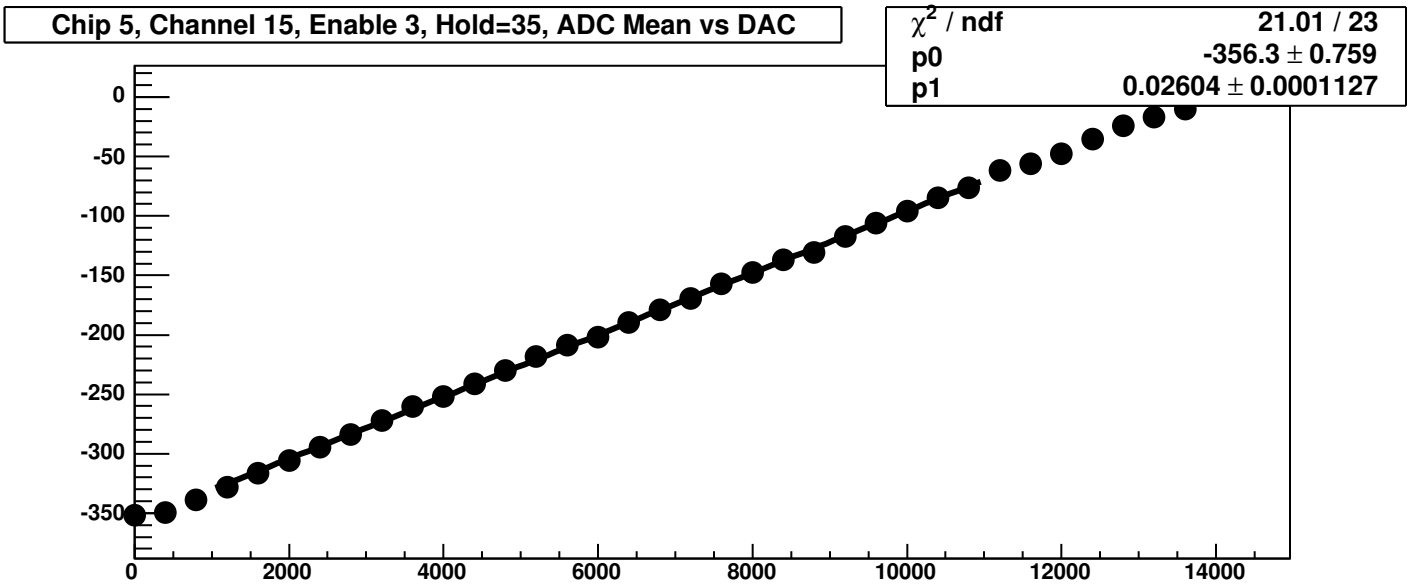

Chip 5, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

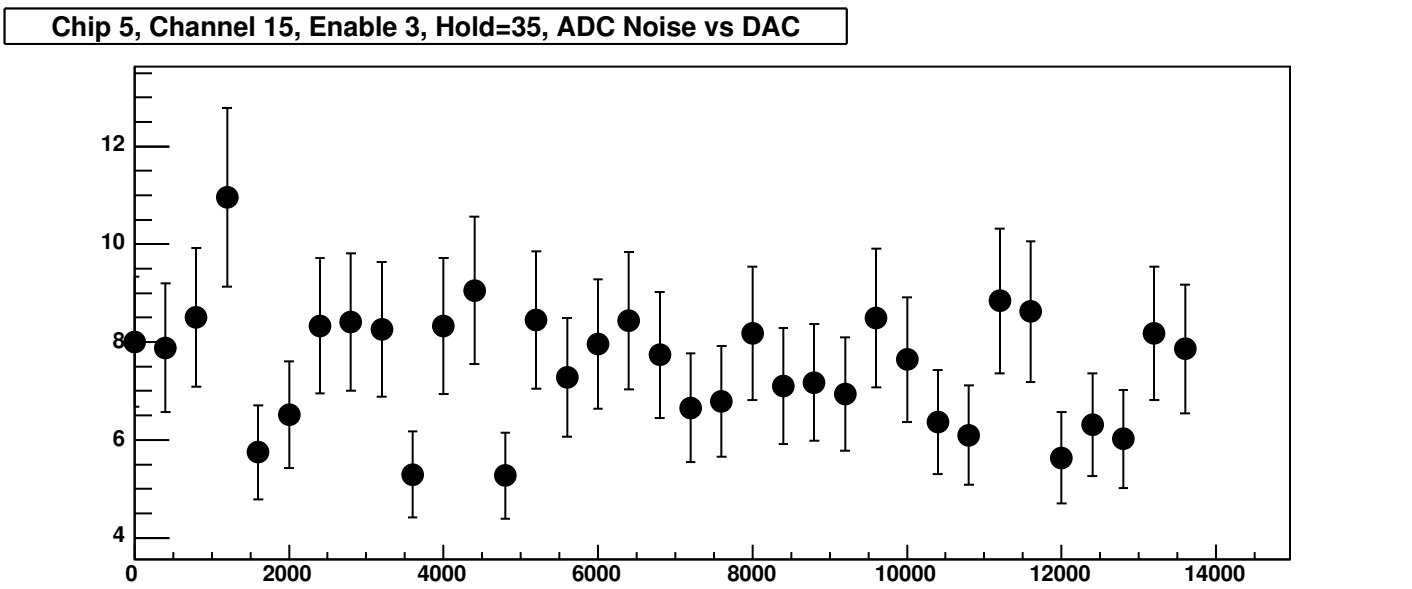

Chip 5, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
0
2000
4000
6000
8000
10000
12000
14000

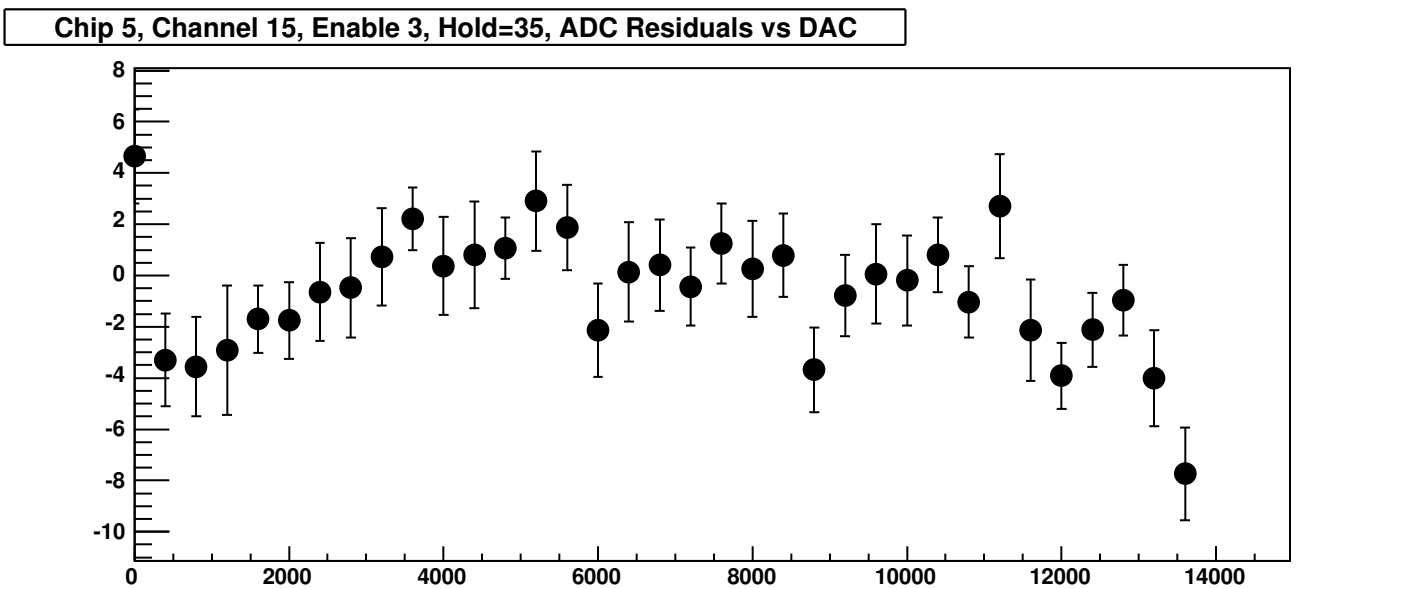

Chip 5, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1036 / 23
p0 -331.2 ± 0.8573
p1 -0.0325 ± 0.0001728
-400
-500
-600
-700
-800
0
2000
4000
6000
8000
10000
12000
14000

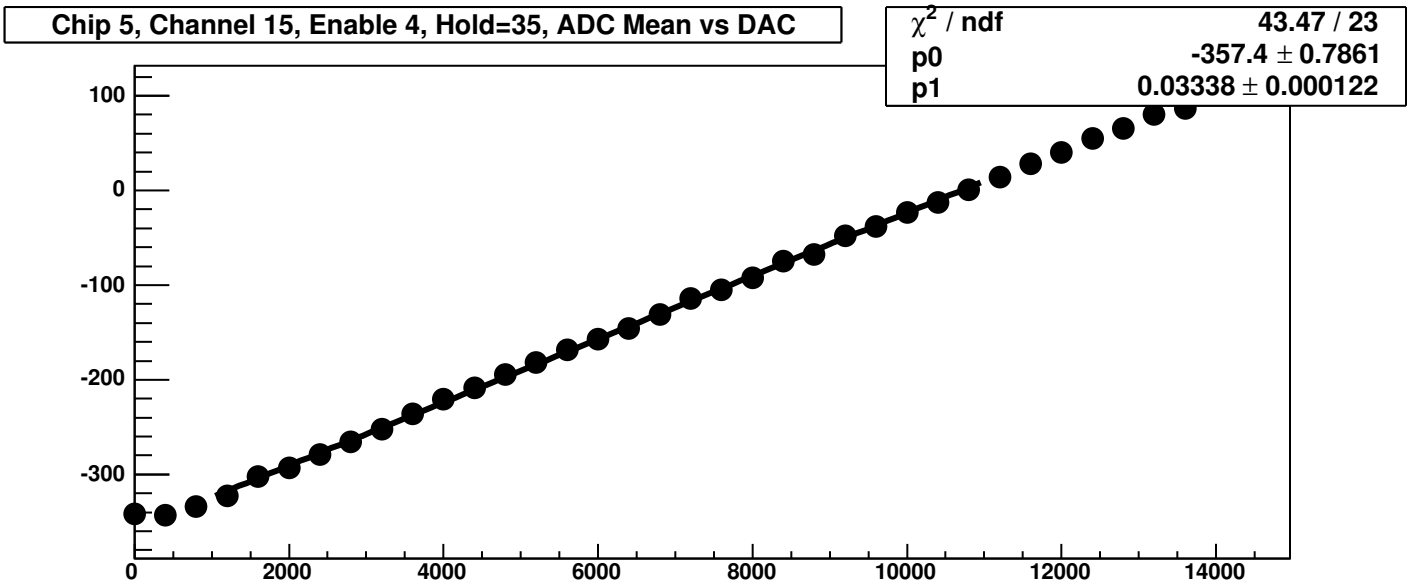

Chip 5, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

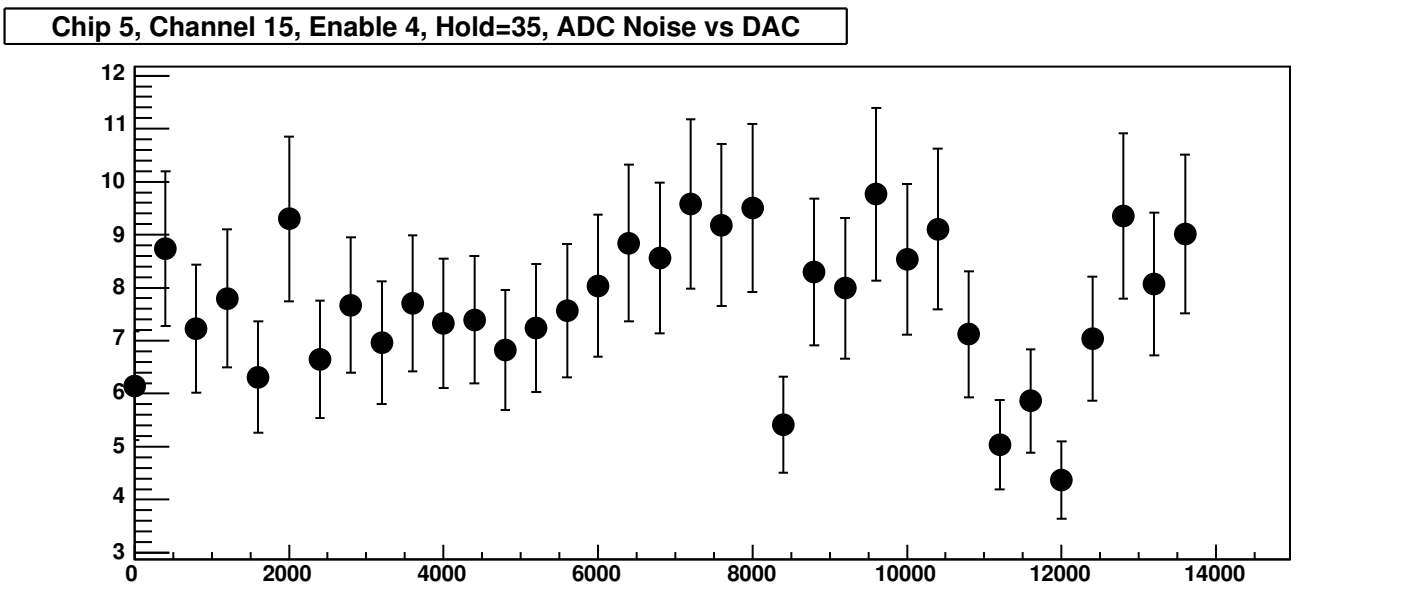

Chip 5, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000

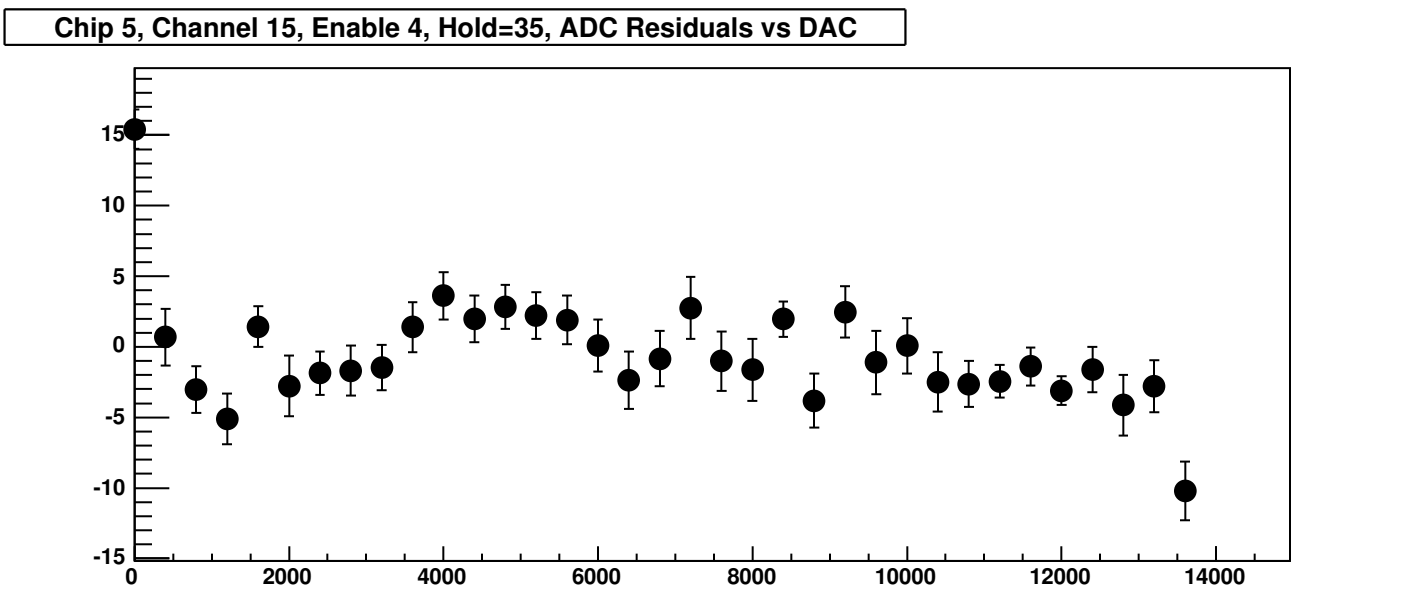

Chip 5, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.01 / 23
p0 -356.3 ± 0.759
p1 0.02604 ± 0.0001127
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

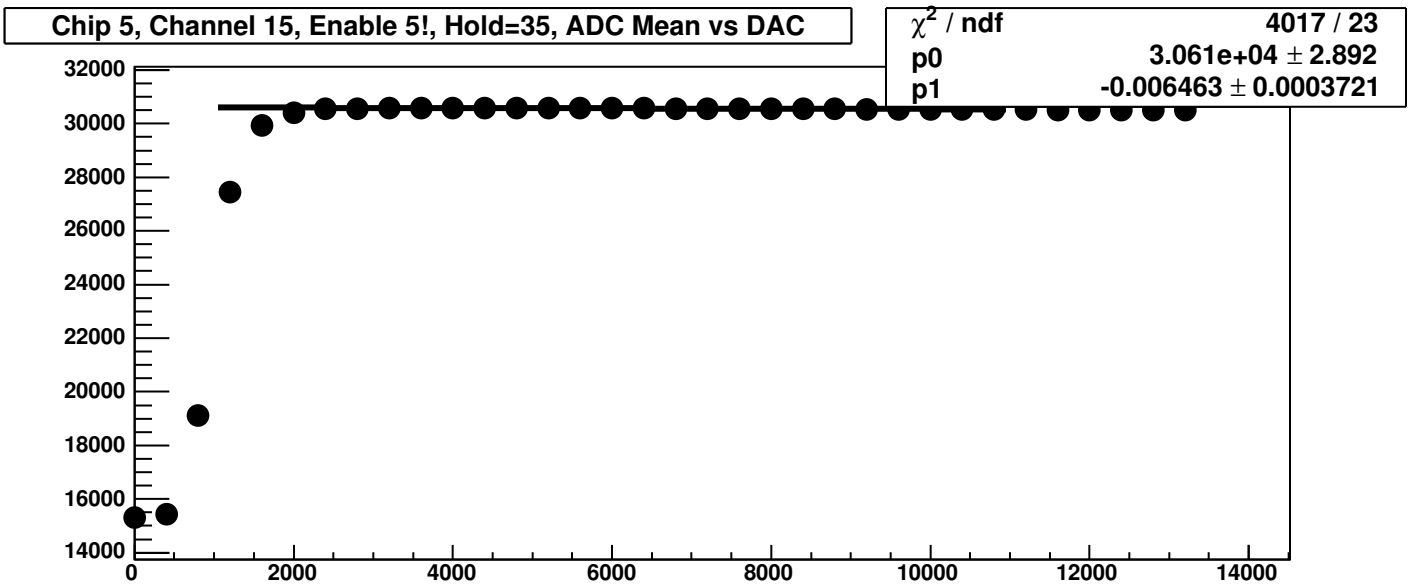

Chip 5, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

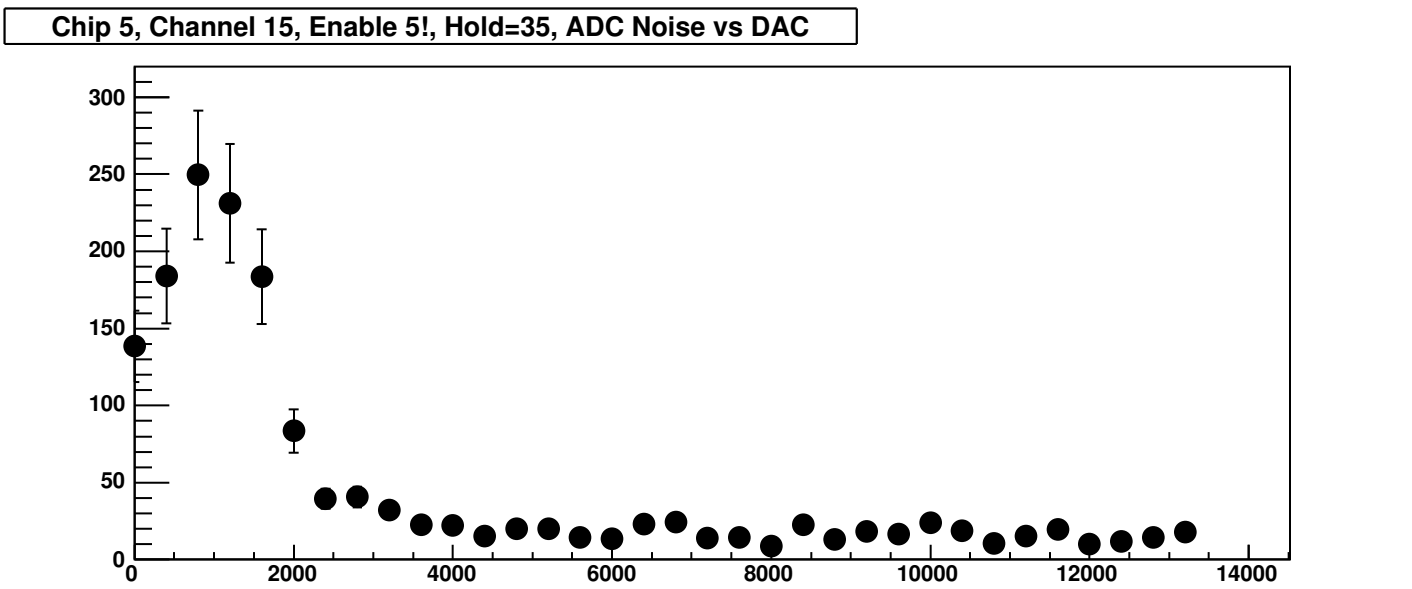

Chip 5, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
0
2000
4000
6000
8000
10000
12000
14000

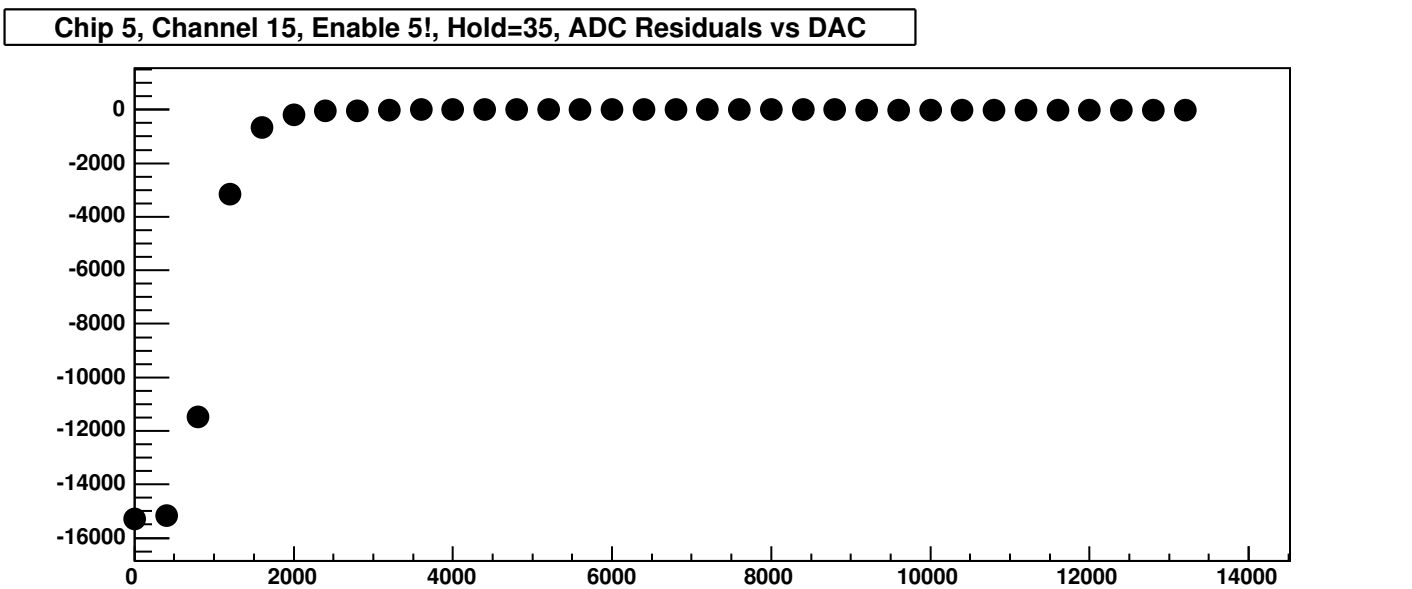

Chip 5, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 43.47 / 23
p0 -357.4 ± 0.7861
p1 0.03338 ± 0.000122
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

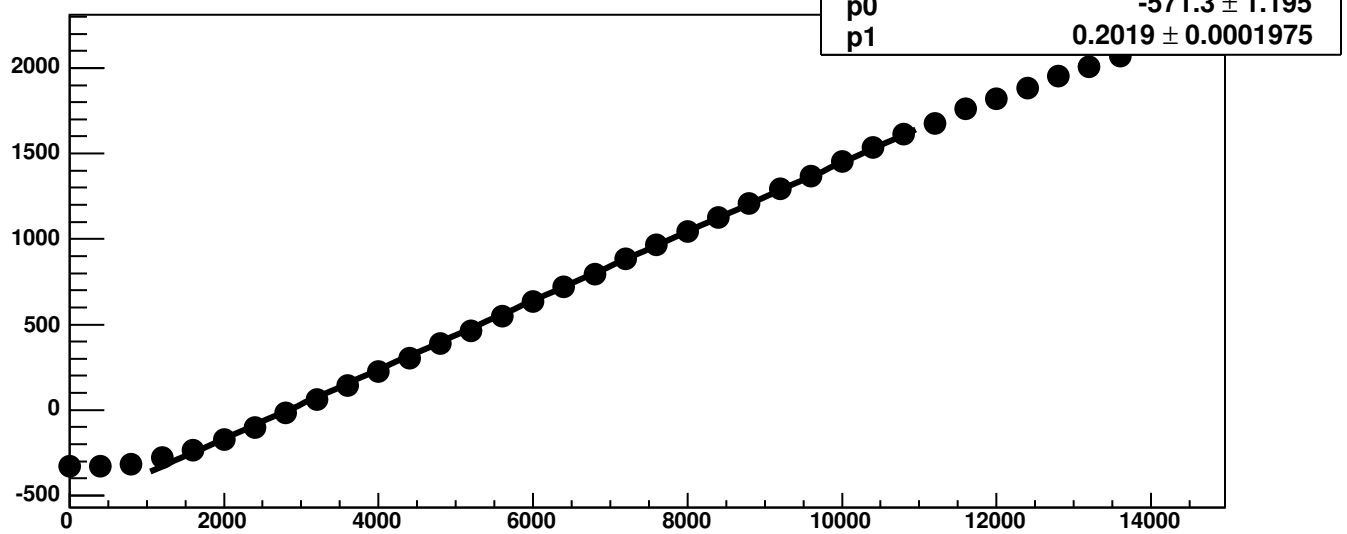

Chip 5, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4017 / 23
p0 3.061e+04 ± 2.892
p1 -0.006463 ± 0.0003721
32000
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

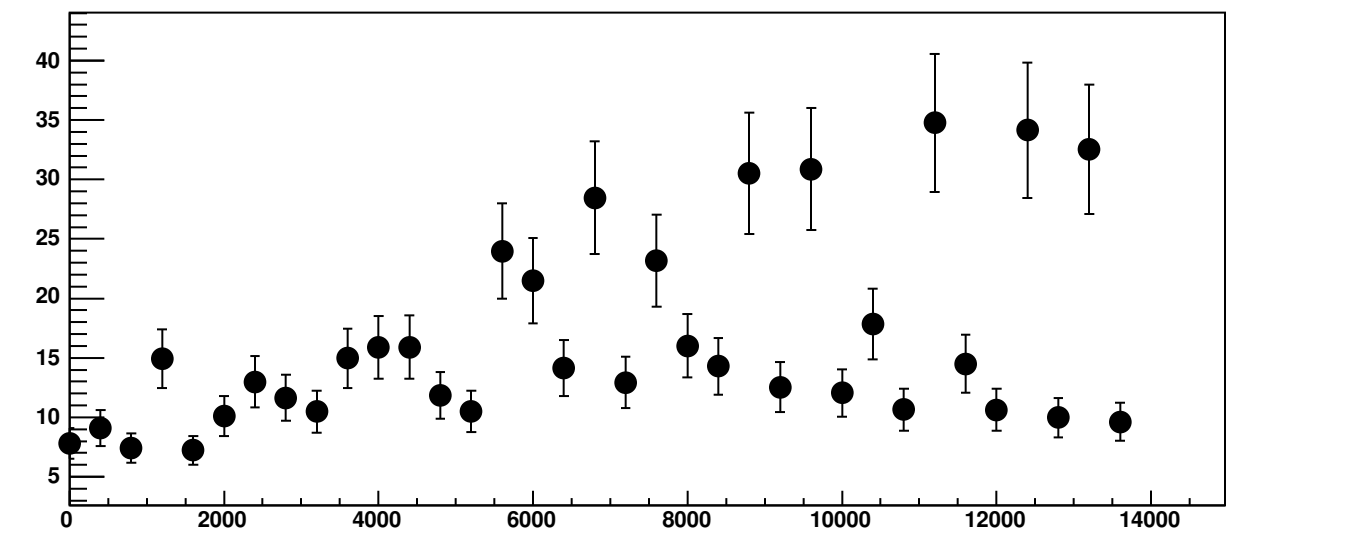

Chip 5, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

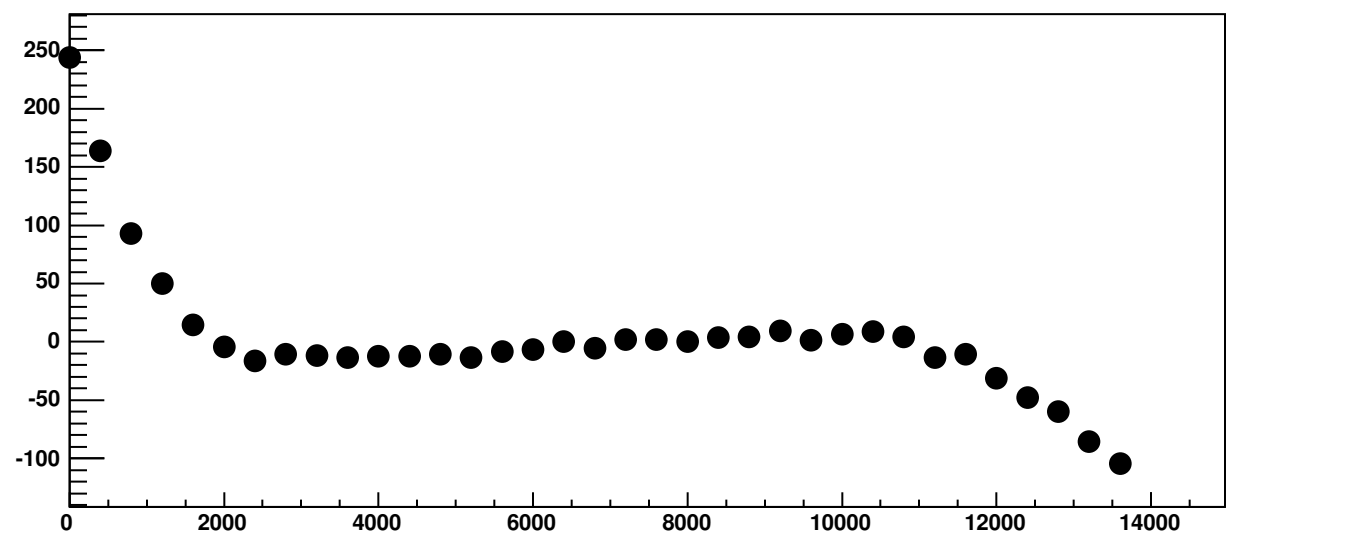
Chip 5, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

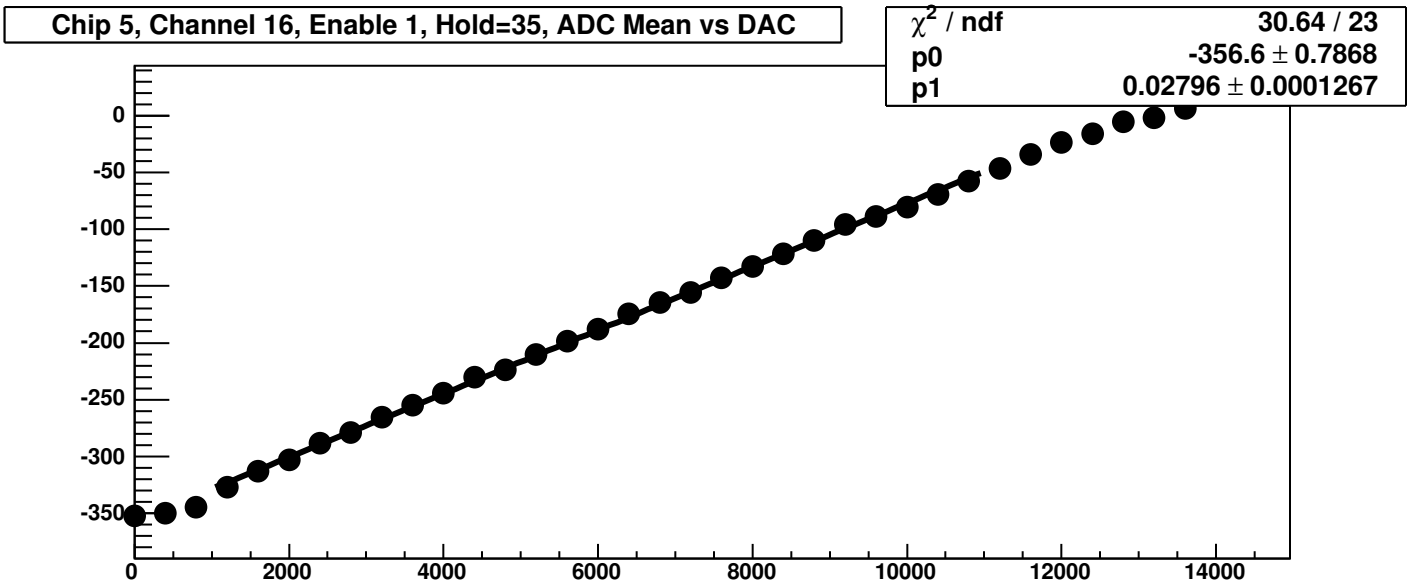


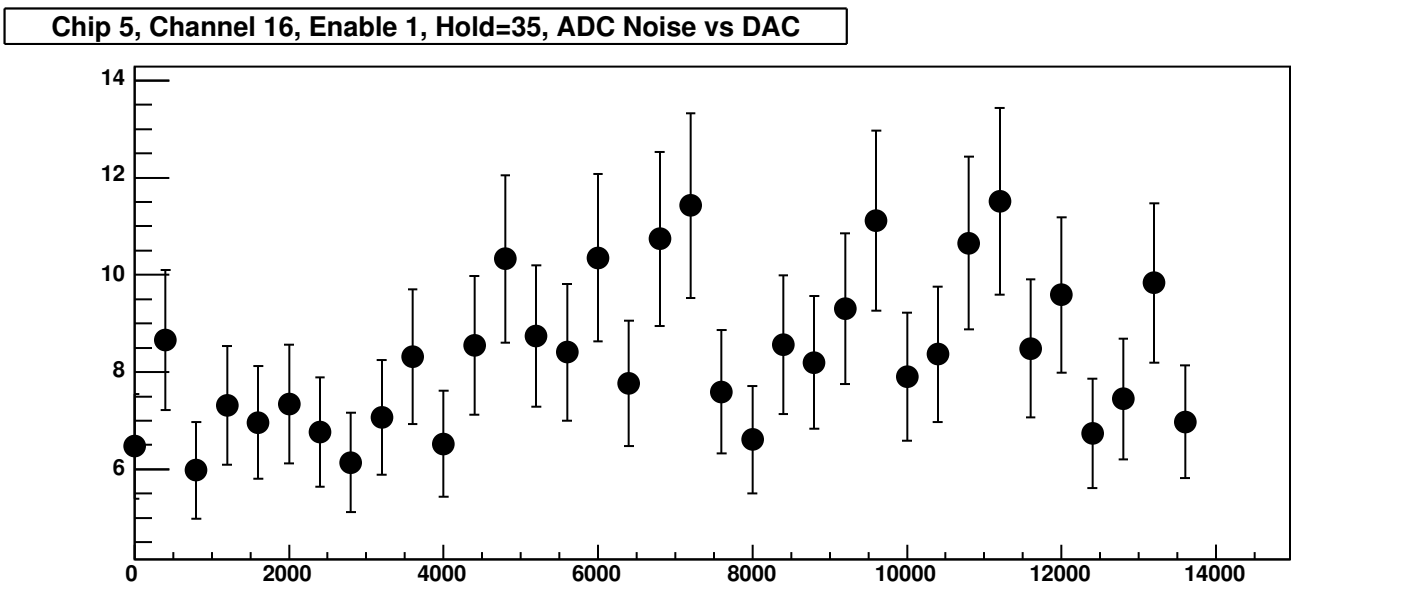
Chip 5, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC

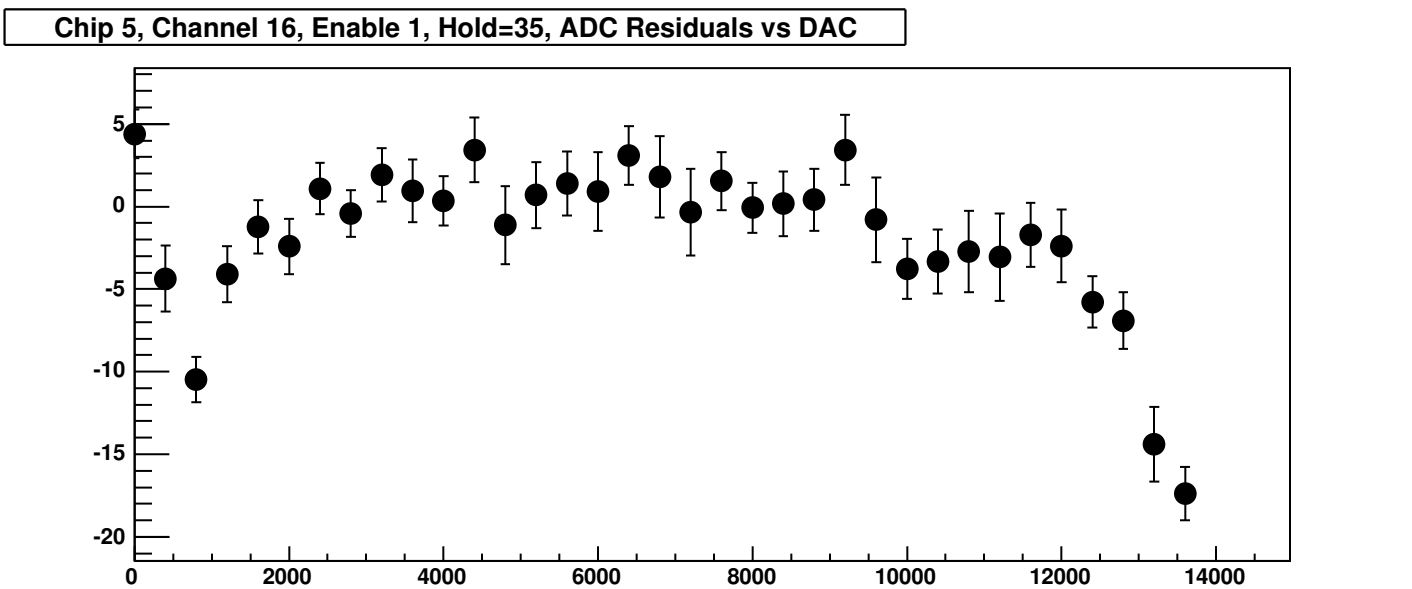


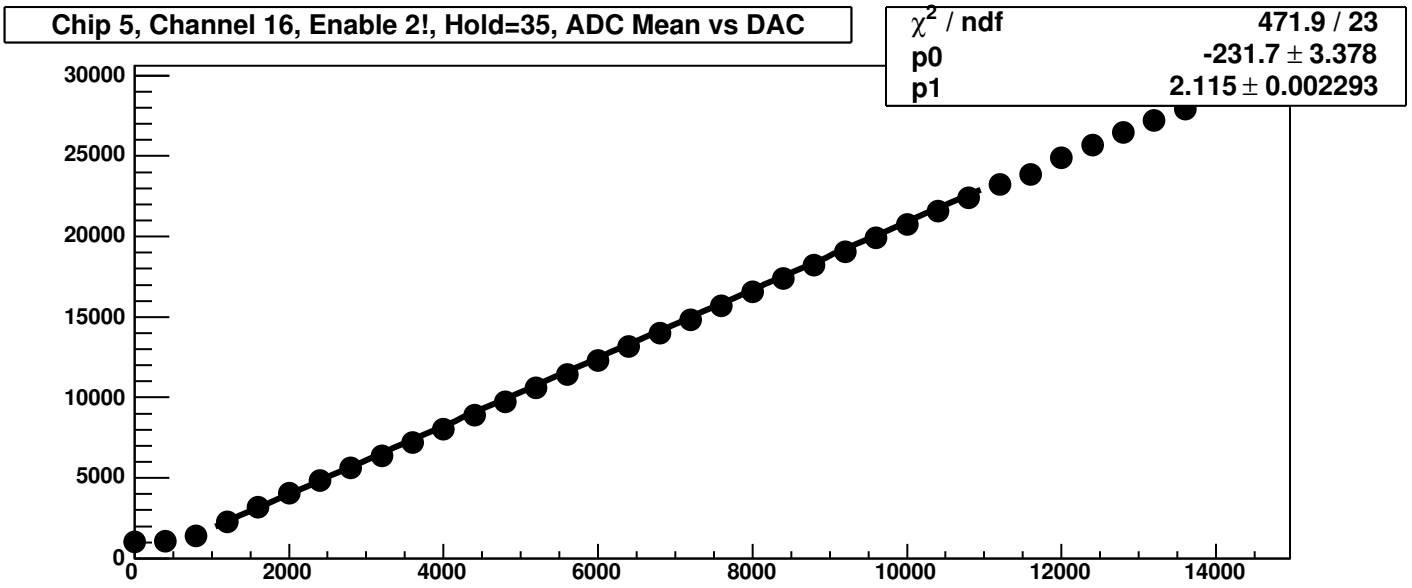
Chip 5, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

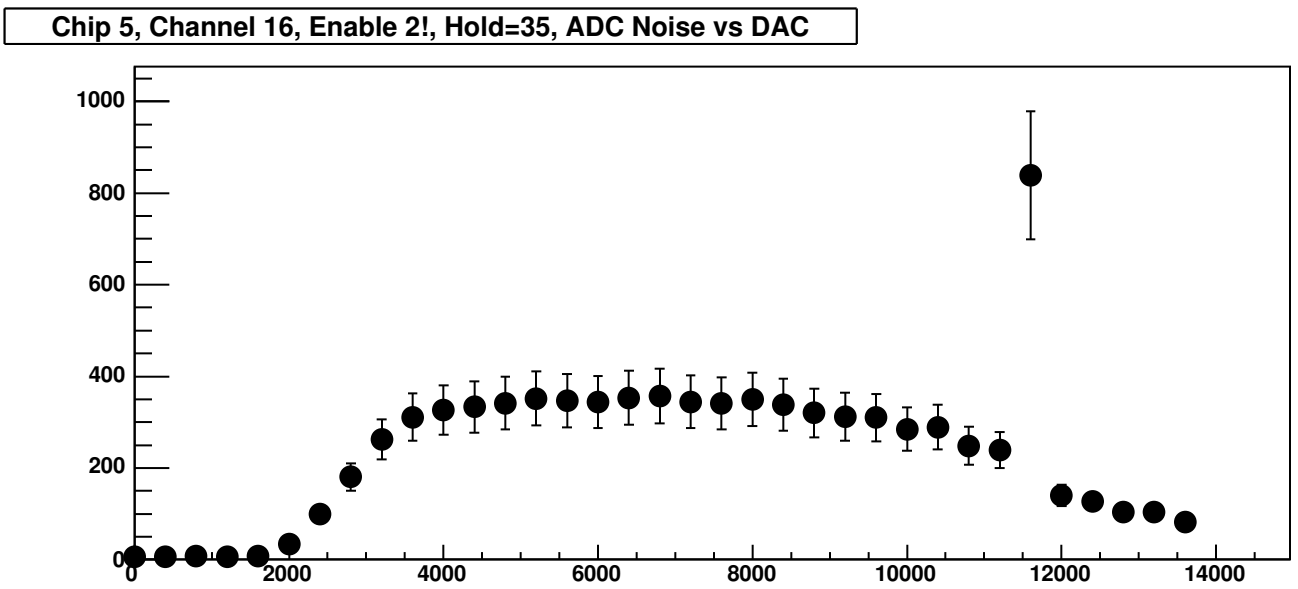


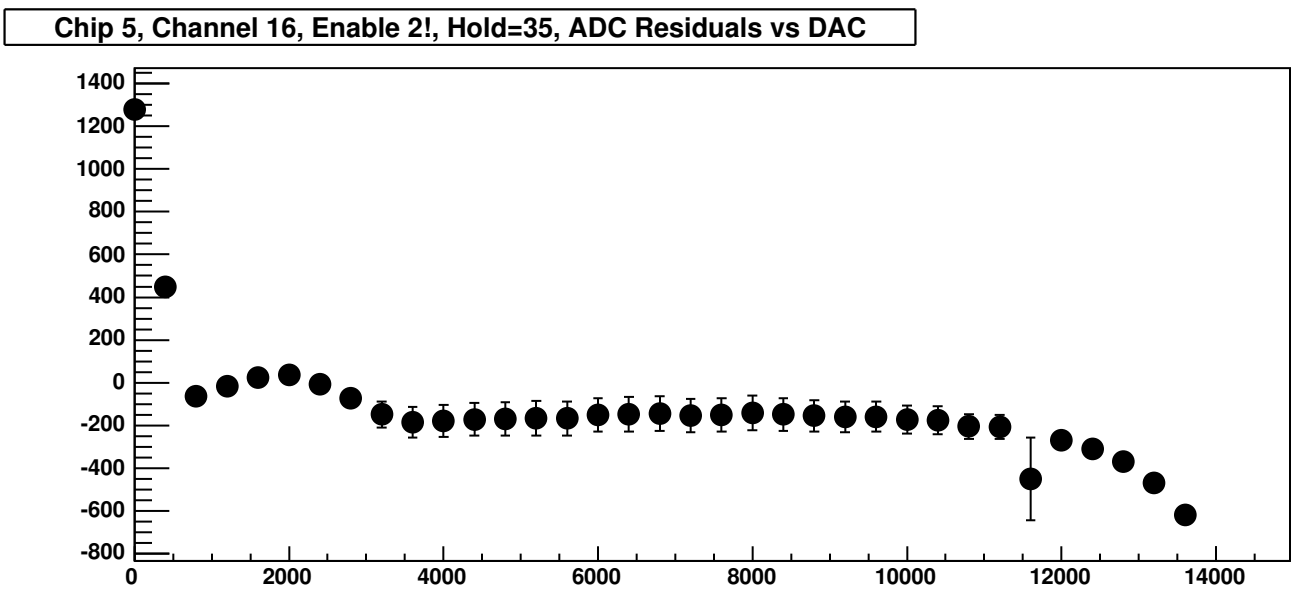

Chip 5, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 30.64 / 23
p0 -356.6 ± 0.7868
p1 0.02796 ± 0.0001267
0
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

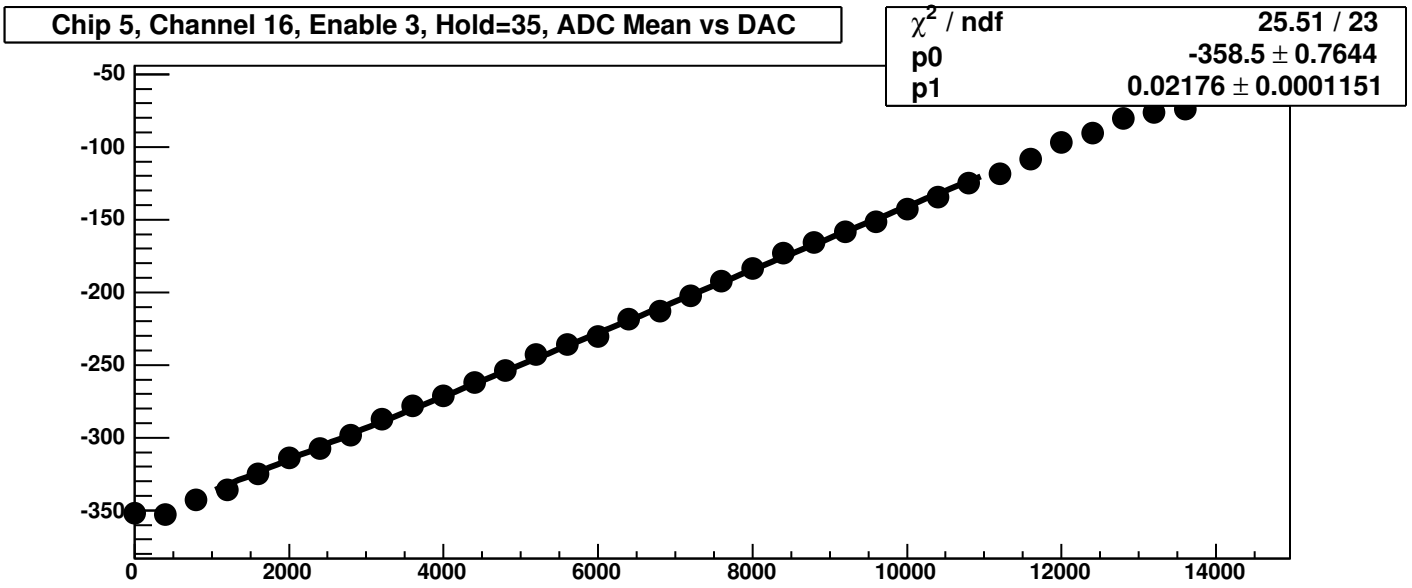

Chip 5, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

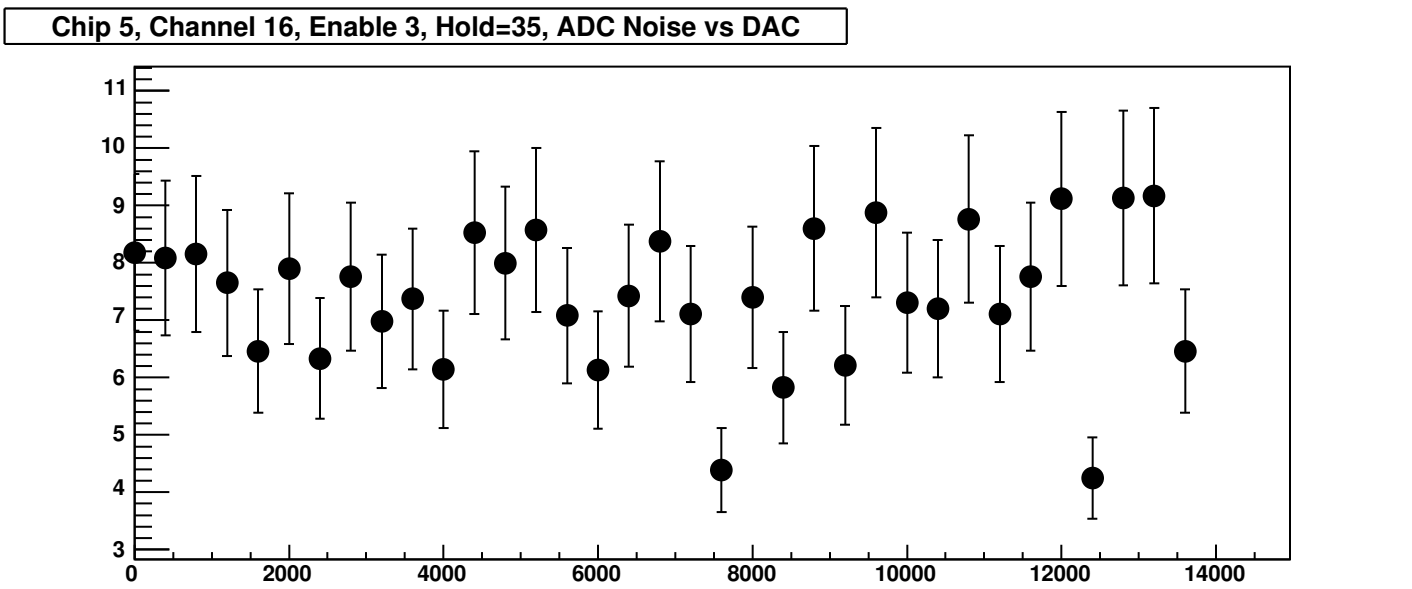

Chip 5, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

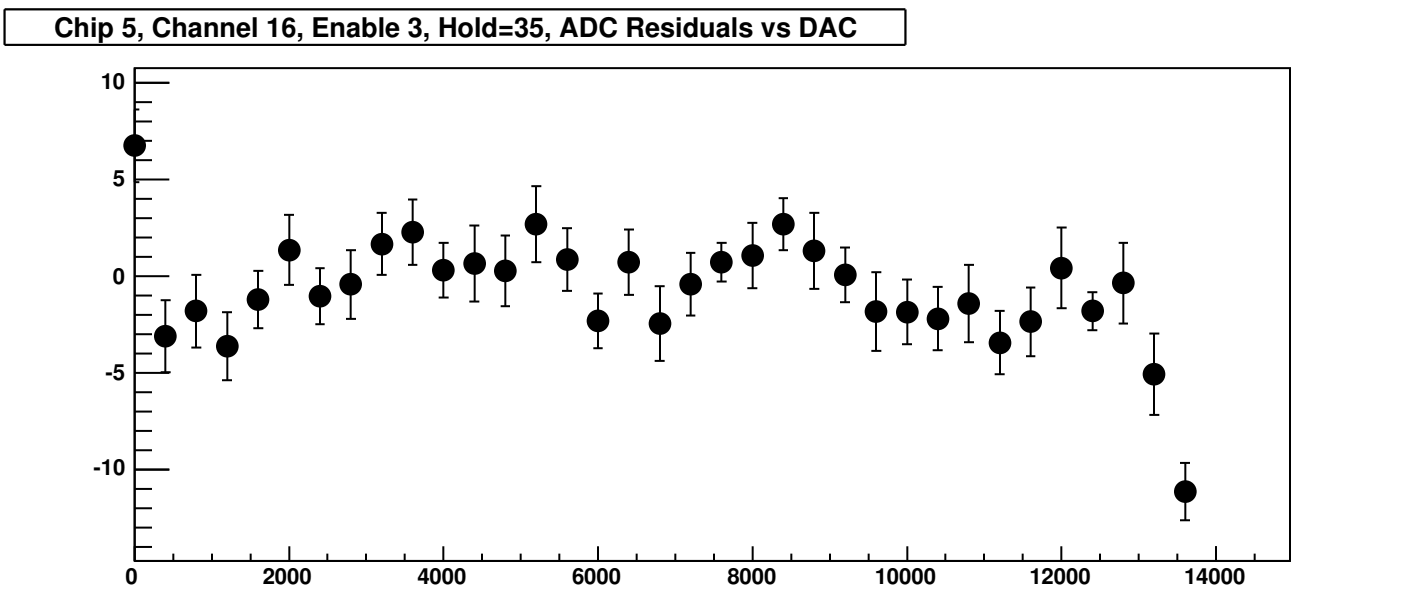

Chip 5, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 471.9 / 23
p0 -231.7 ± 3.378
p1 2.115 ± 0.002293
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

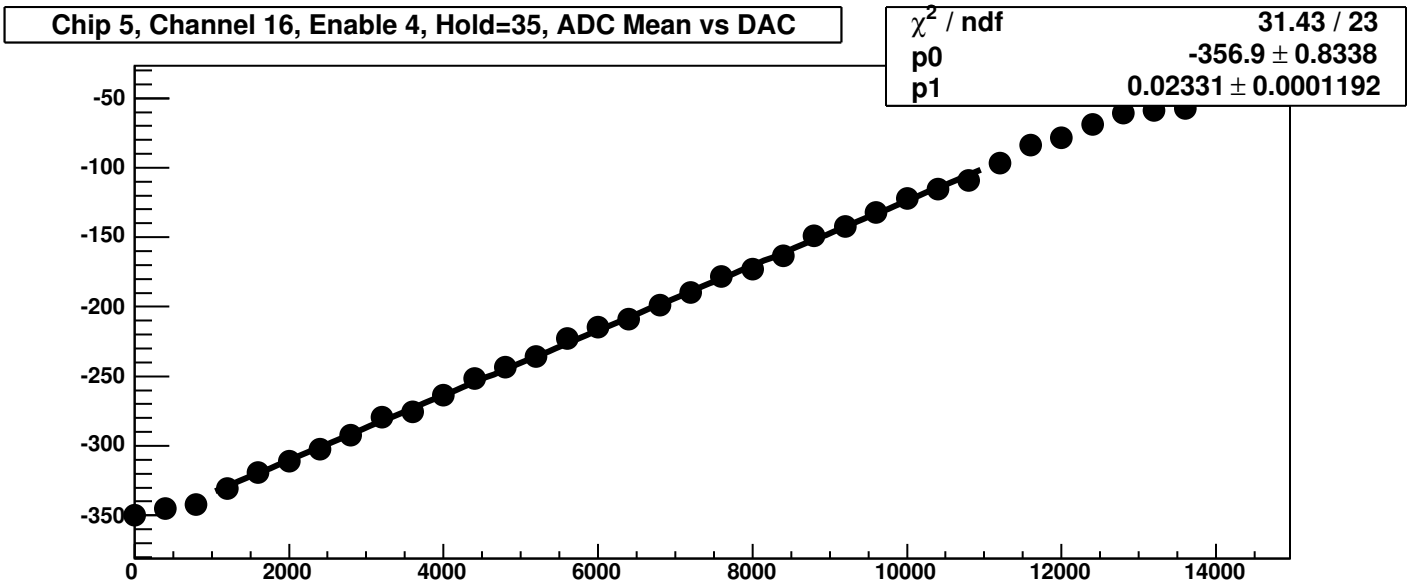

Chip 5, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

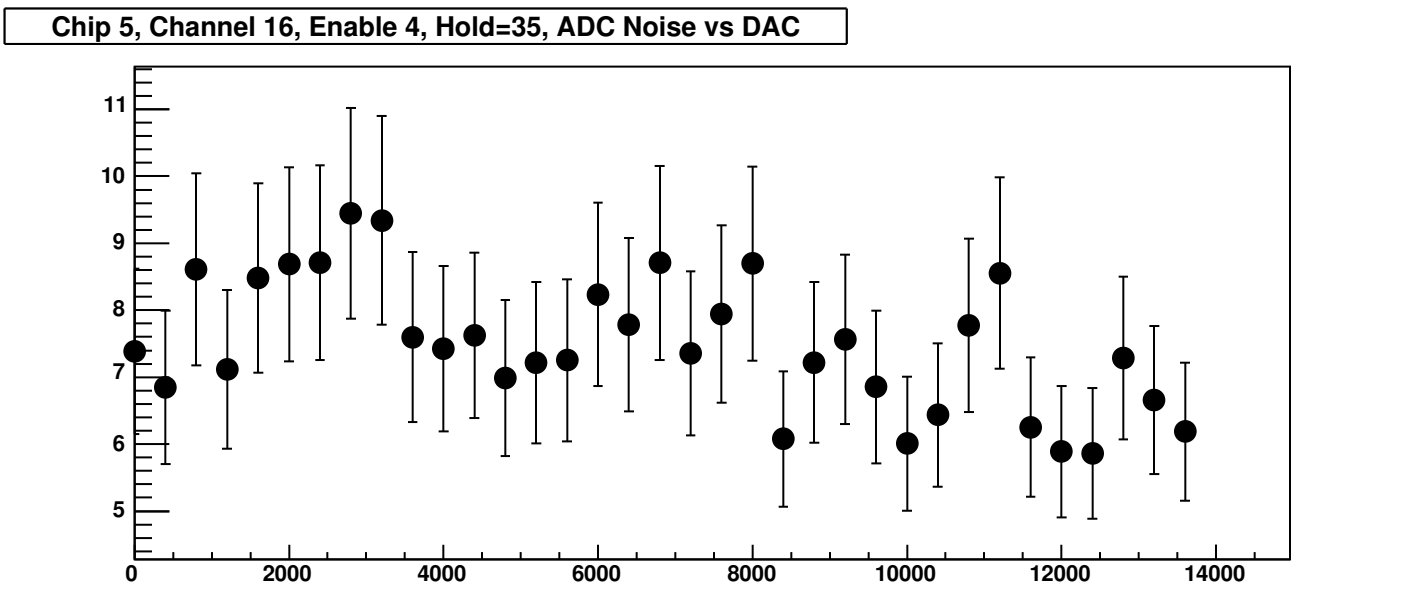

Chip 5, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

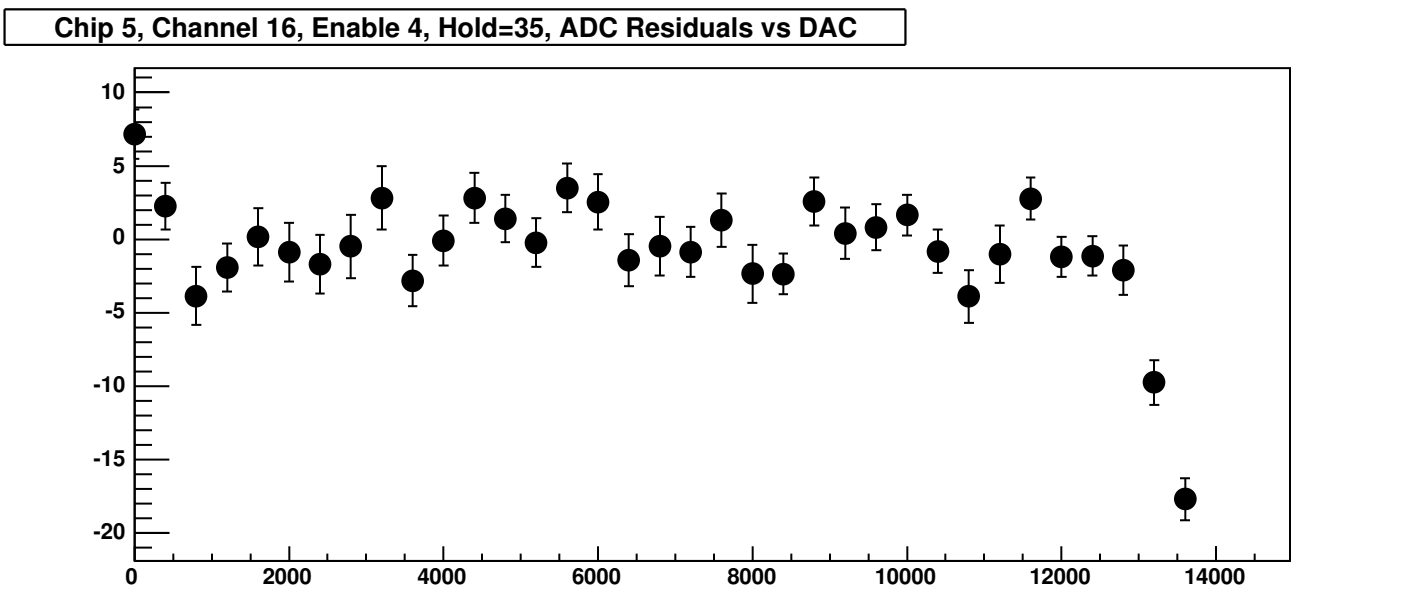

Chip 5, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.51 / 23
p0 -358.5 ± 0.7644
p1 0.02176 ± 0.0001151
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

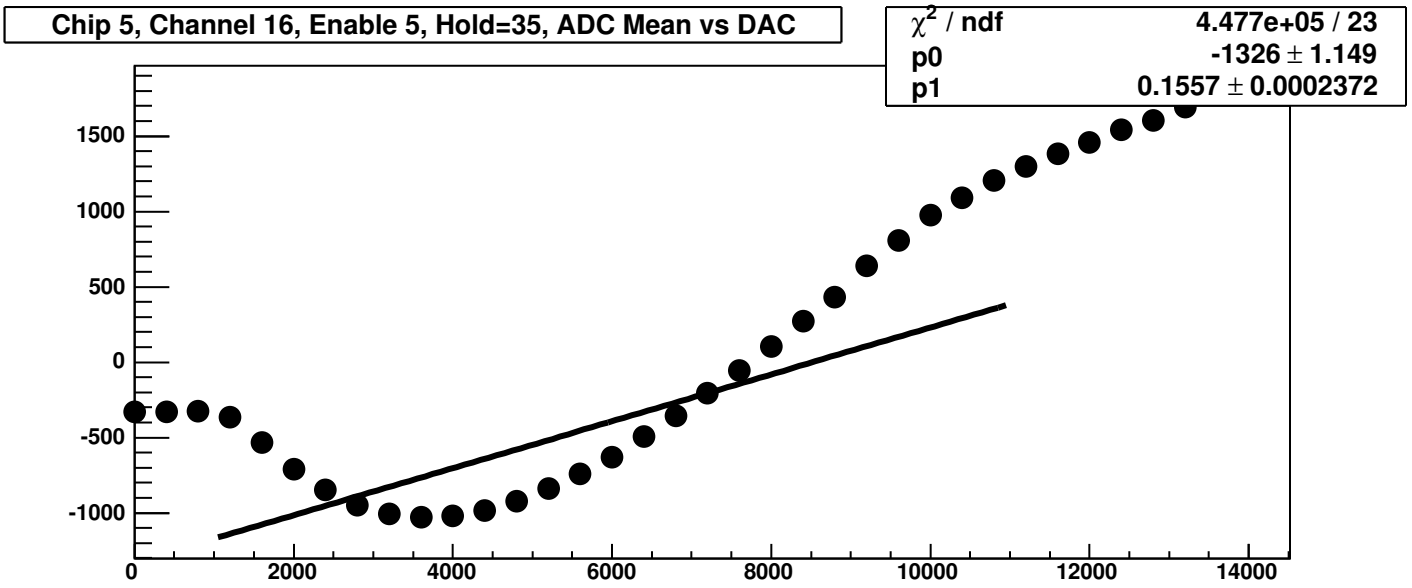

Chip 5, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

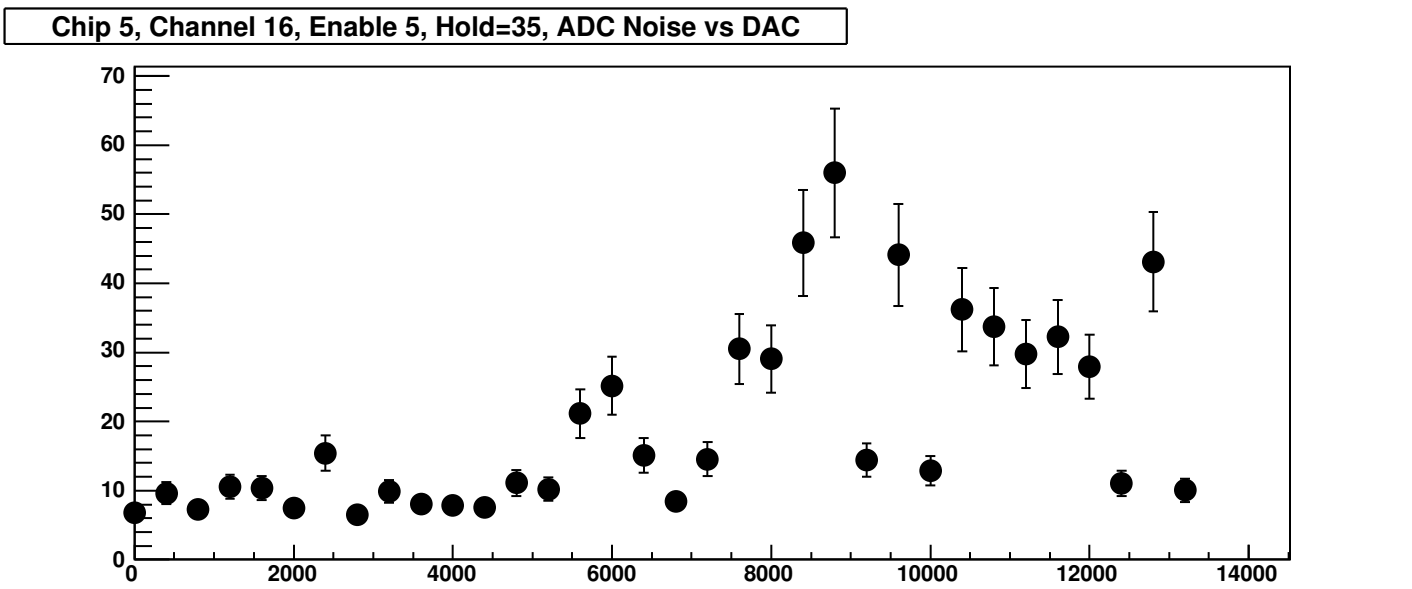

Chip 5, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

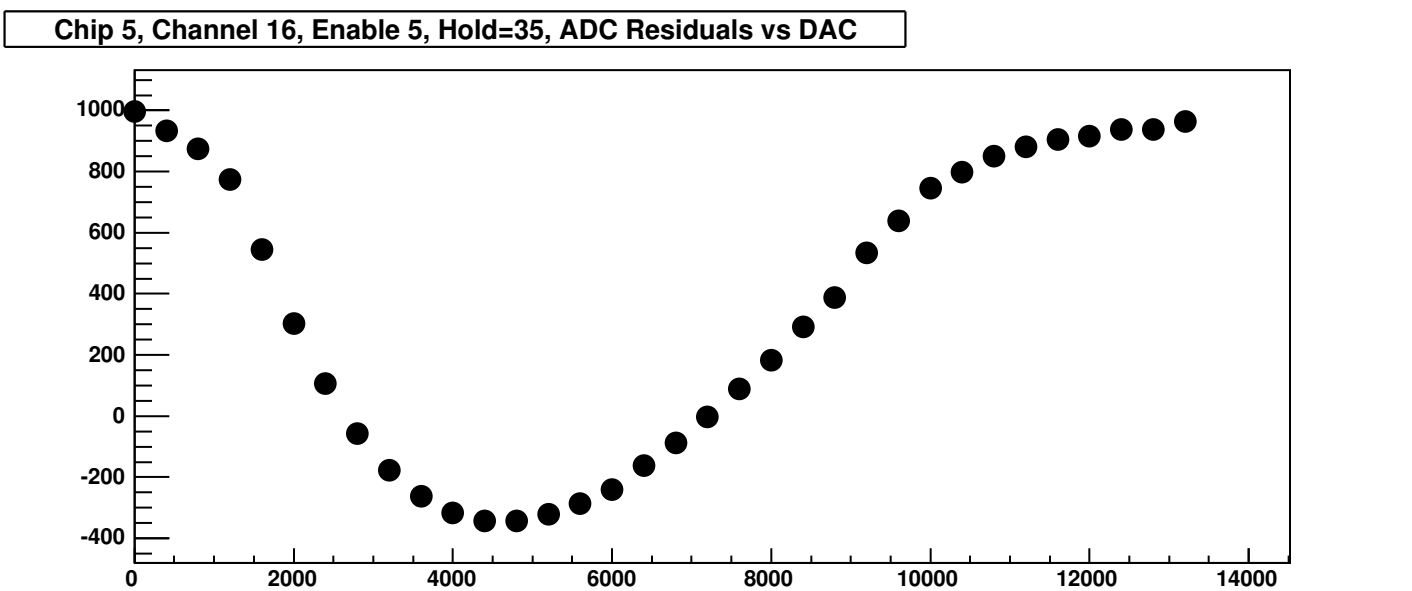

Chip 5, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 31.43 / 23
p0 -356.9 ± 0.8338
p1 0.02331 ± 0.0001192
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

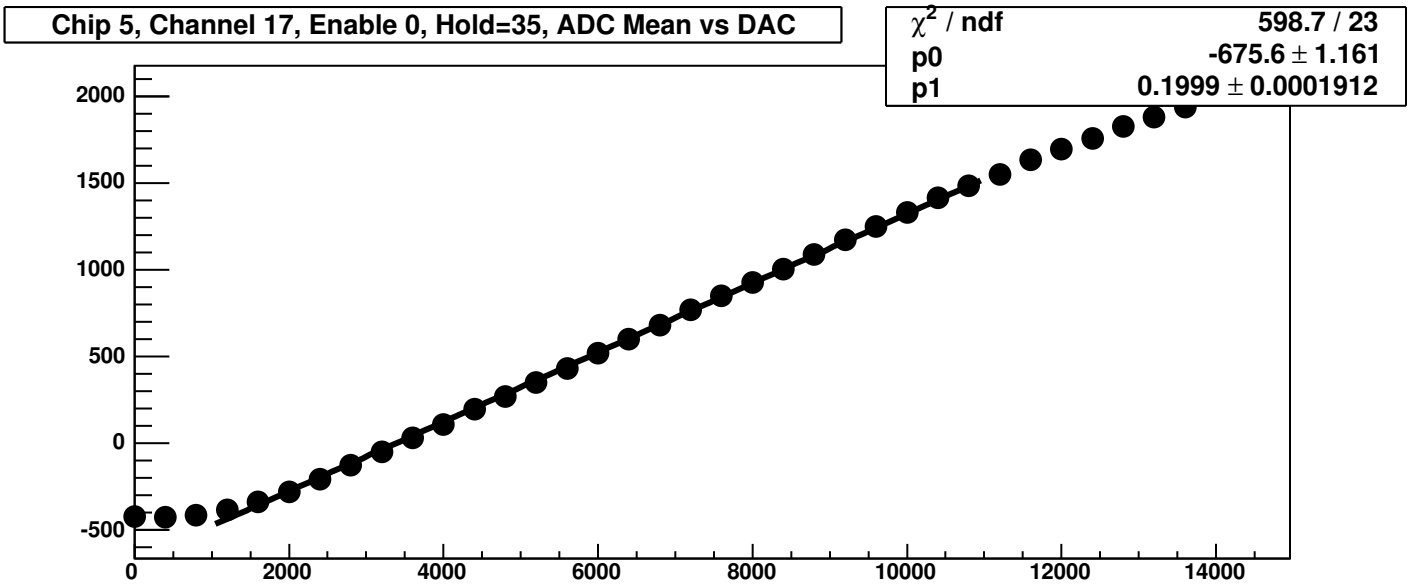

Chip 5, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

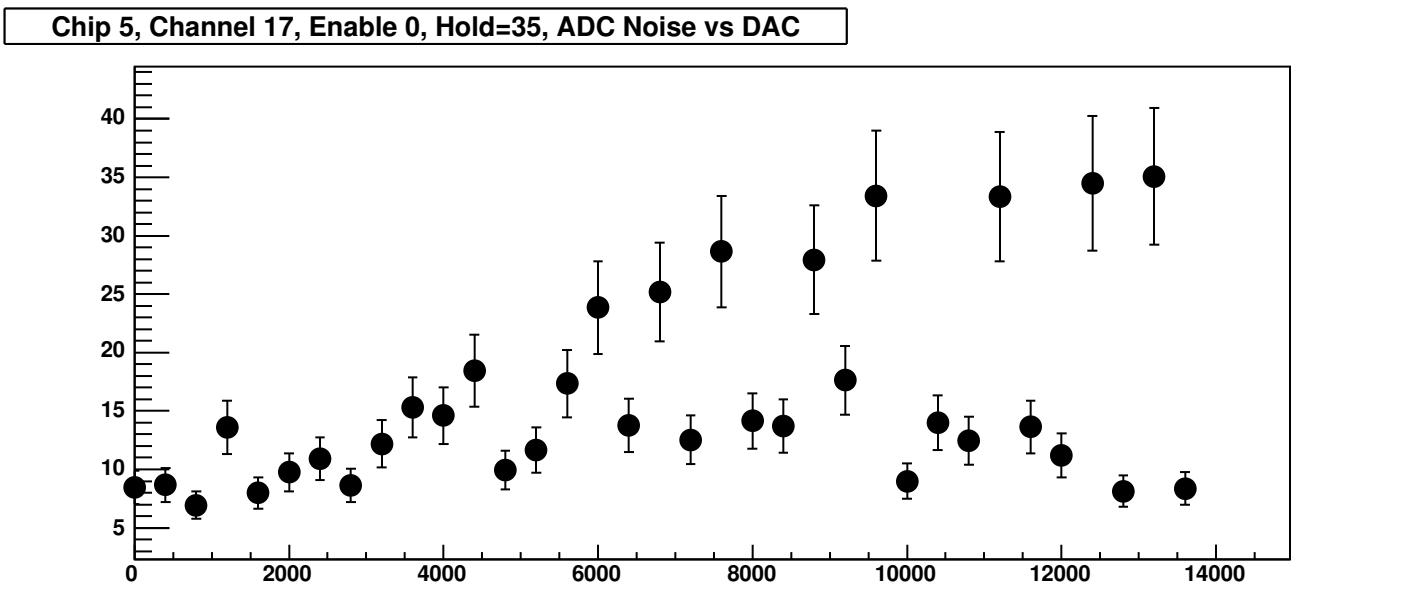

Chip 5, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

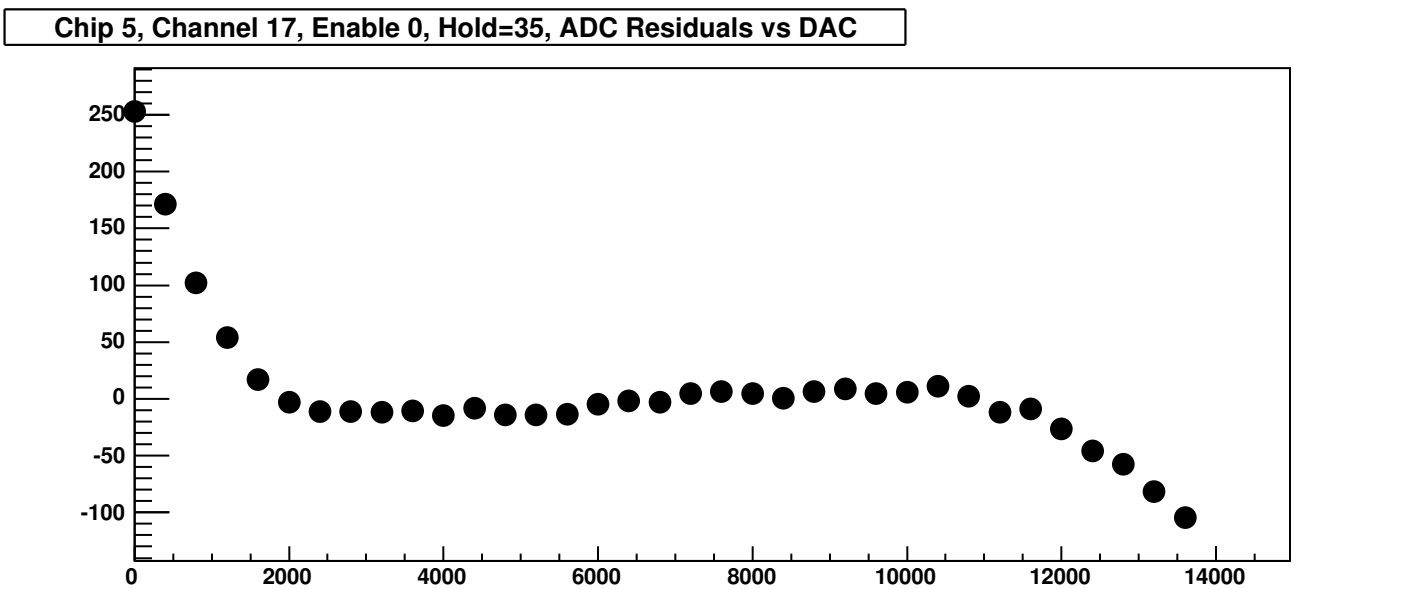

Chip 5, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4.477e+05 / 23
p0 -1326 ± 1.149
p1 0.1557 ± 0.0002372
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

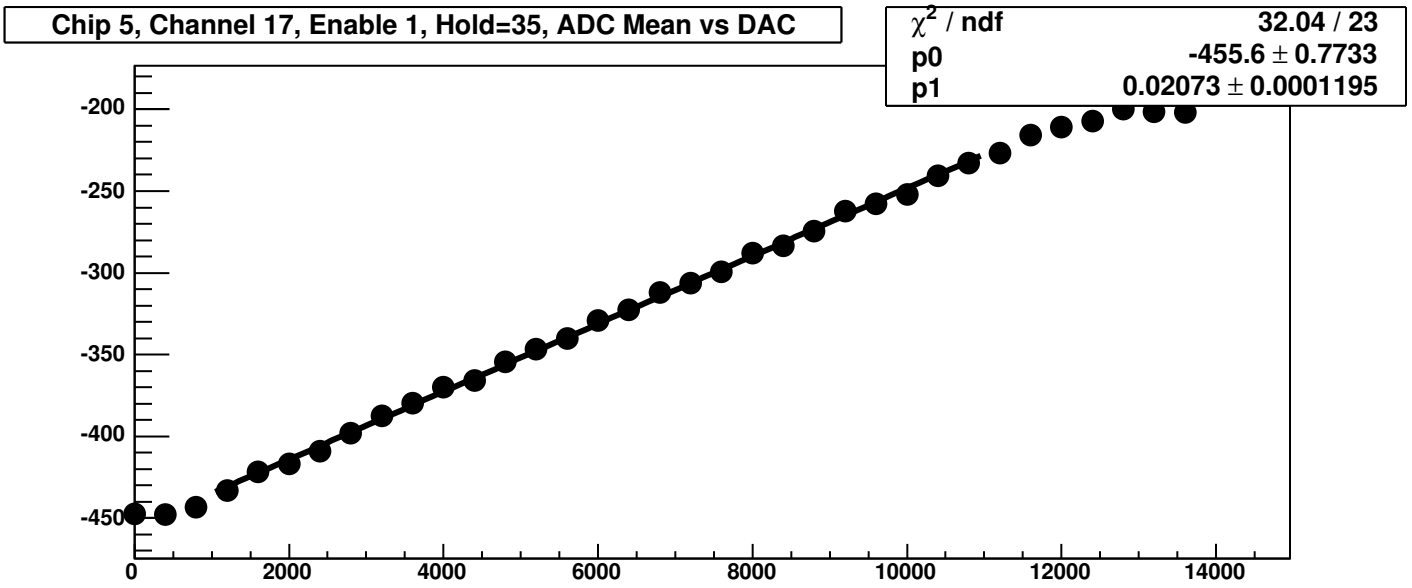

Chip 5, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

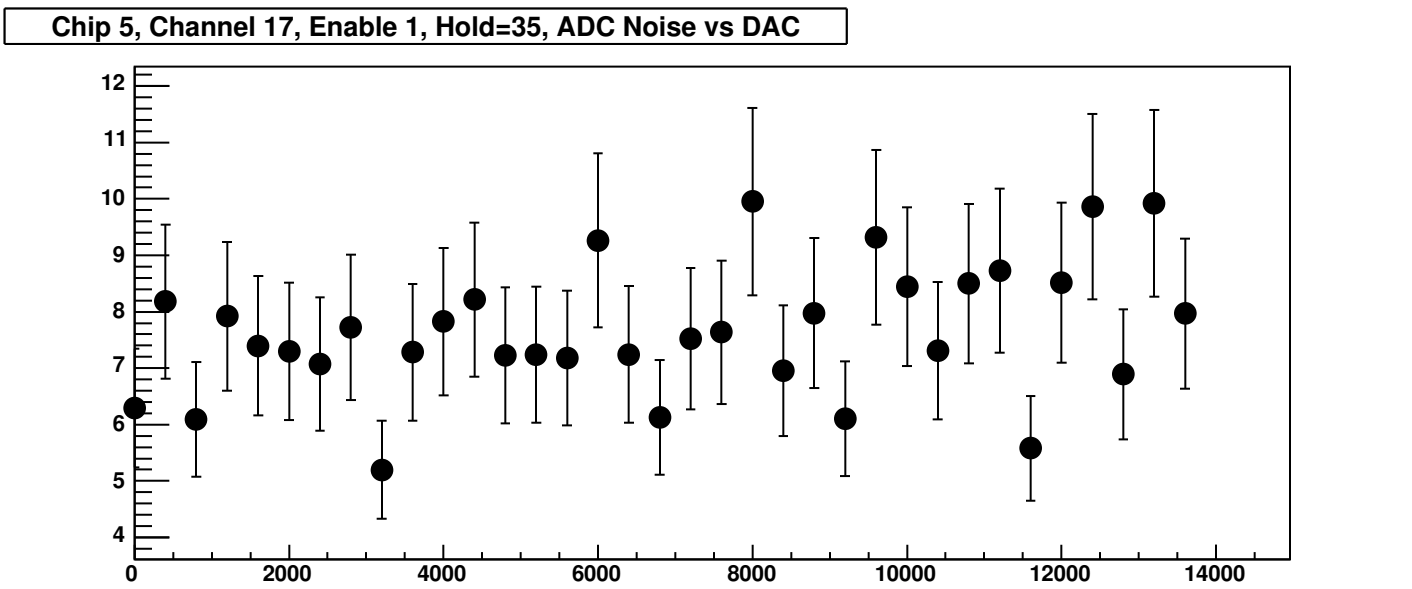

Chip 5, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

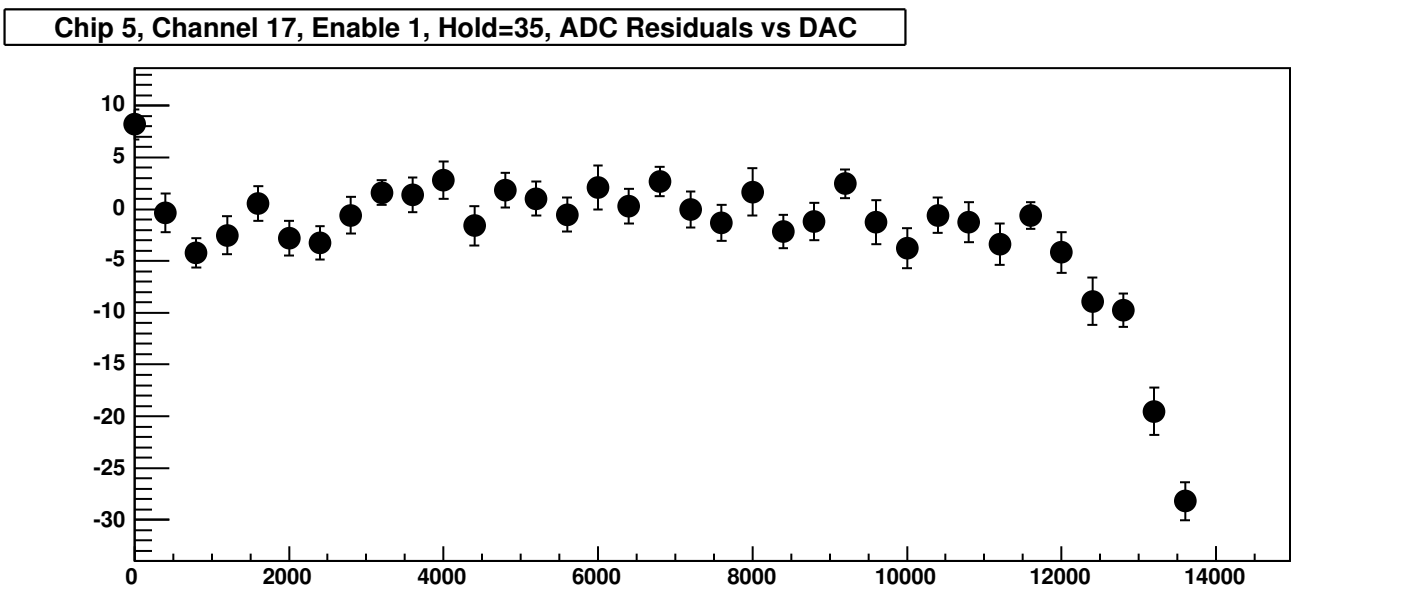

Chip 5, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 598.7 / 23
p0 -675.6 ± 1.161
p1 0.1999 ± 0.0001912
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

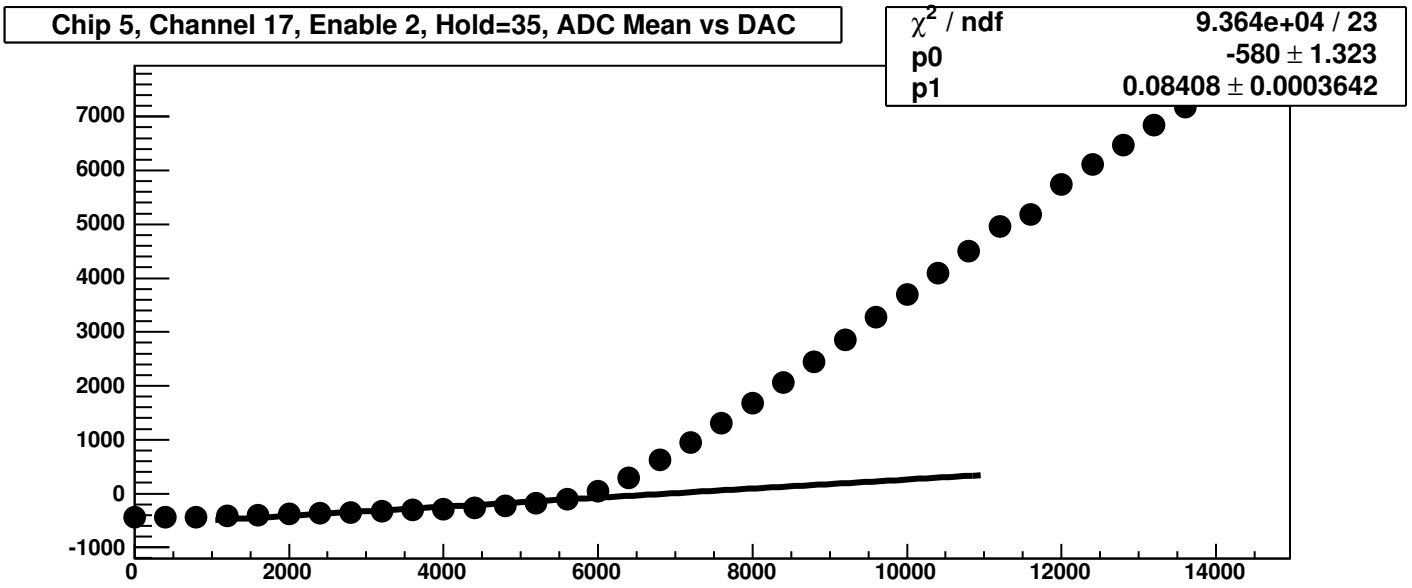

Chip 5, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

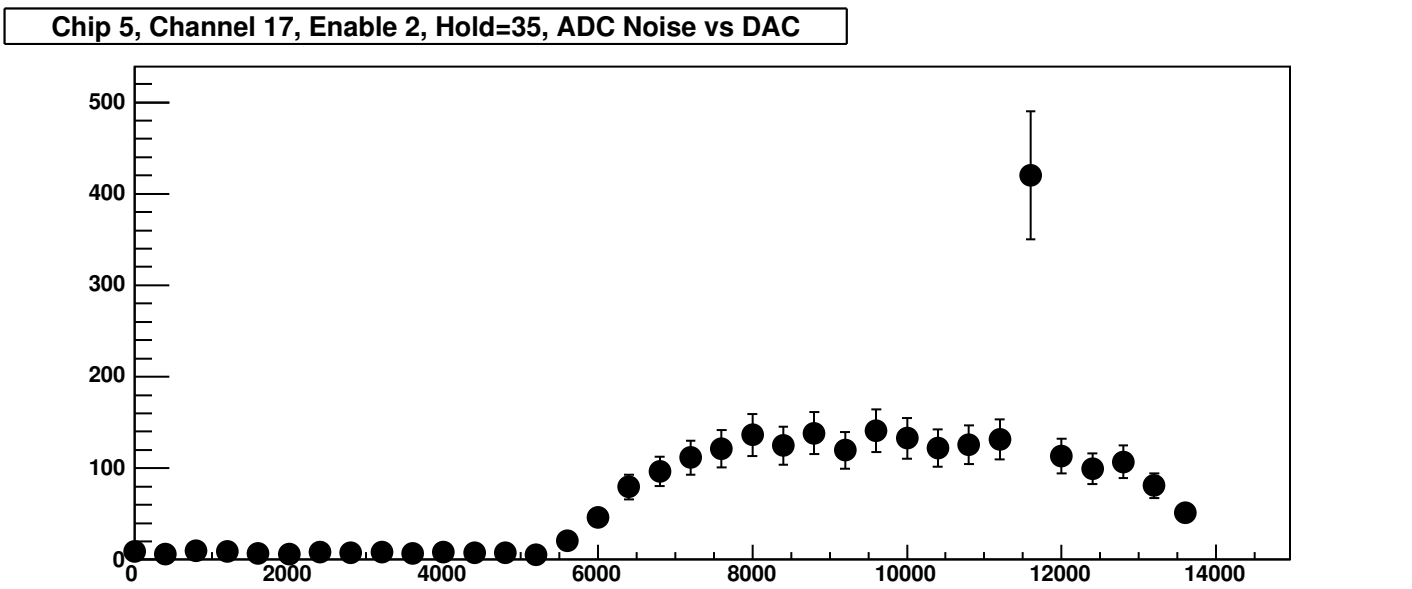

Chip 5, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

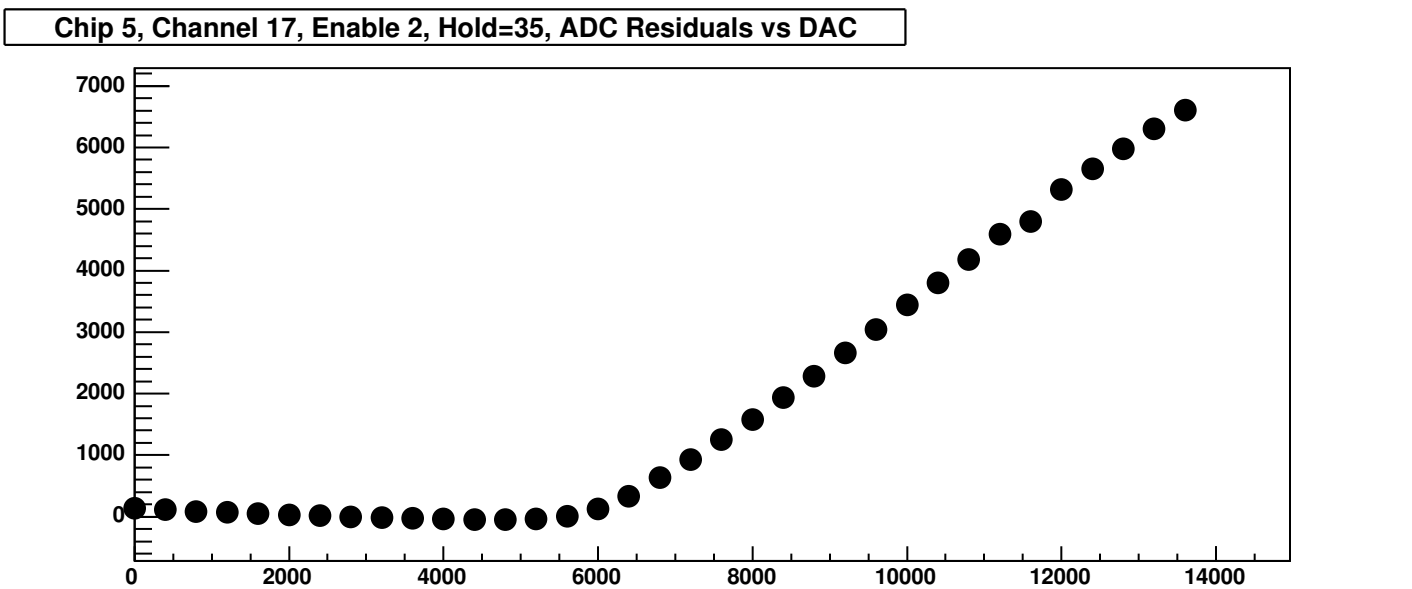

Chip 5, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.04 / 23
p0 -455.6 ± 0.7733
p1 0.02073 ± 0.0001195
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

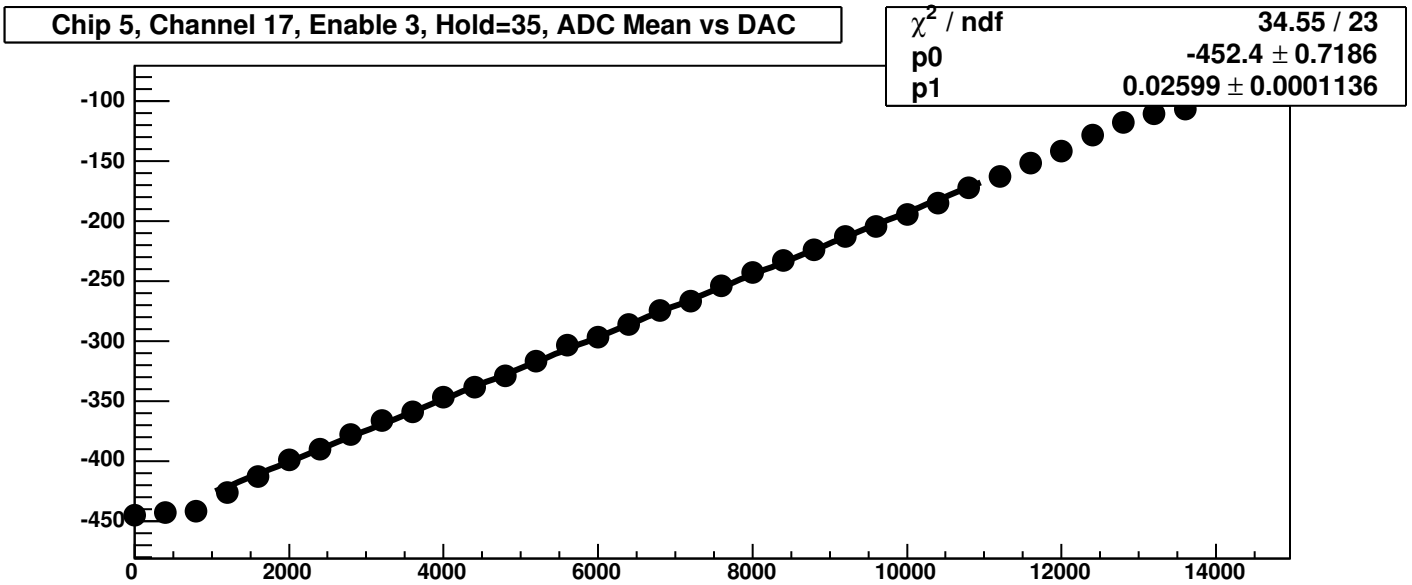

Chip 5, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

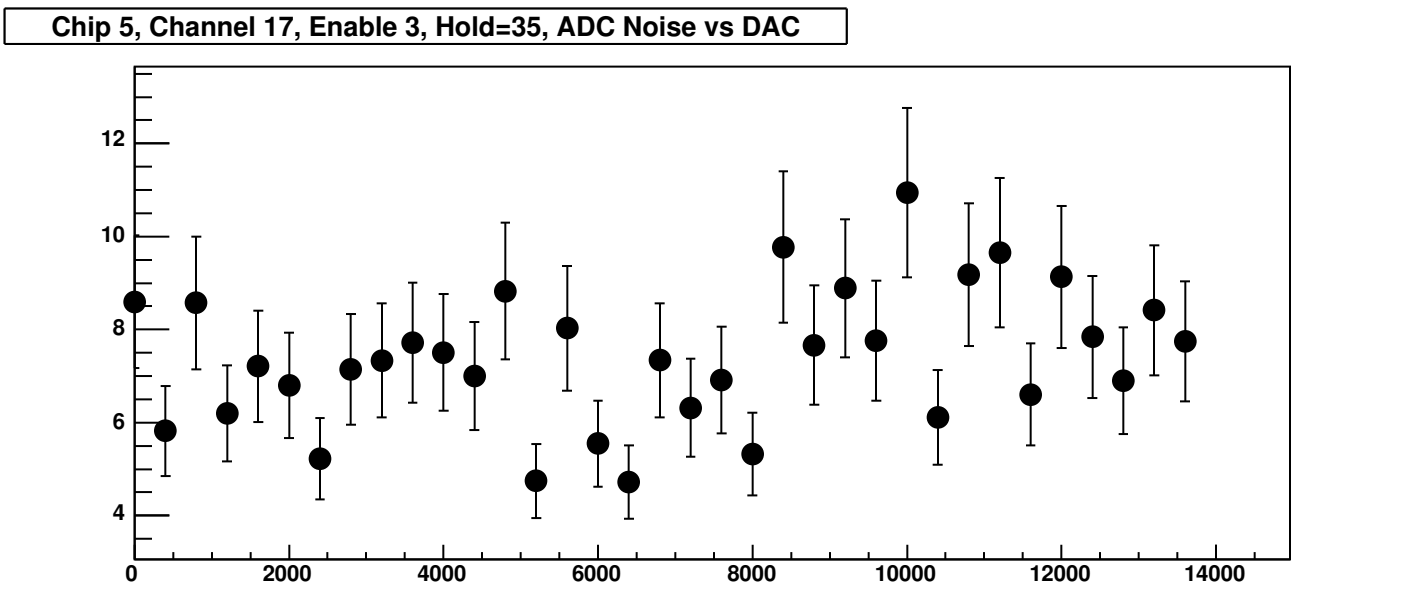

Chip 5, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
0
2000
4000
6000
8000
10000
12000
14000

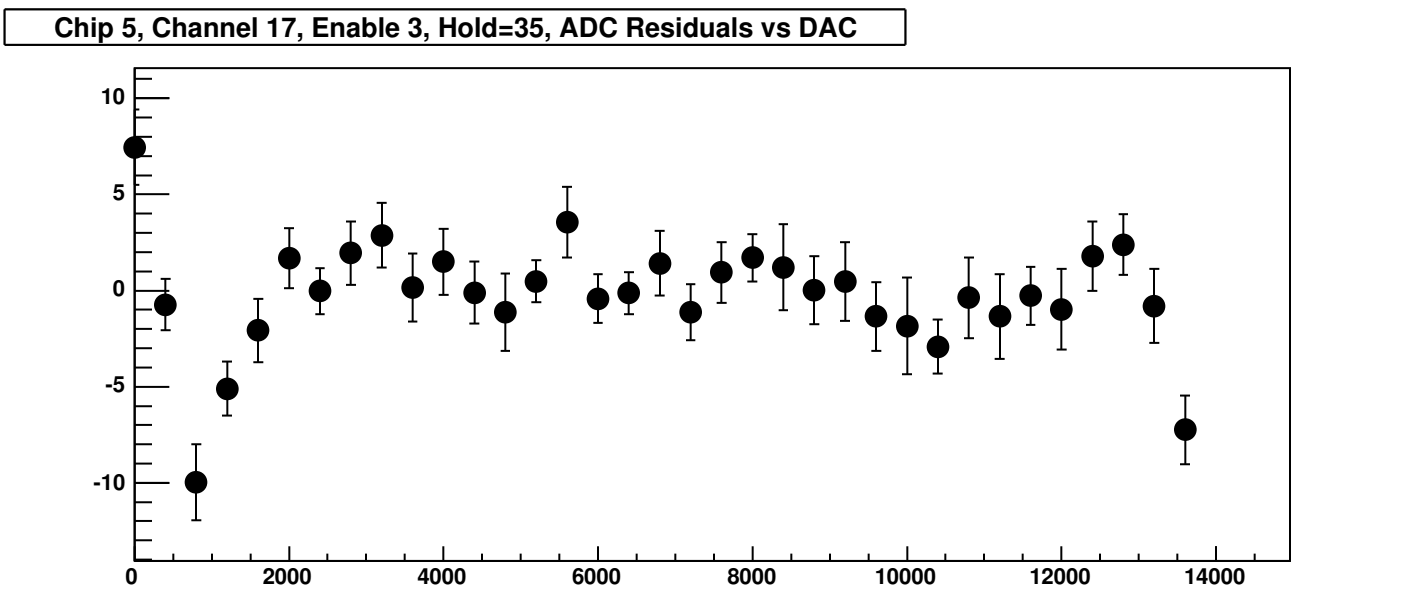

Chip 5, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 9.364e+04 / 23
p0 -580 ± 1.323
p1 0.08408 ± 0.0003642
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

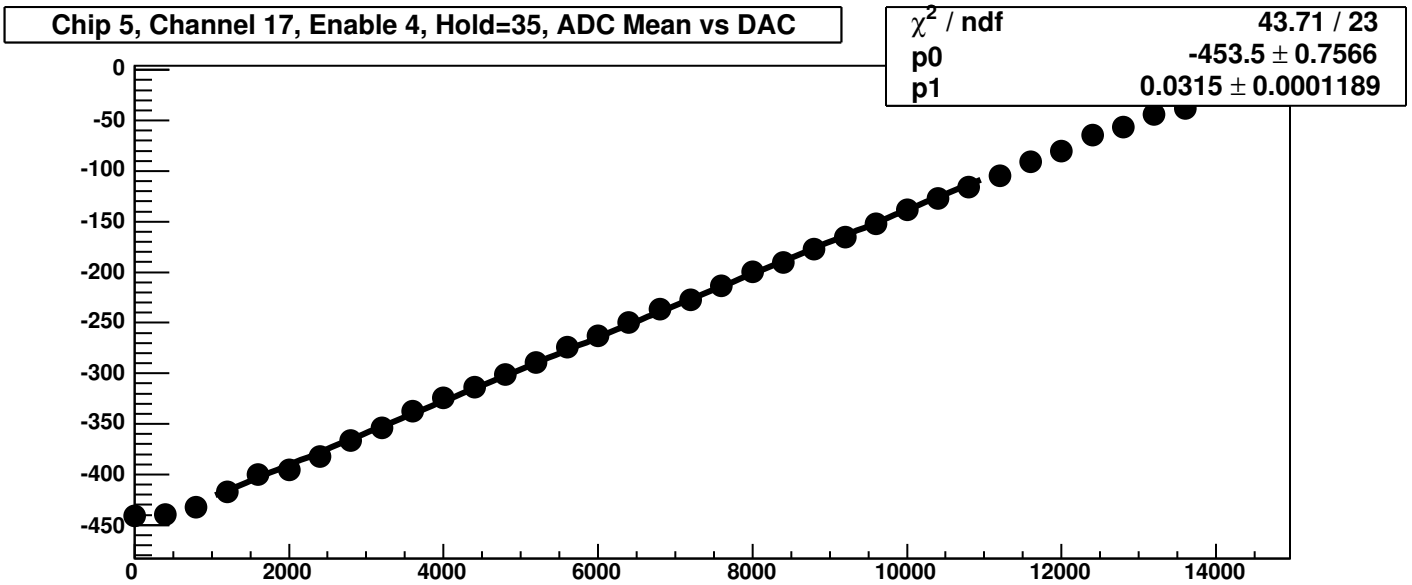

Chip 5, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

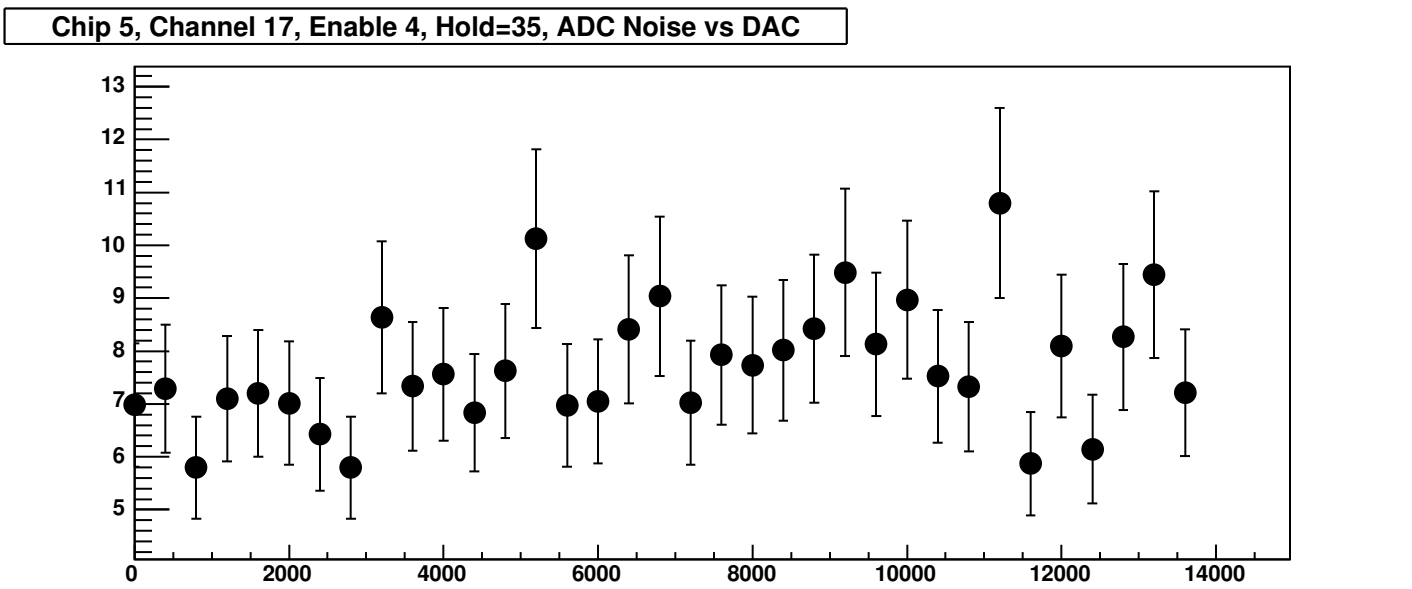

Chip 5, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
7000
6000
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

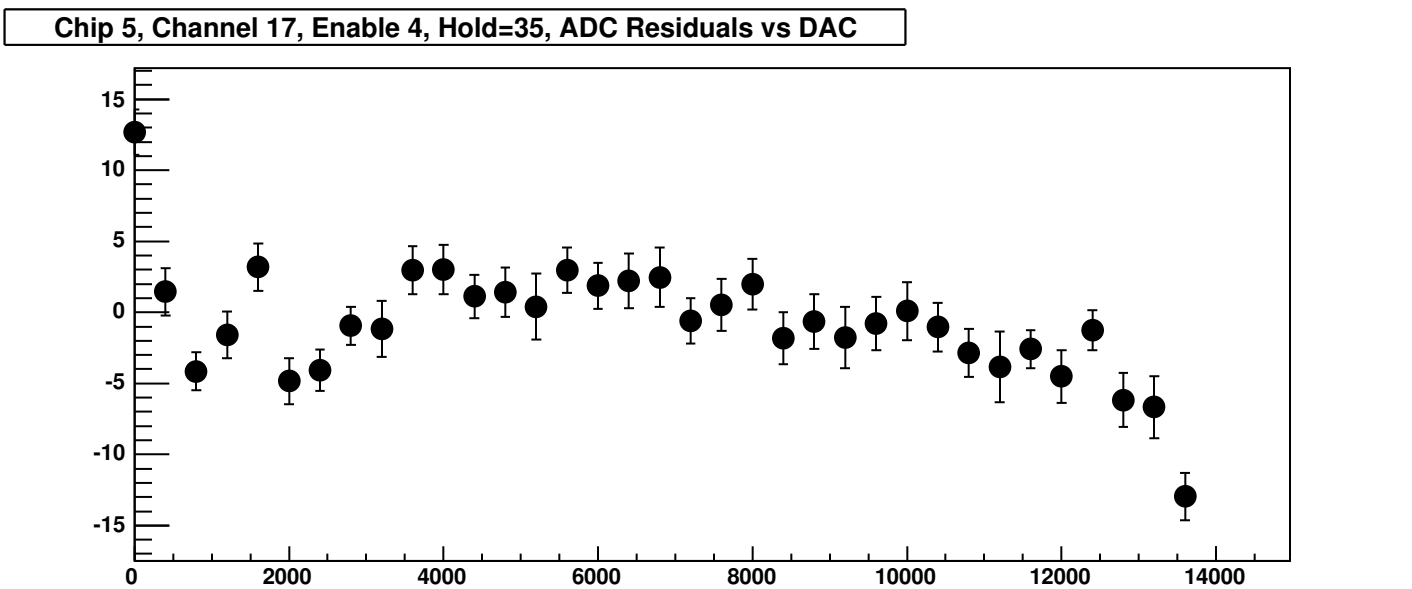

Chip 5, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 34.55 / 23
p0 -452.4 ± 0.7186
p1 0.02599 ± 0.0001136
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

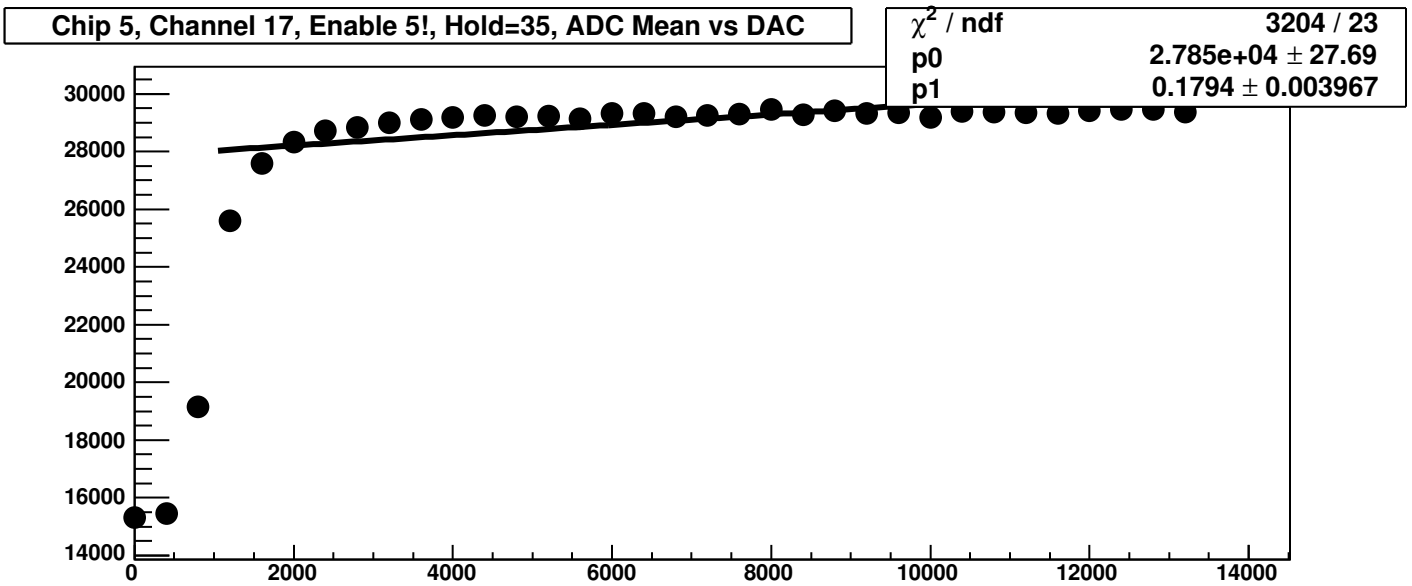

Chip 5, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

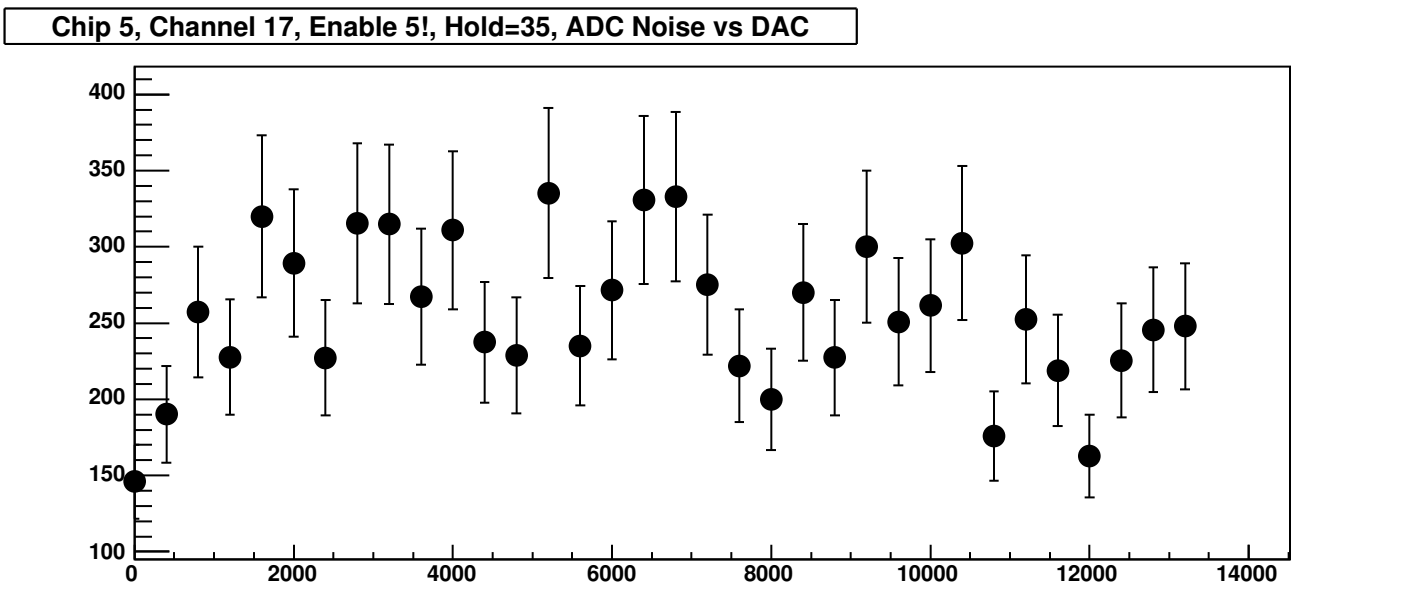

Chip 5, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

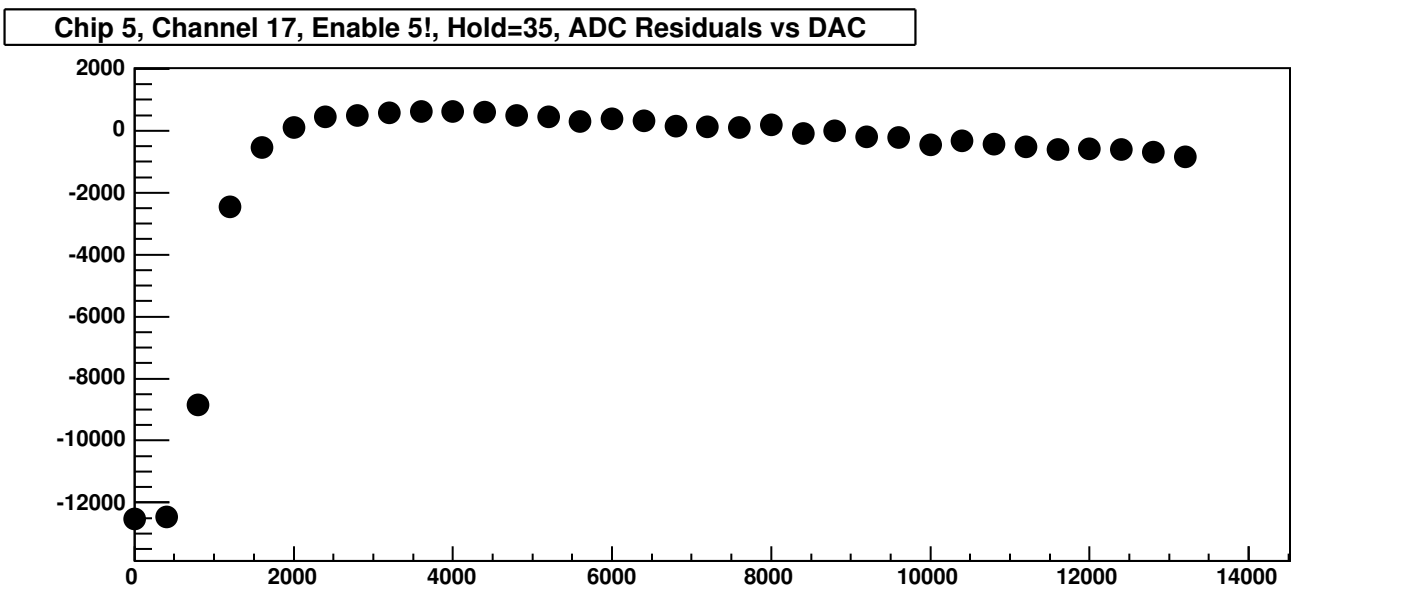

Chip 5, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ^2 / ndf 43.71 / 23
p0 -453.5 ± 0.7566
p1 0.0315 ± 0.0001189
0
-50
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

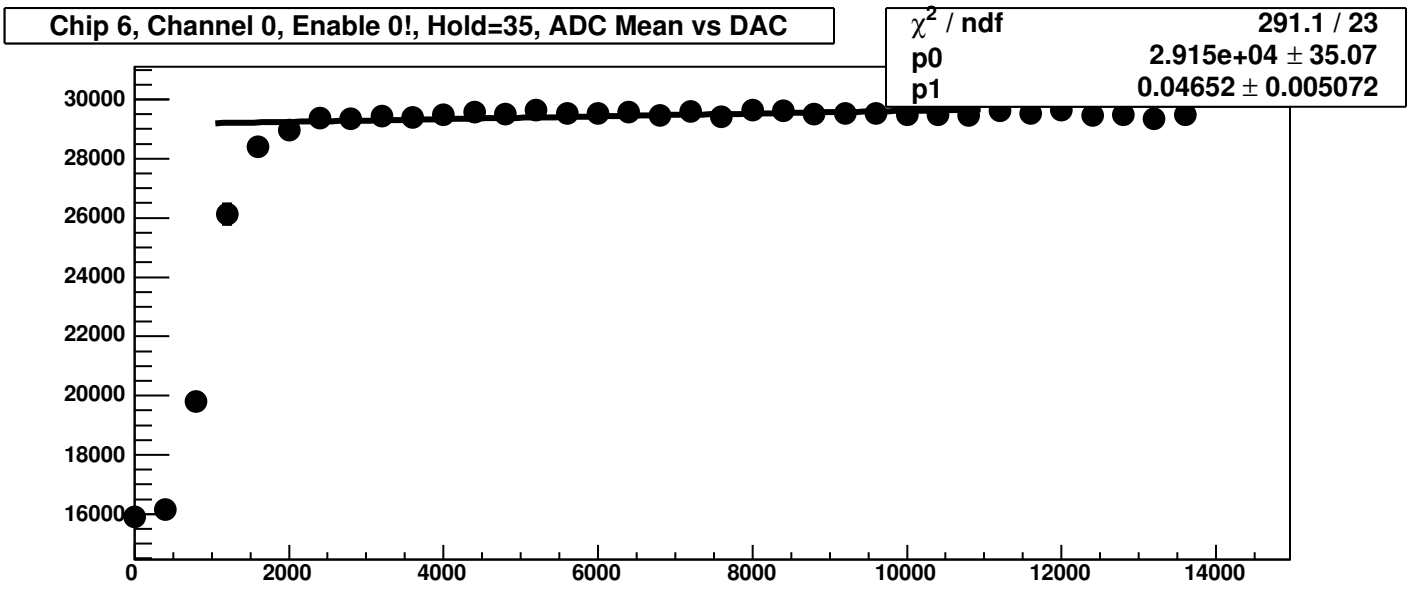

Chip 5, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

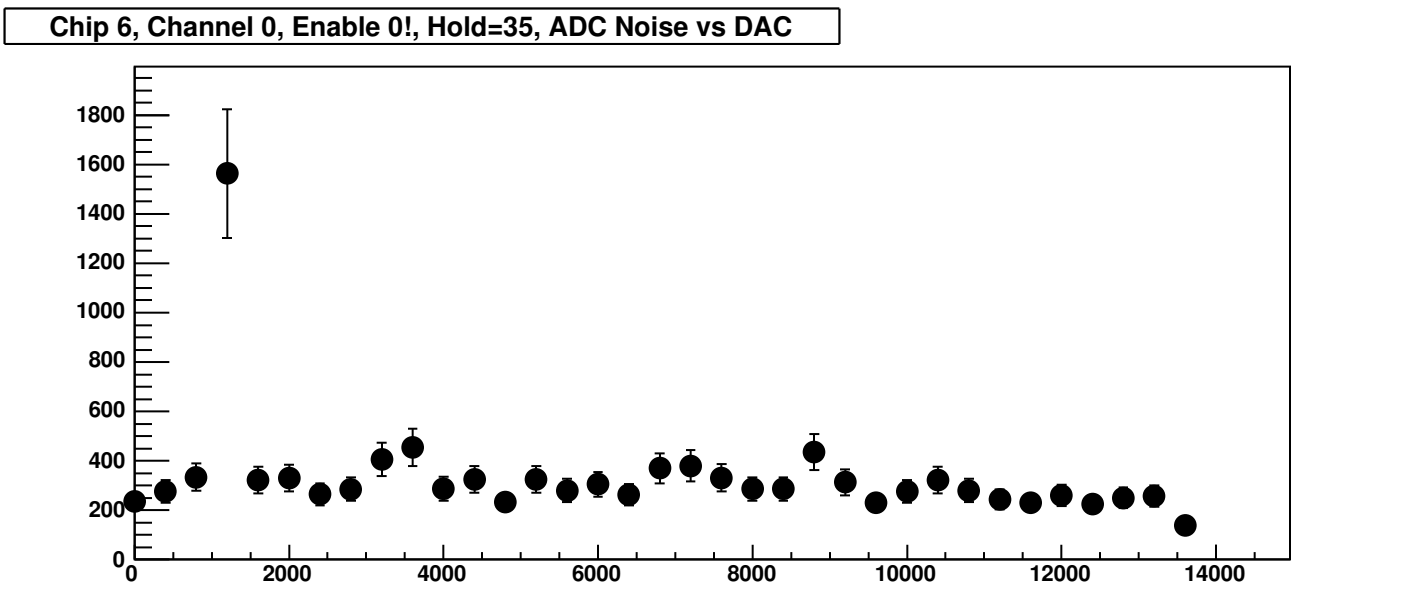

Chip 5, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

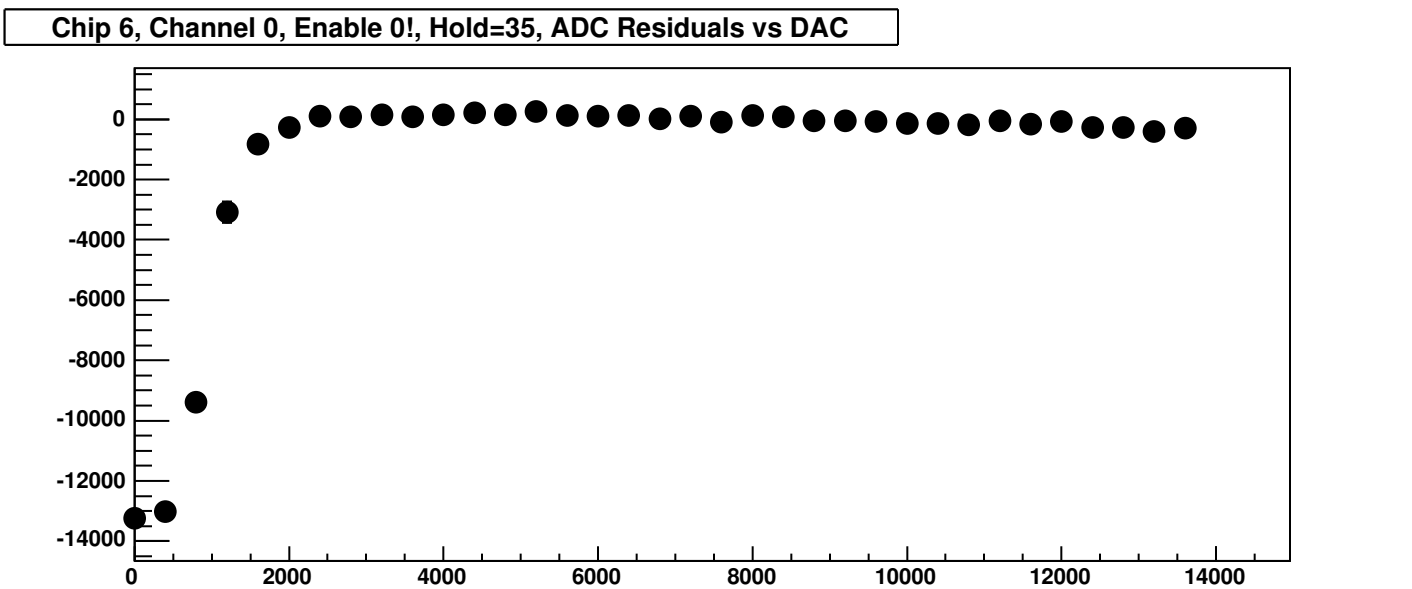

Chip 5, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3204 / 23
p0 2.785e+04 ± 27.69
p1 0.1794 ± 0.003967
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000


Chip 5, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

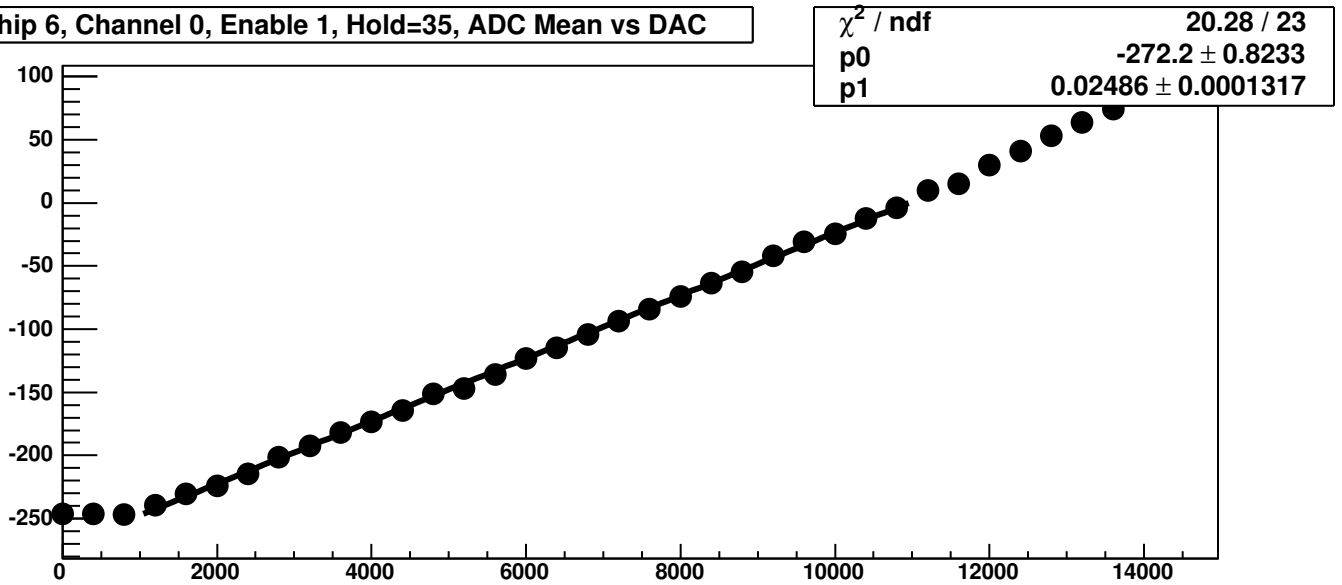

Chip 5, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
0
-2000
-4000
-6000
-8000
-10000
-12000
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 291.1 / 23
p0 2.915e+04 ± 35.07
p1 0.04652 ± 0.005072
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

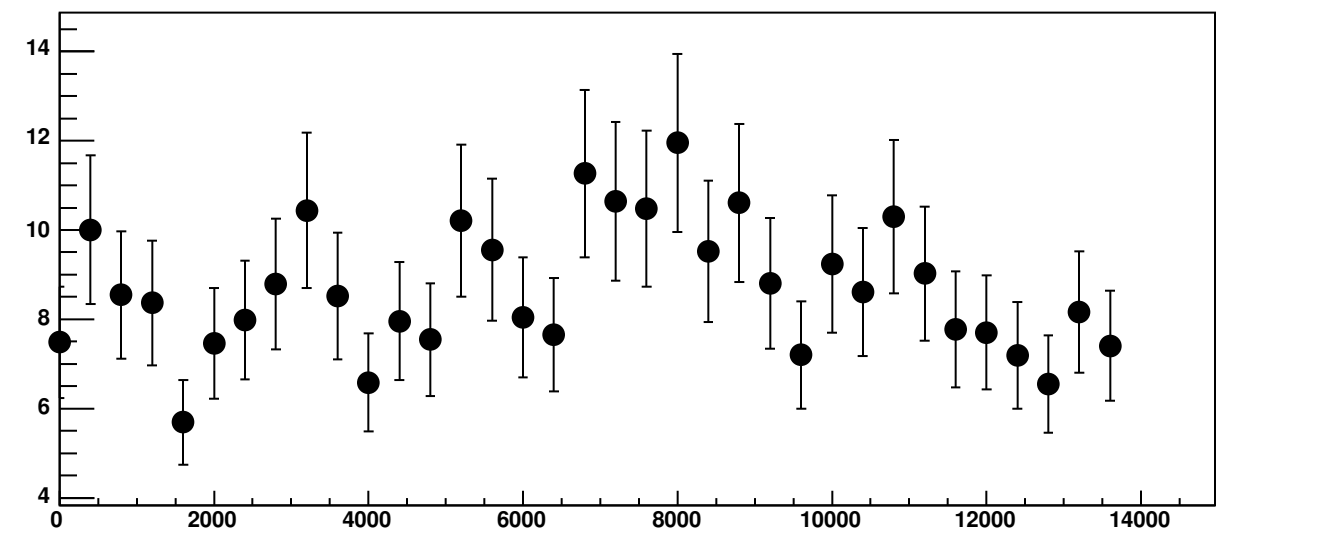

Chip 6, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

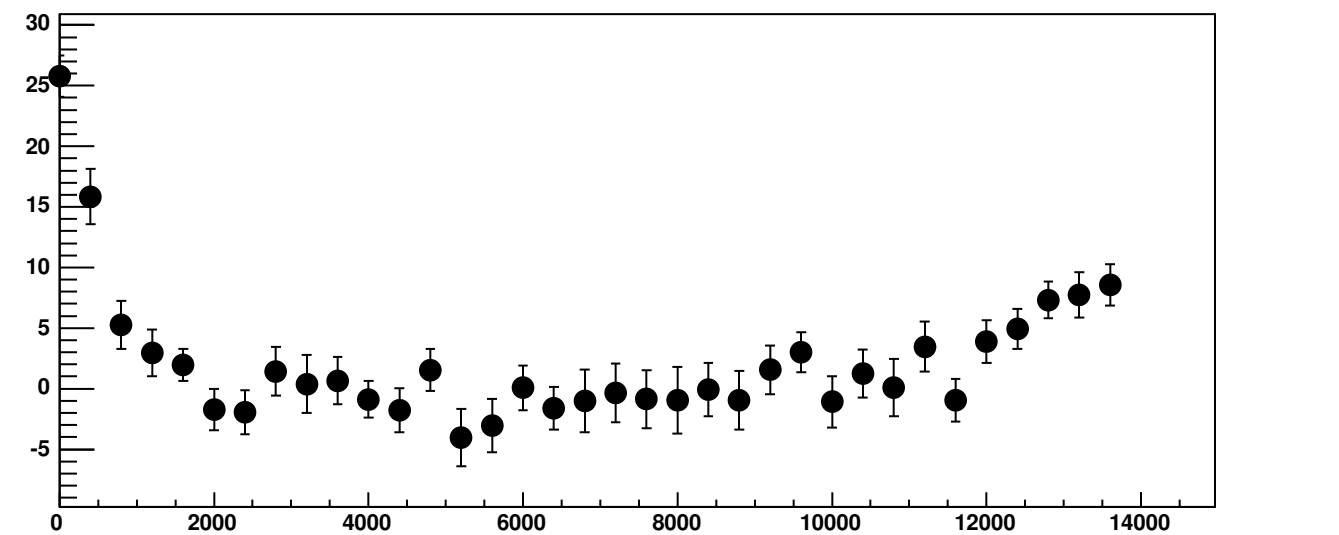
Chip 6, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

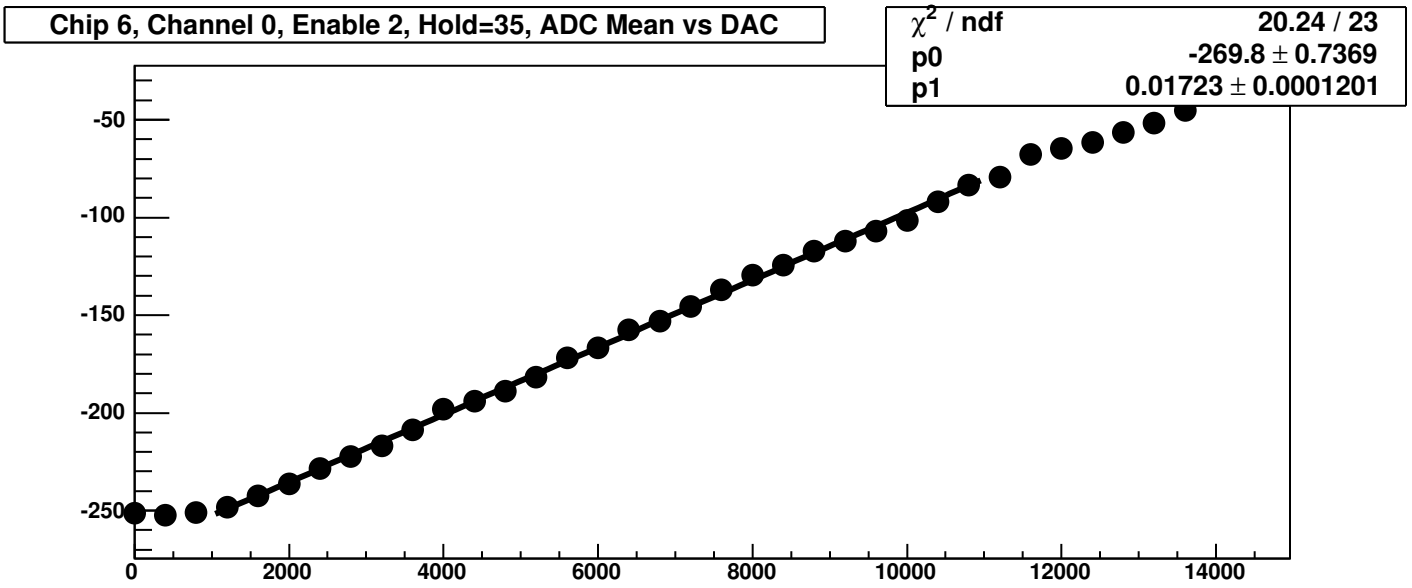


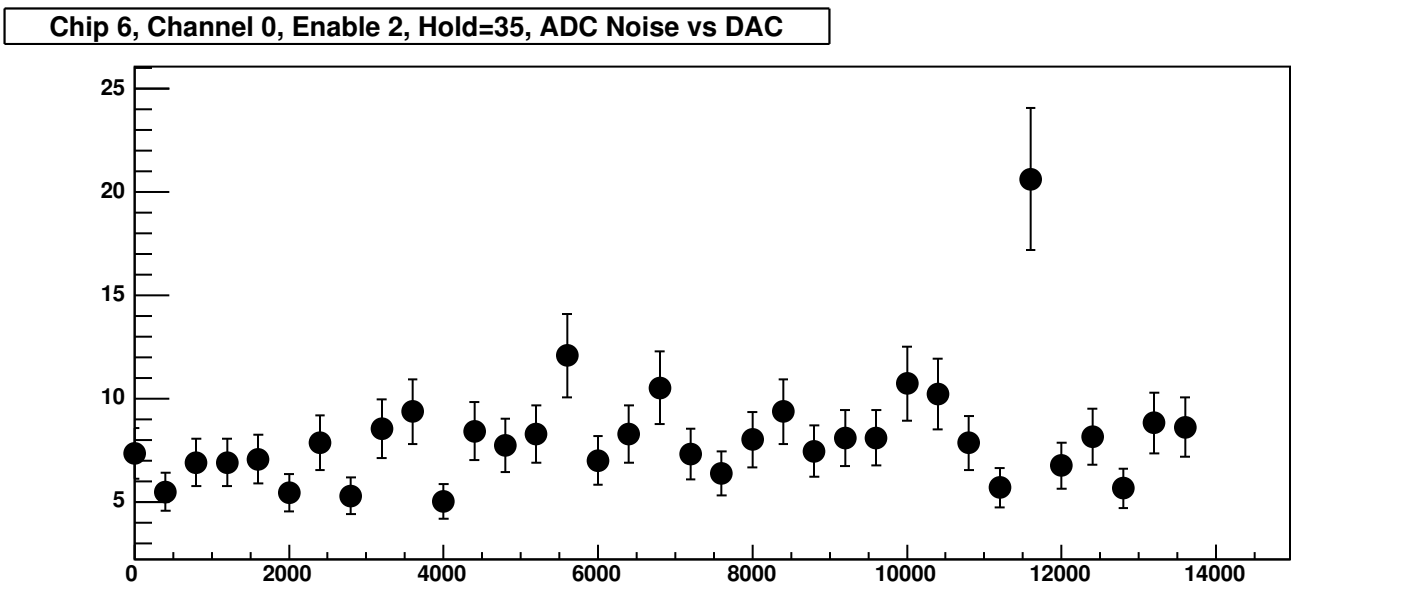
Chip 6, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC

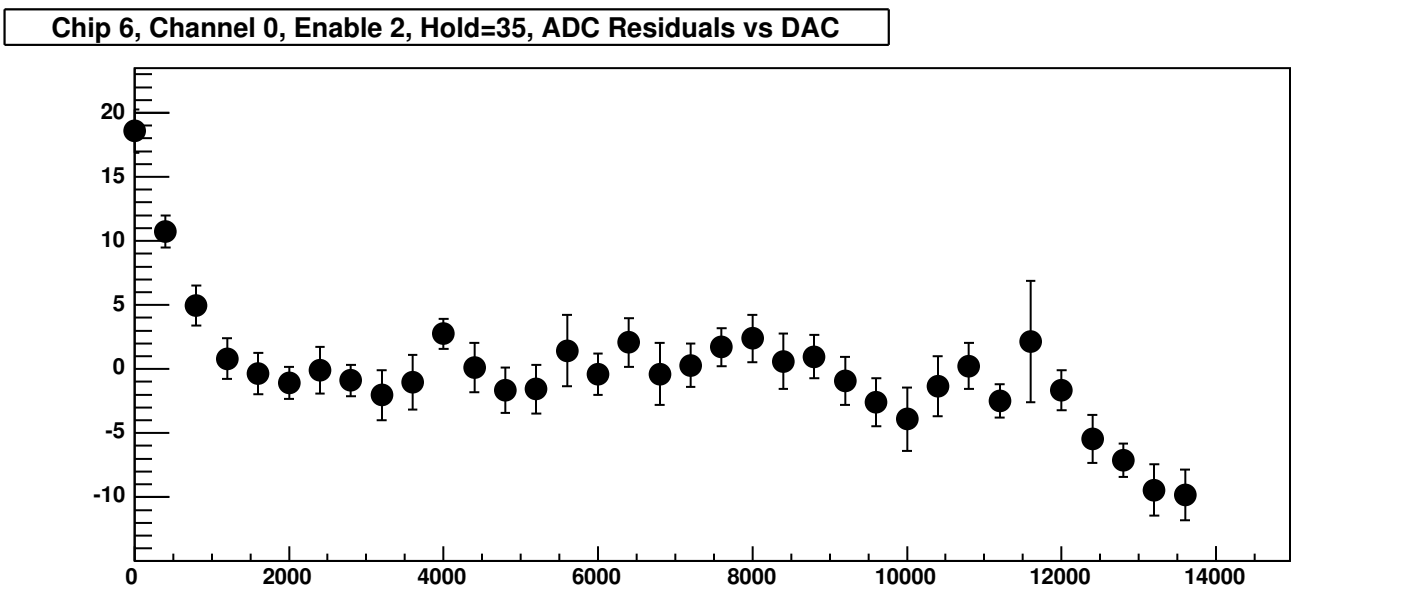


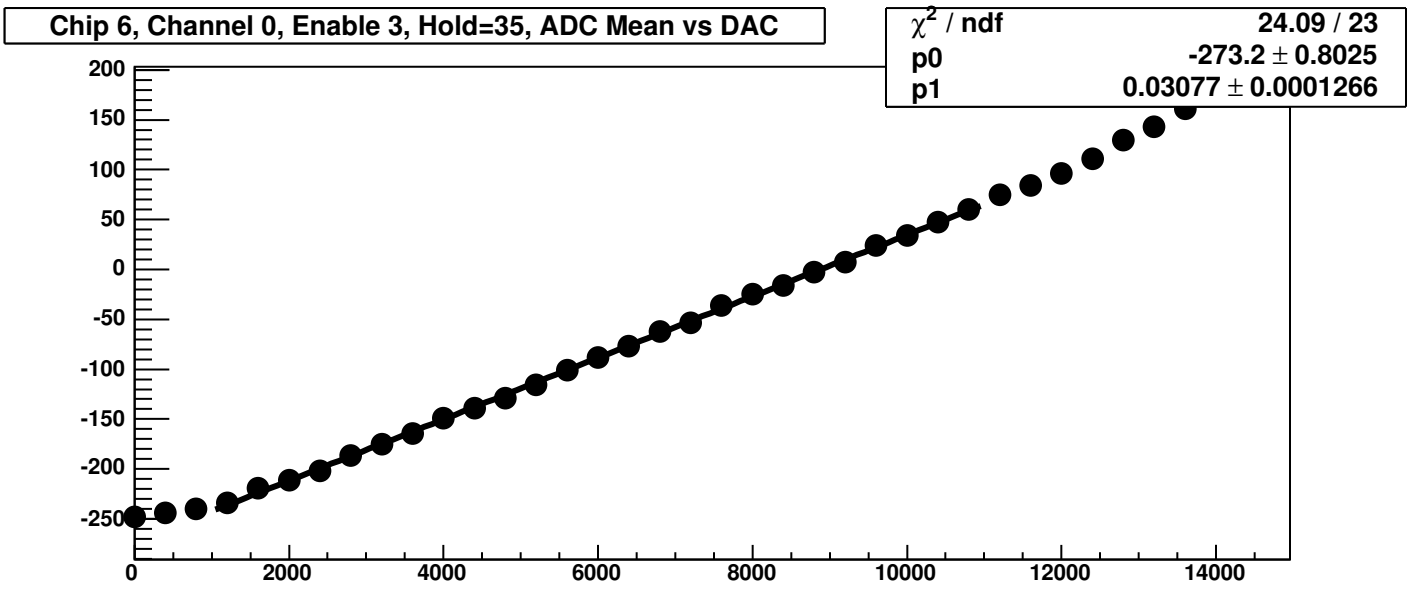
Chip 6, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

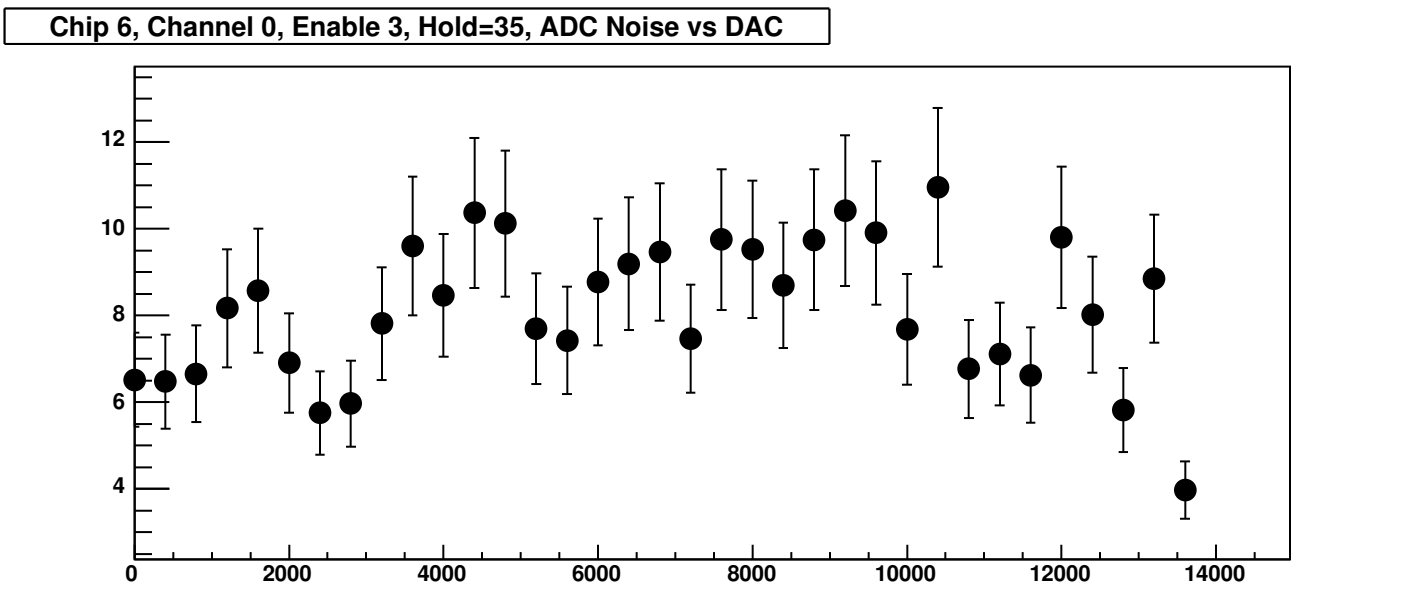


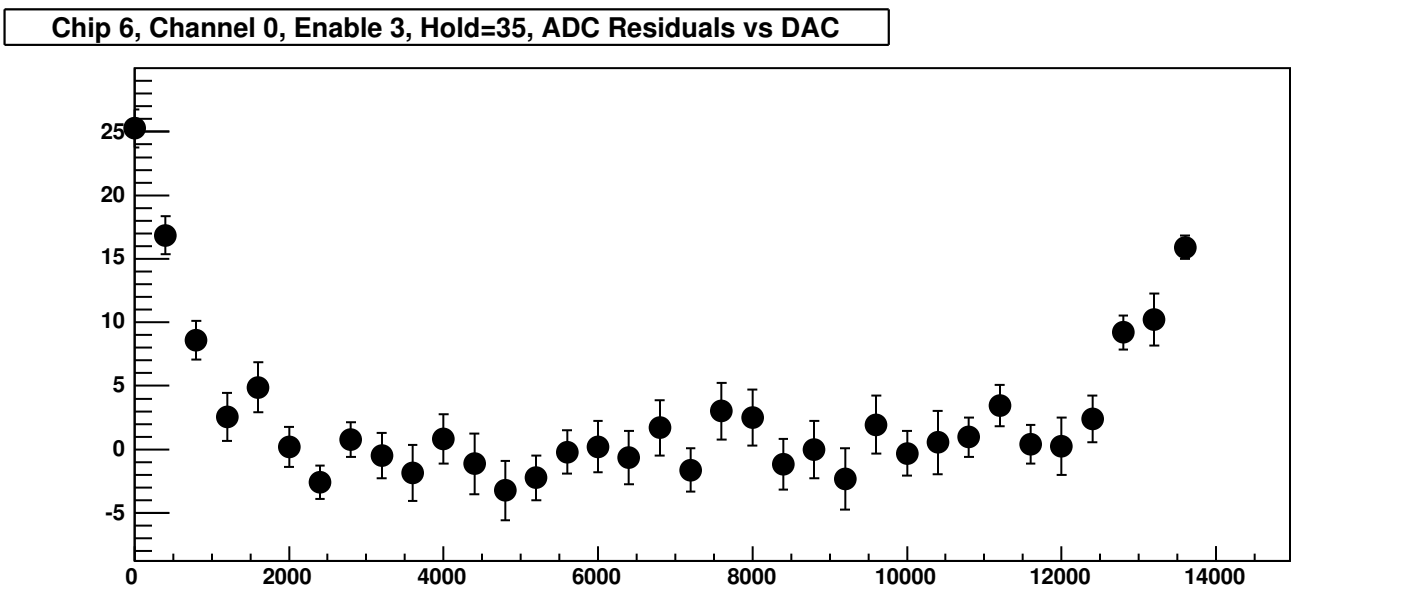

Chip 6, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 20.24 / 23
p0 -269.8 ± 0.7369
p1 0.01723 ± 0.0001201
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

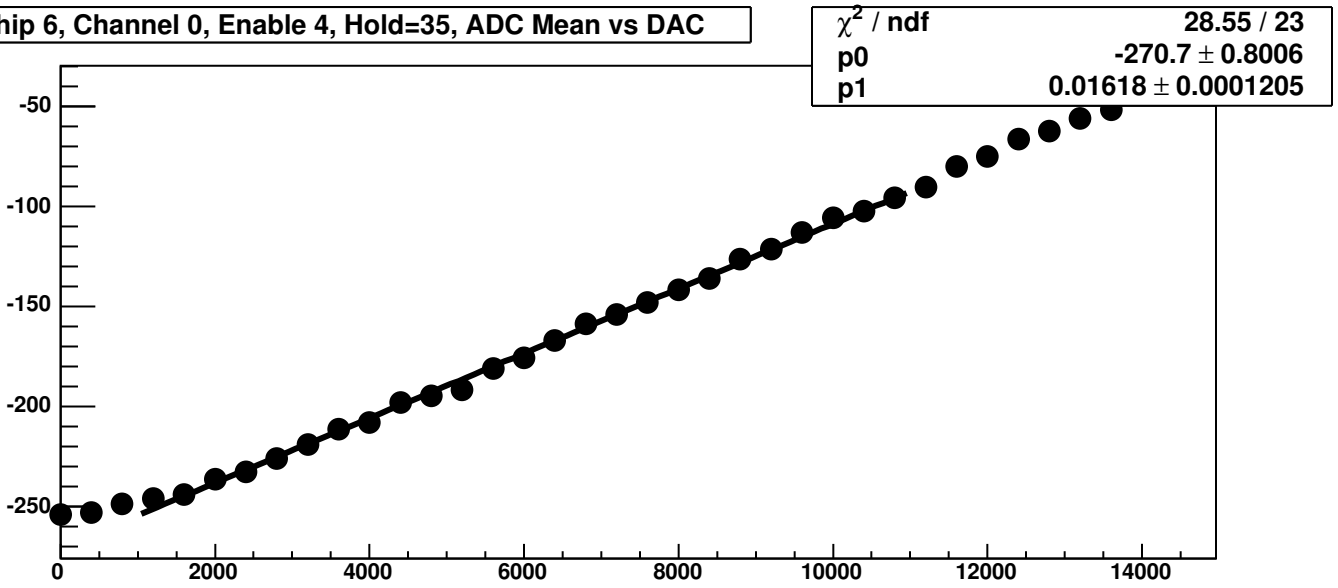

Chip 6, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.09 / 23
p0 -273.2 ± 0.8025
p1 0.03077 ± 0.0001266
200
150
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

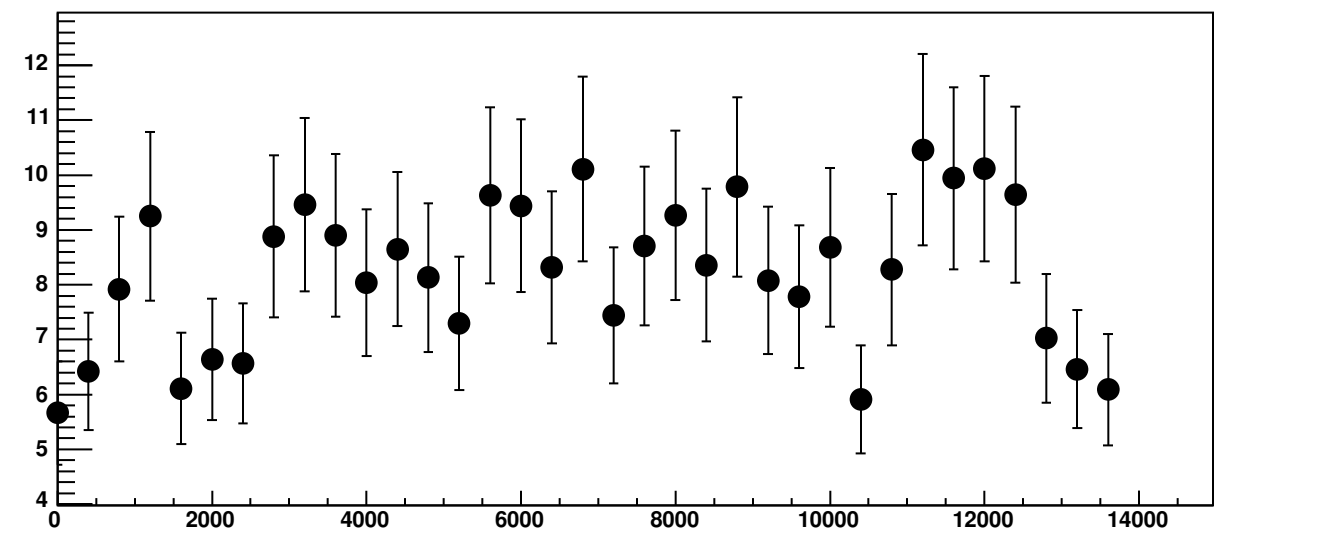

Chip 6, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

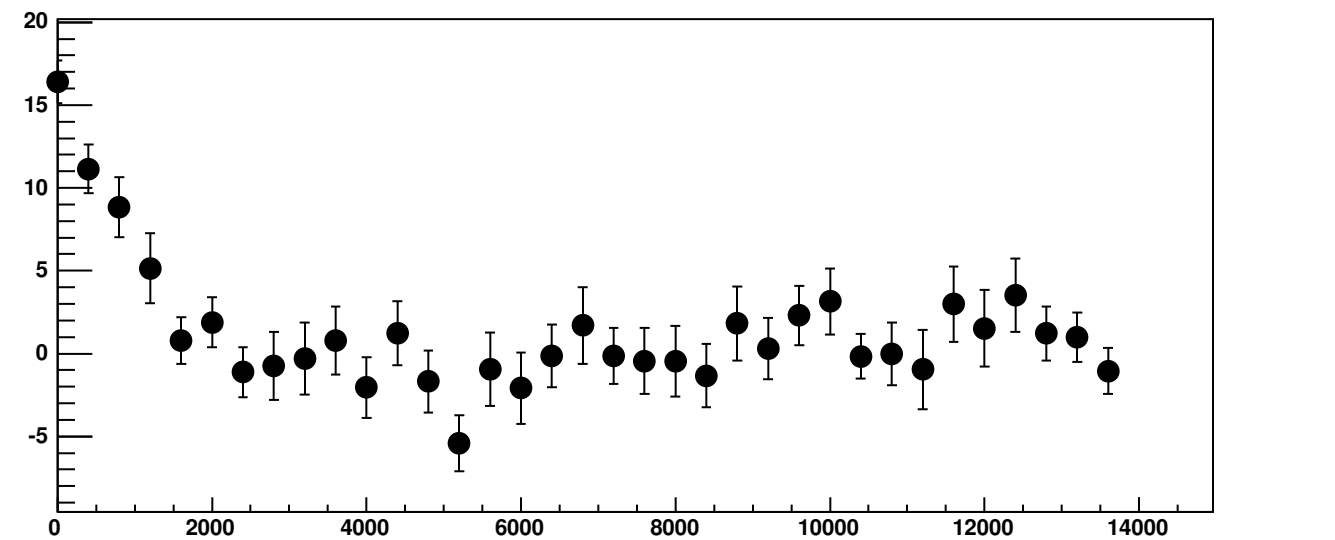
Chip 6, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

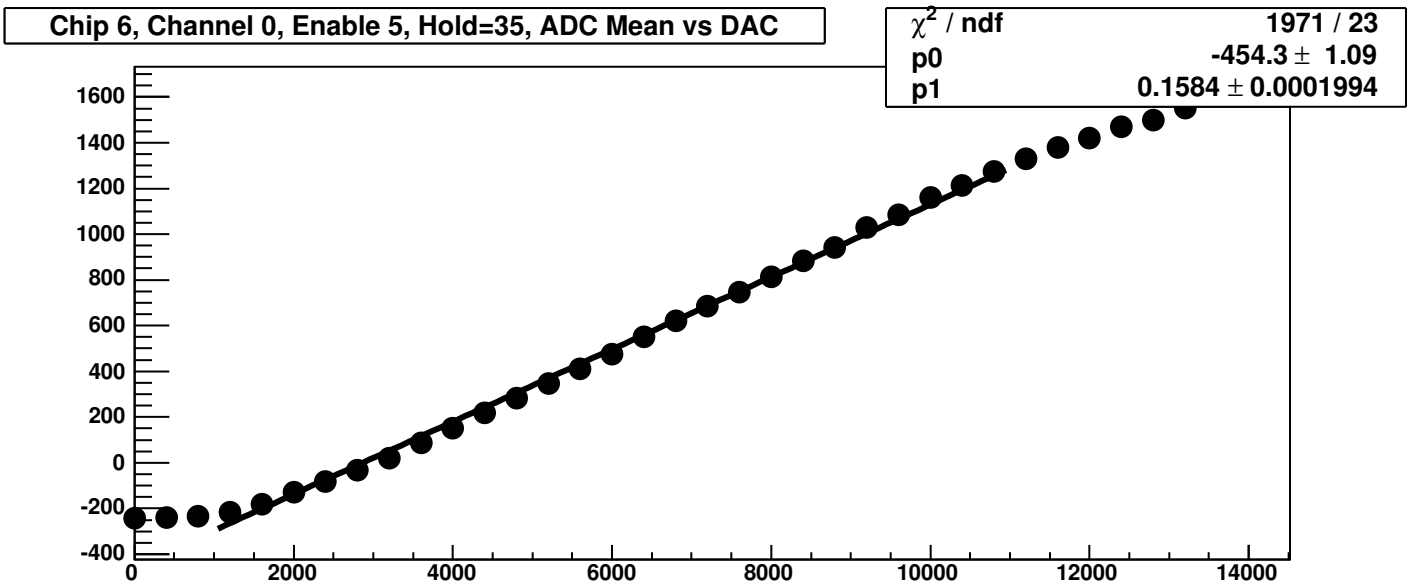


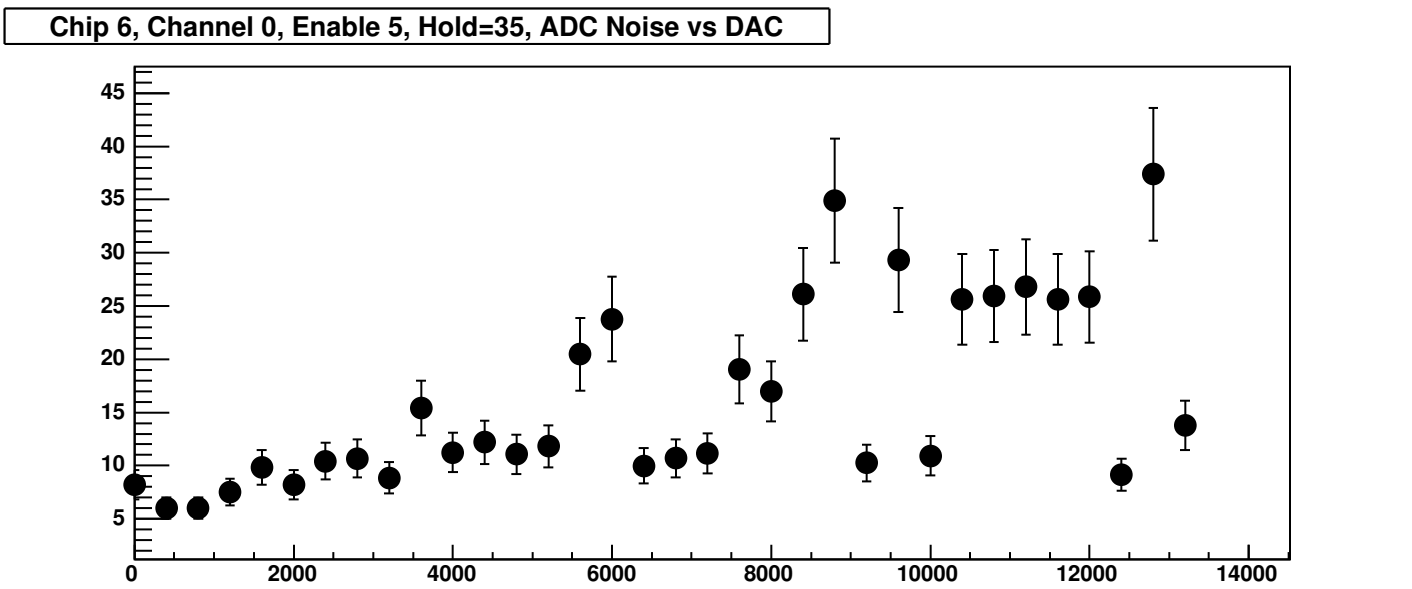
Chip 6, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

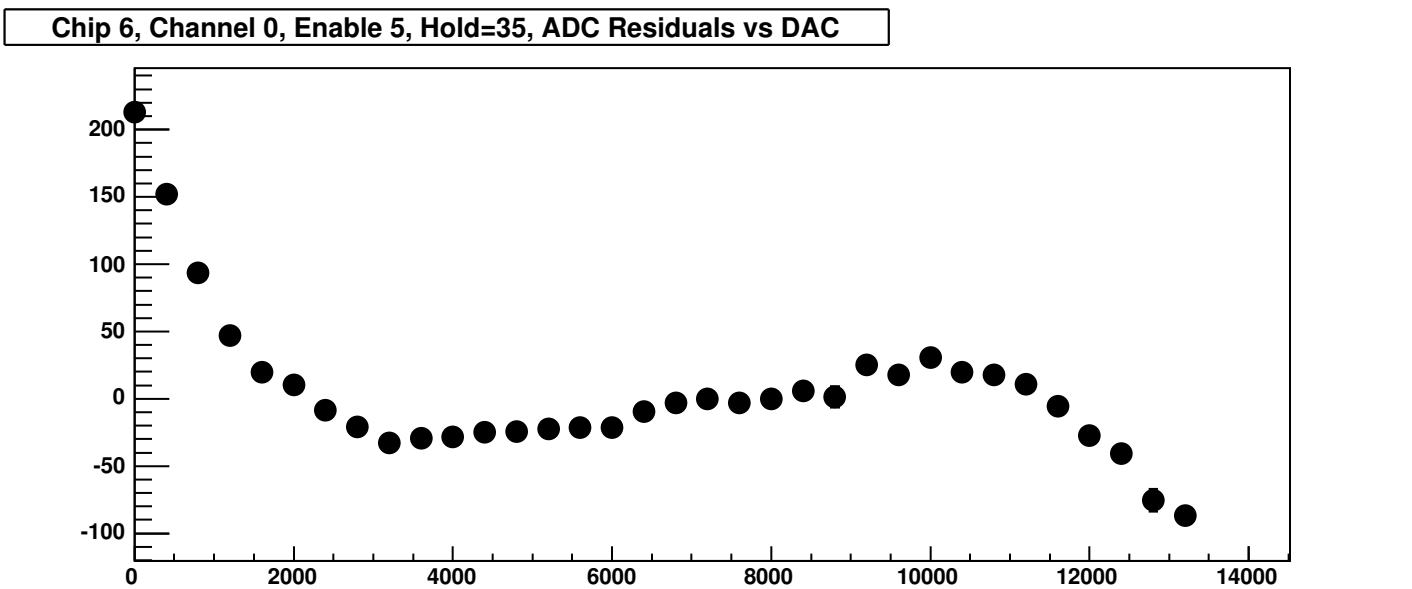


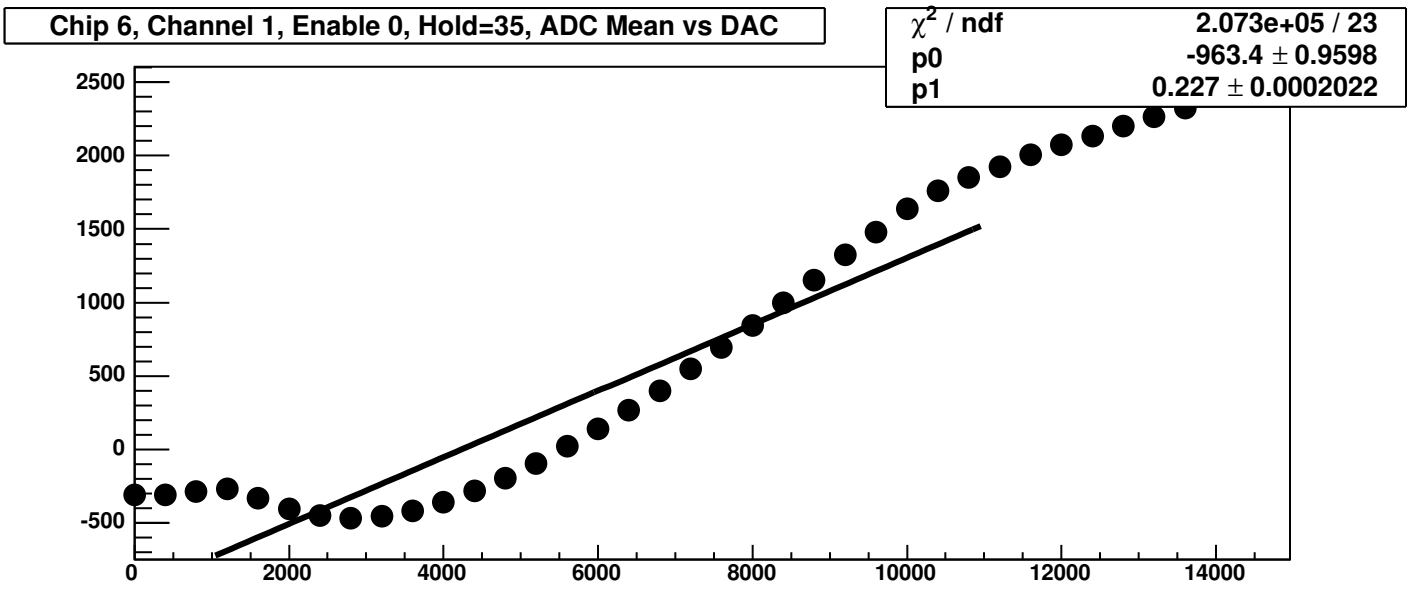
Chip 6, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

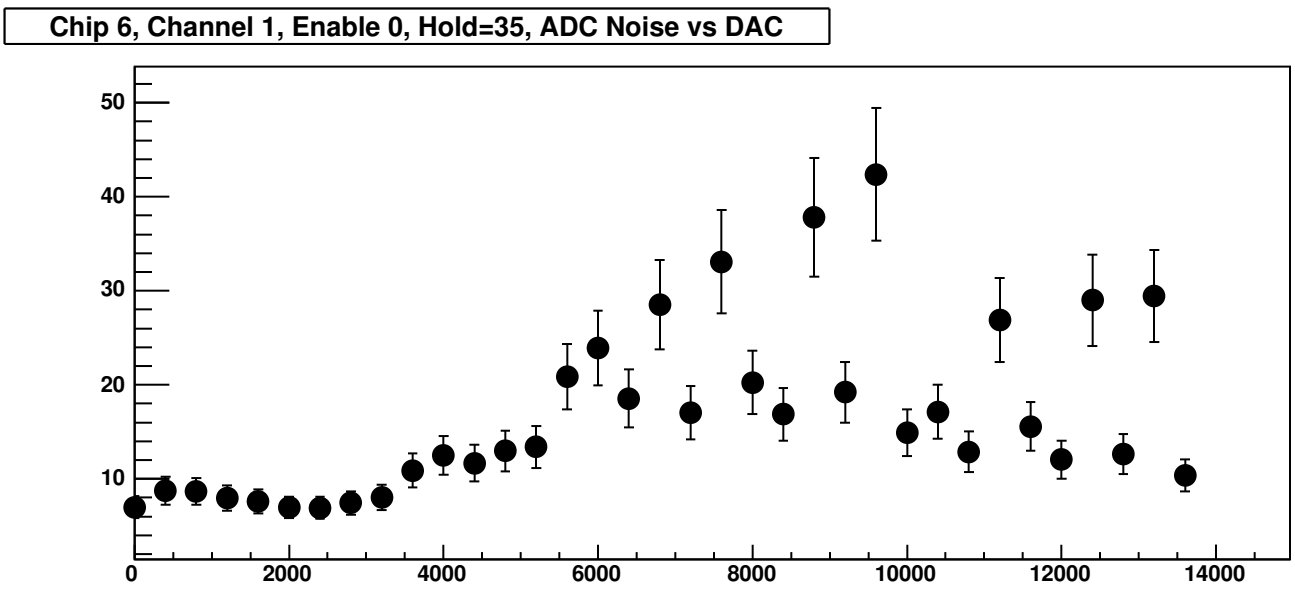


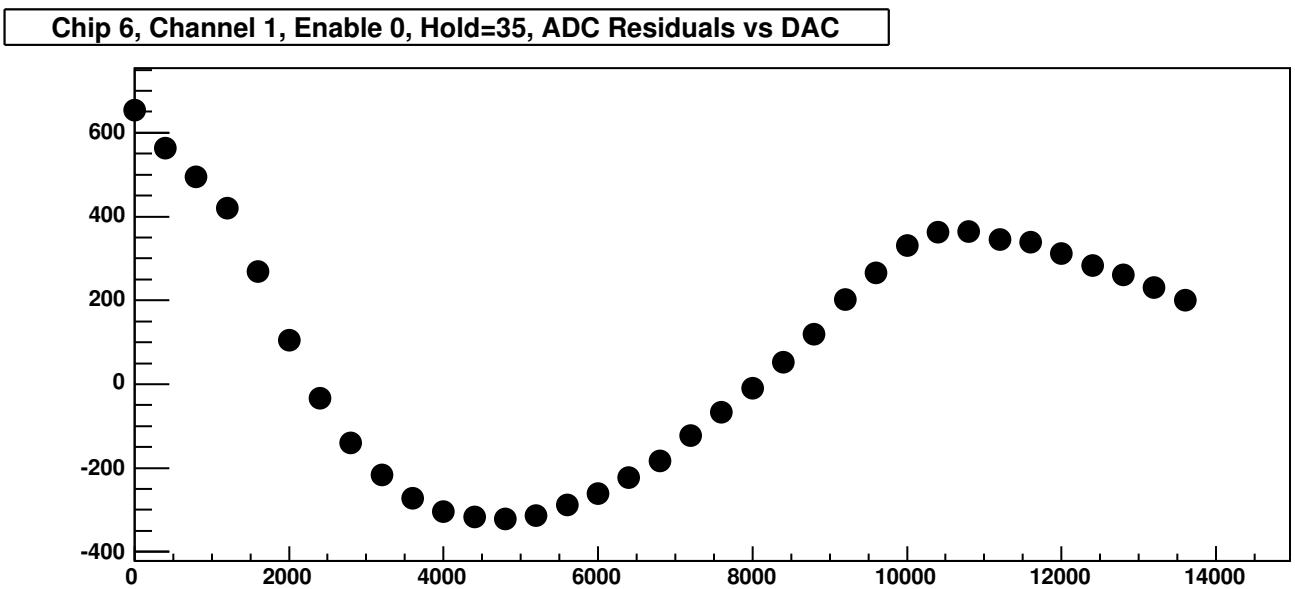

Chip 6, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1971 / 23
p0 -454.3 ± 1.09
p1 0.1584 ± 0.0001994
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

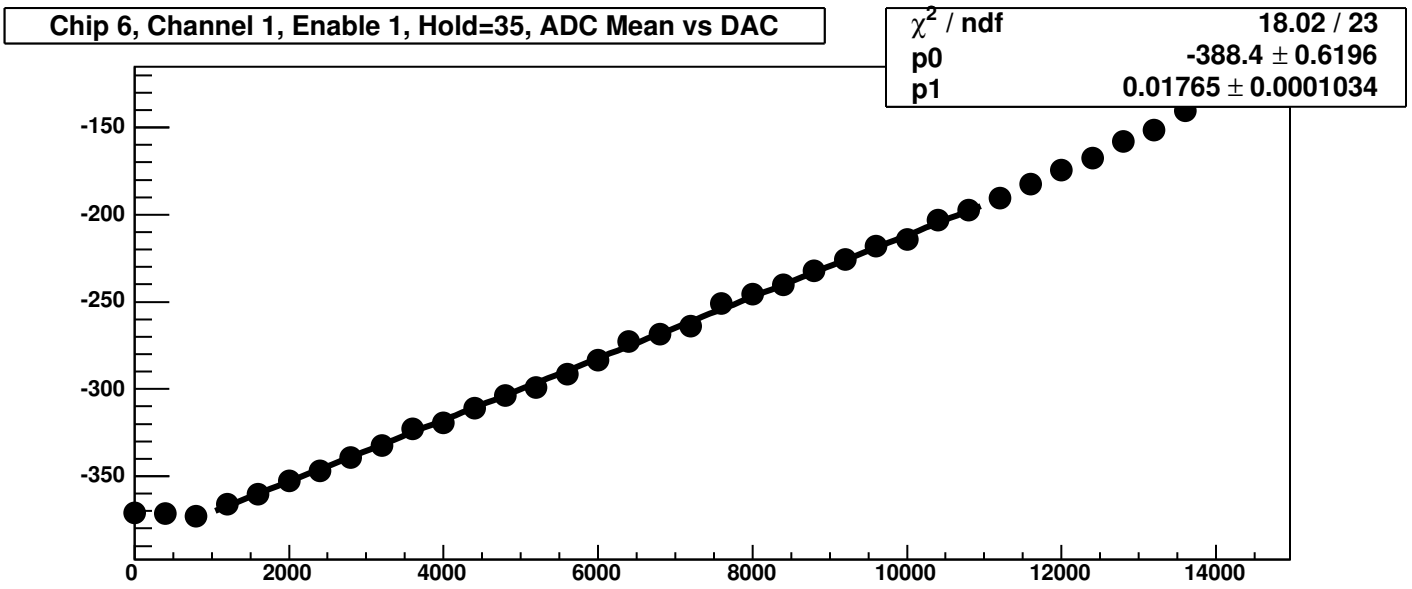

Chip 6, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

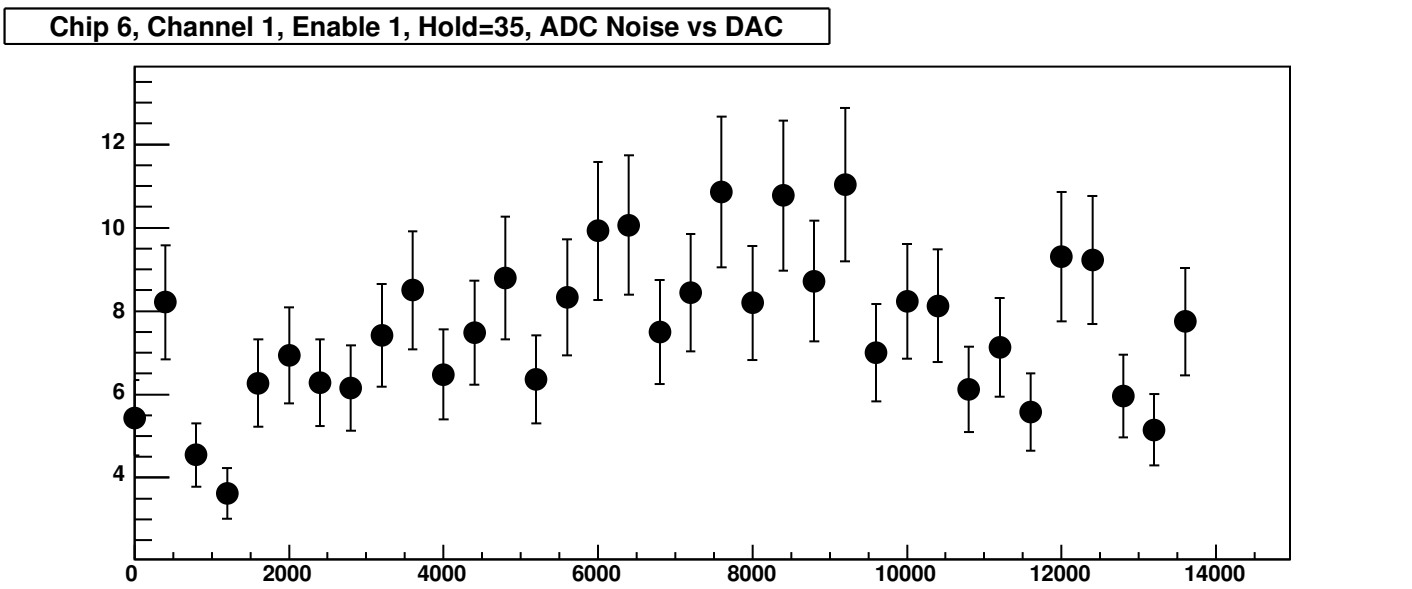

Chip 6, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

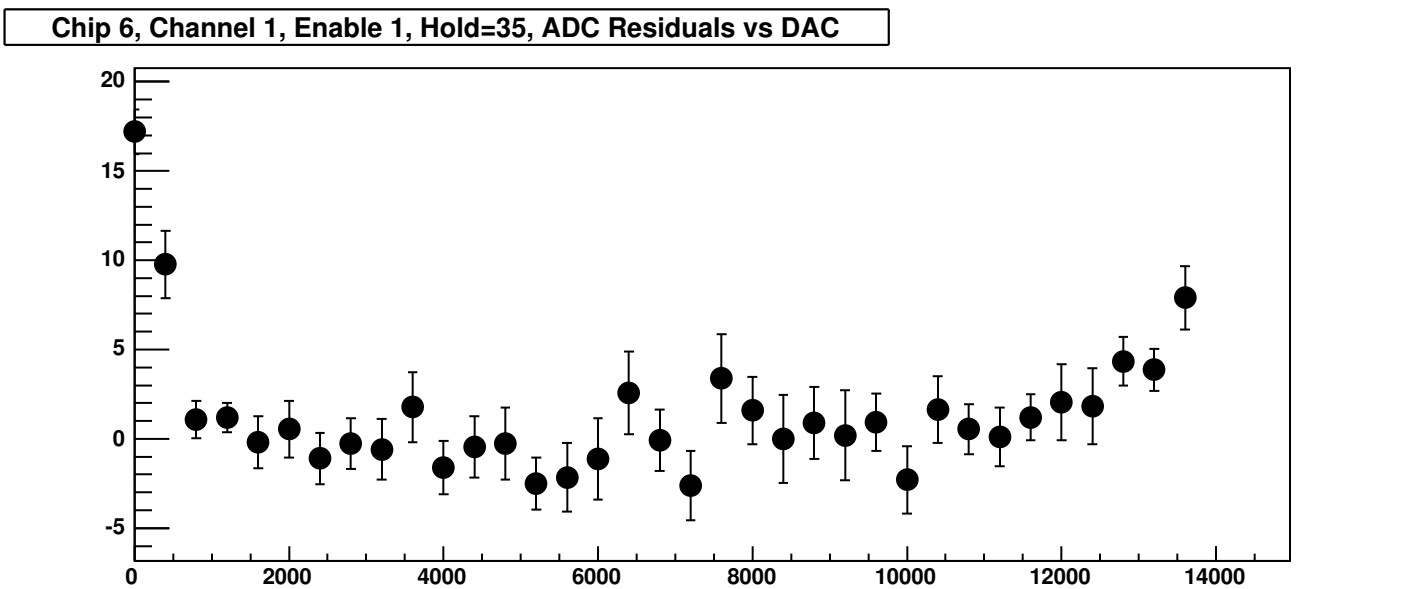

Chip 6, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.073e+05 / 23
p0 -963.4 ± 0.9598
p1 0.227 ± 0.0002022
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

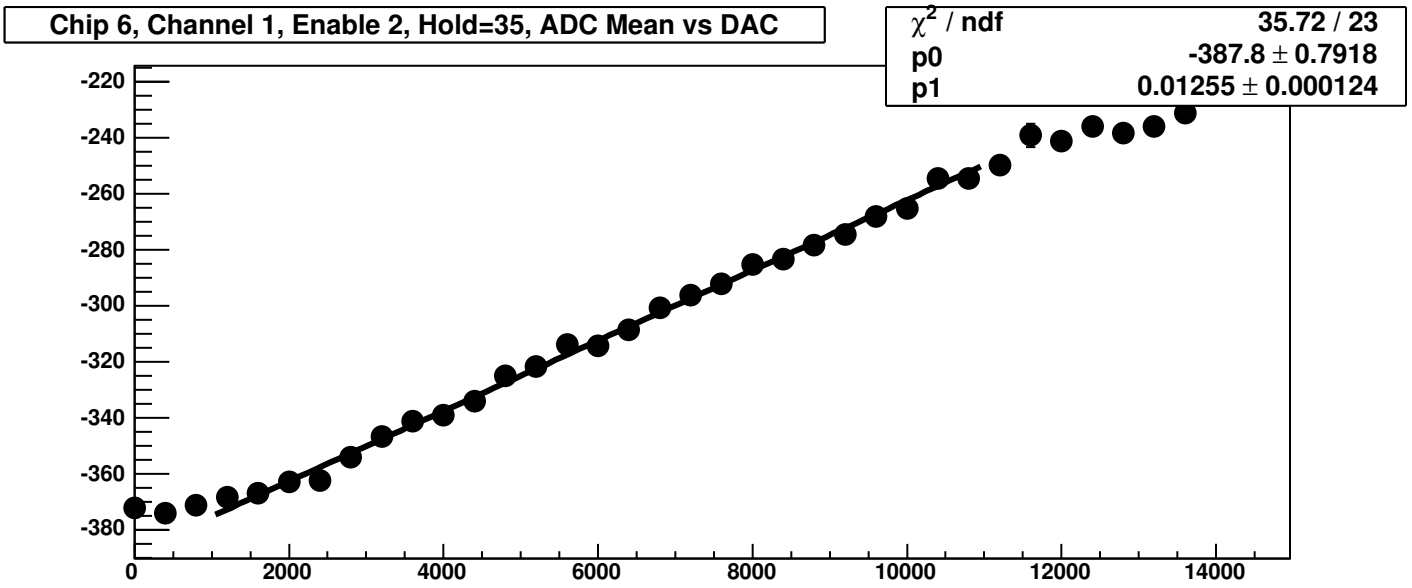

Chip 6, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

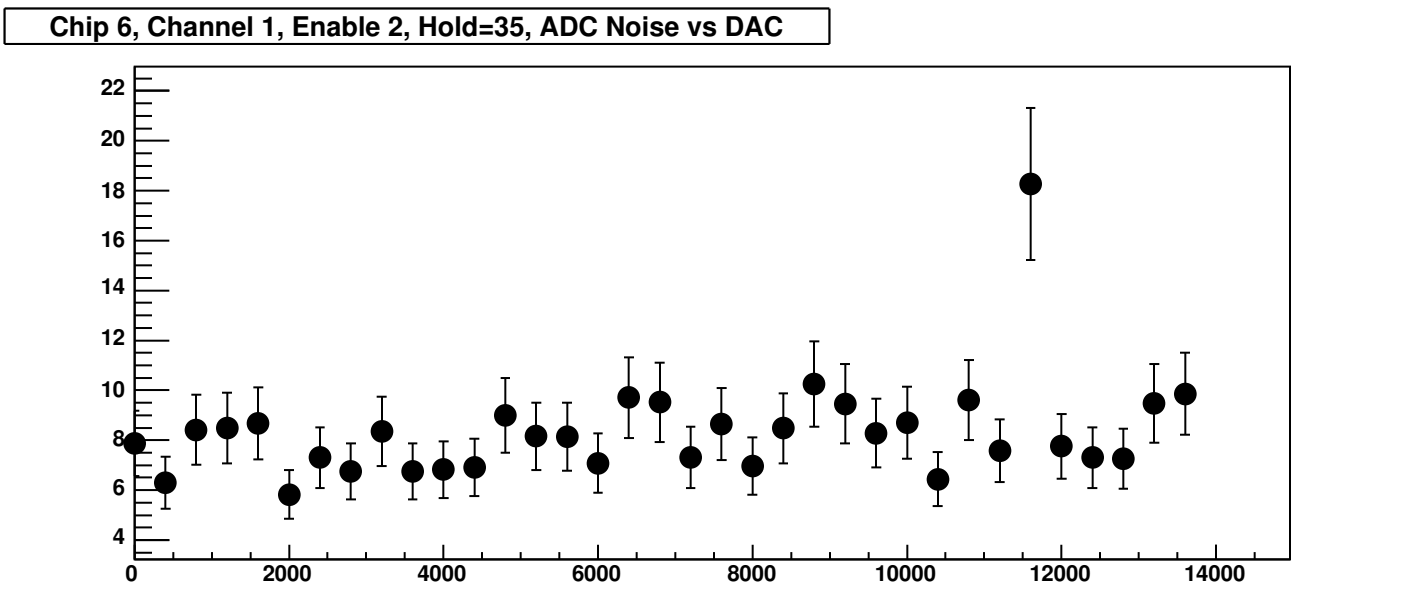

Chip 6, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

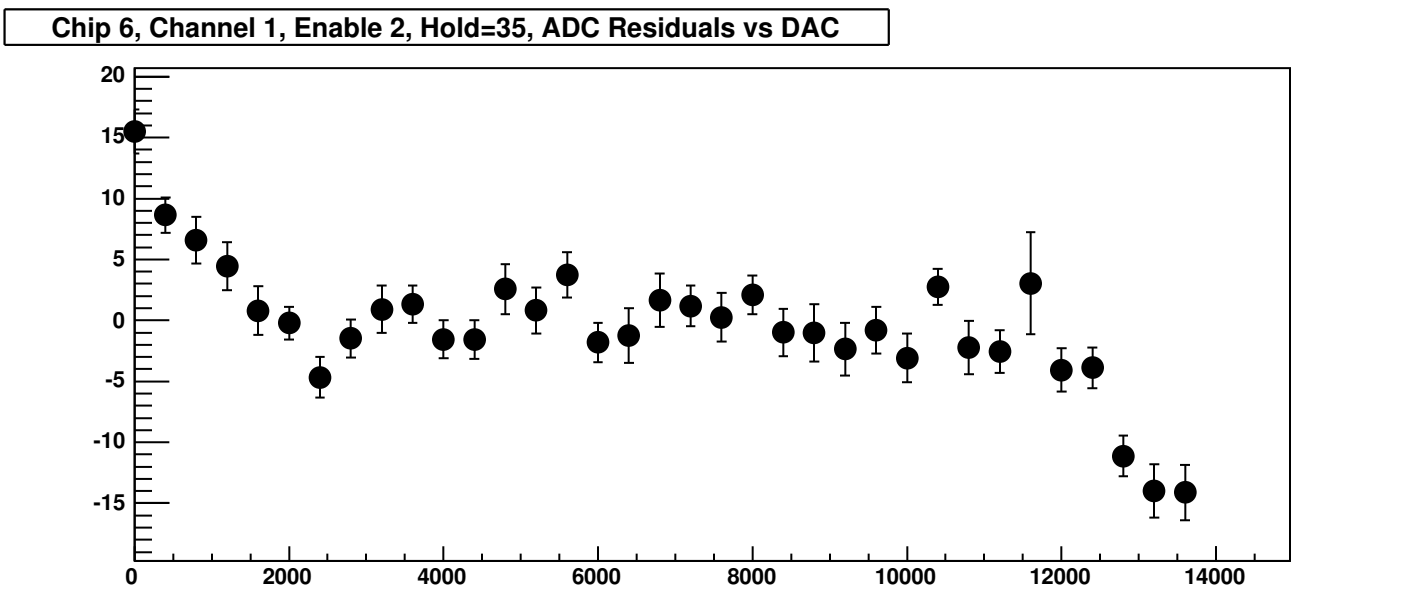

Chip 6, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 18.02 / 23
p0 -388.4 ± 0.6196
p1 0.01765 ± 0.0001034
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

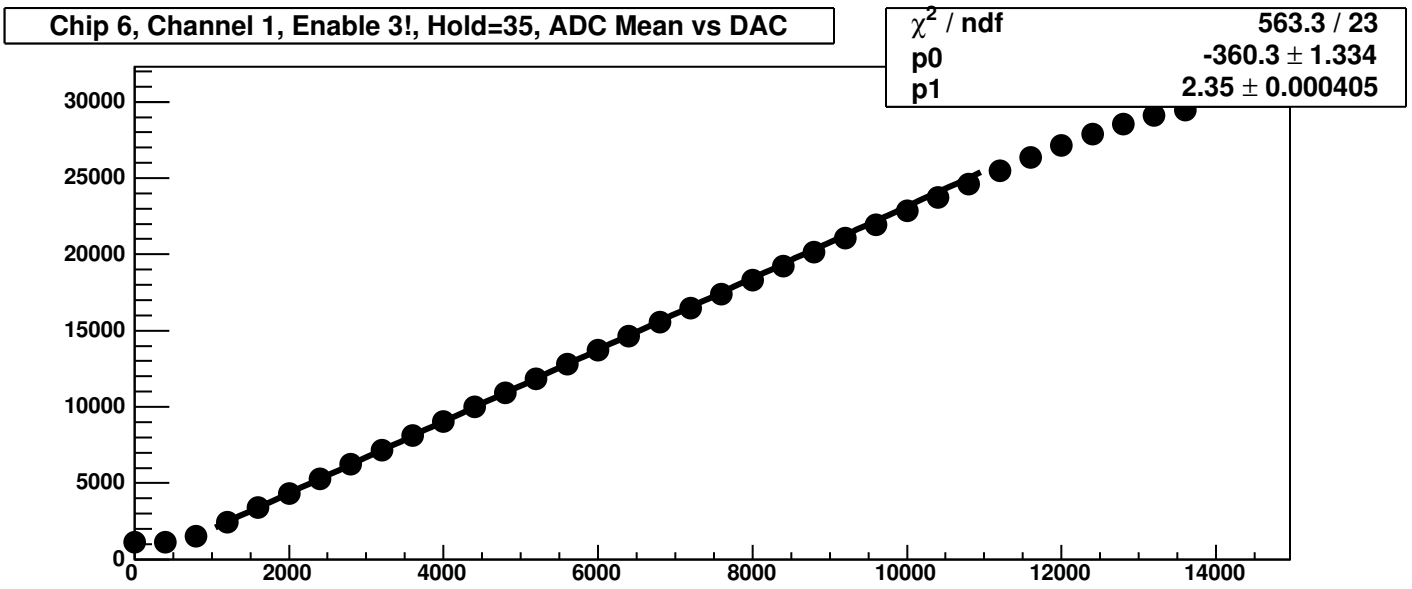

Chip 6, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

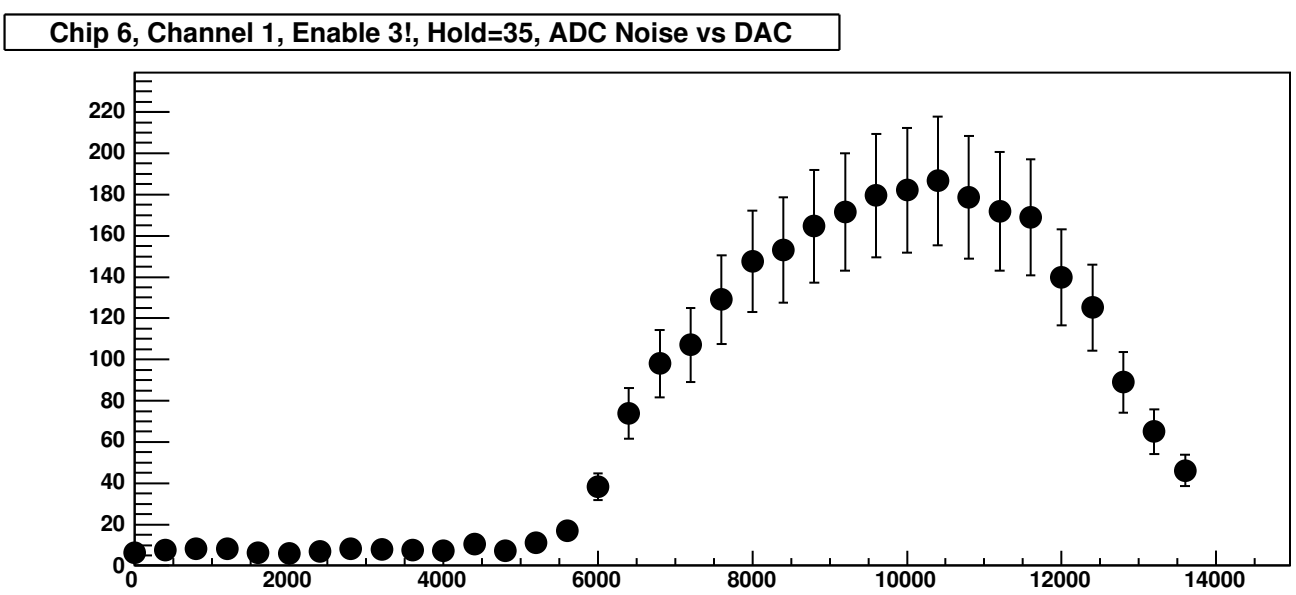

Chip 6, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

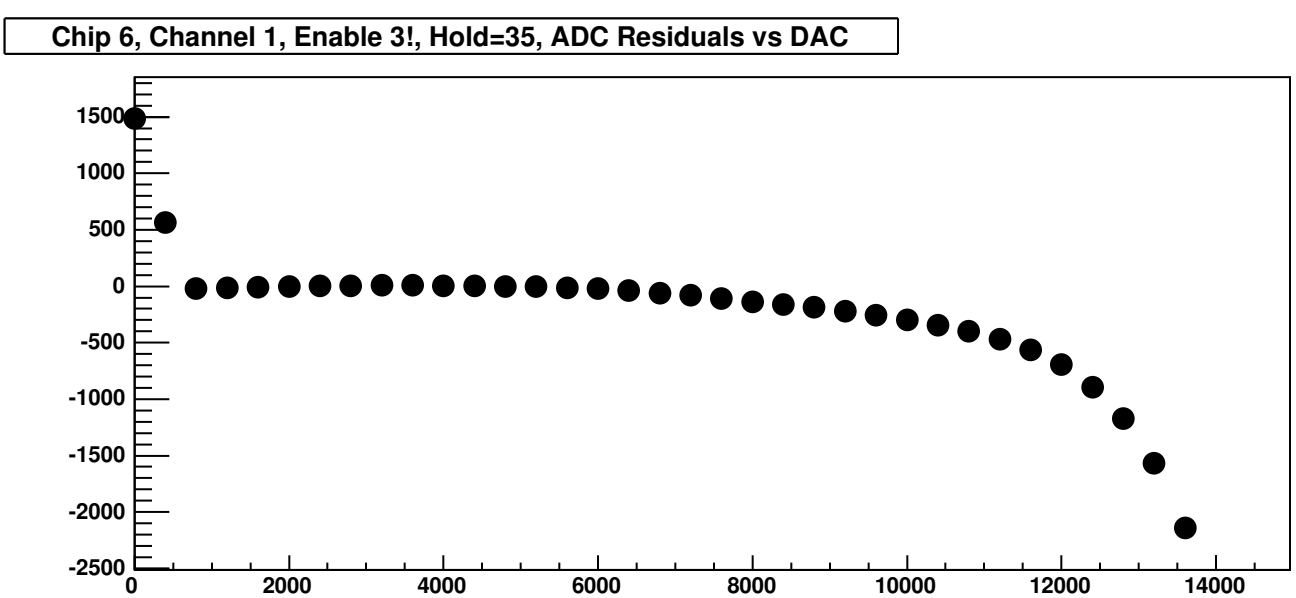

Chip 6, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 35.72 / 23
p0 -387.8 ± 0.7918
p1 0.01255 ± 0.000124
-220
-240
-260
-280
-300
-320
-340
-360
-380
0
2000
4000
6000
8000
10000
12000
14000

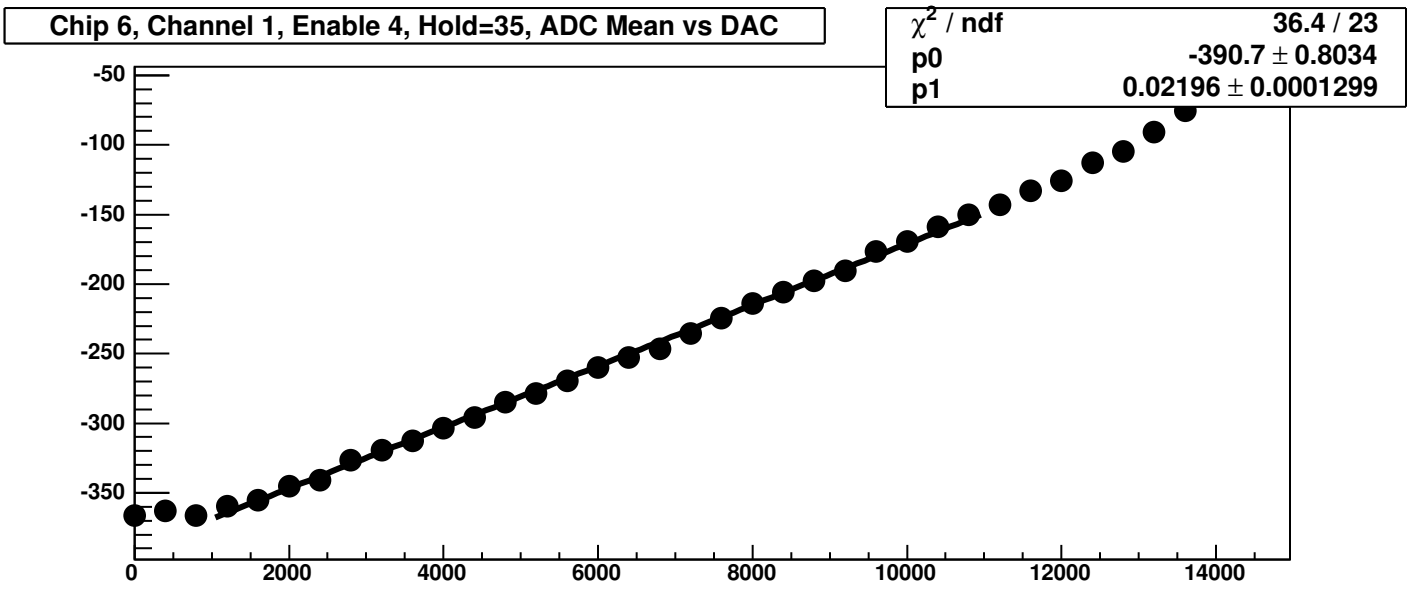

Chip 6, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

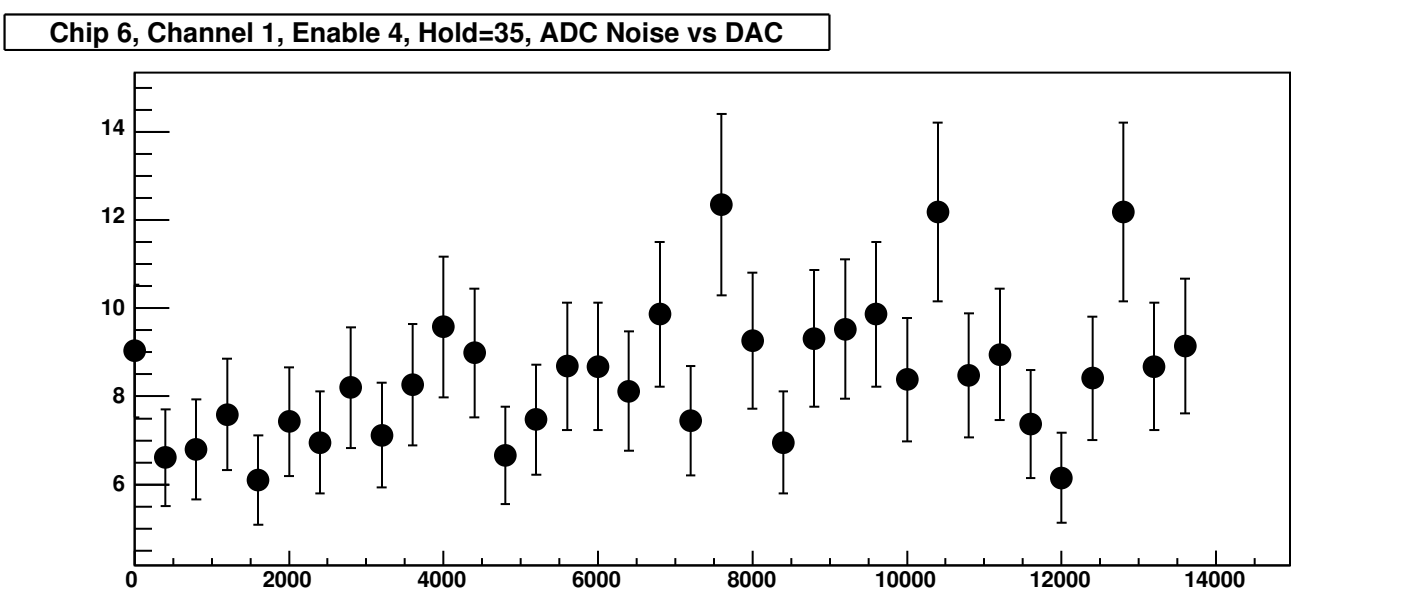

Chip 6, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

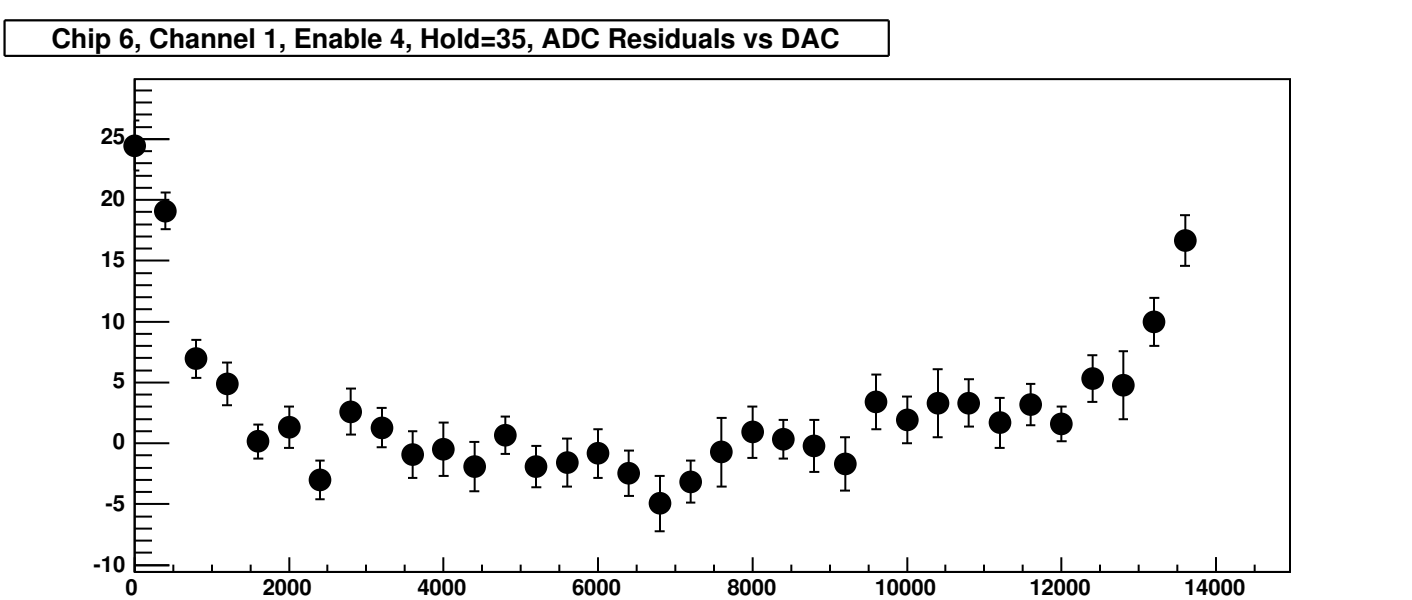

Chip 6, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 563.3 / 23
p0 -360.3 ± 1.334
p1 2.35 ± 0.000405
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

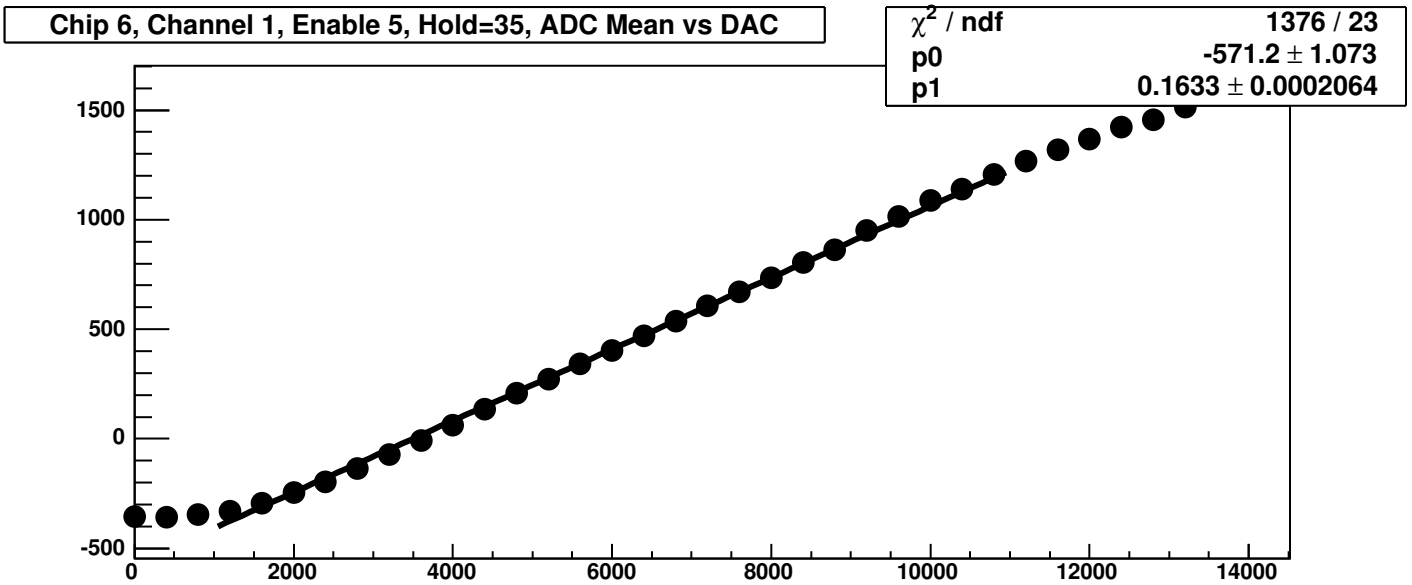

Chip 6, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

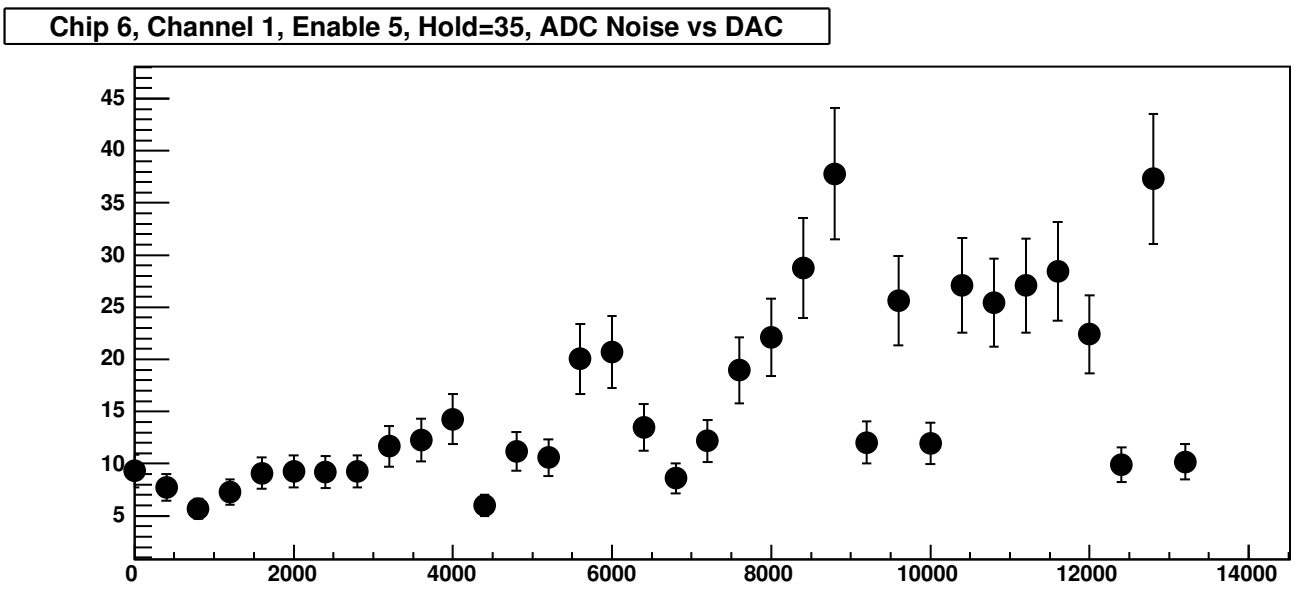

Chip 6, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

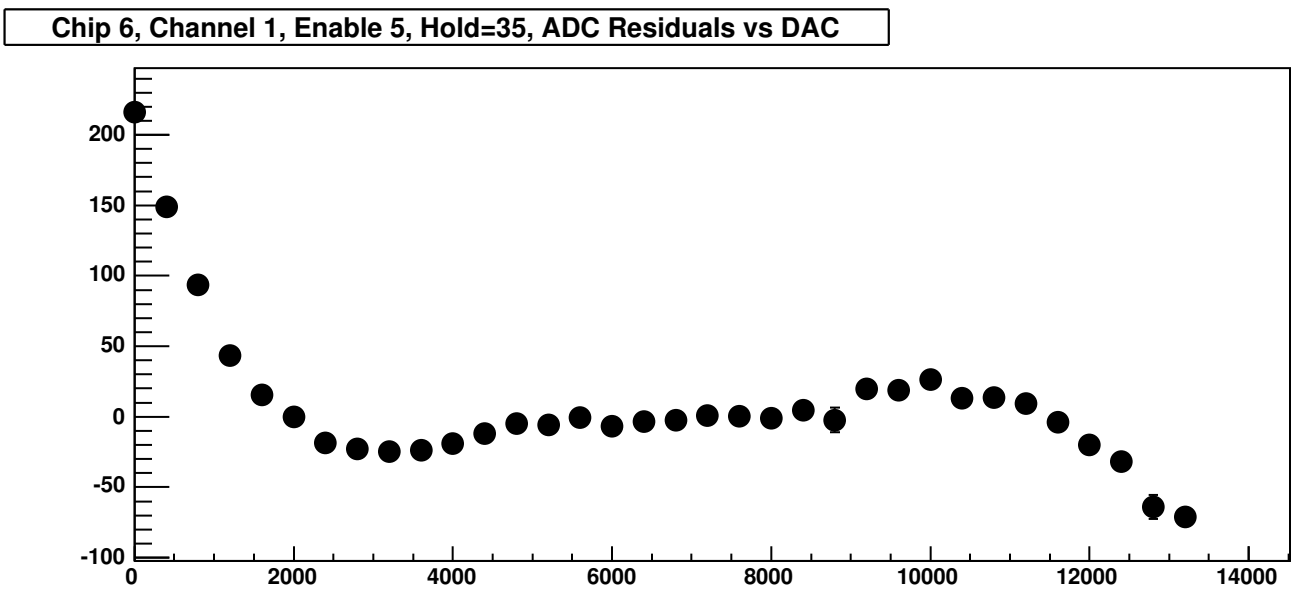

Chip 6, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 36.4 / 23
p0 -390.7 ± 0.8034
p1 0.02196 ± 0.0001299
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

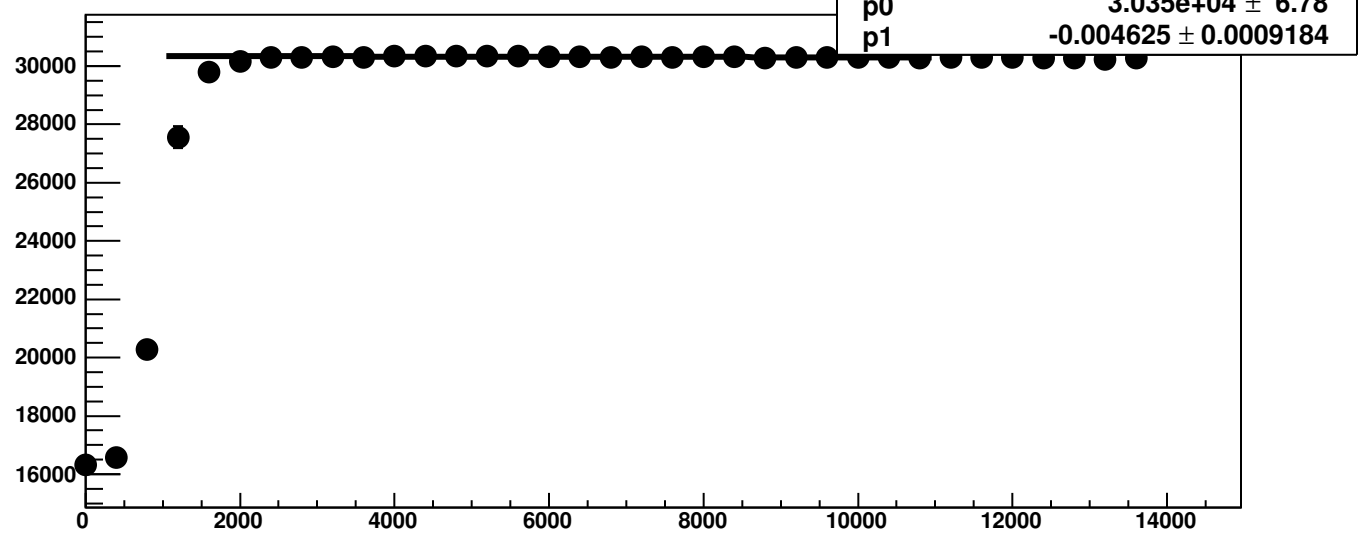

Chip 6, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1376 / 23
p0 -571.2 ± 1.073
p1 0.1633 ± 0.0002064
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

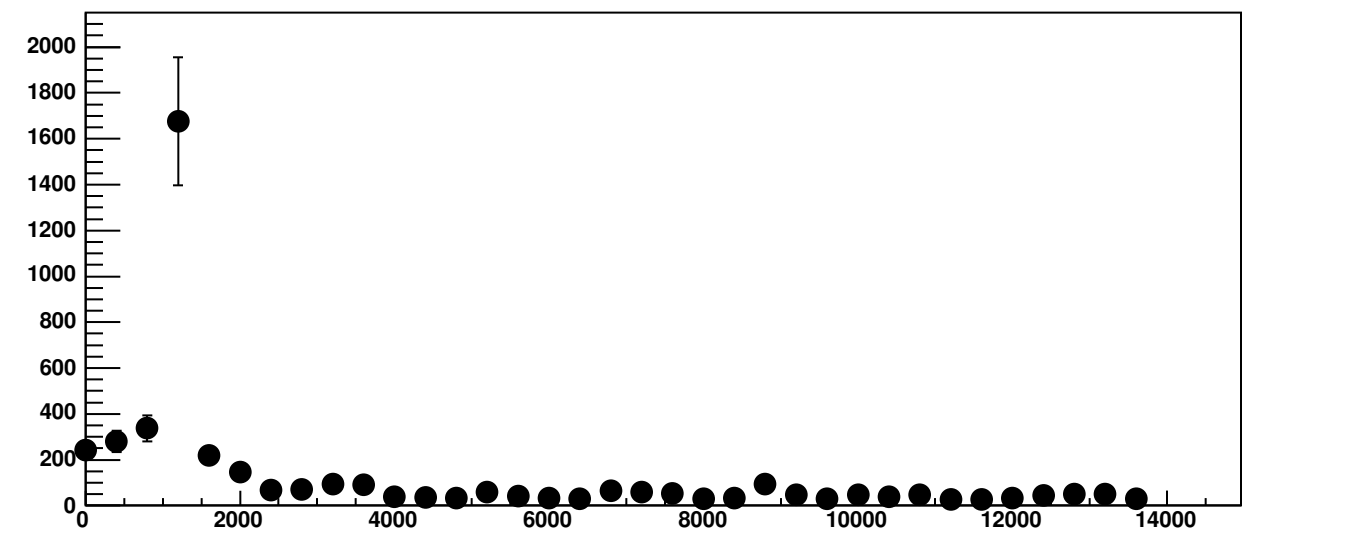

Chip 6, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

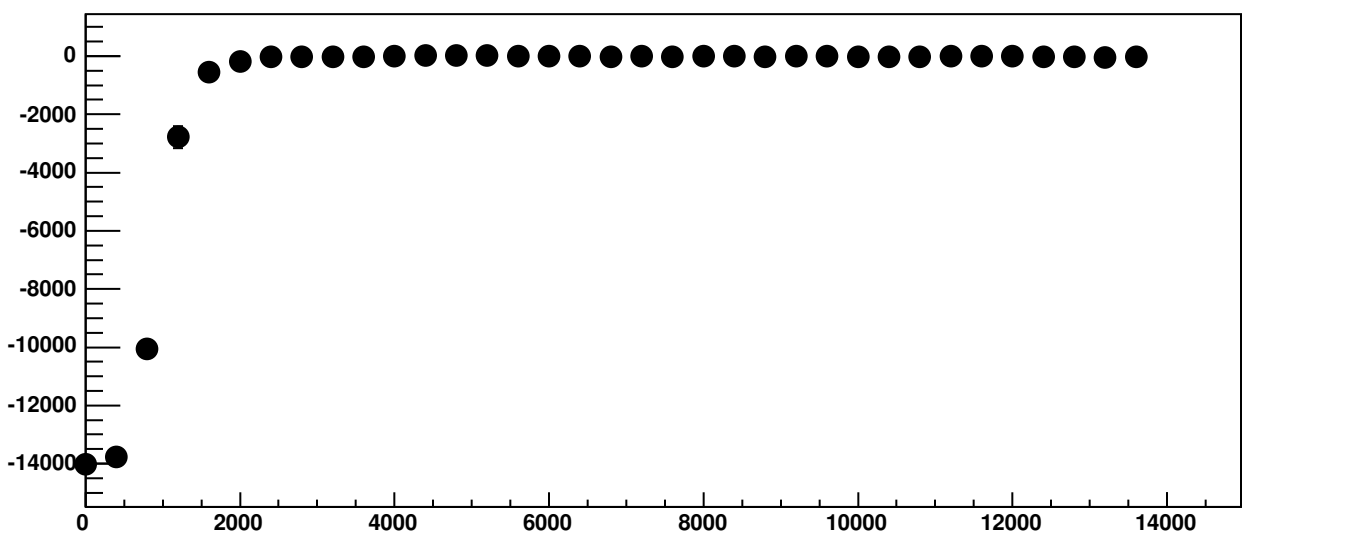
Chip 6, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC



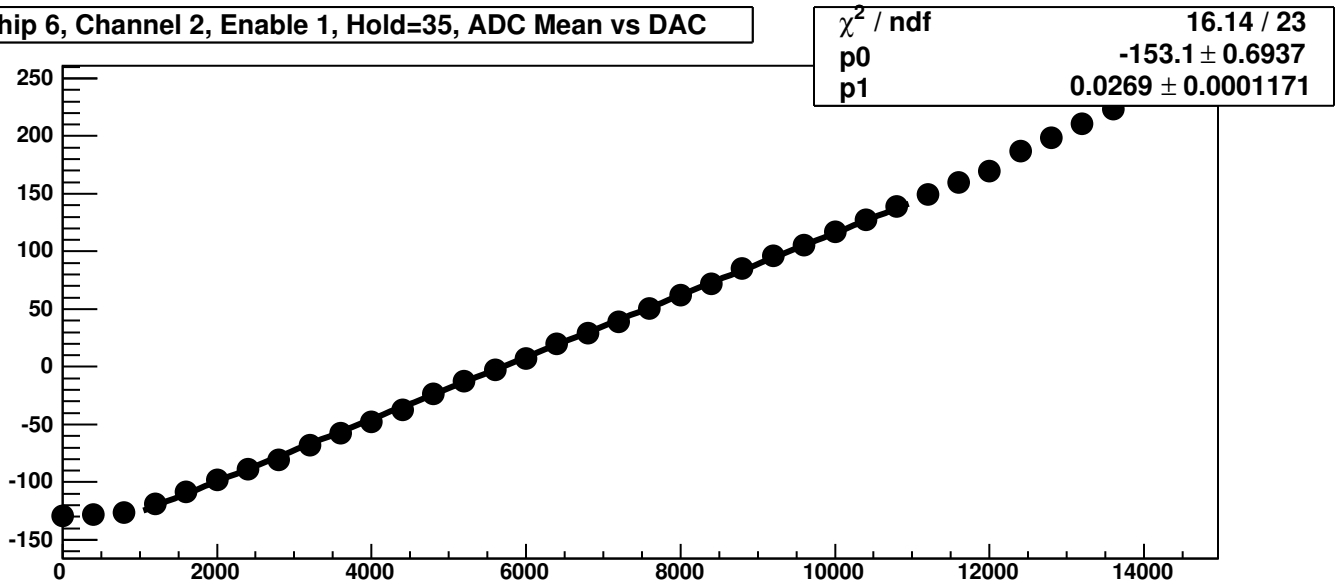
Chip 6, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC



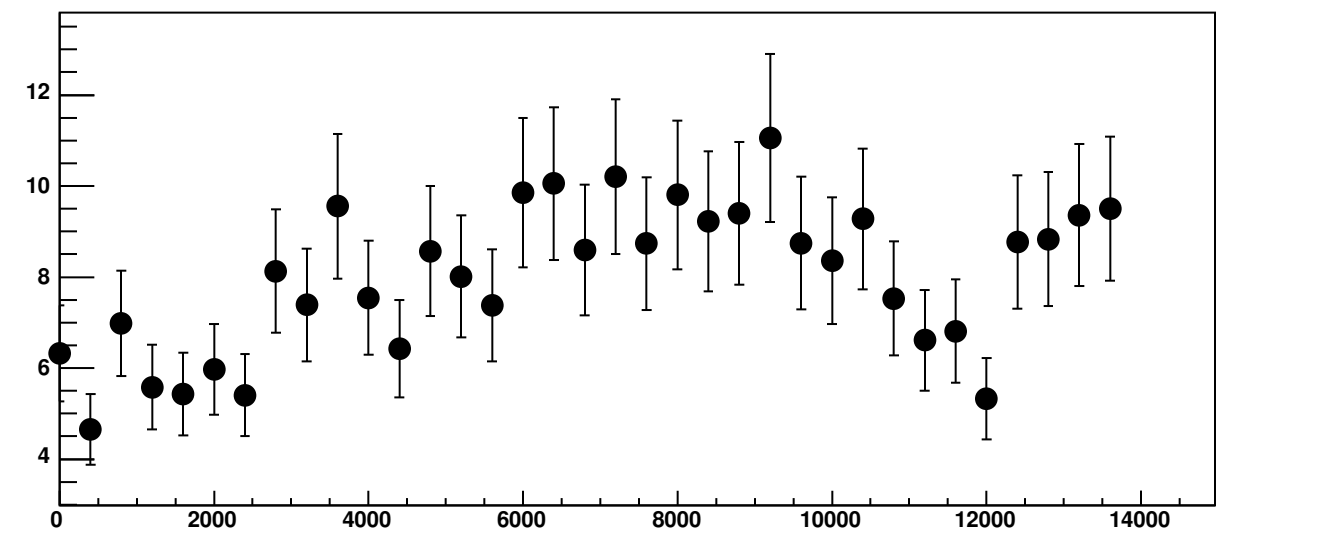
Chip 6, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC



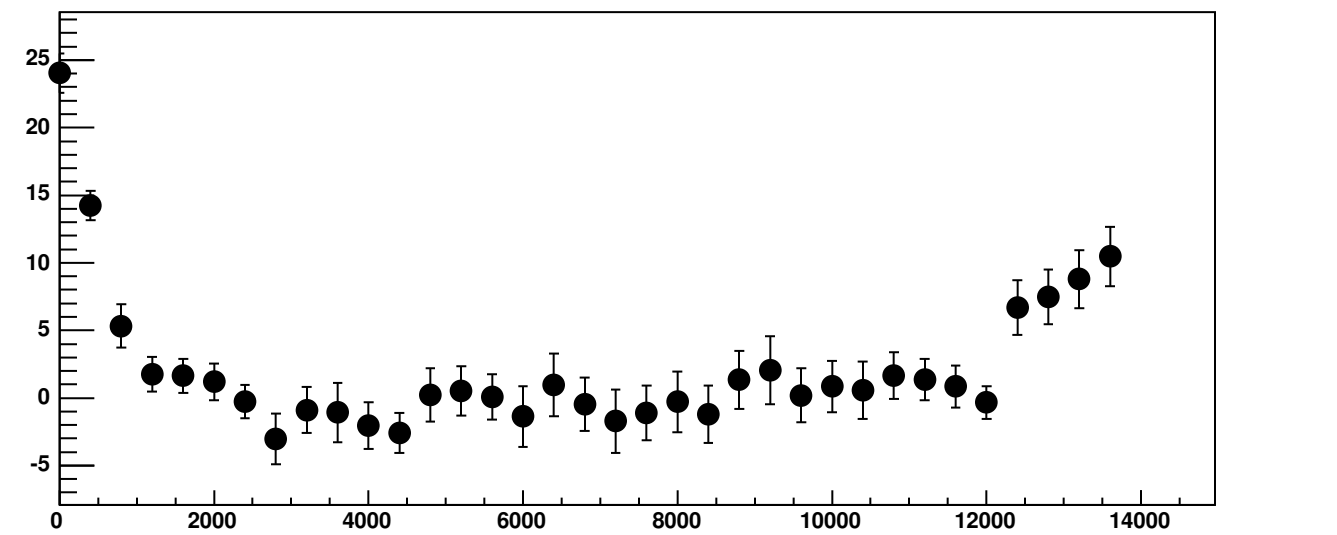
Chip 6, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



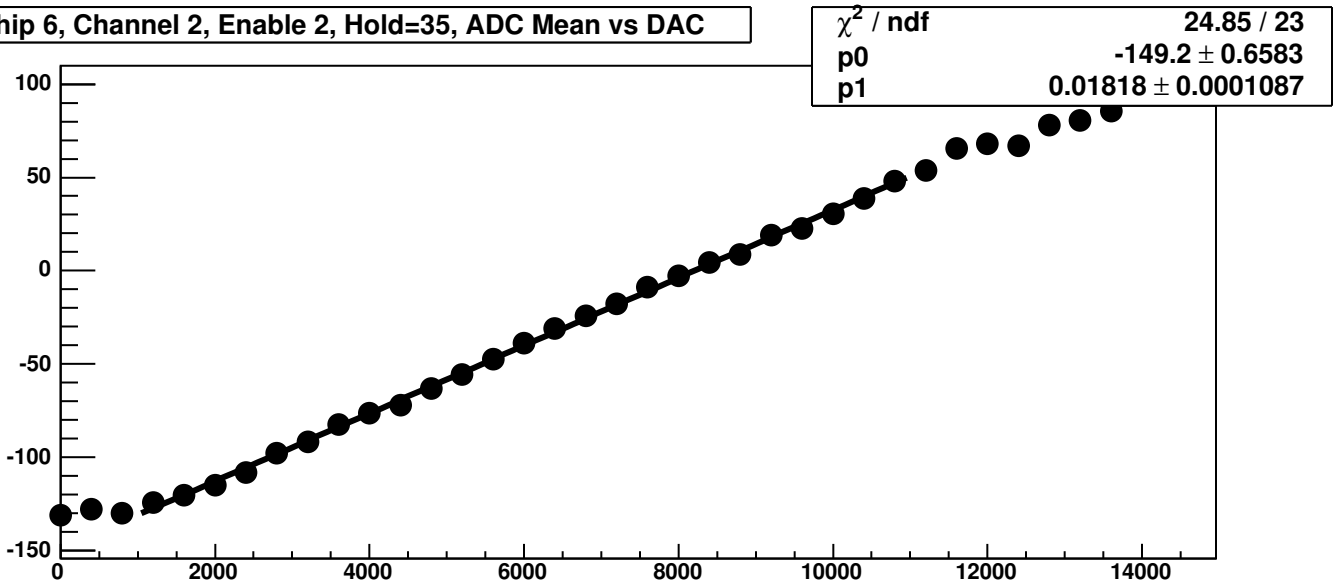
Chip 6, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



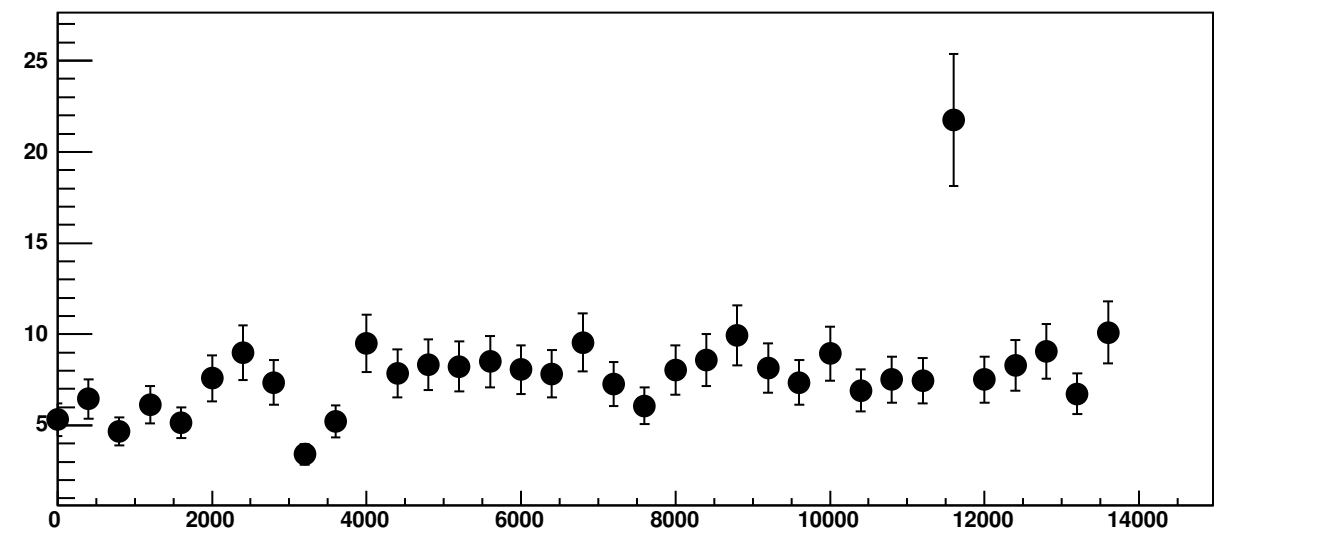
Chip 6, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



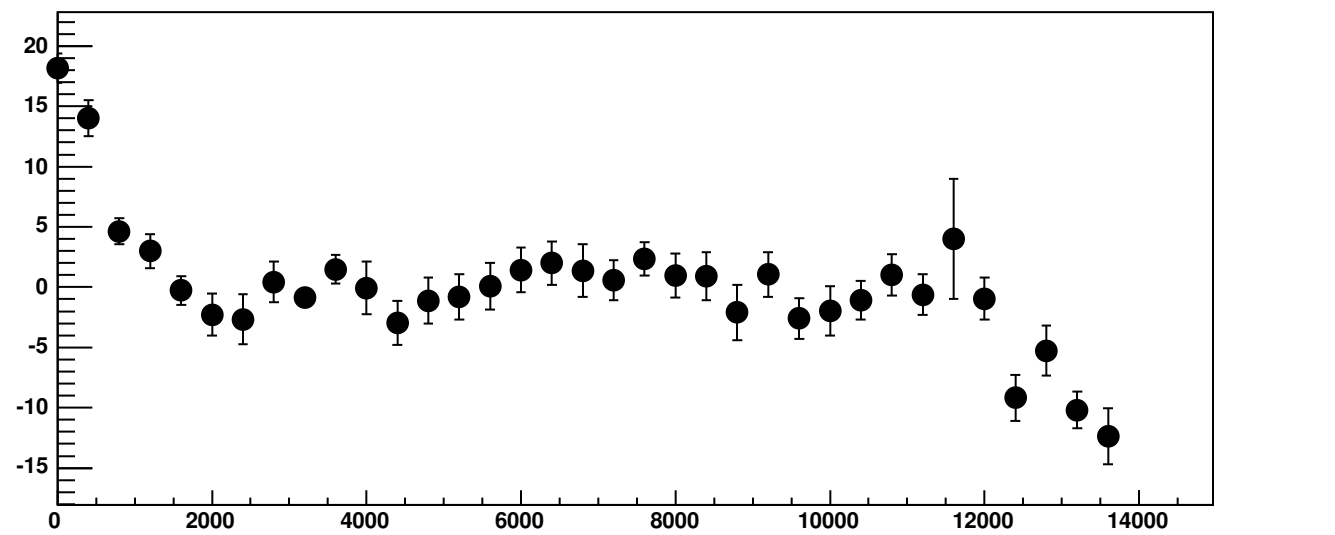
Chip 6, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC



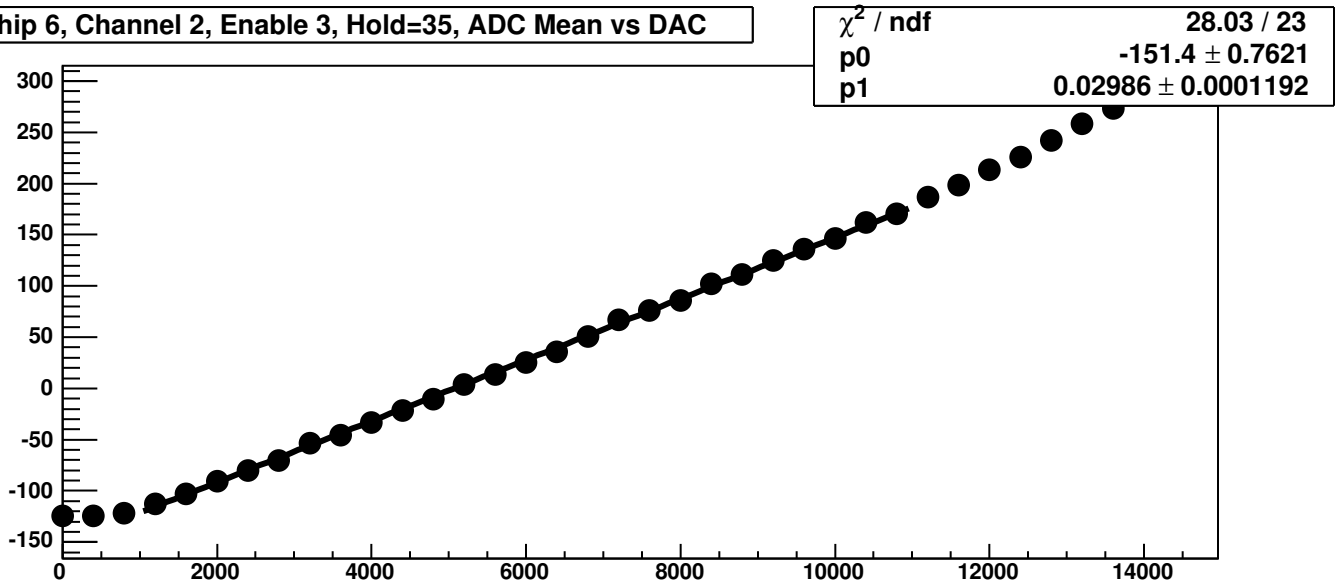
Chip 6, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC



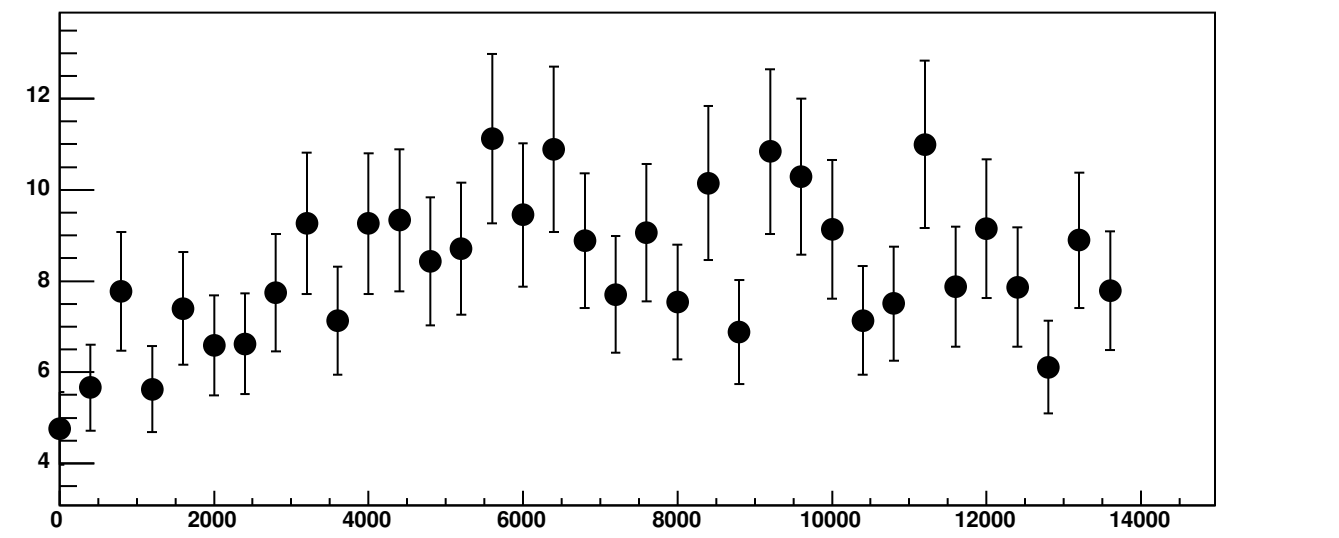
Chip 6, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC



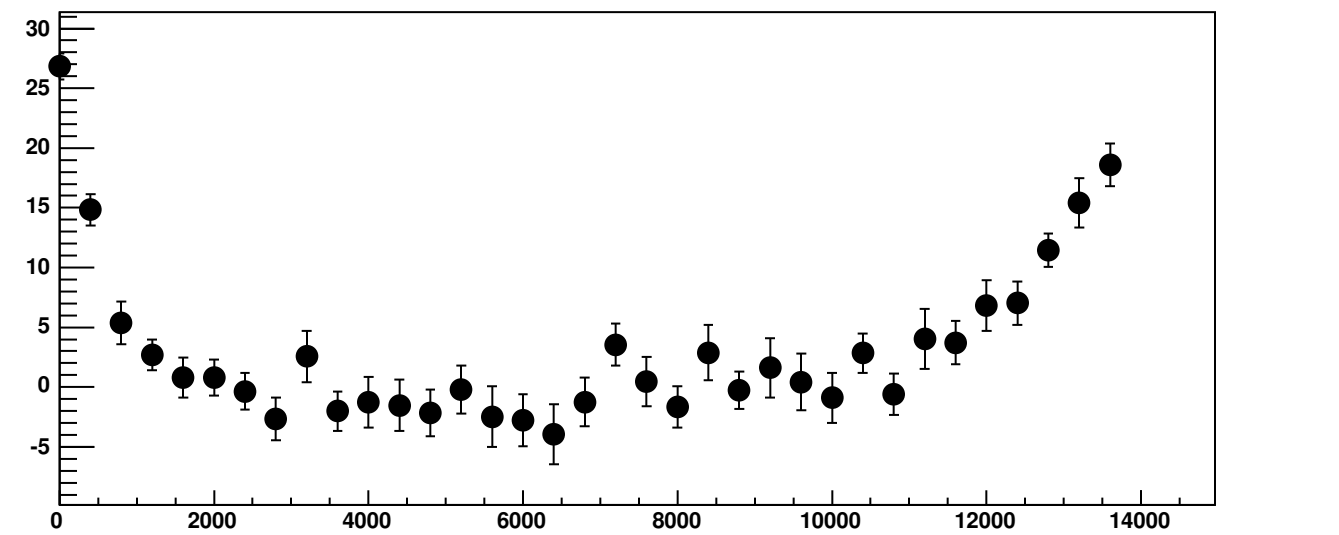
Chip 6, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC

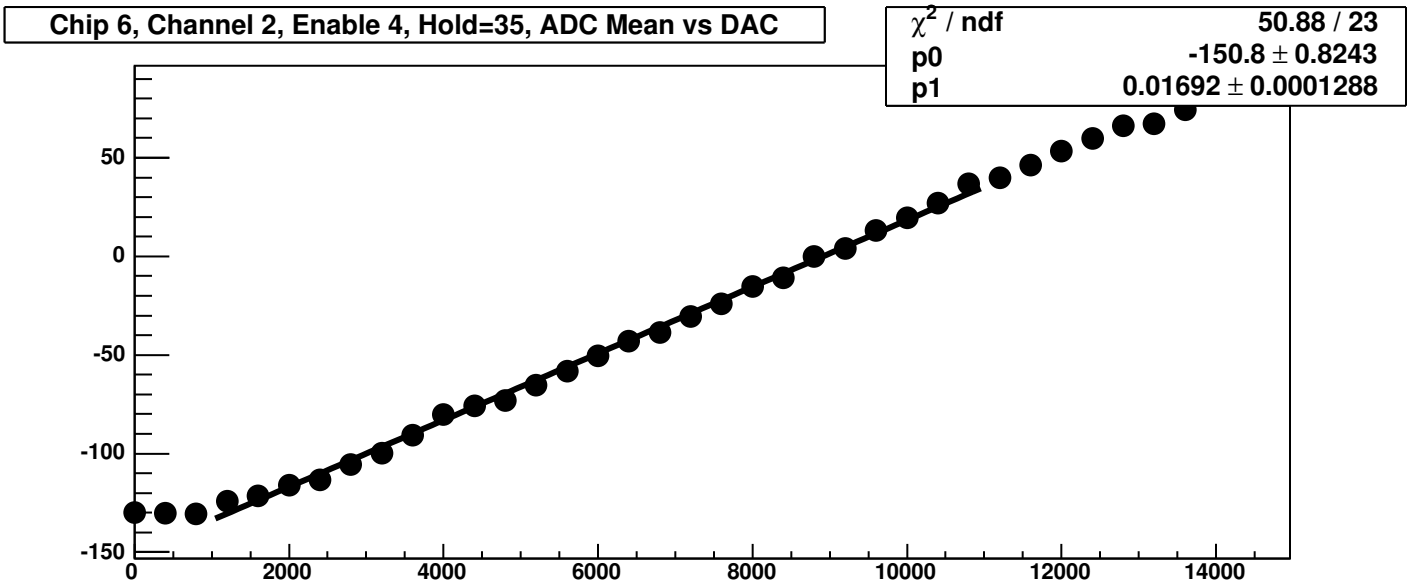


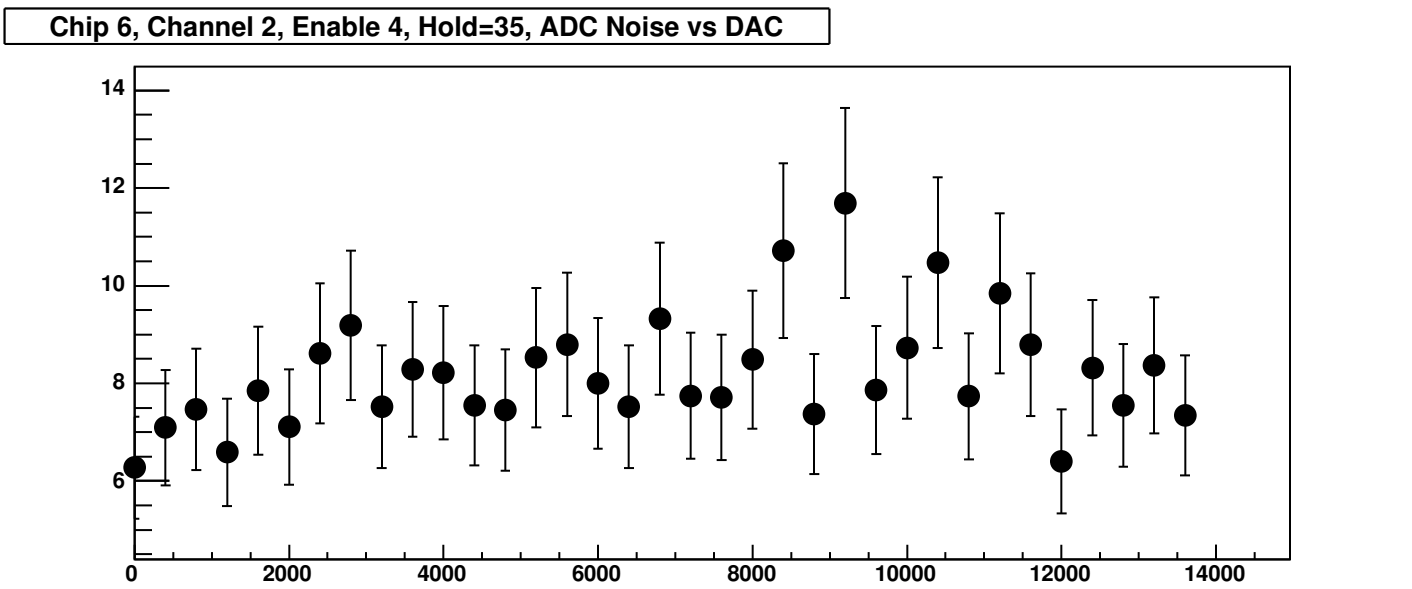
Chip 6, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC

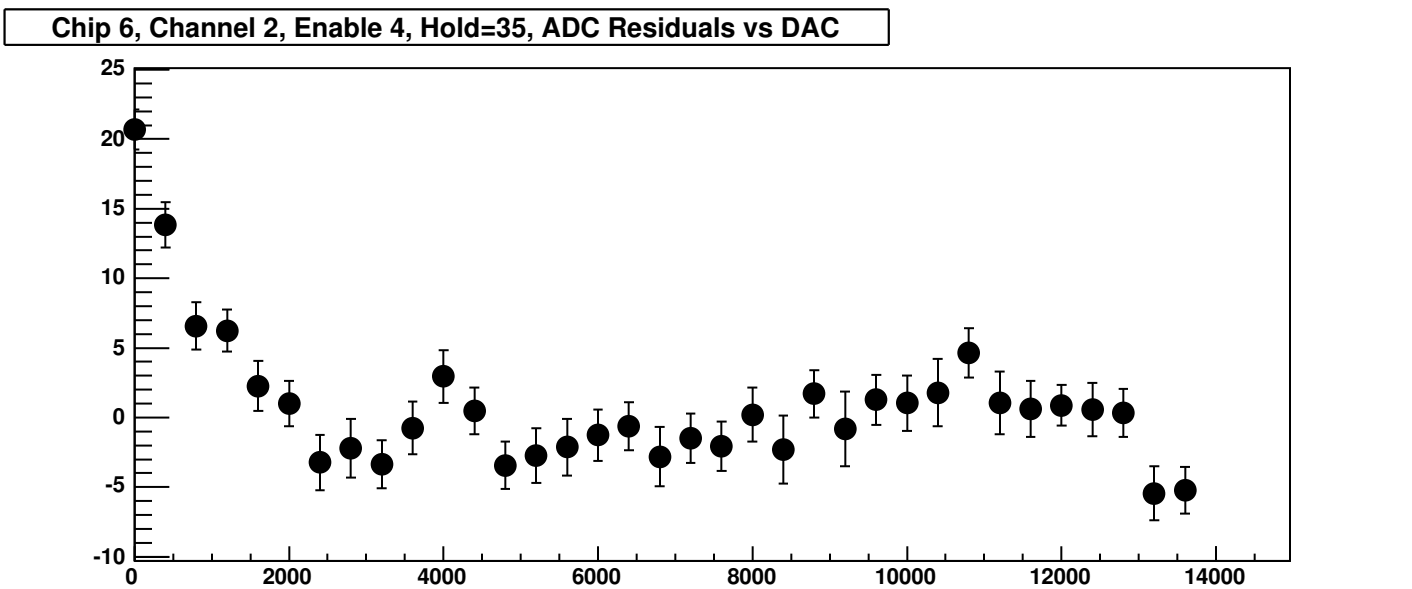


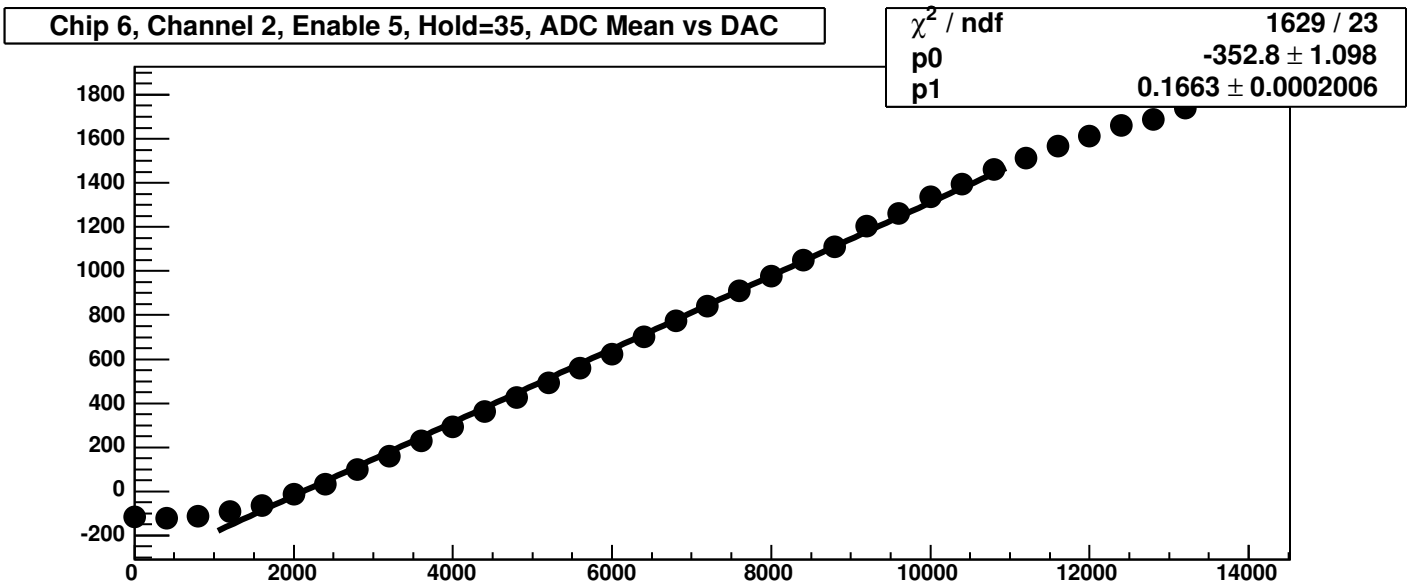
Chip 6, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC

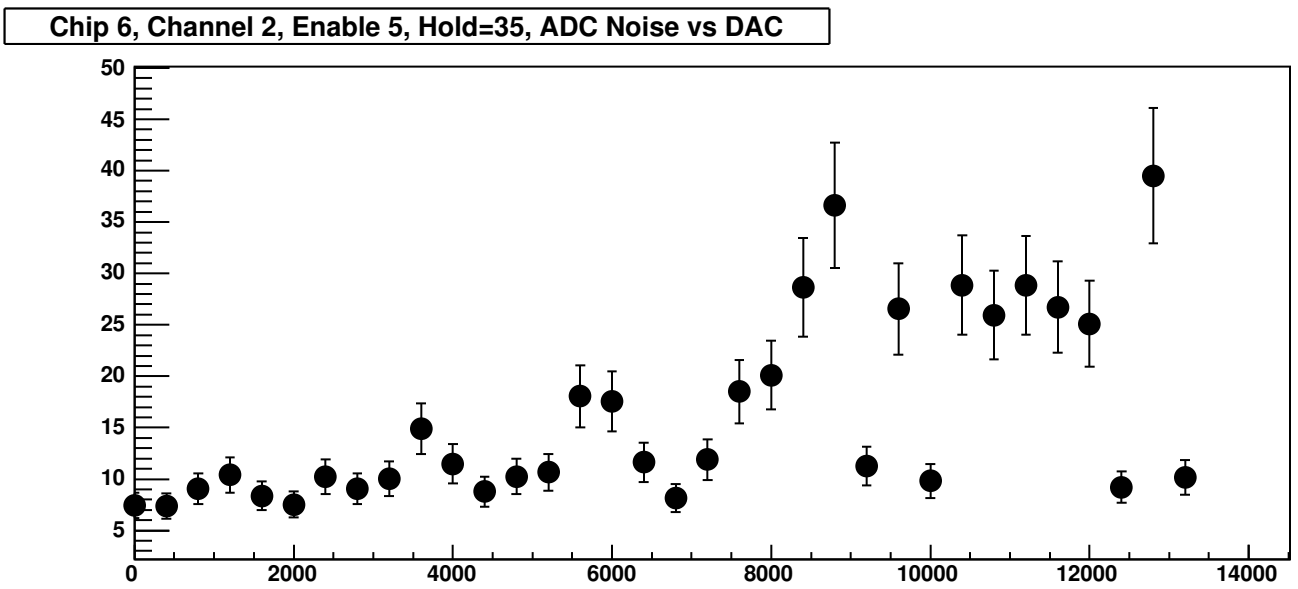


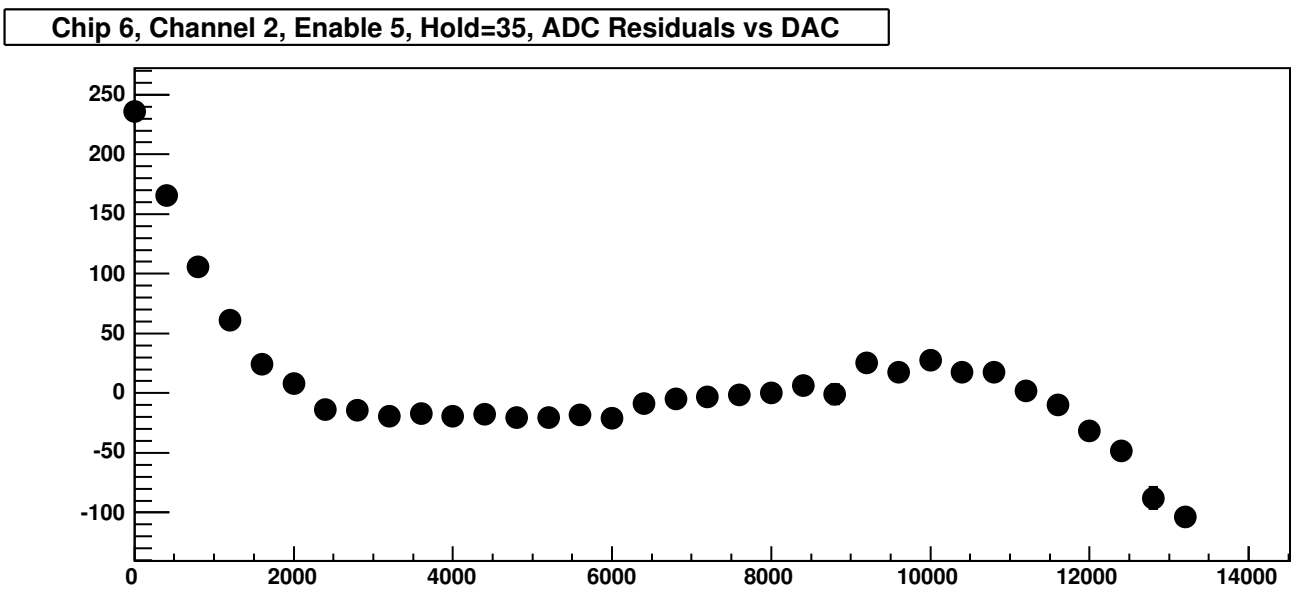

Chip 6, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 50.88 / 23
p0 -150.8 ± 0.8243
p1 0.01692 ± 0.0001288
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

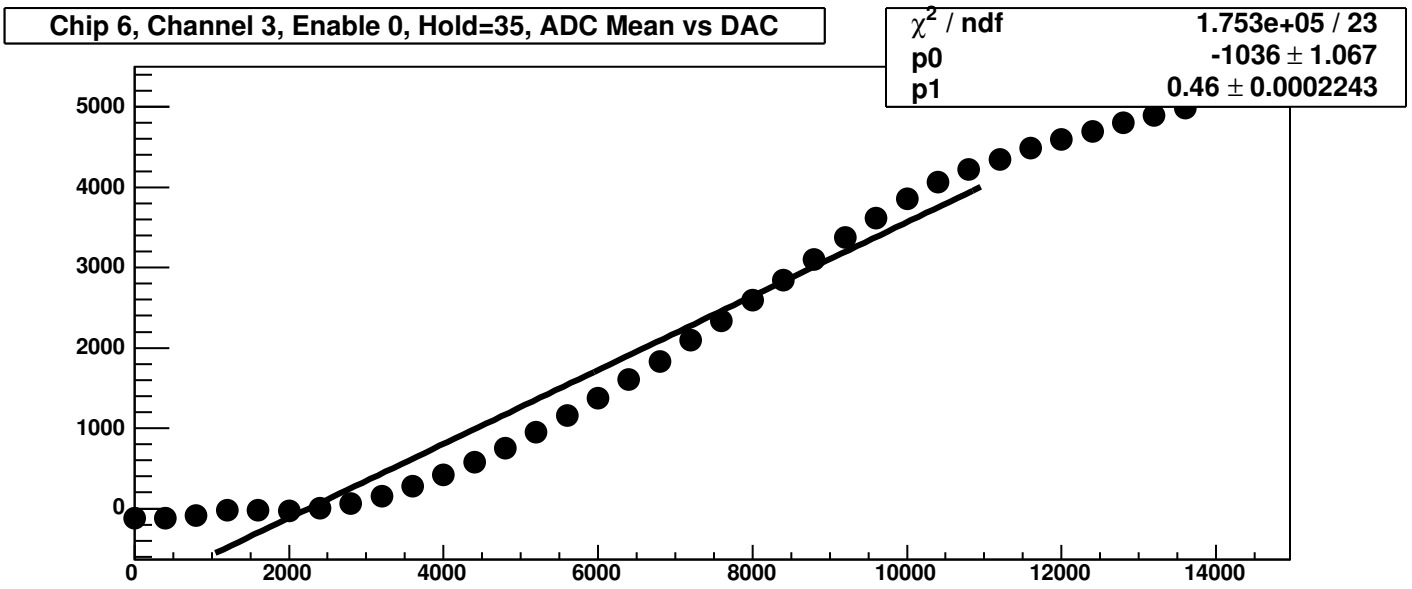

Chip 6, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

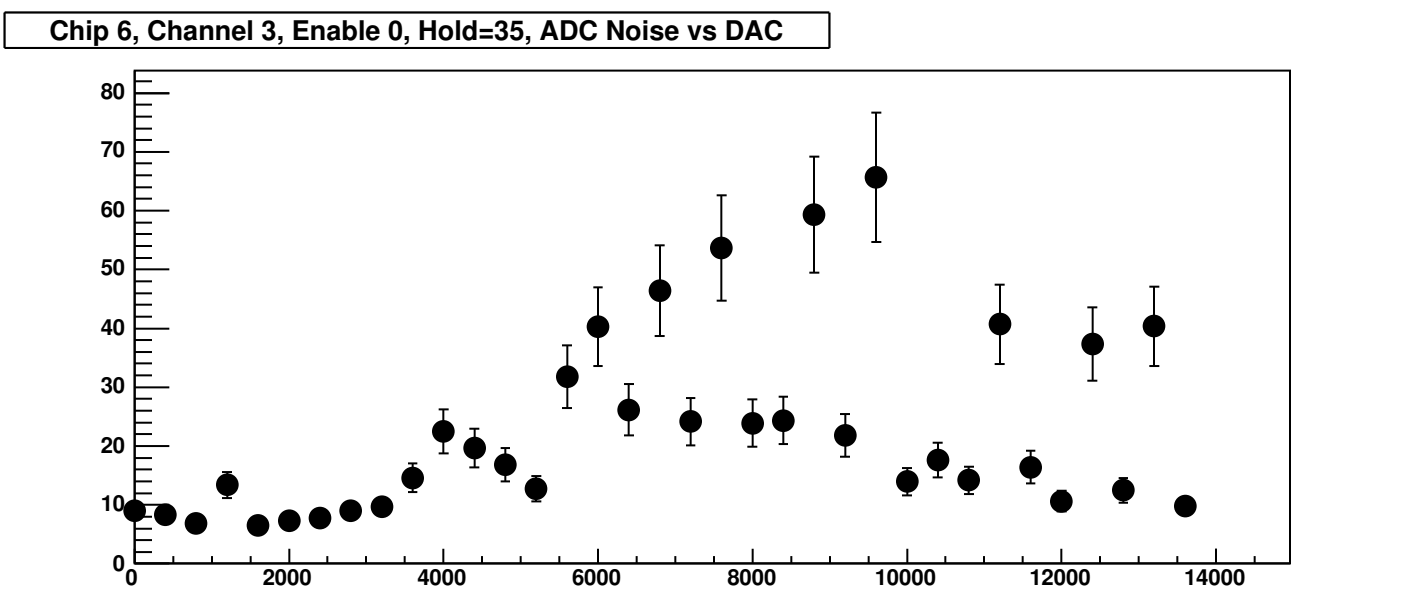

Chip 6, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

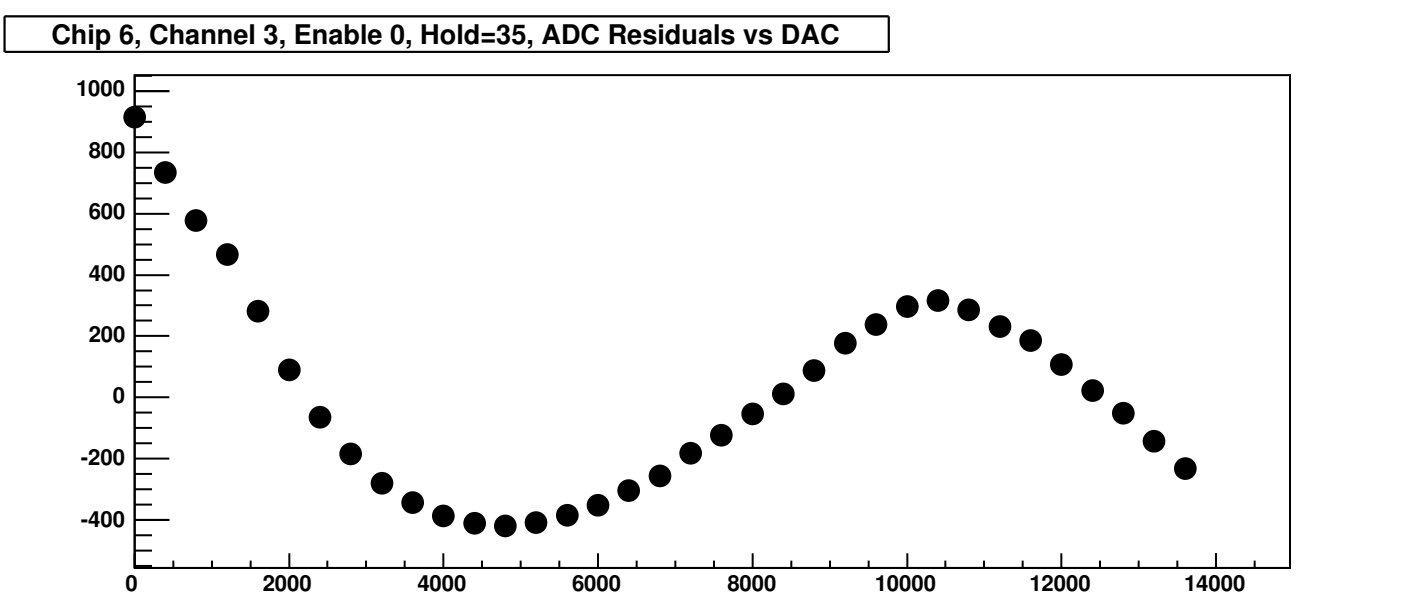

Chip 6, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1629 / 23
p0 -352.8 ± 1.098
p1 0.1663 ± 0.0002006
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

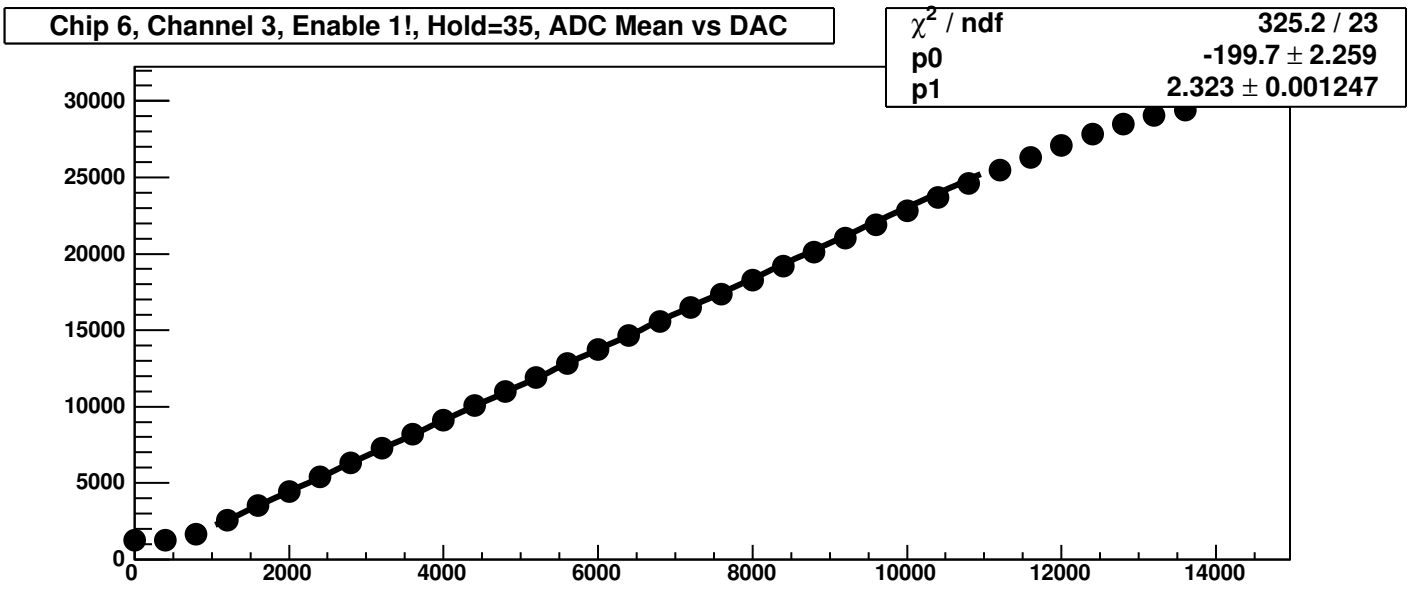

Chip 6, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

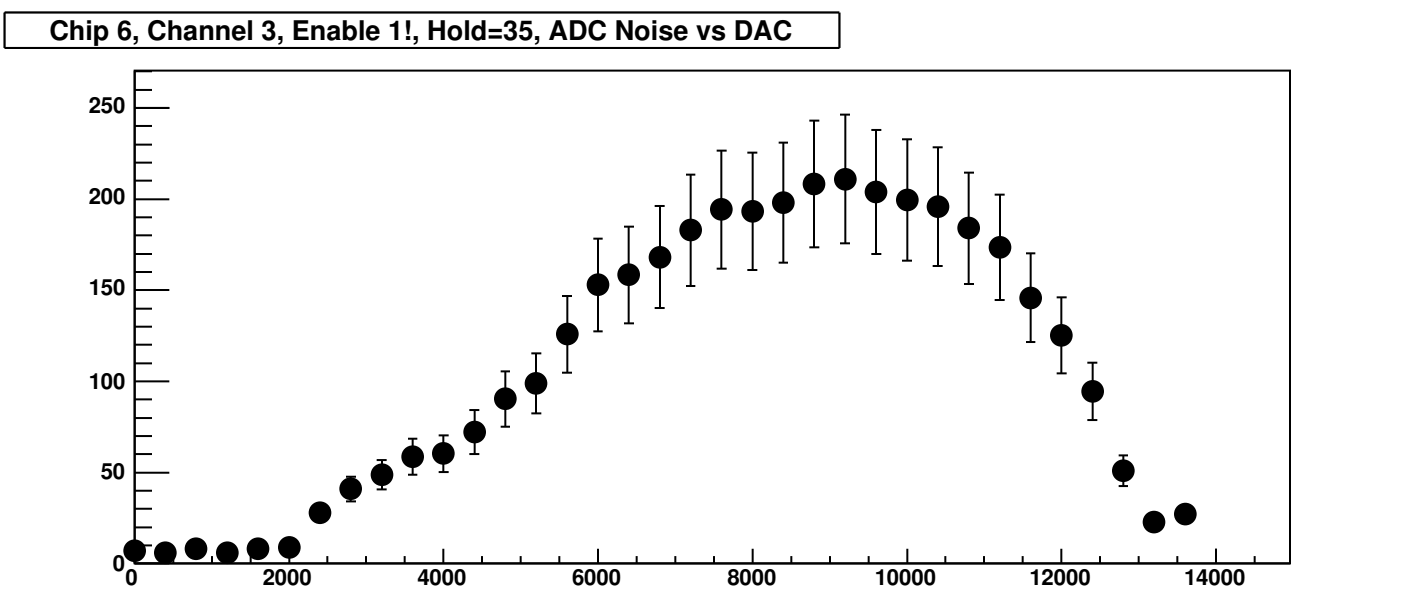

Chip 6, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

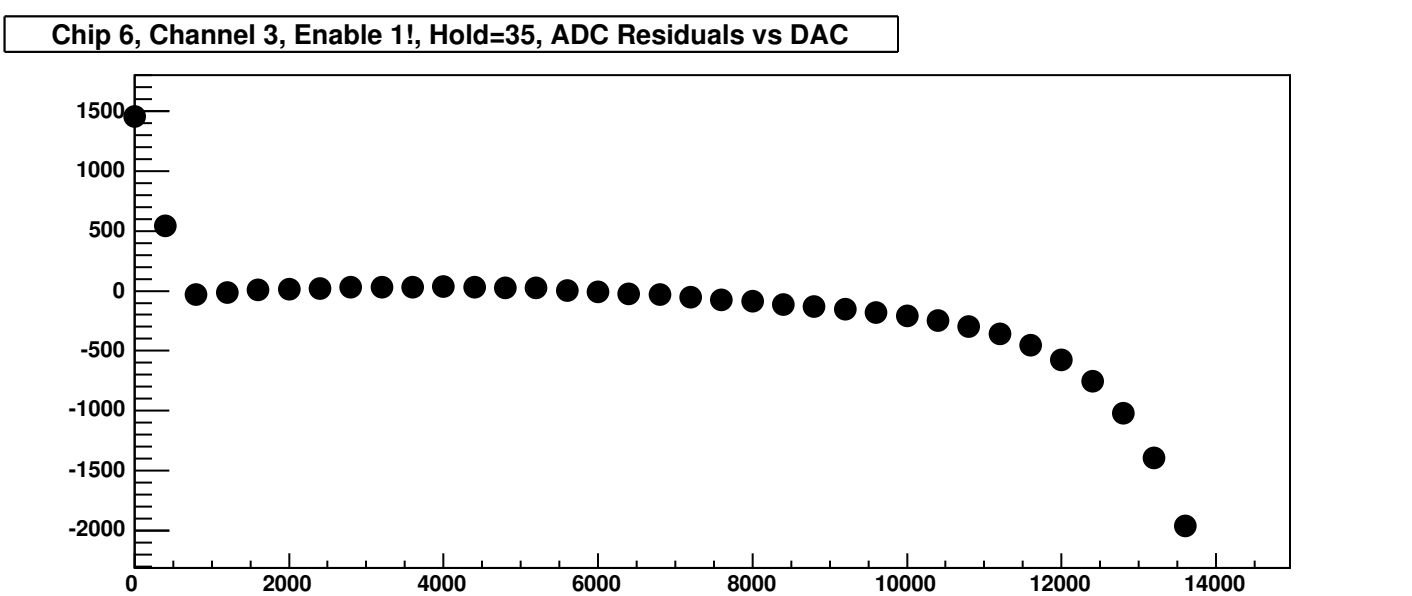

Chip 6, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.753e+05 / 23
p0 -1036 ± 1.067
p1 0.46 ± 0.0002243
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

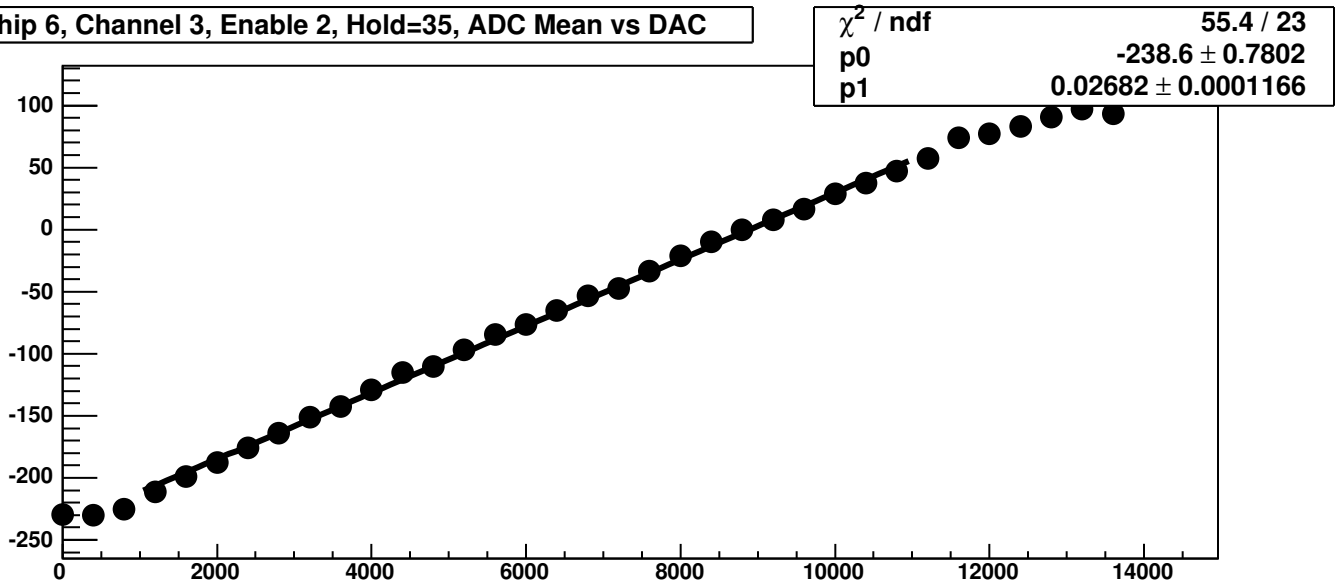

Chip 6, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 325.2 / 23
p0 -199.7 ± 2.259
p1 2.323 ± 0.001247
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

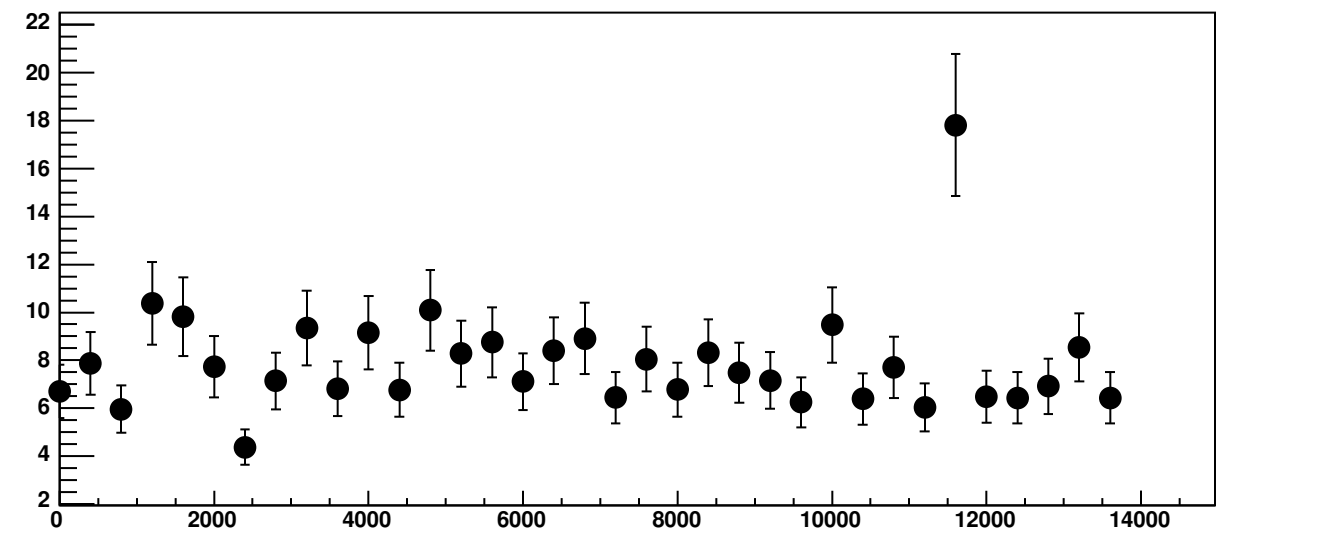

Chip 6, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

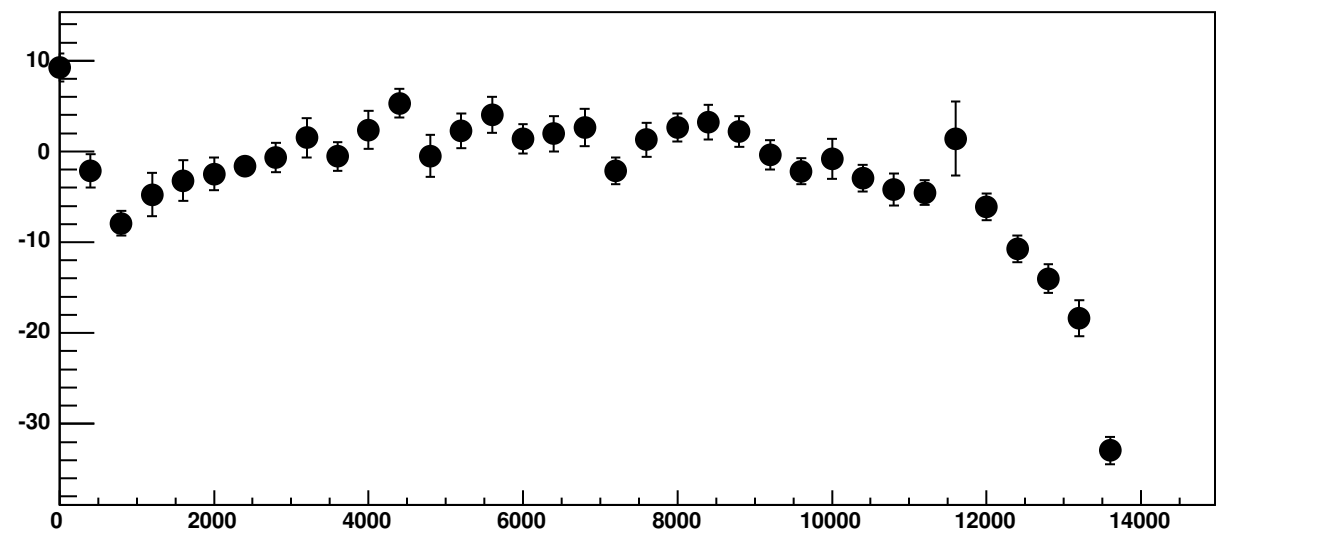
Chip 6, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

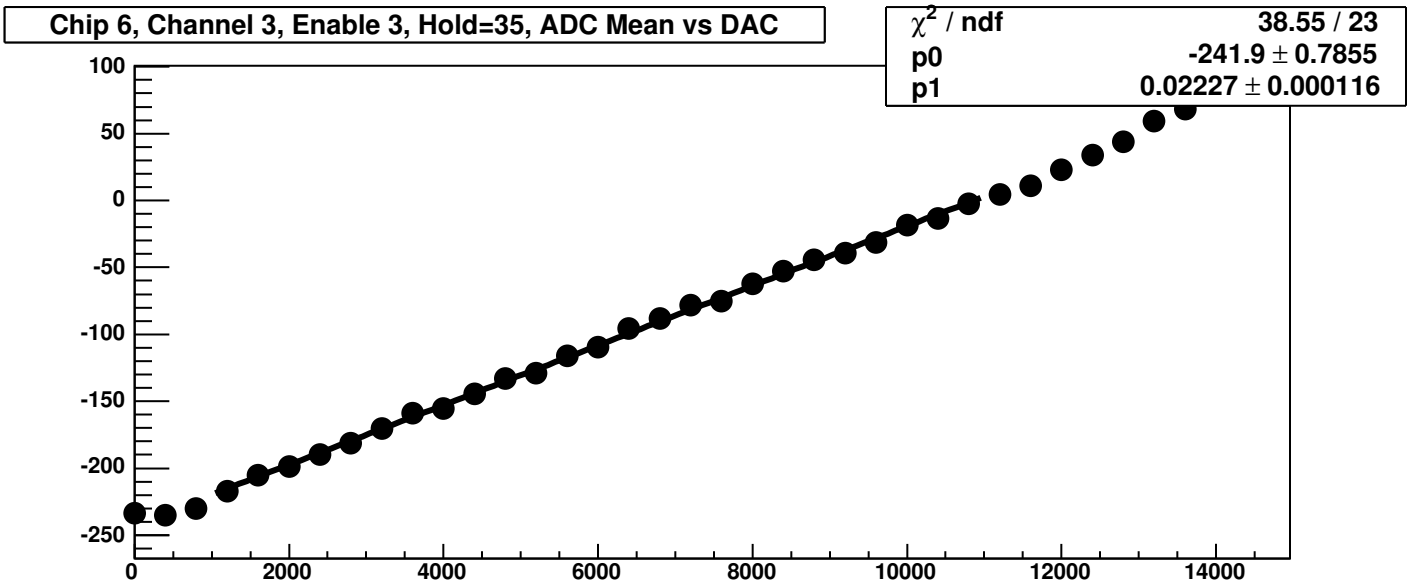


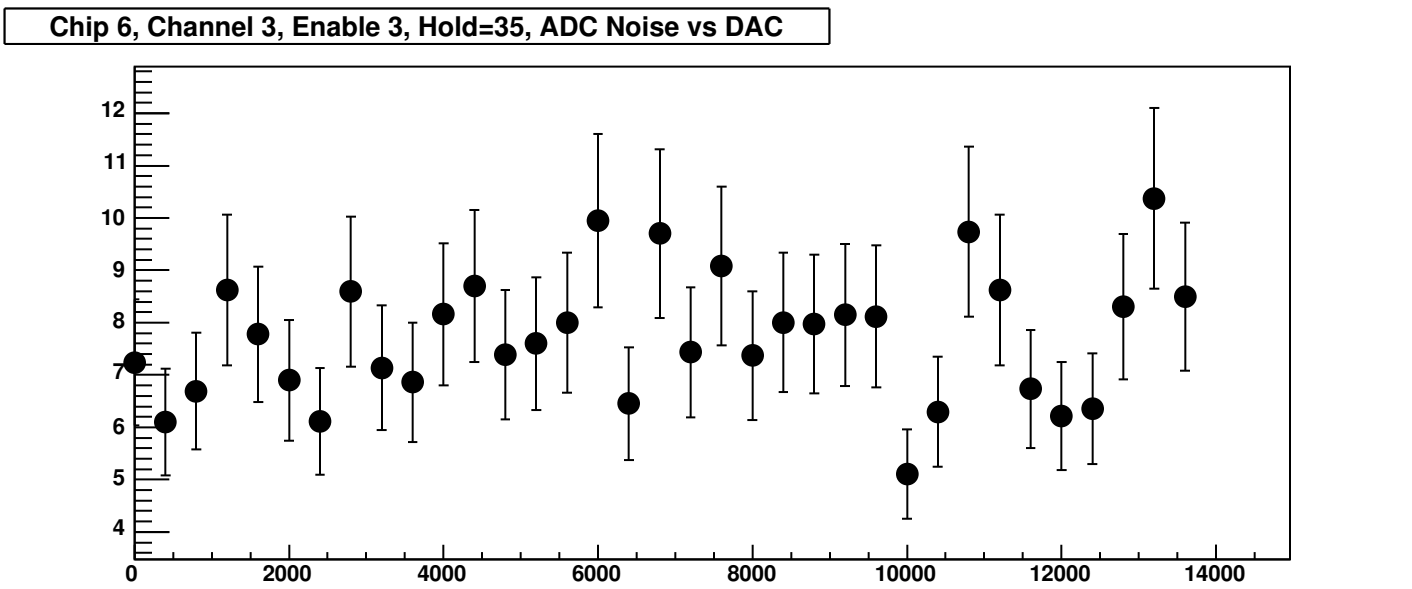
Chip 6, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC

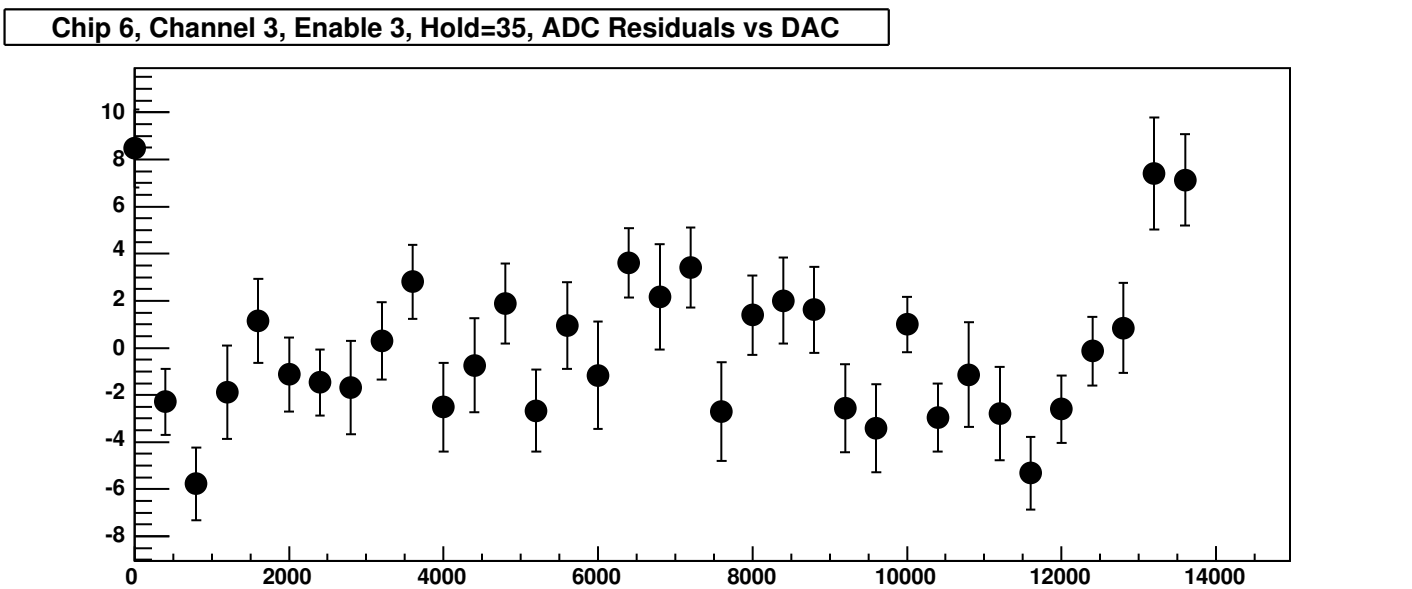


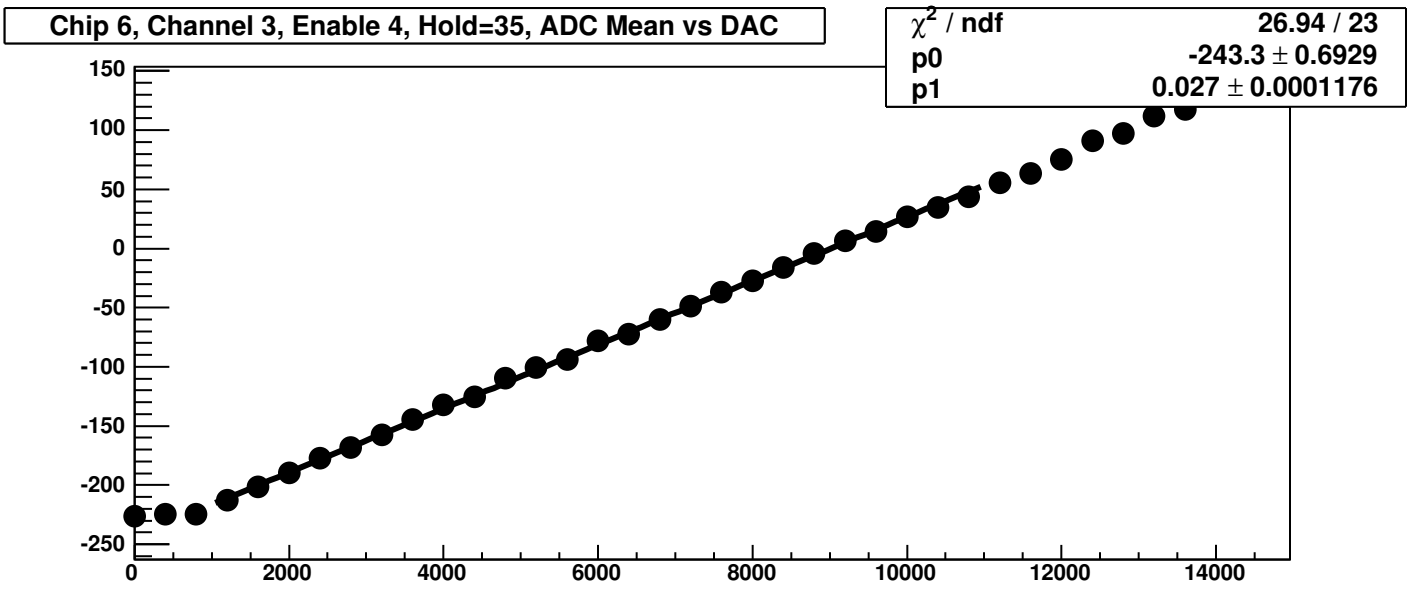
Chip 6, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

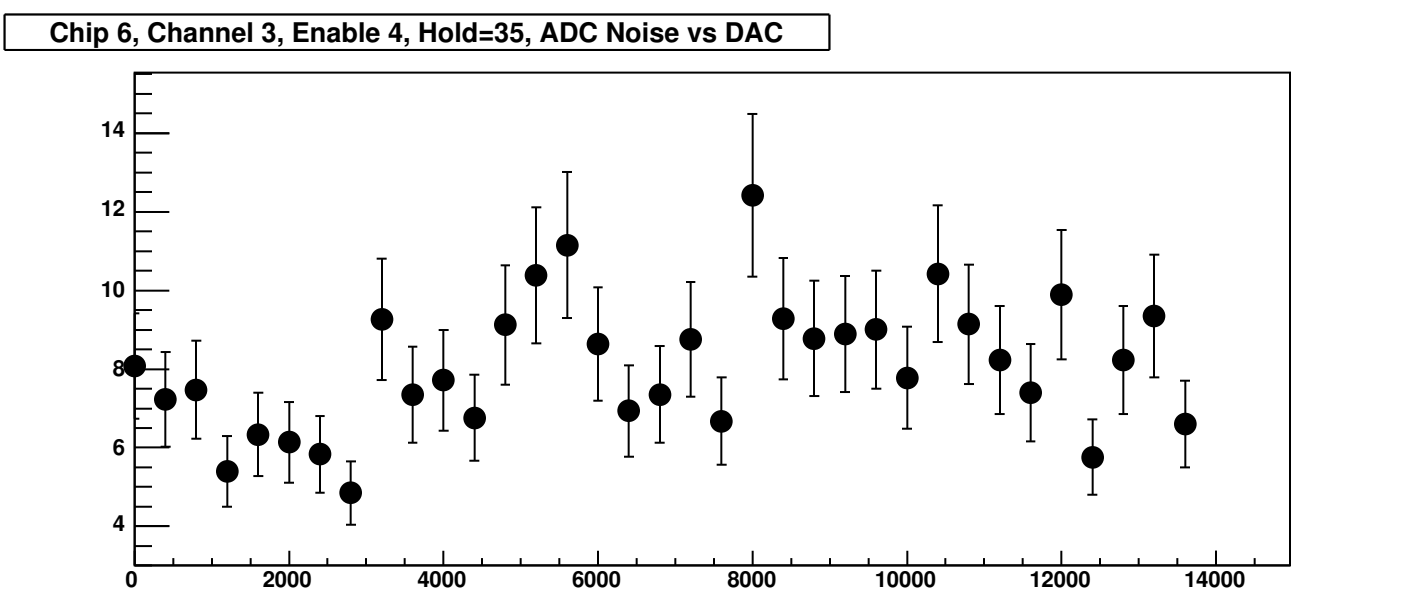


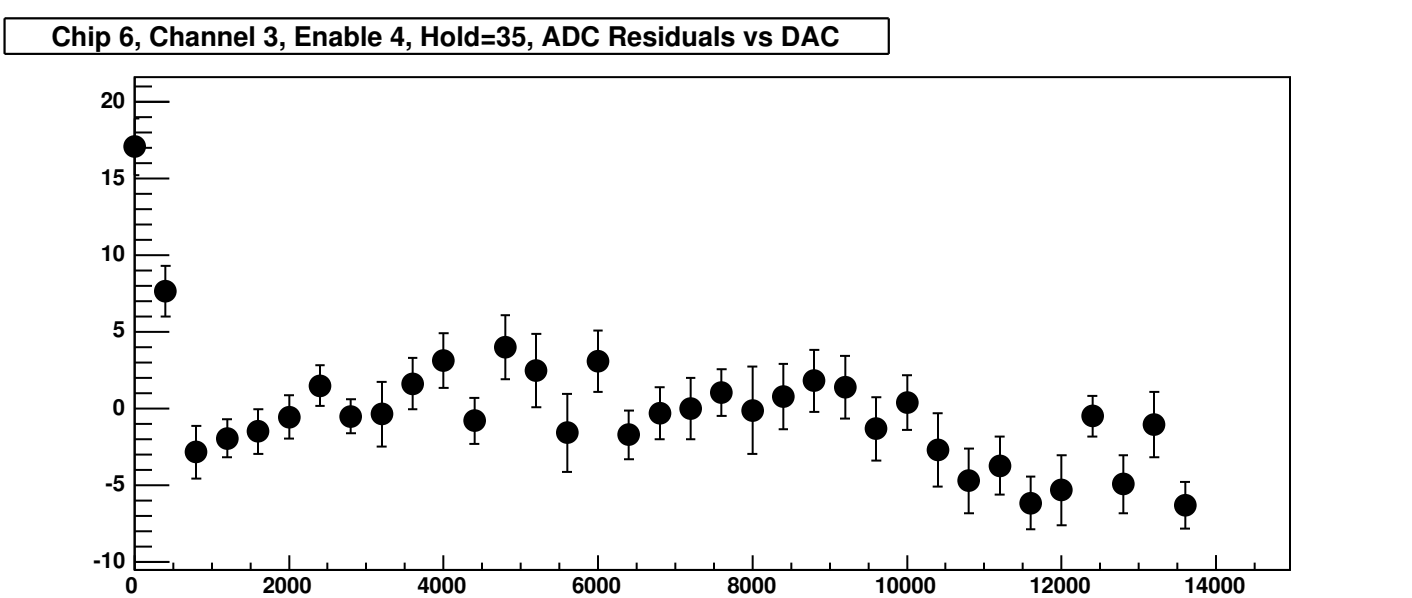

Chip 6, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.55 / 23
p0 -241.9 ± 0.7855
p1 0.02227 ± 0.000116
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

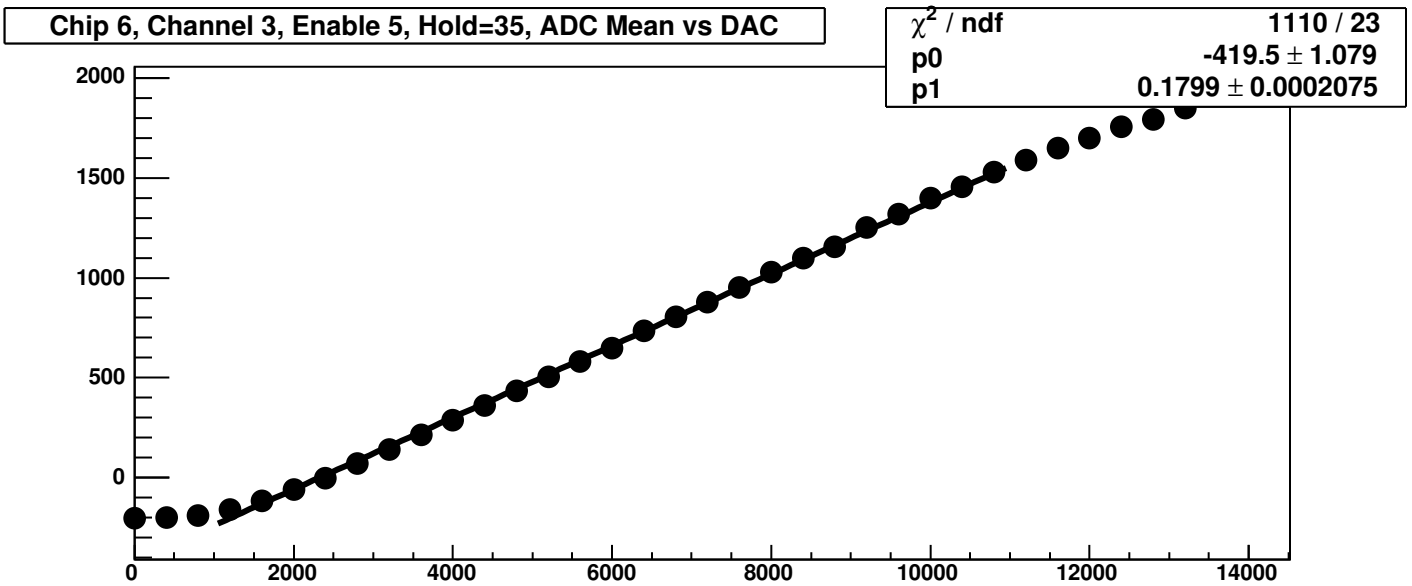

Chip 6, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

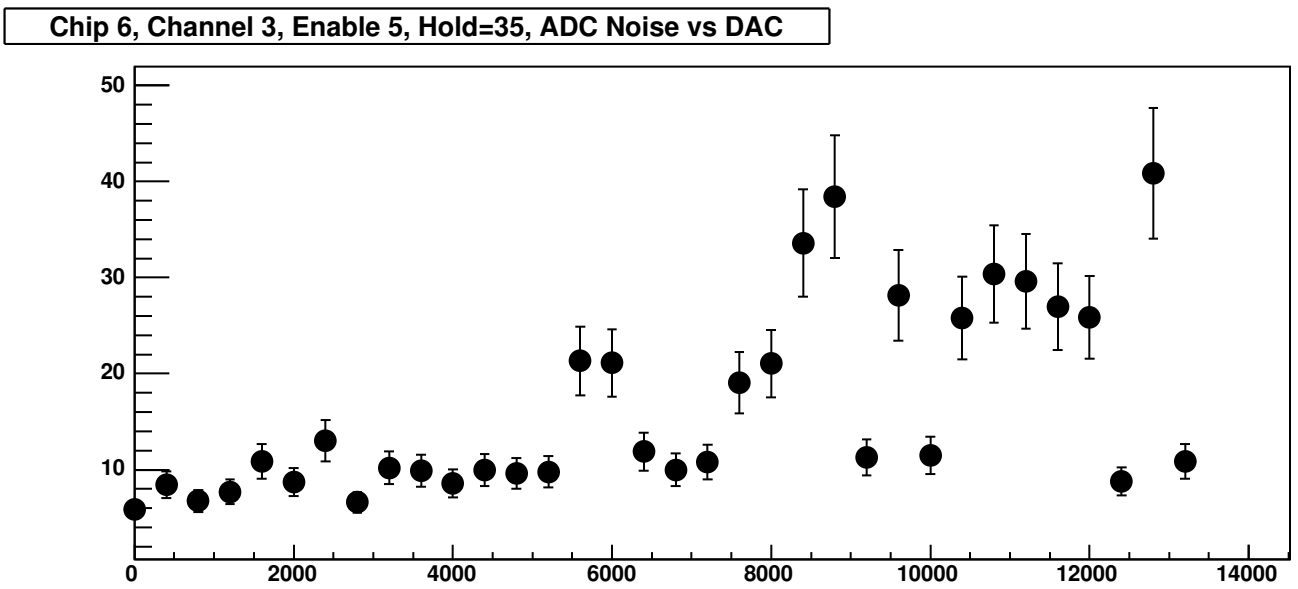

Chip 6, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

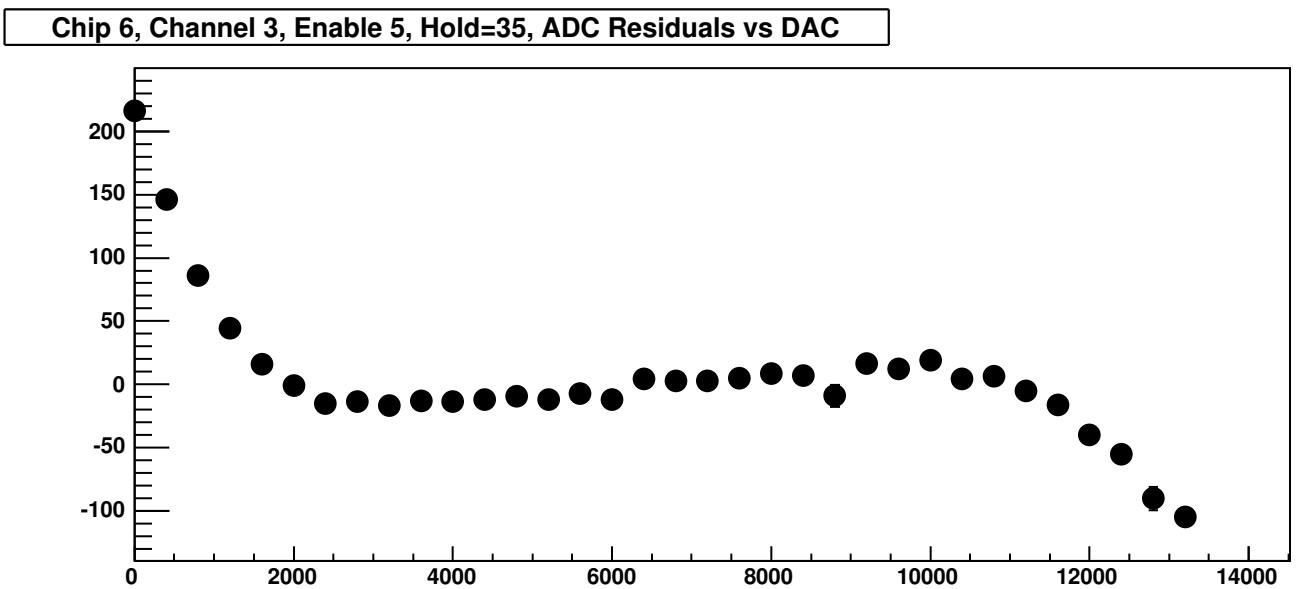

Chip 6, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.94 / 23
p0 -243.3 ± 0.6929
p1 0.027 ± 0.0001176
150
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

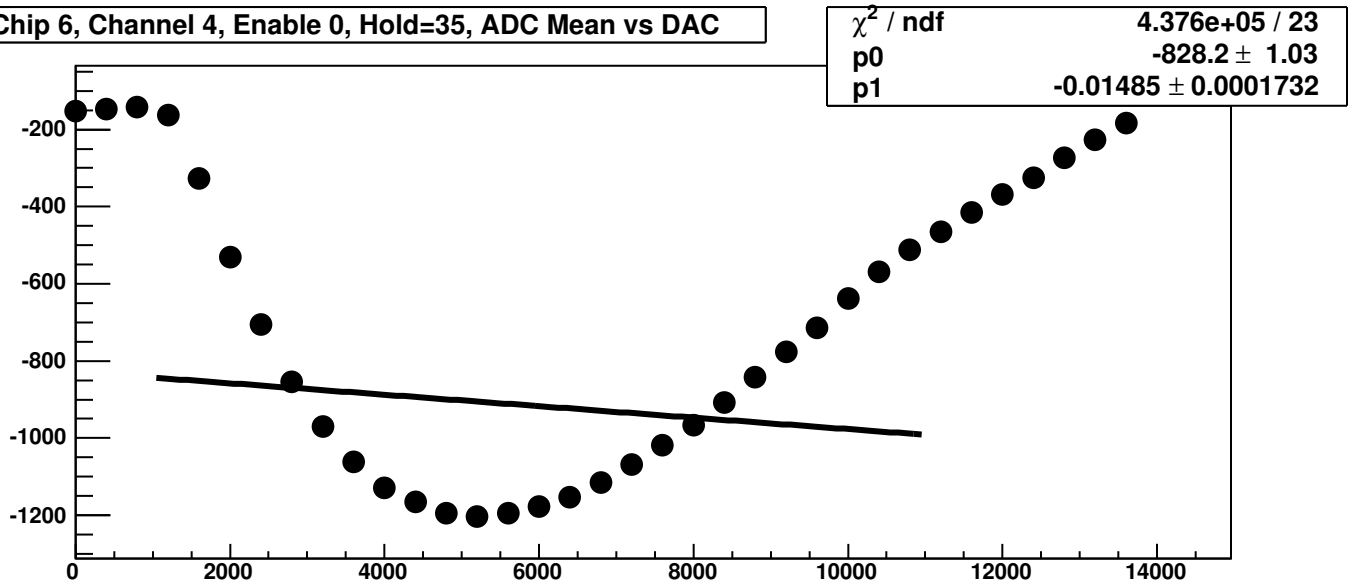

Chip 6, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1110 / 23
p0 -419.5 ± 1.079
p1 0.1799 ± 0.0002075
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

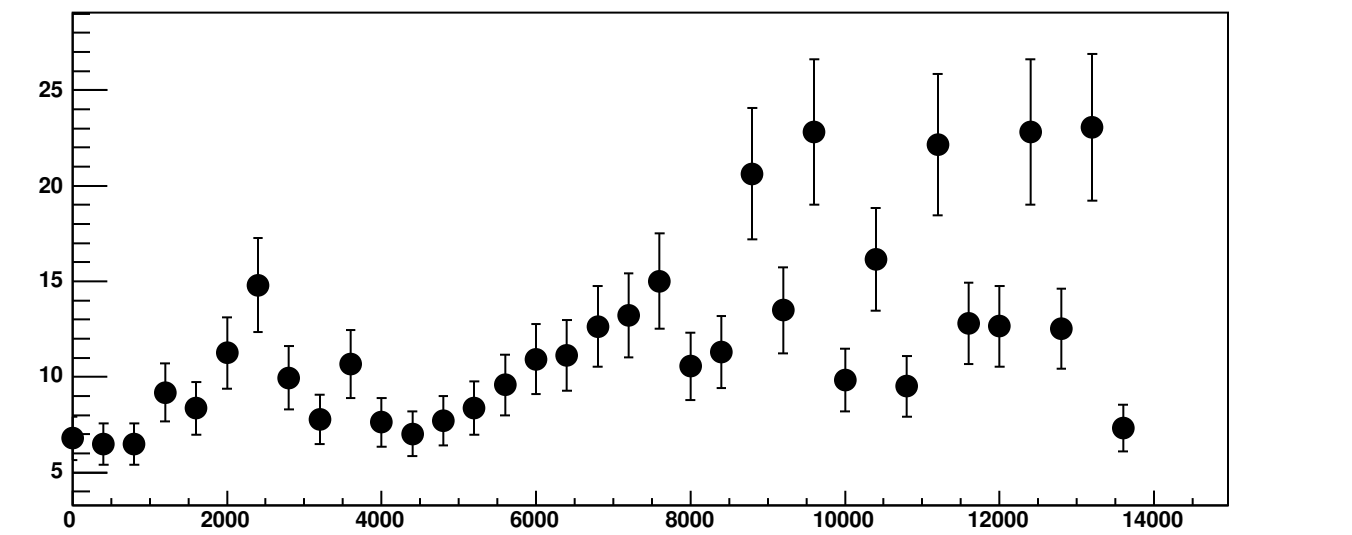

Chip 6, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

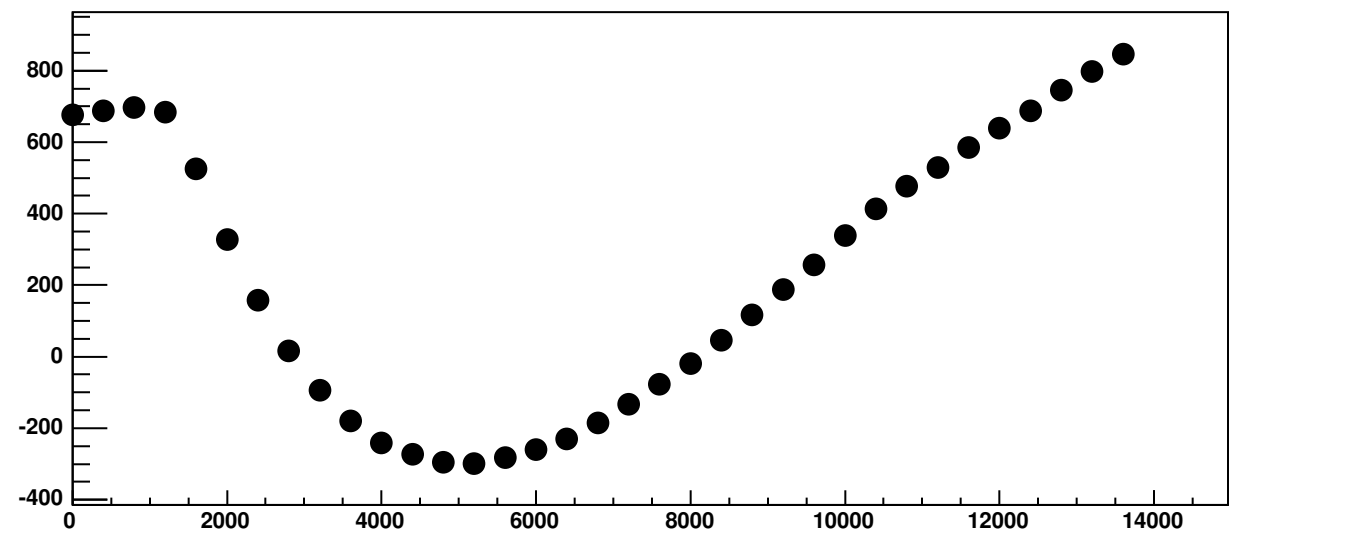
Chip 6, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC



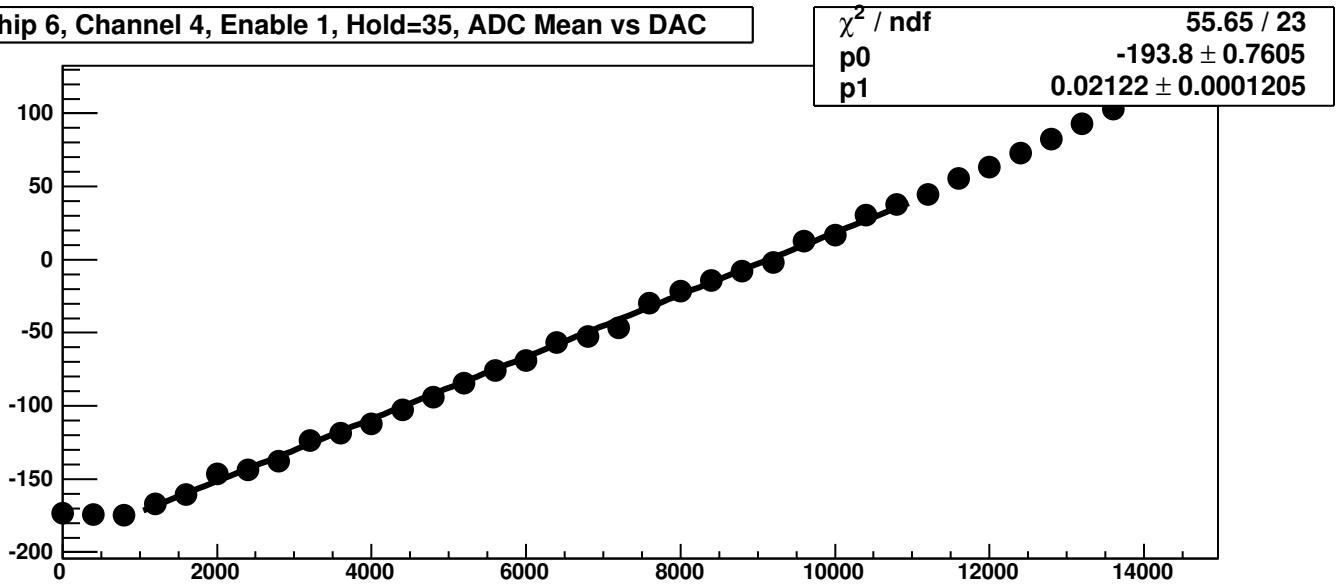
Chip 6, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC



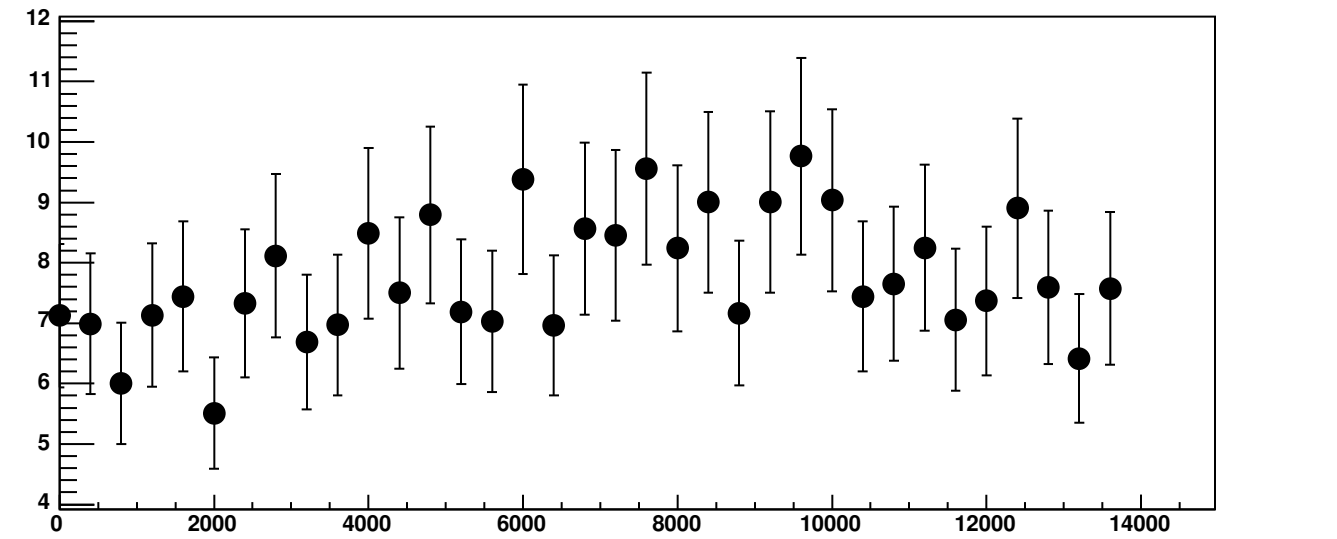
Chip 6, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC



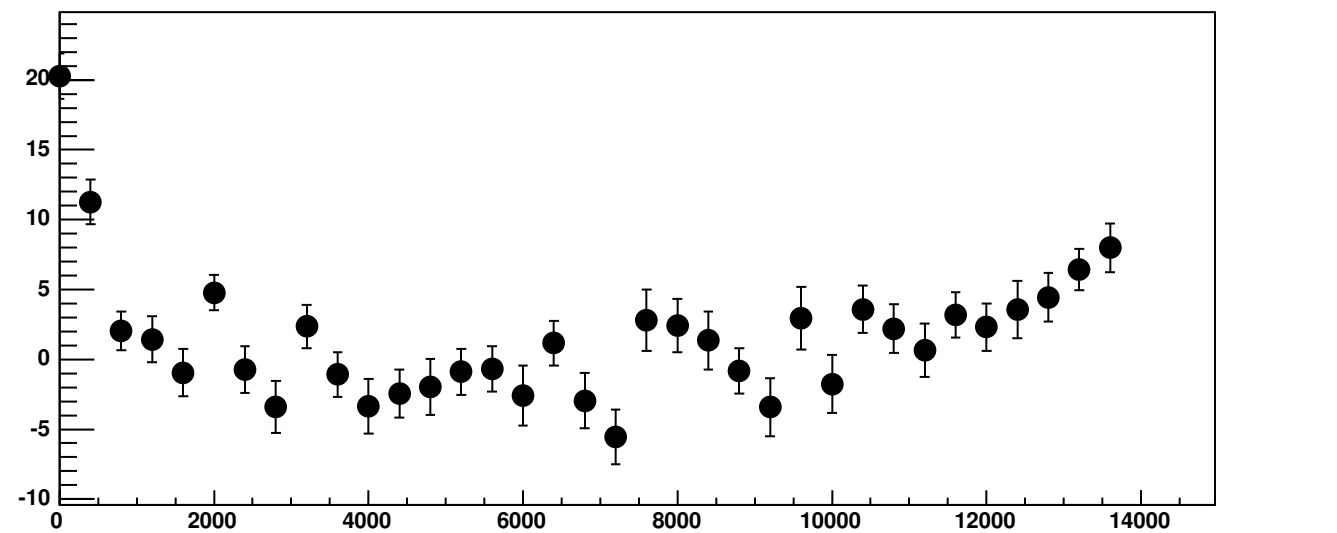
Chip 6, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC

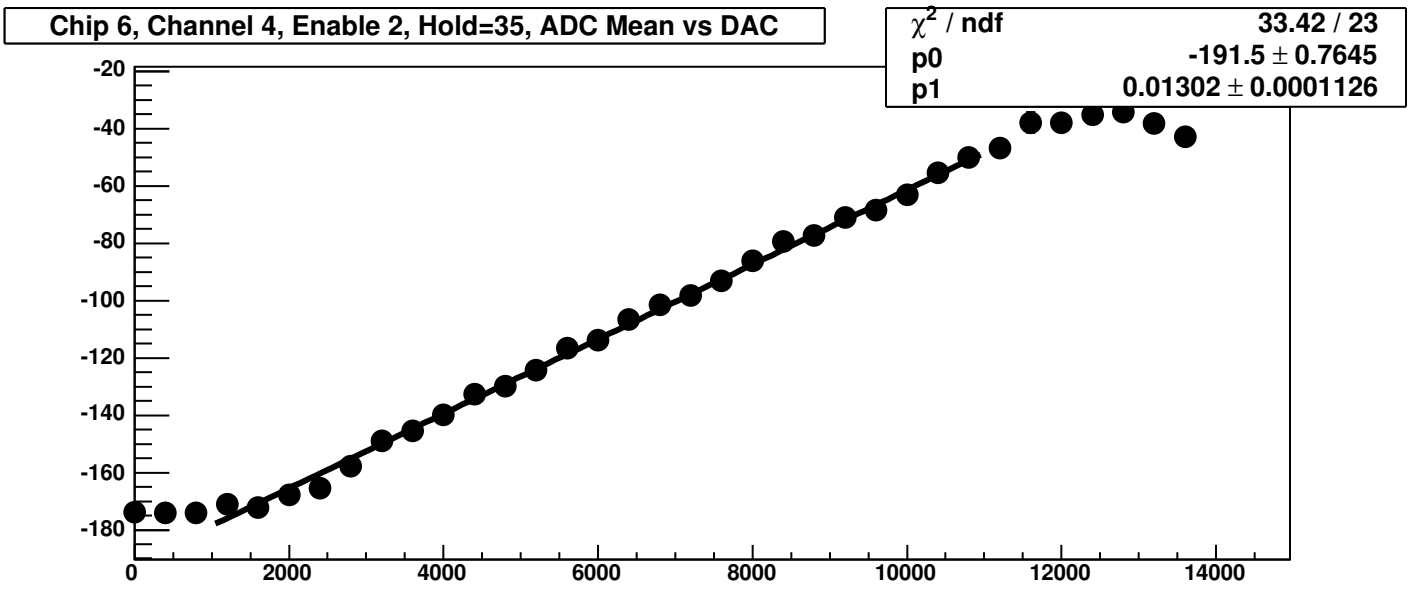


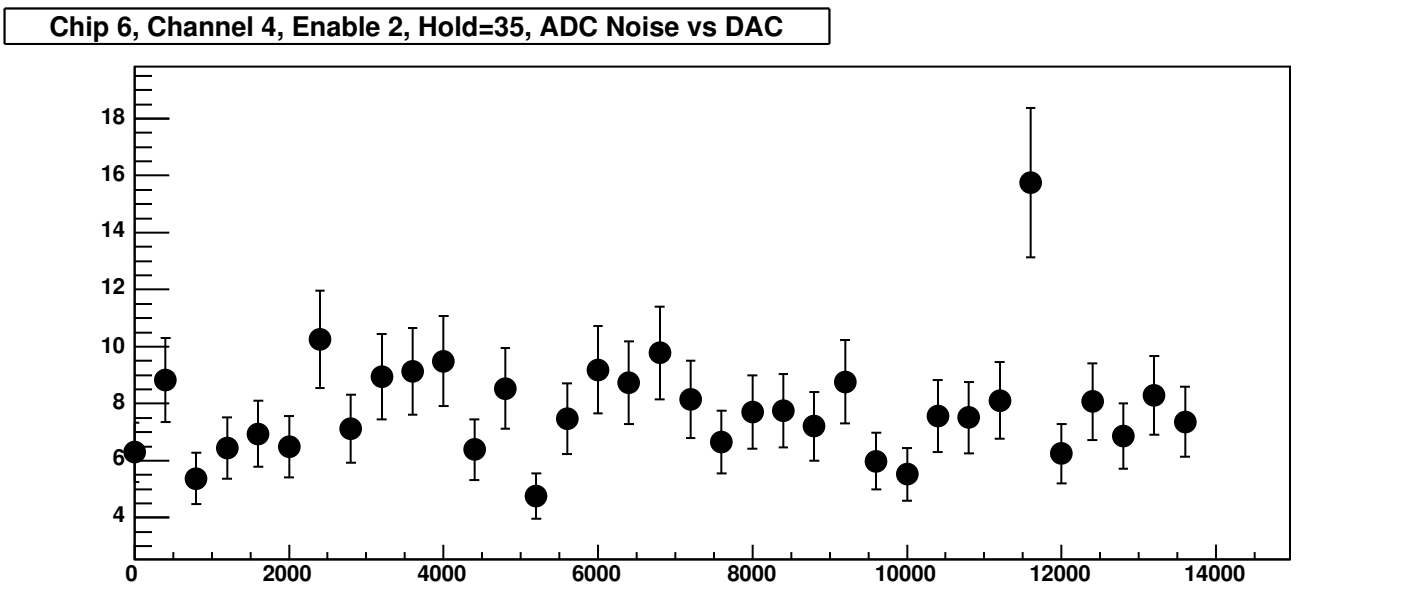
Chip 6, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC

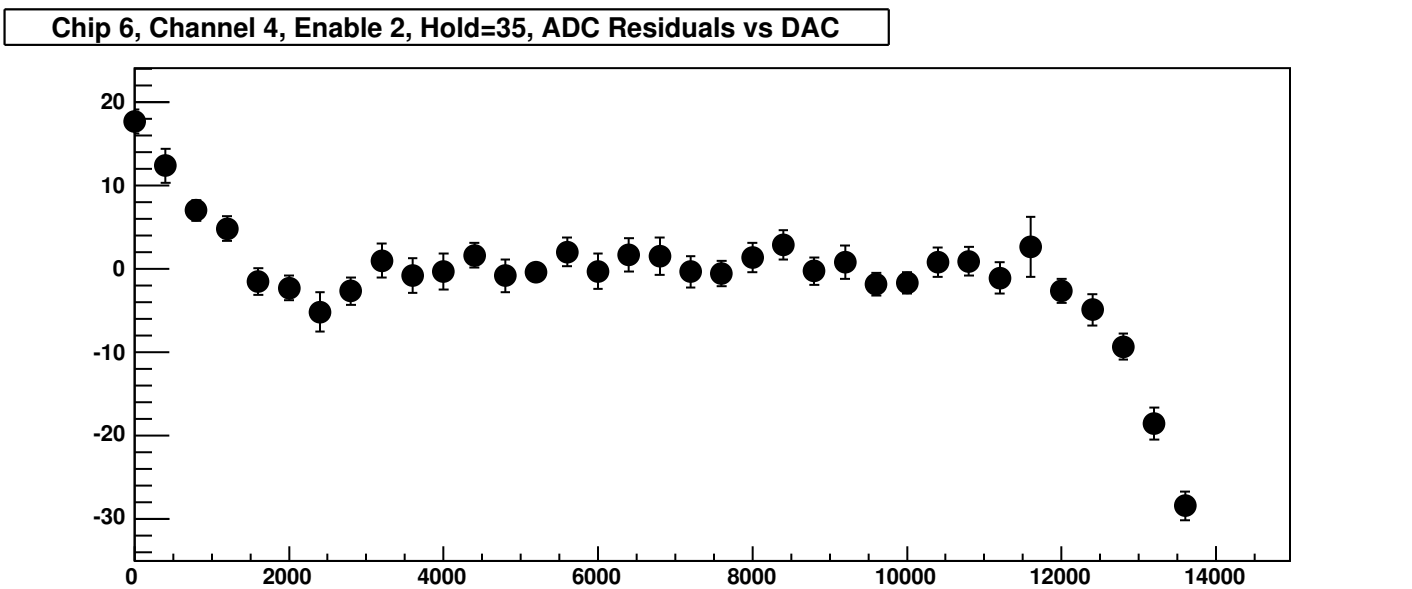


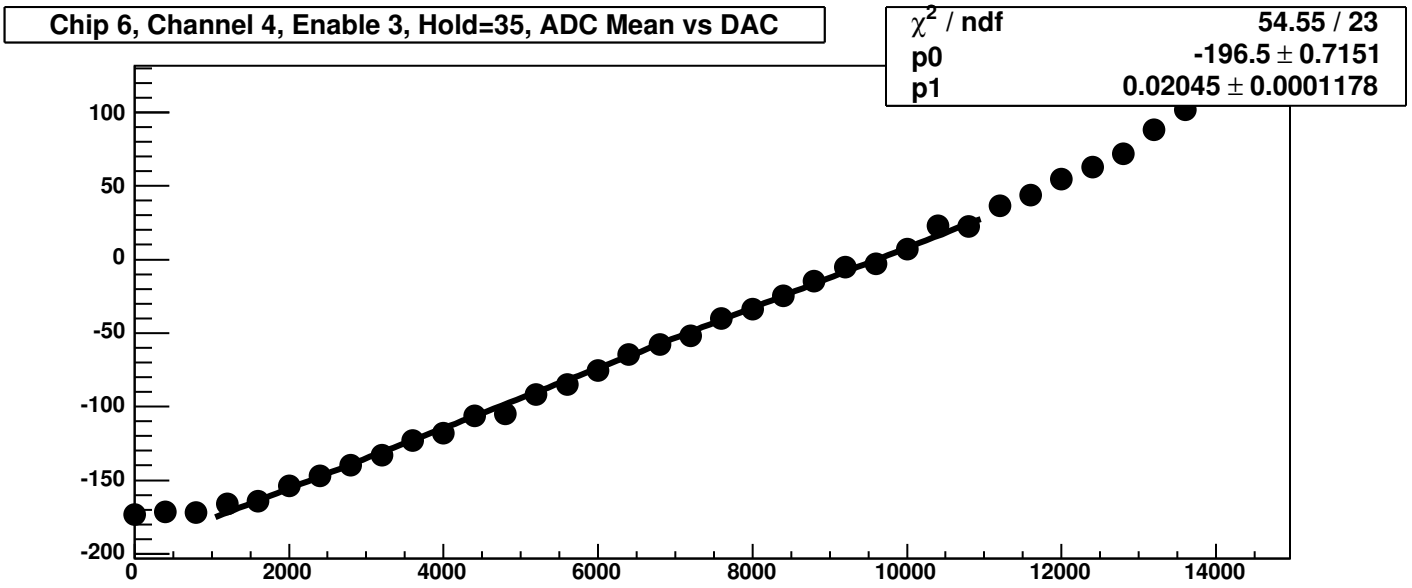
Chip 6, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC

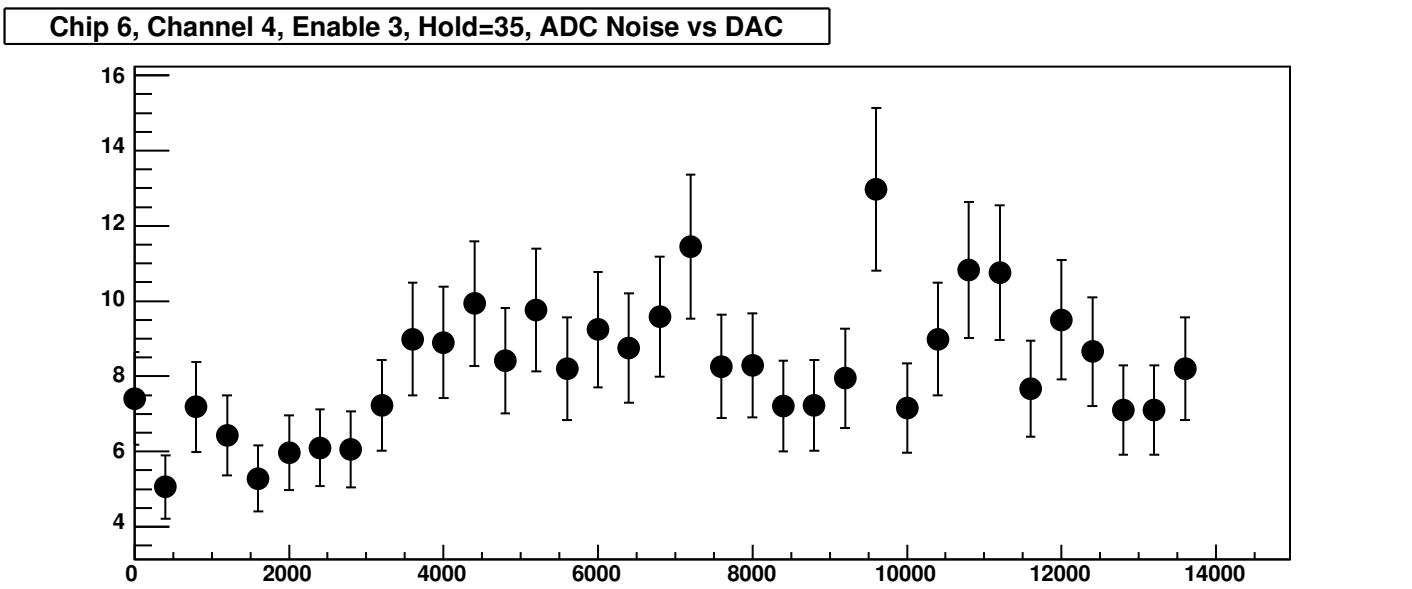


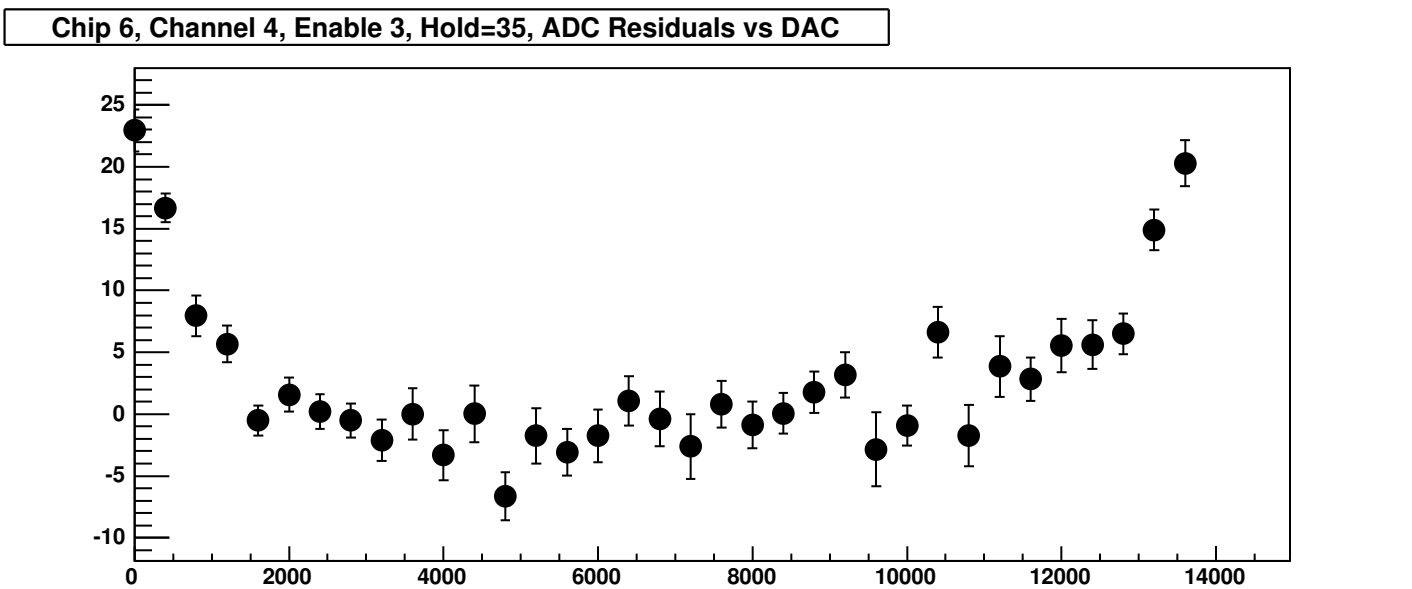

Chip 6, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 33.42 / 23
p0 -191.5 ± 0.7645
p1 0.01302 ± 0.0001126
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000

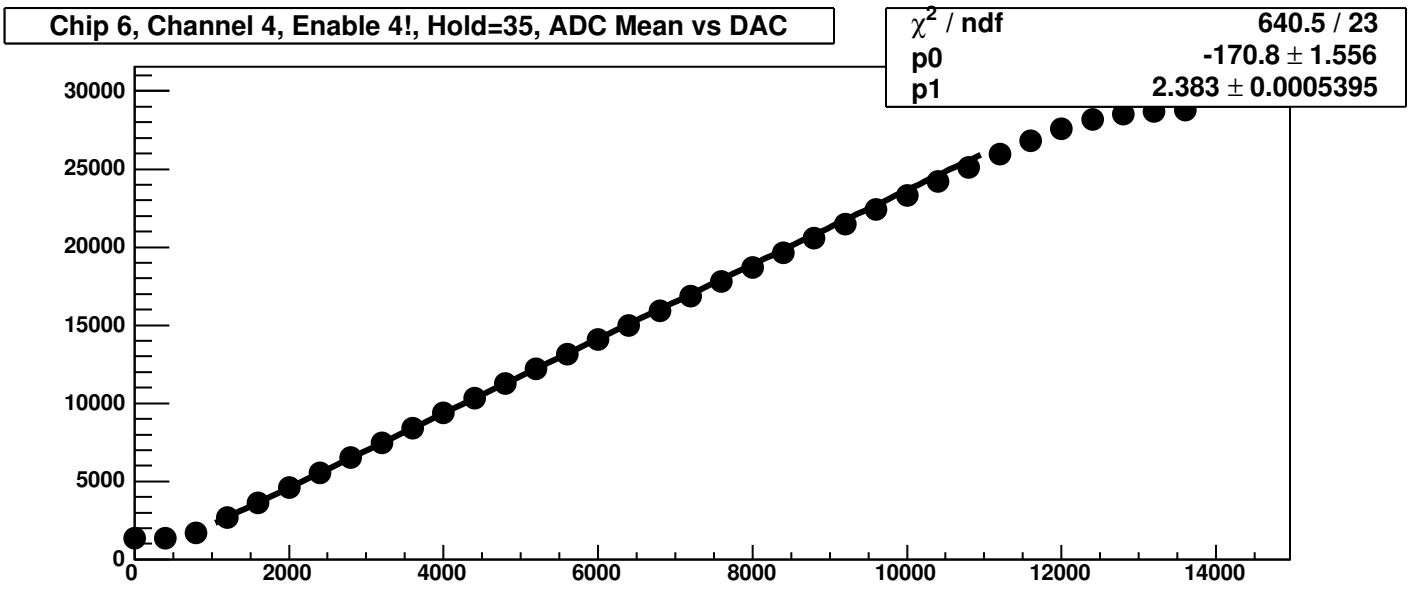

Chip 6, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

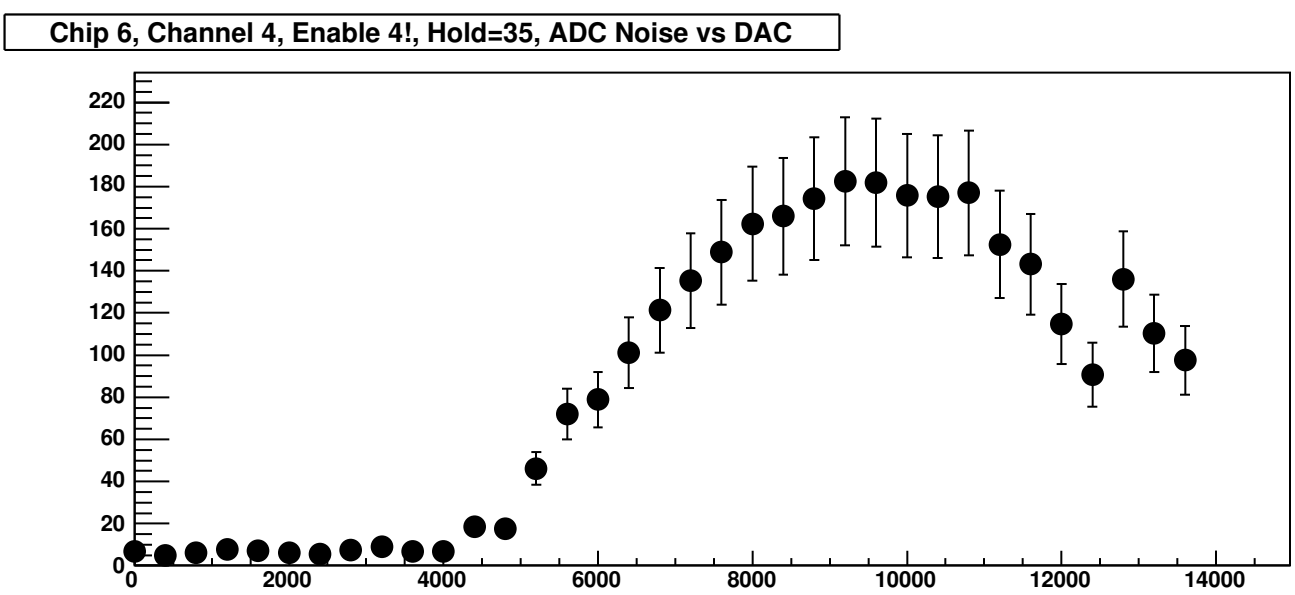

Chip 6, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

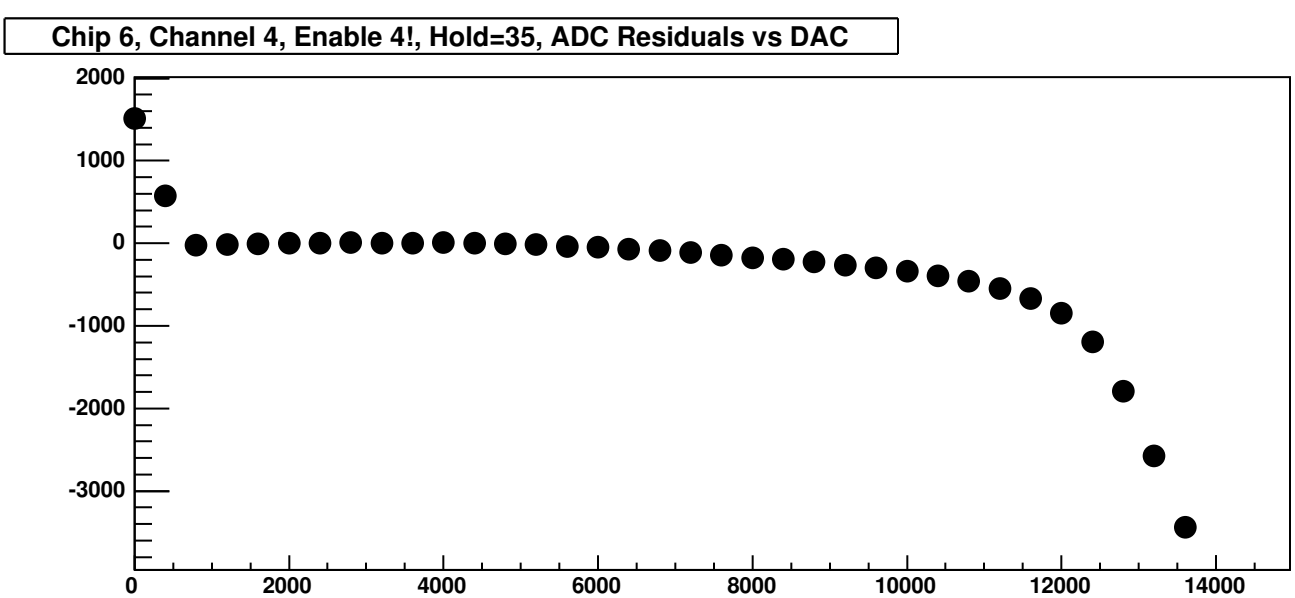

Chip 6, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 54.55 / 23
p0 -196.5 ± 0.7151
p1 0.02045 ± 0.0001178
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

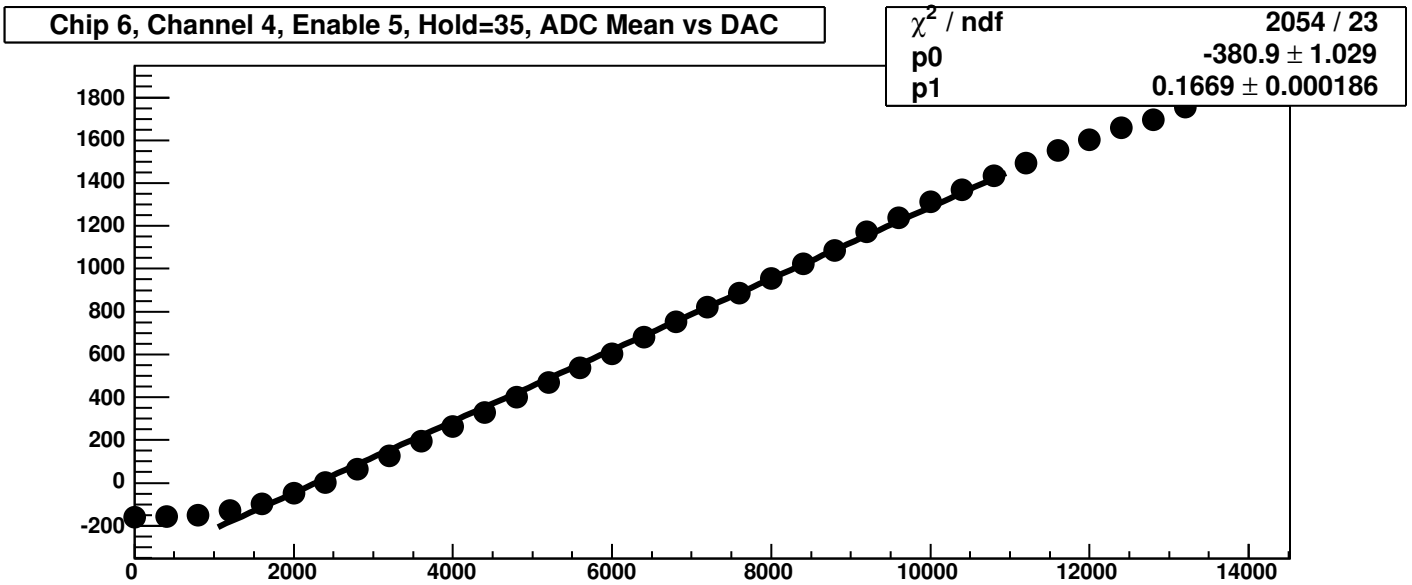

Chip 6, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

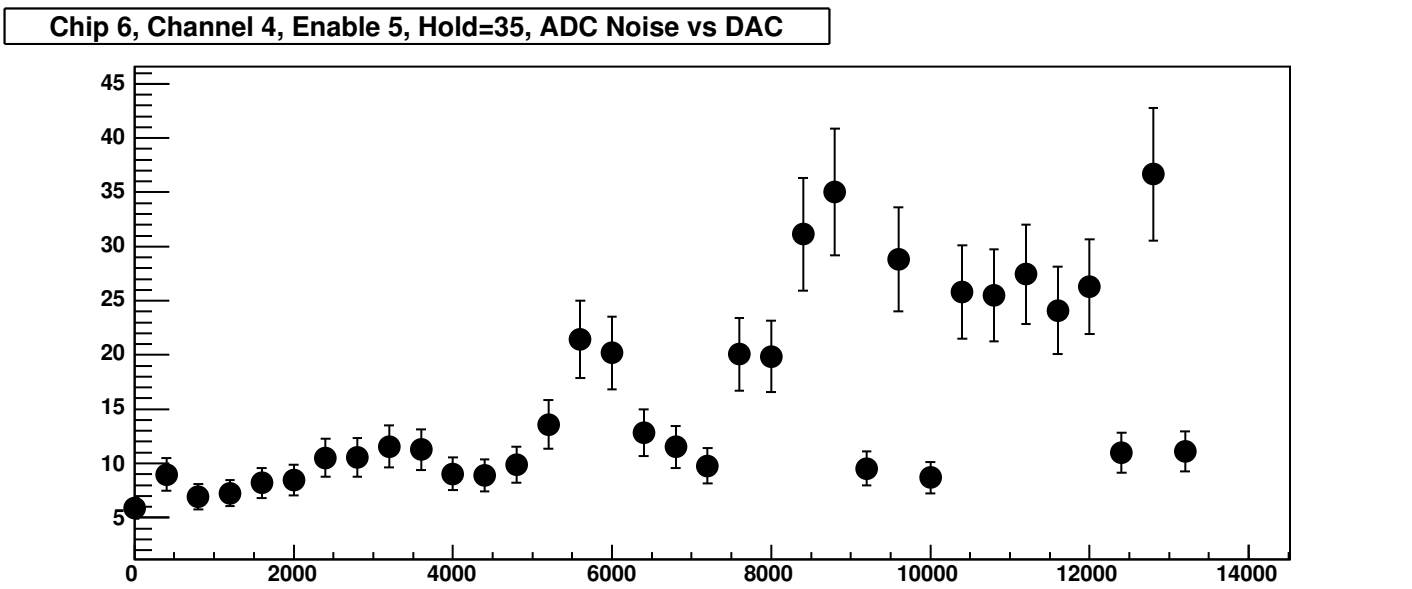

Chip 6, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

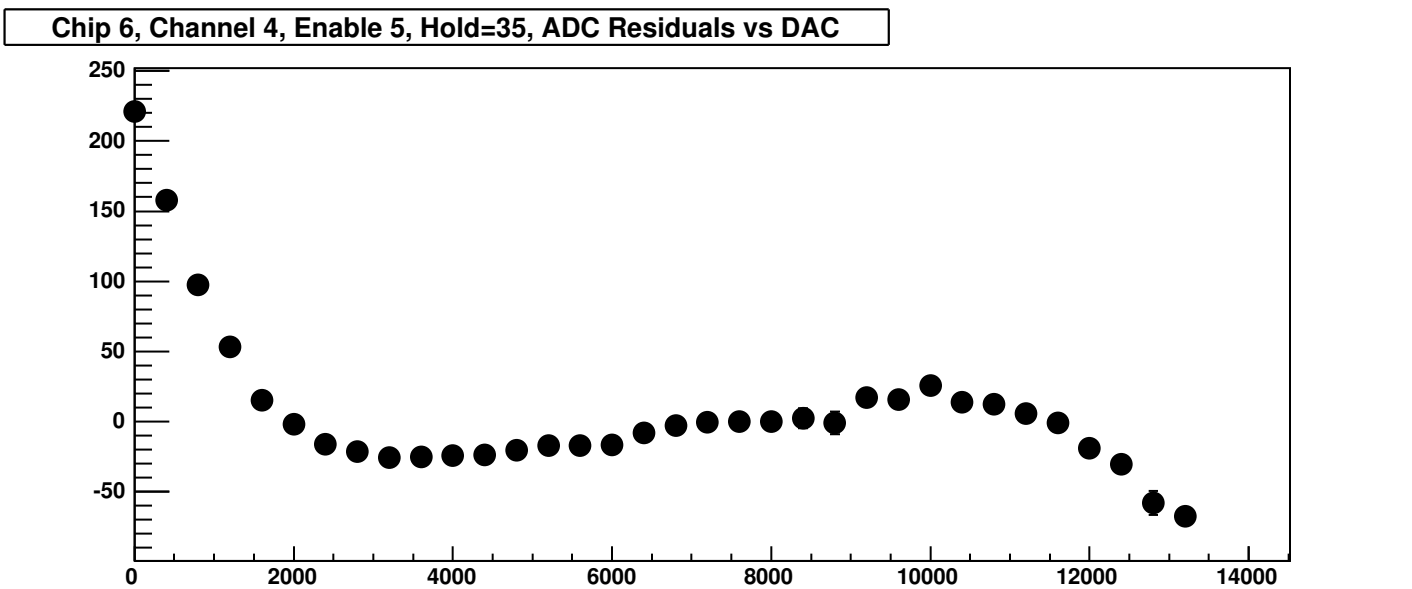

Chip 6, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 640.5 / 23
p0 -170.8 ± 1.556
p1 2.383 ± 0.0005395
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

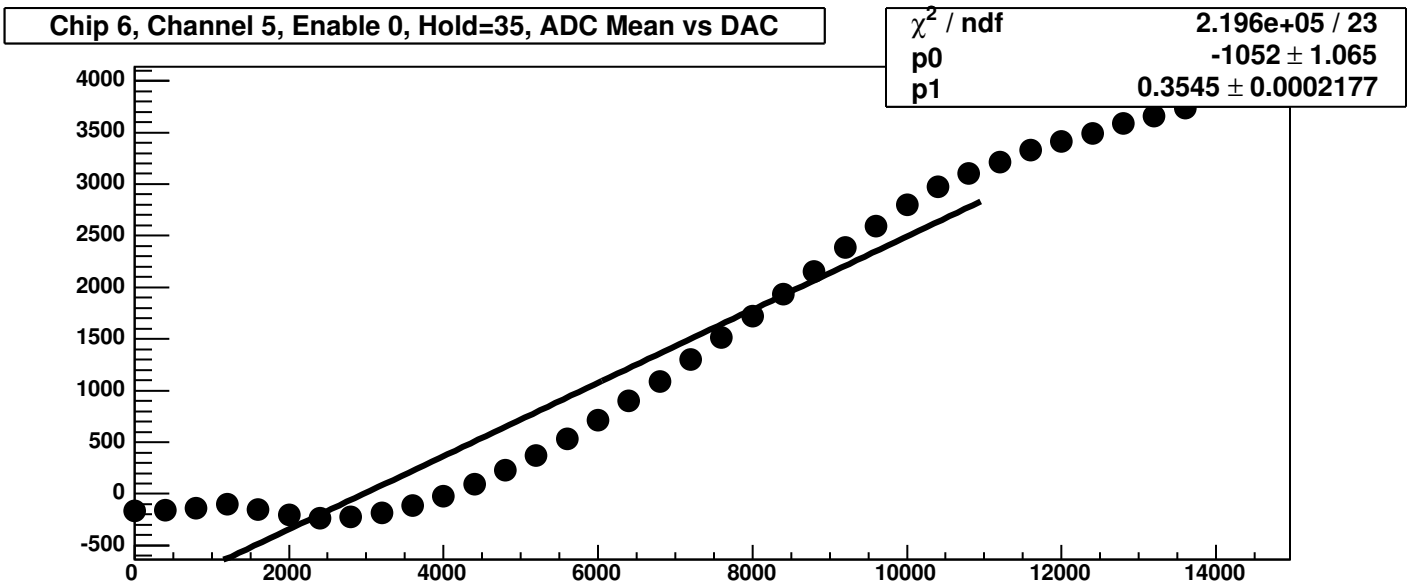

Chip 6, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

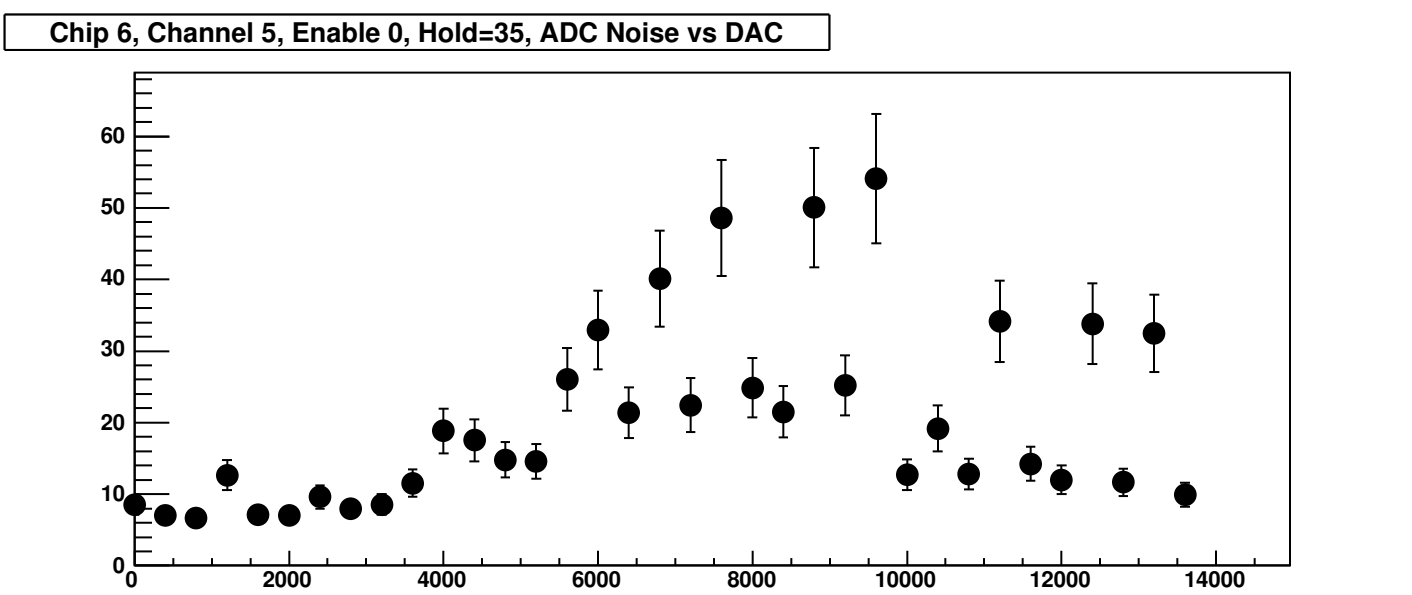

Chip 6, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

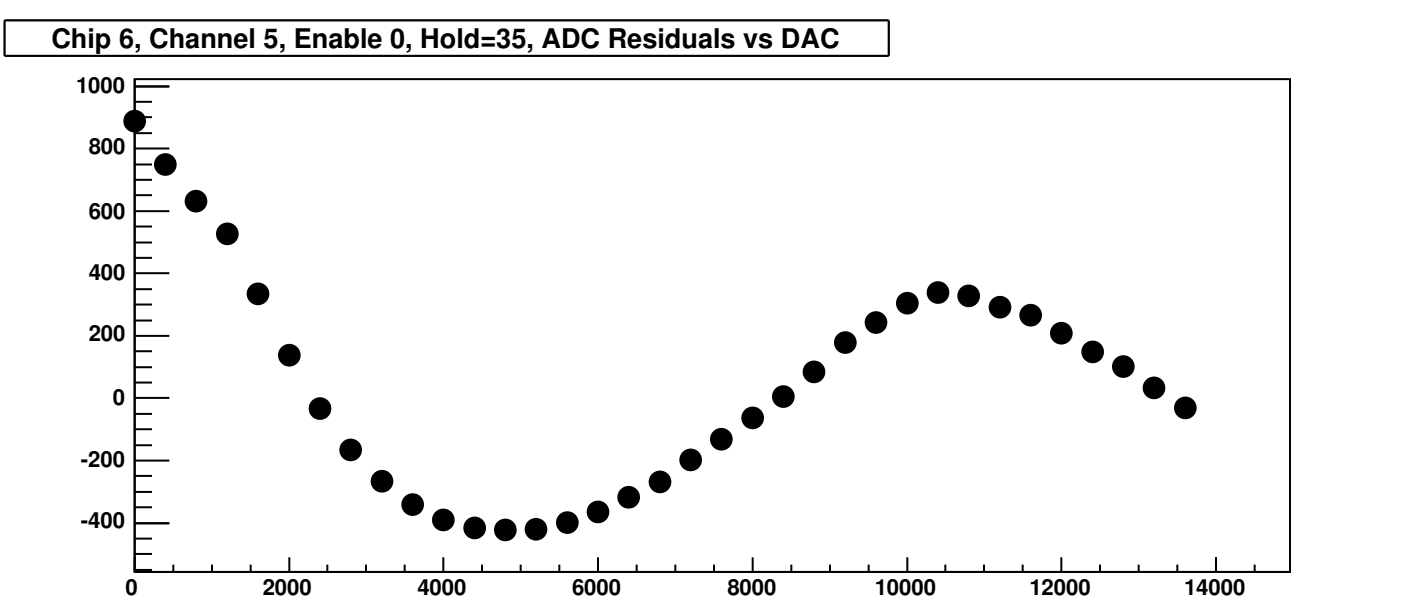

Chip 6, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2054 / 23
p0 -380.9 ± 1.029
p1 0.1669 ± 0.000186
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

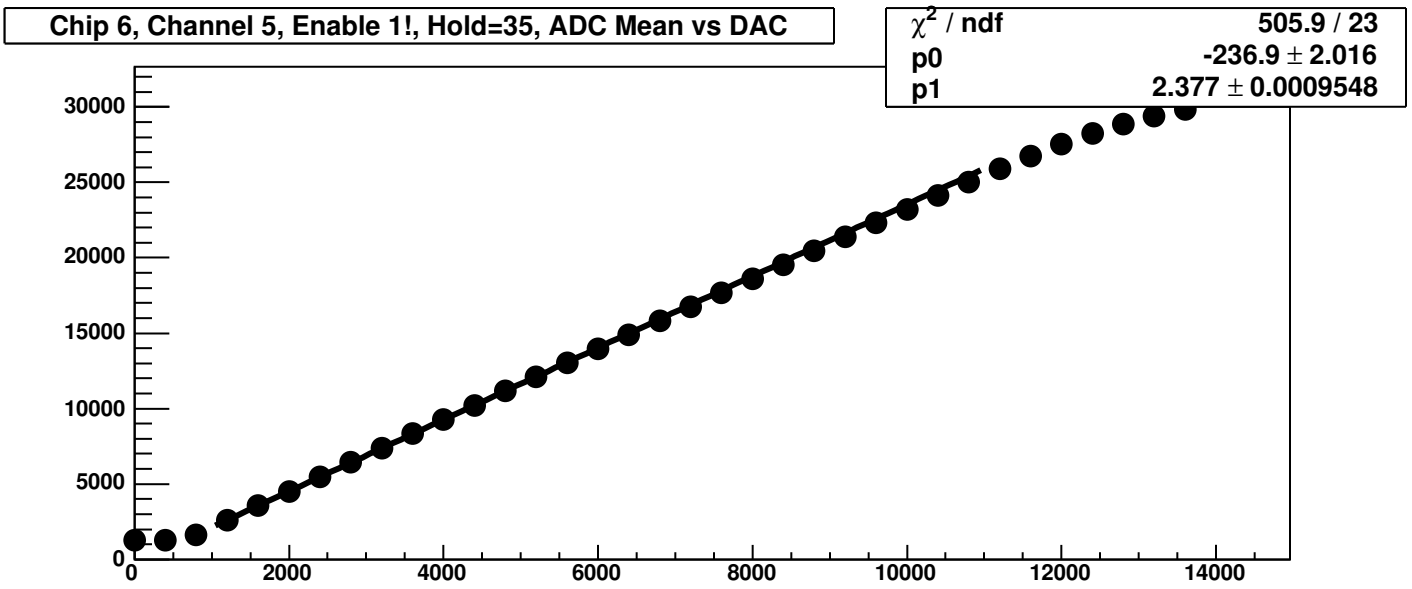

Chip 6, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

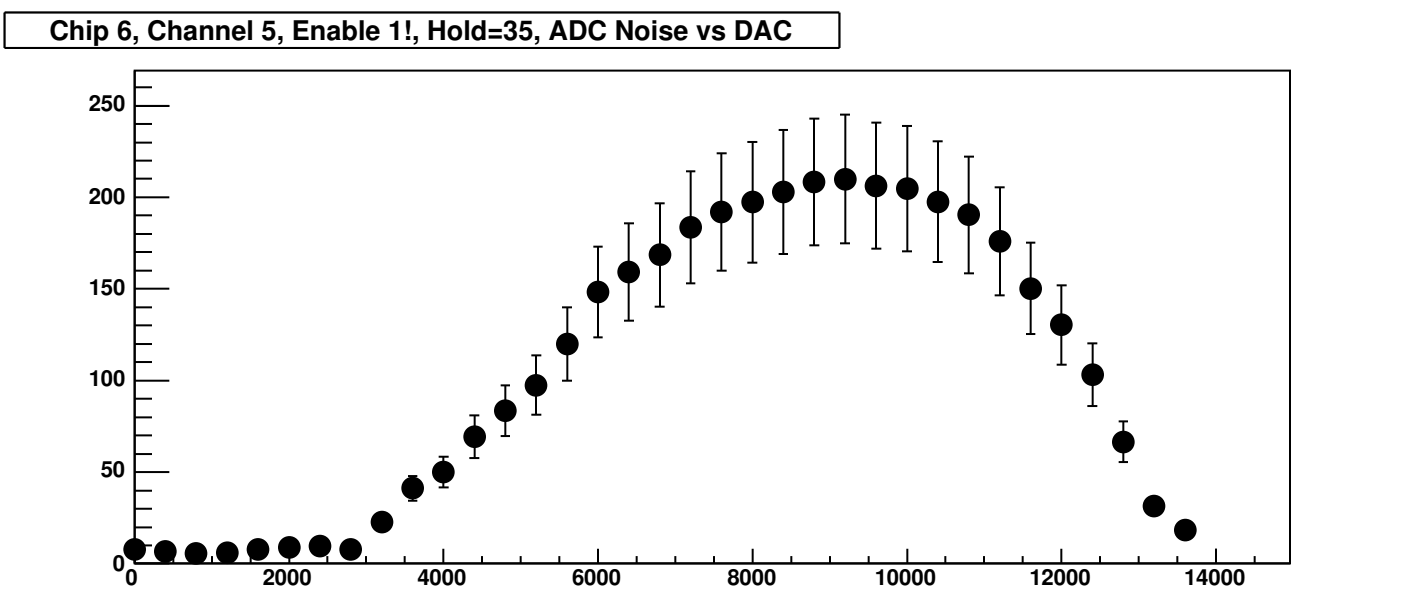

Chip 6, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

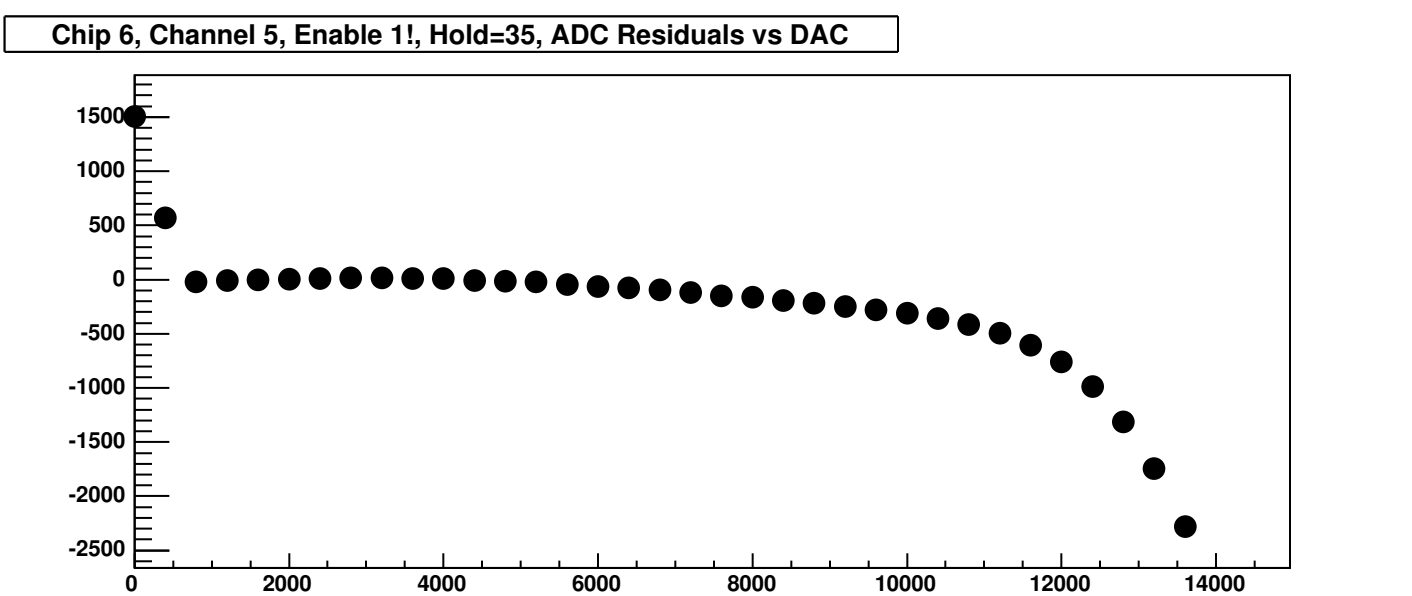

Chip 6, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.196e+05 / 23
p0 -1052 ± 1.065
p1 0.3545 ± 0.0002177
4000
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

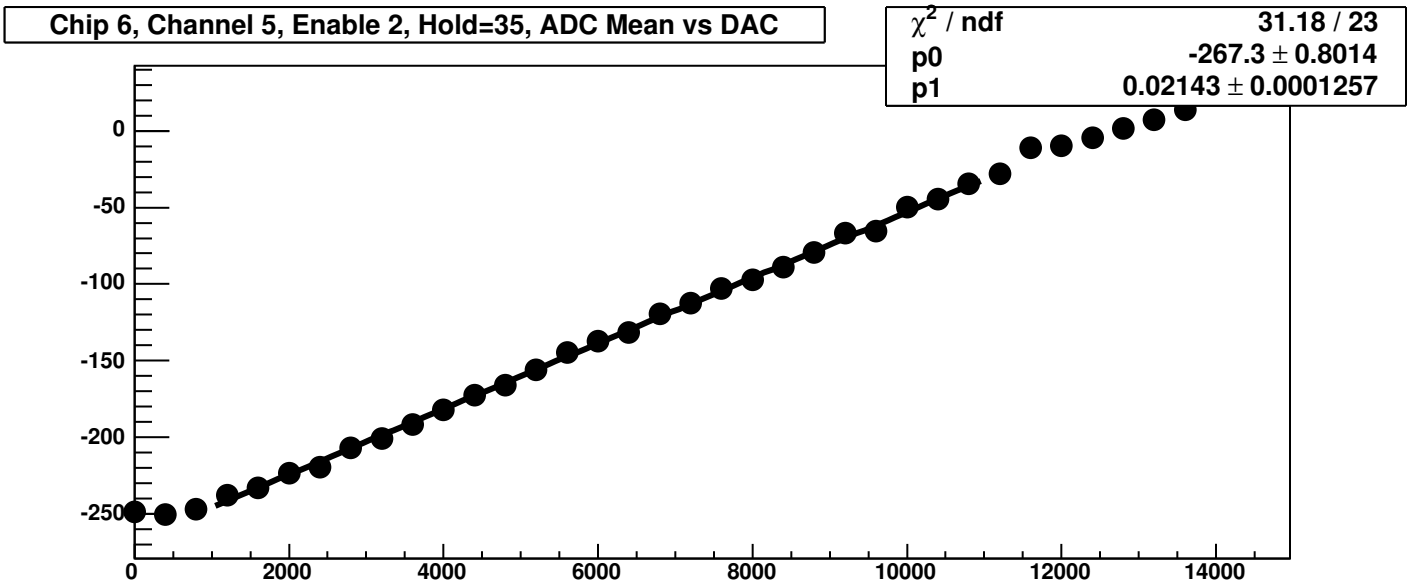

Chip 6, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

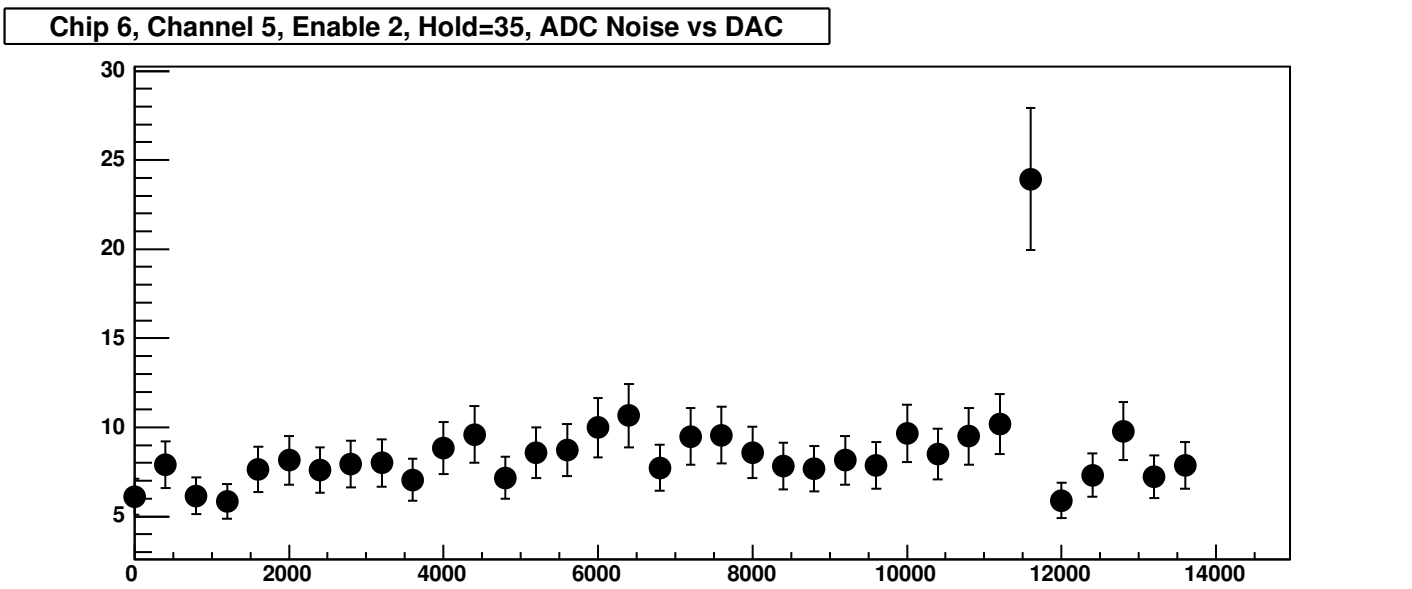

Chip 6, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

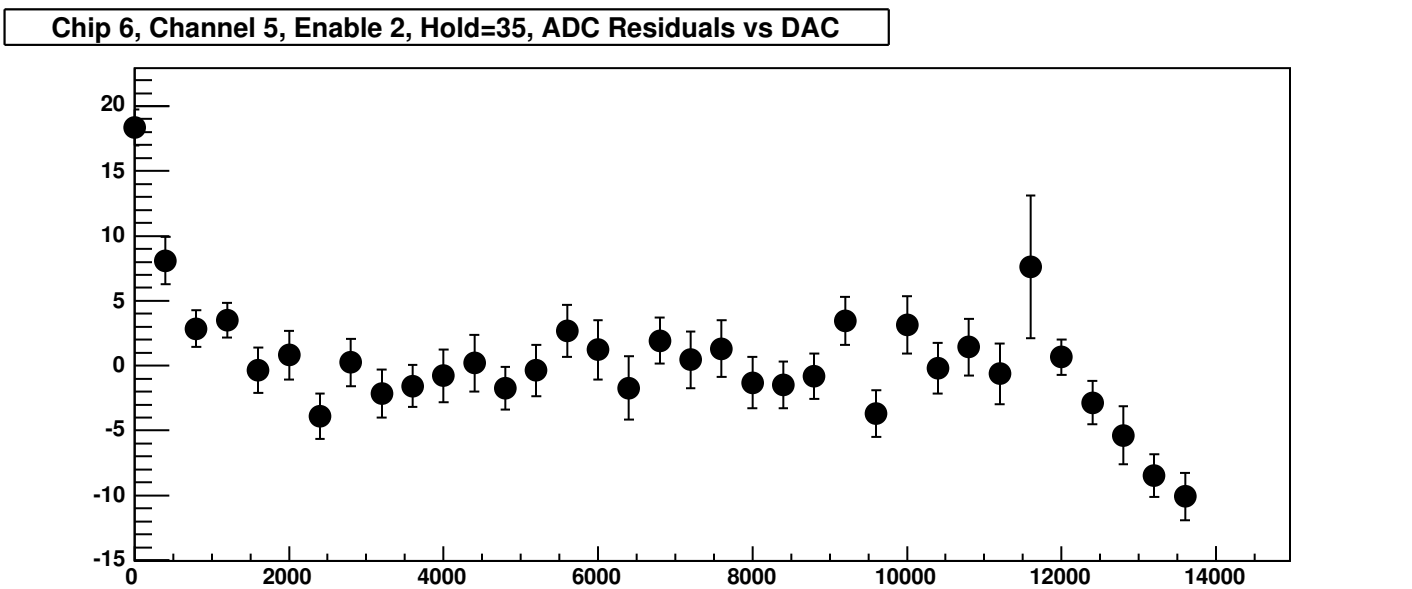

Chip 6, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 505.9 / 23
p0 -236.9 ± 2.016
p1 2.377 ± 0.0009548
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

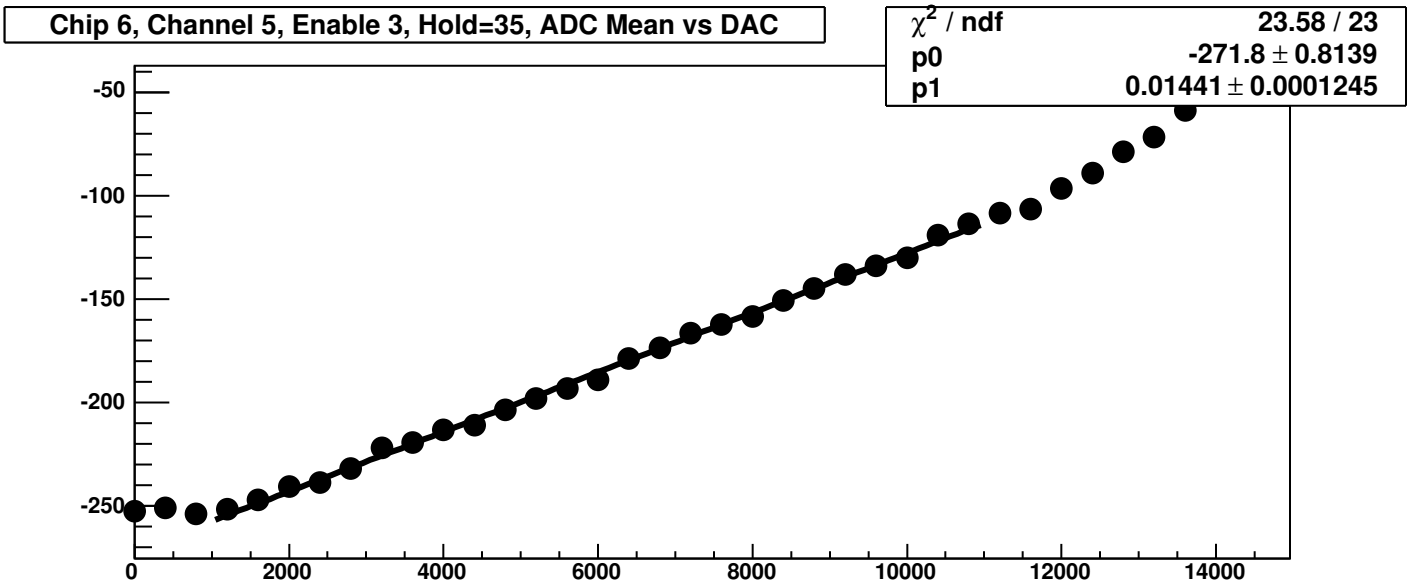

Chip 6, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

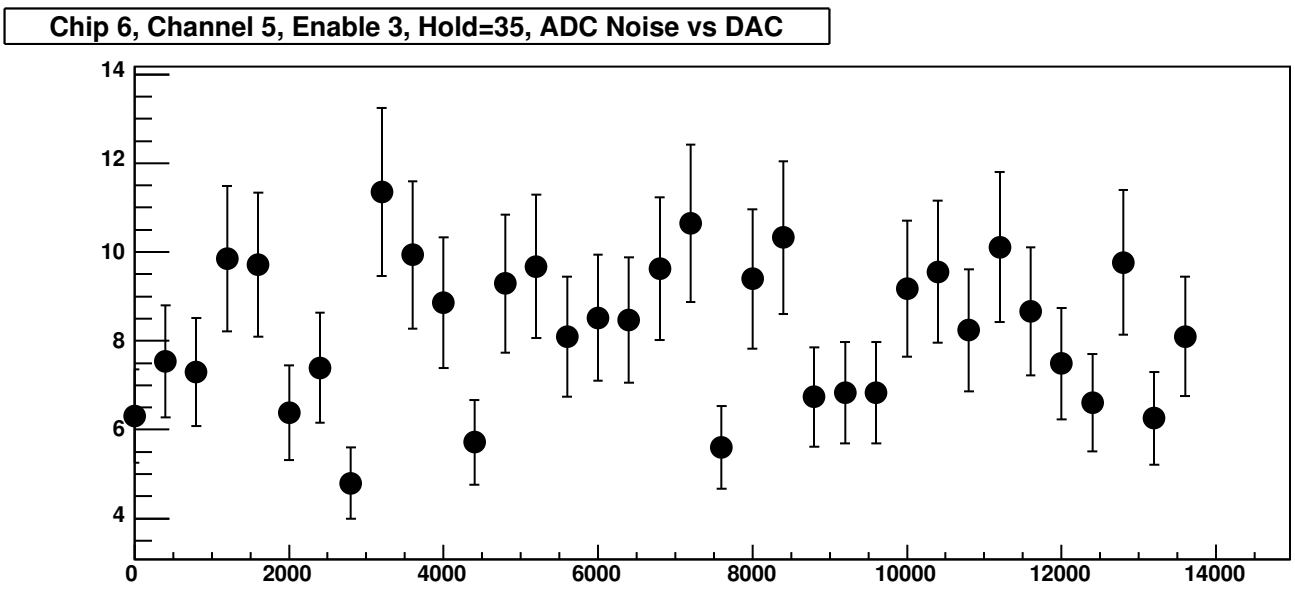

Chip 6, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

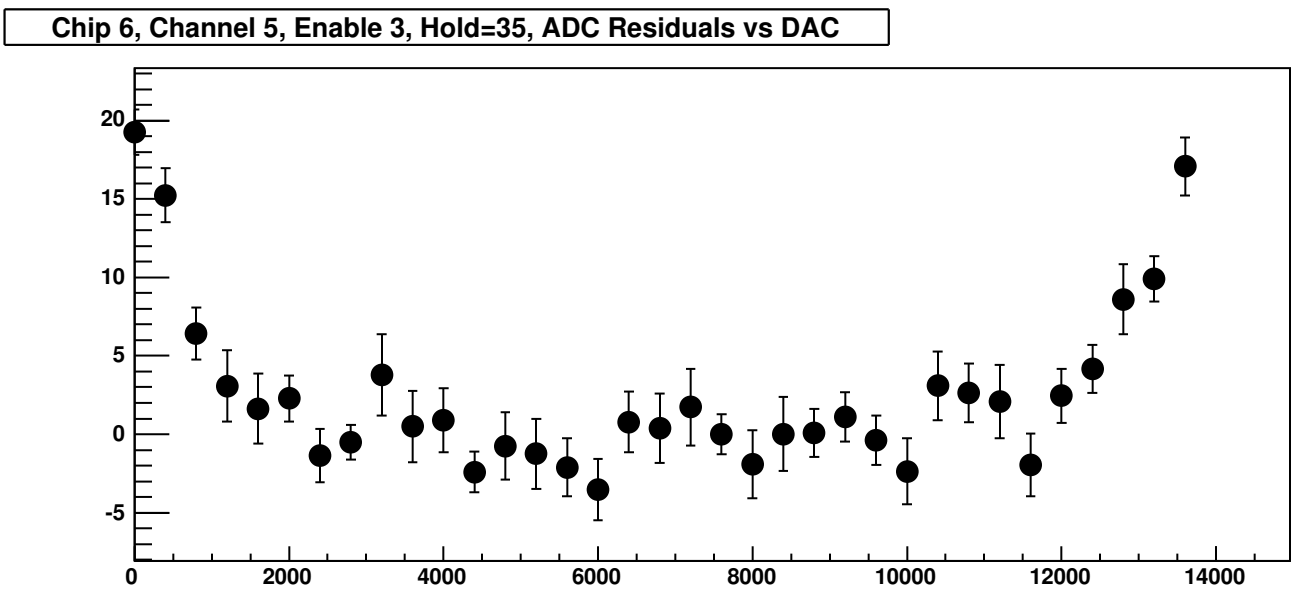

Chip 6, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 31.18 / 23
p0 -267.3 ± 0.8014
p1 0.02143 ± 0.0001257
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

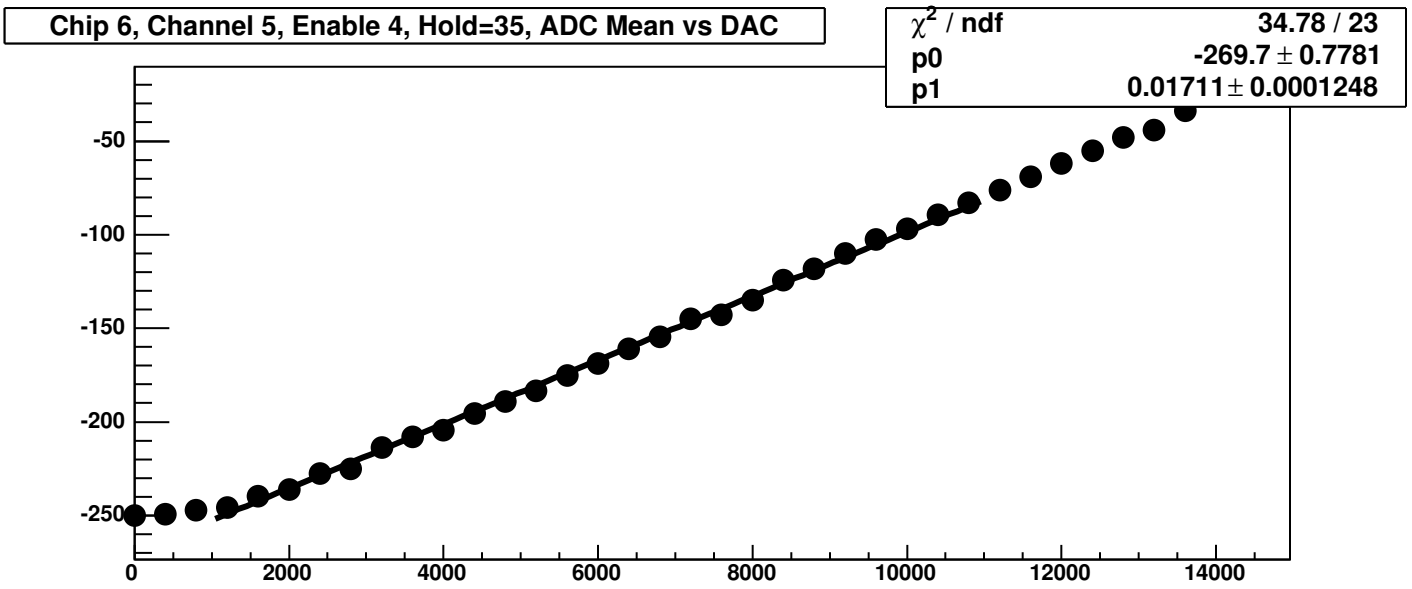

Chip 6, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

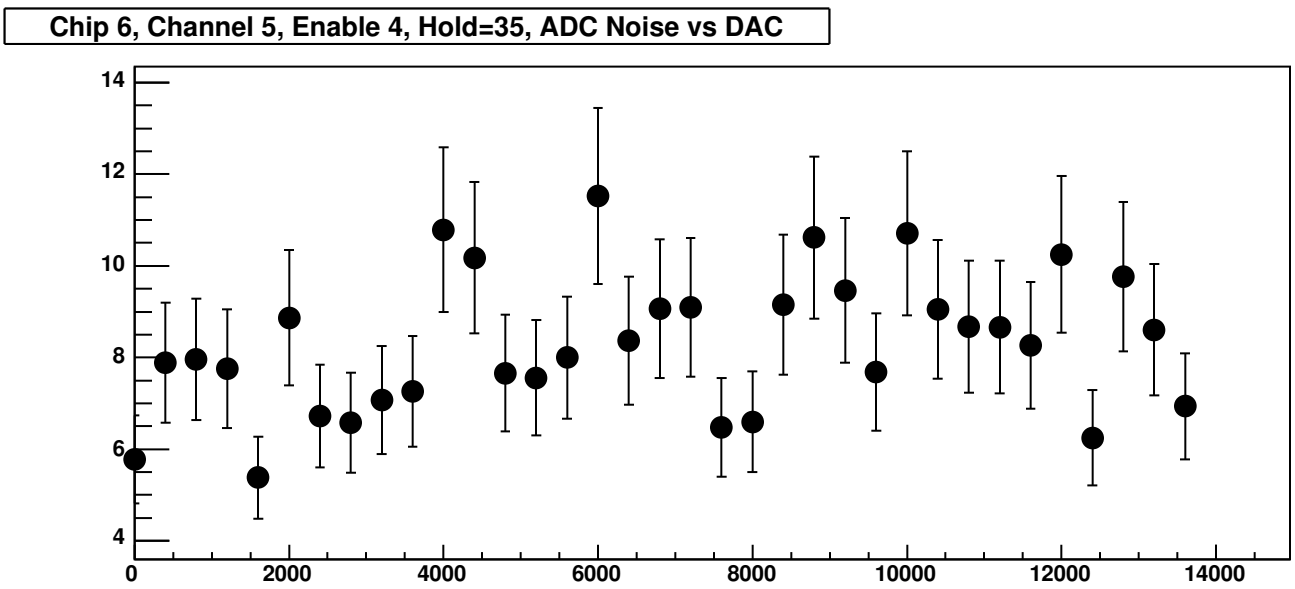

Chip 6, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

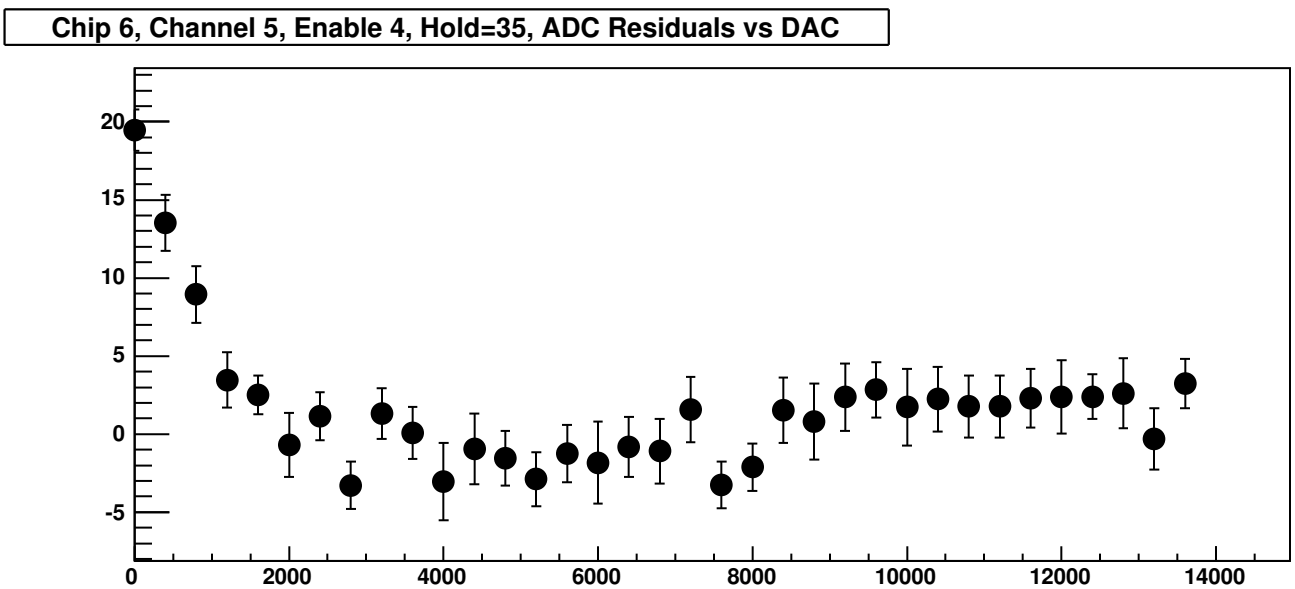

Chip 6, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.58 / 23
p0 -271.8 ± 0.8139
p1 0.01441 ± 0.0001245
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

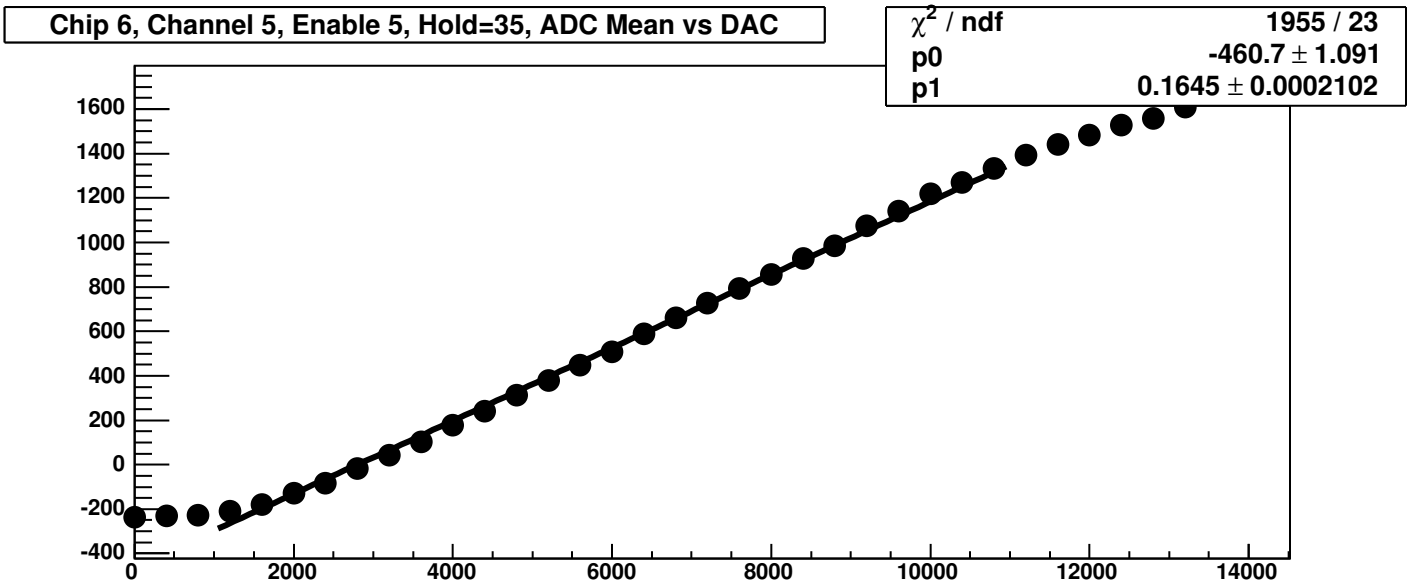

Chip 6, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

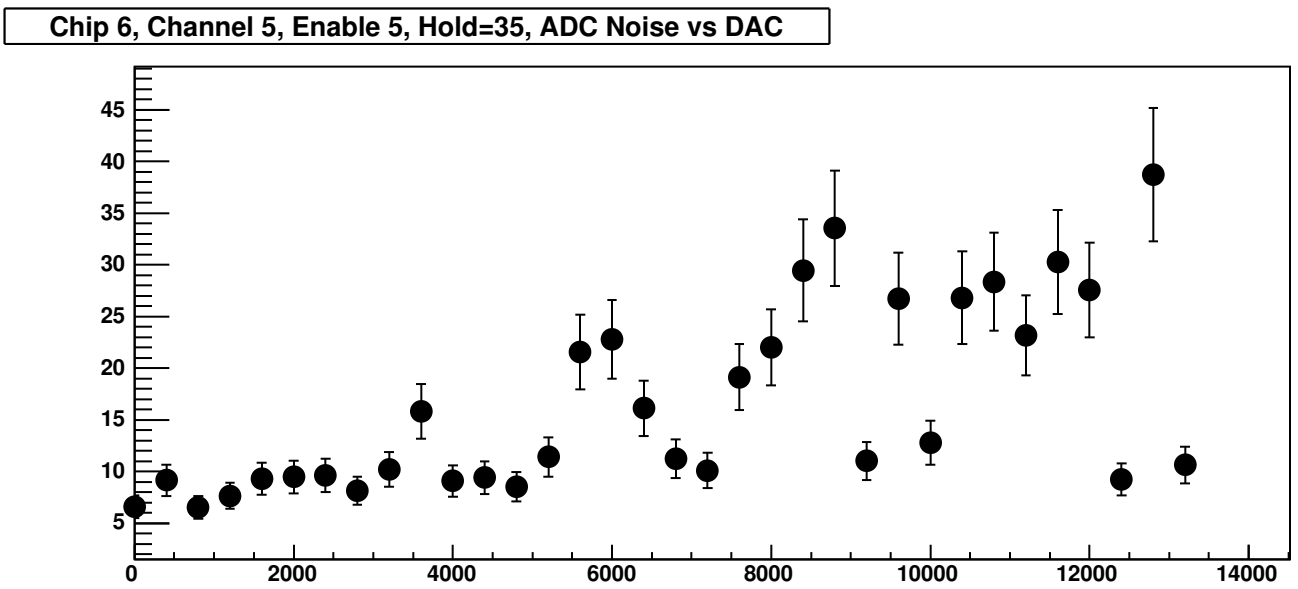

Chip 6, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

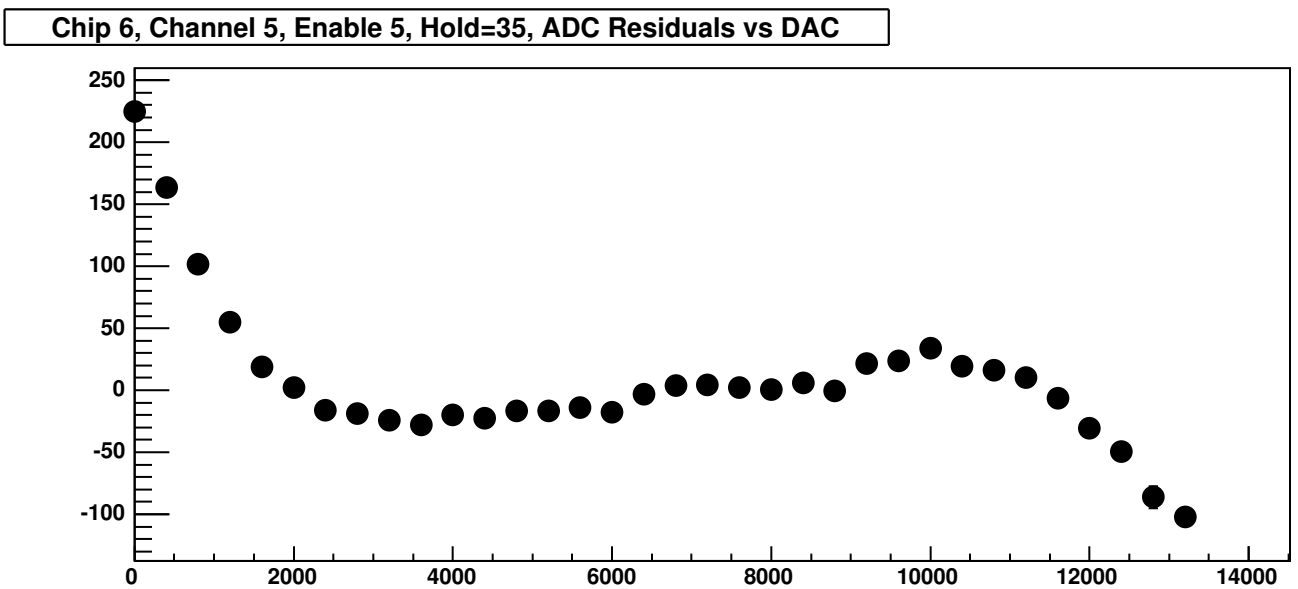

Chip 6, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 34.78 / 23
p0 -269.7 ± 0.7781
p1 0.01711 ± 0.0001248
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

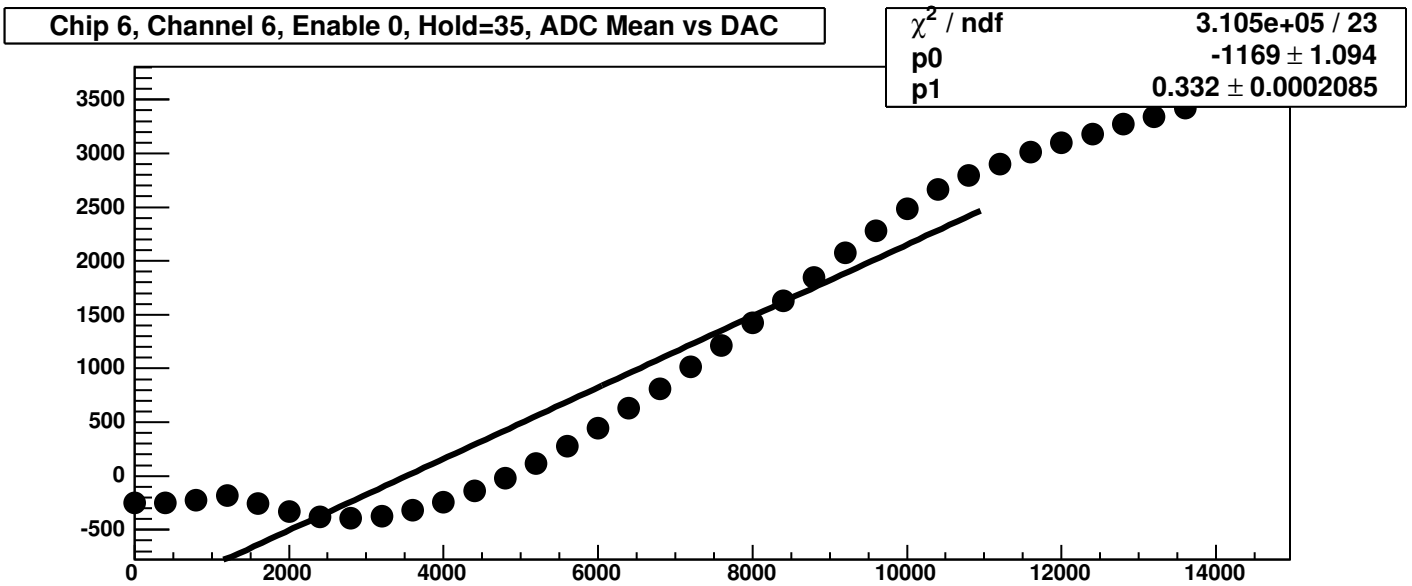

Chip 6, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

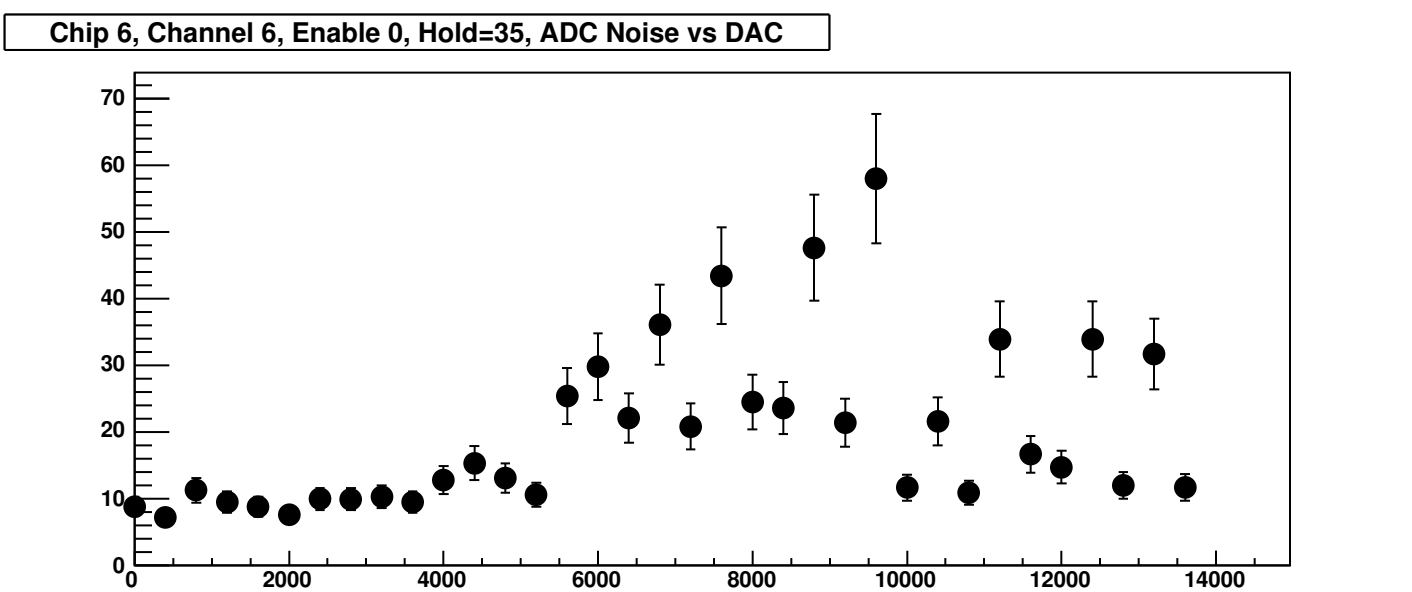

Chip 6, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

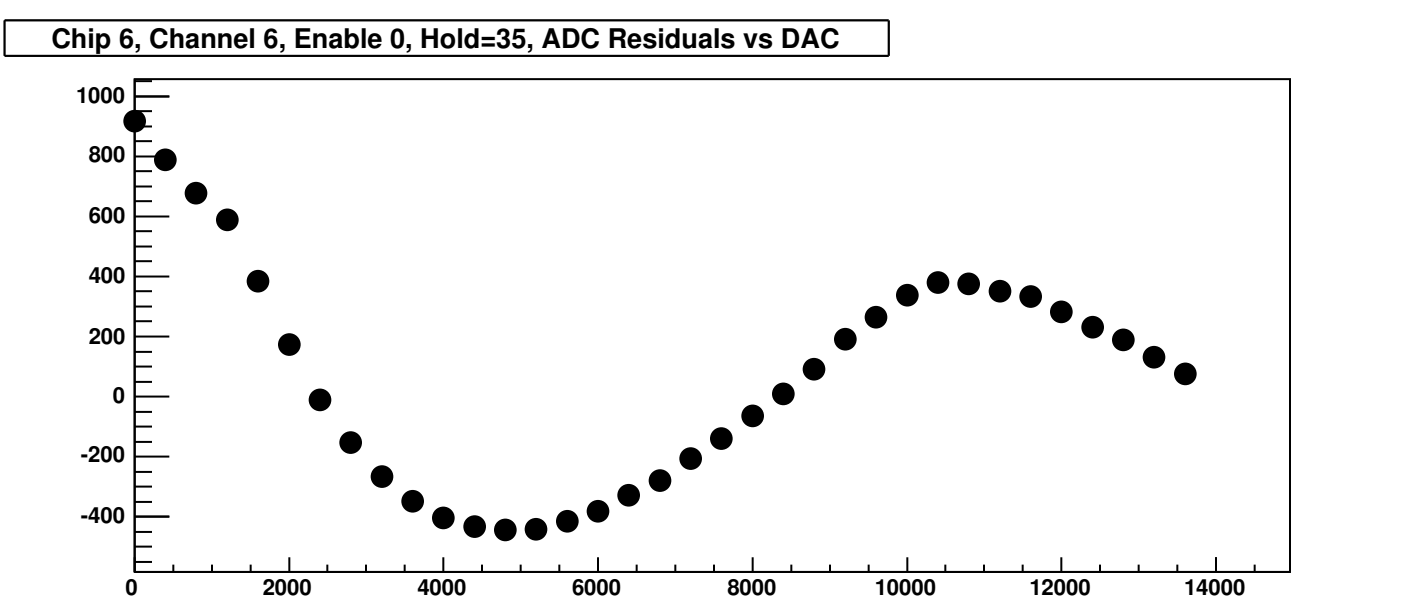

Chip 6, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1955 / 23
p0 -460.7 ± 1.091
p1 0.1645 ± 0.0002102
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

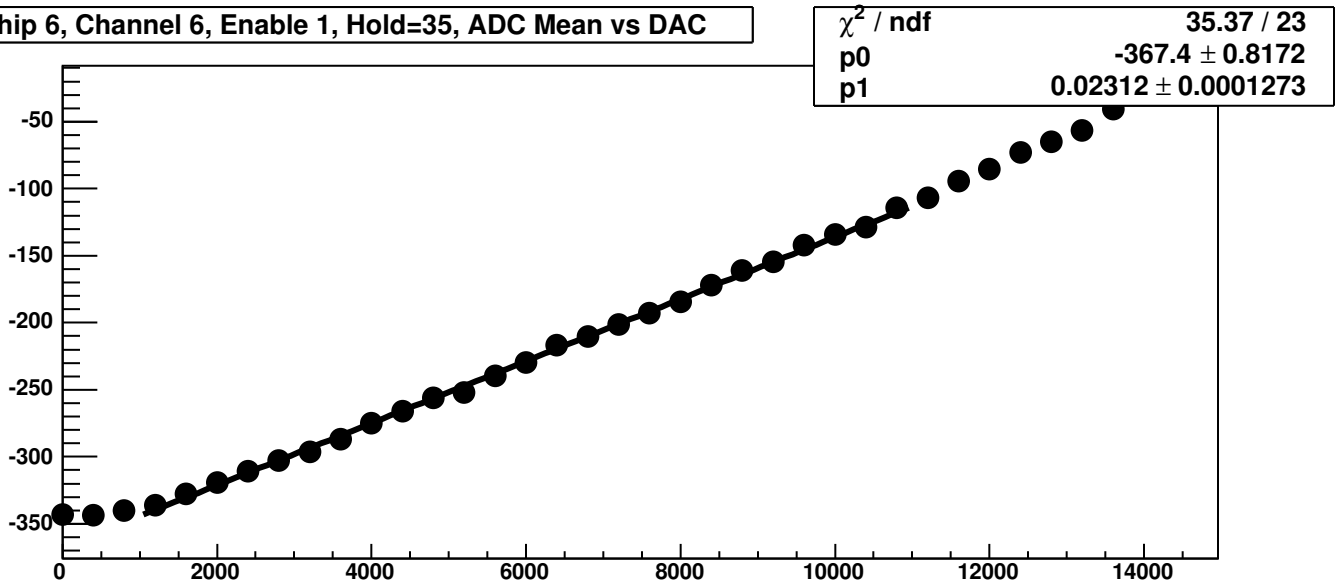

Chip 6, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.105e+05 / 23
p0 -1169 ± 1.094
p1 0.332 ± 0.0002085
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

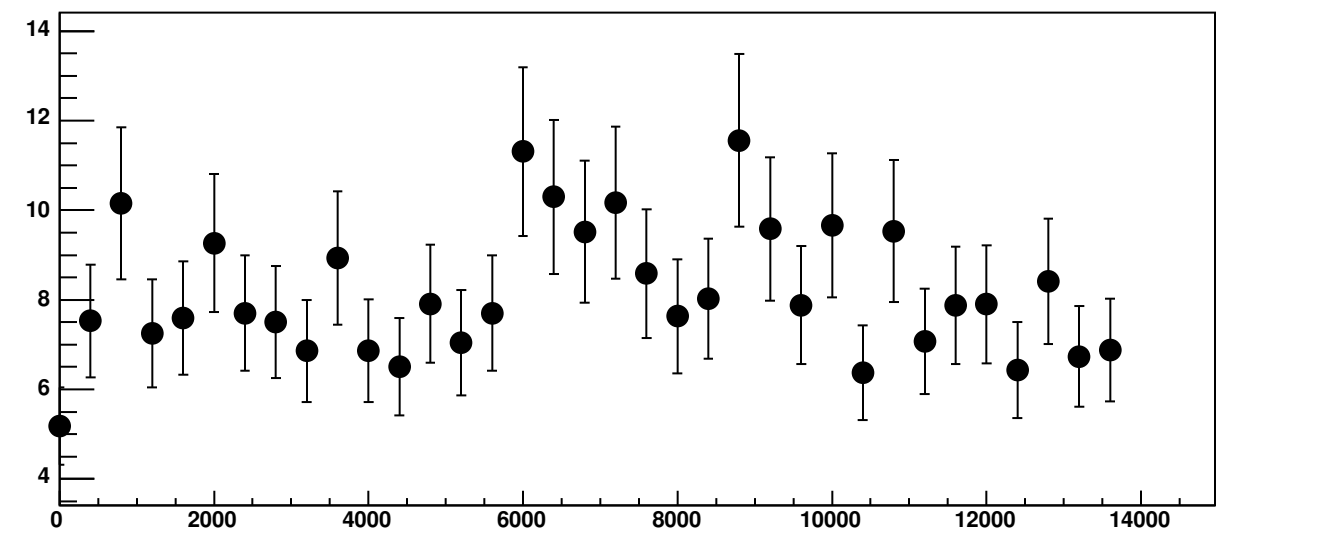

Chip 6, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

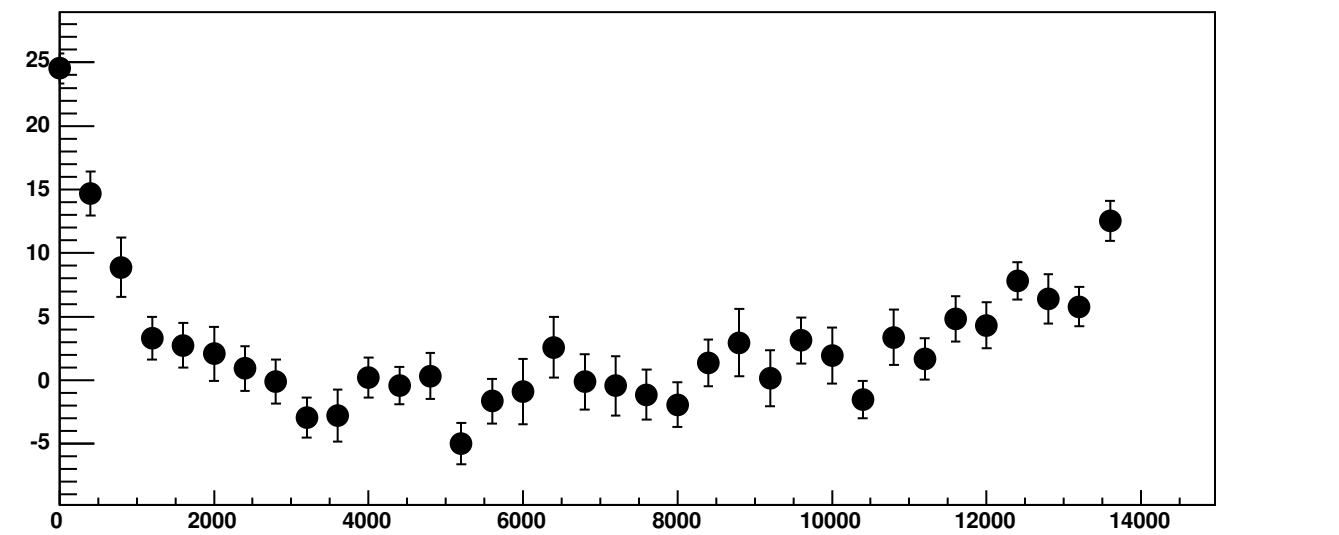
Chip 6, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

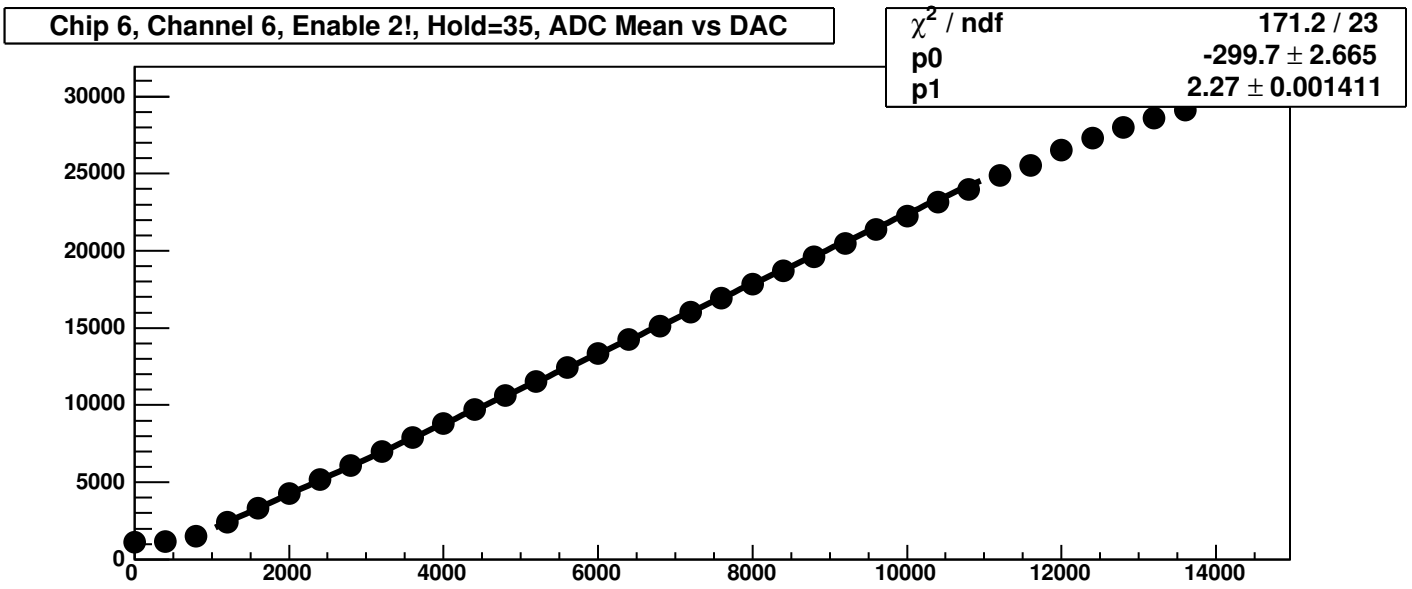


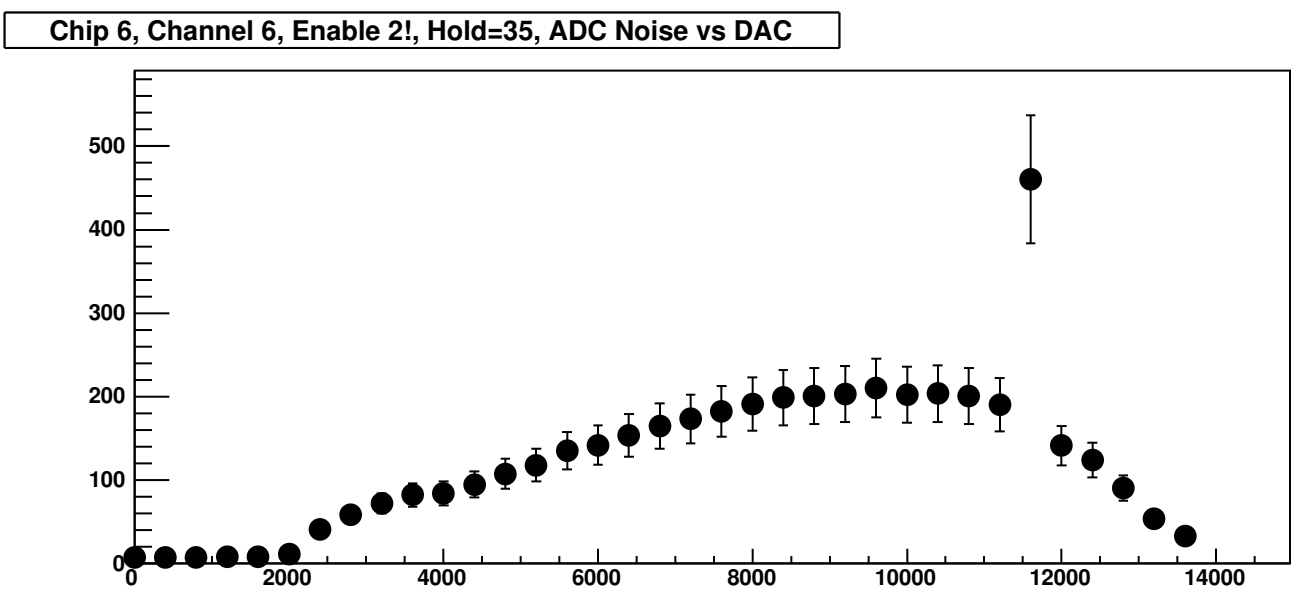
Chip 6, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC

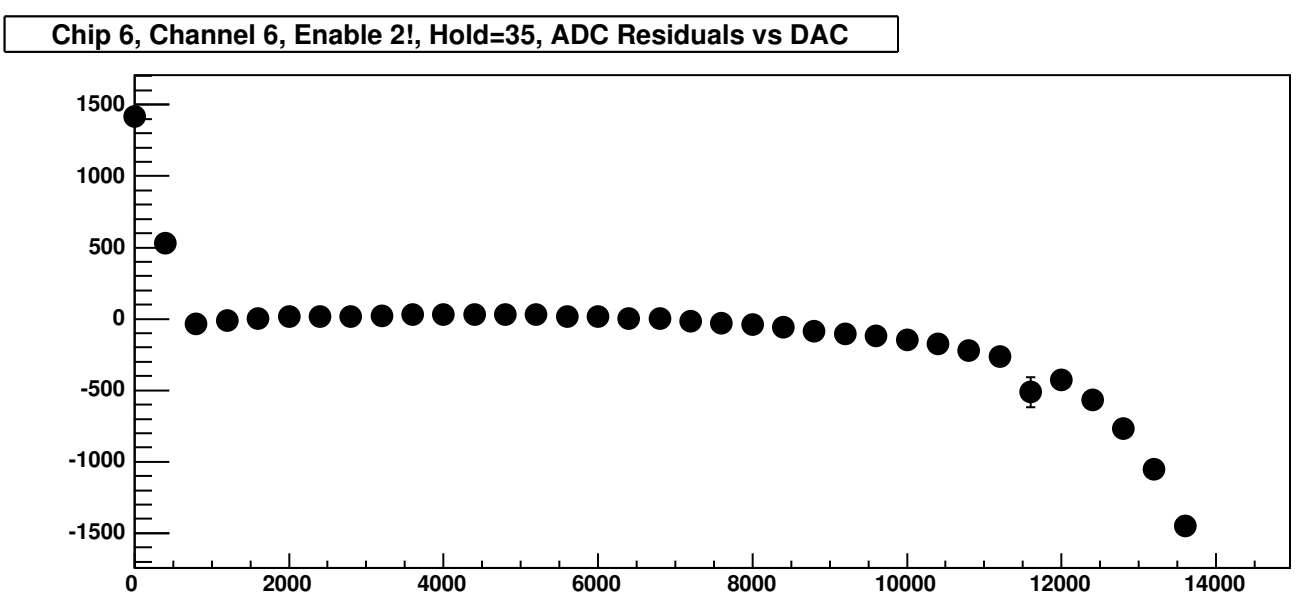


Chip 6, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

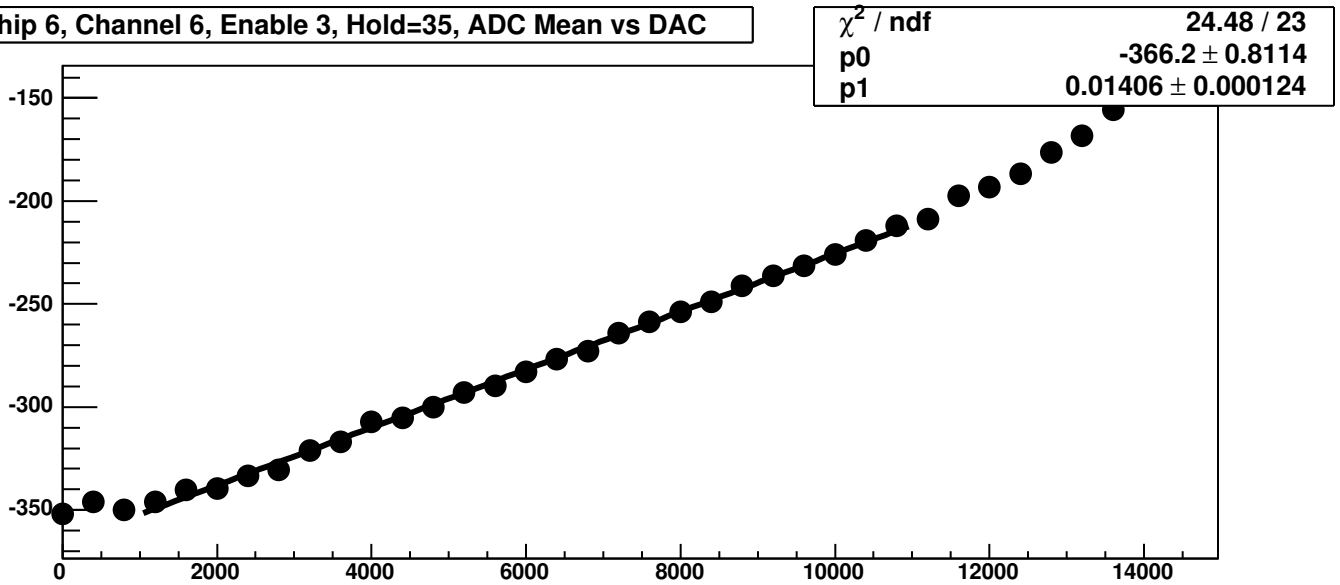



Chip 6, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 171.2 / 23
p0 -299.7 ± 2.665
p1 2.27 ± 0.001411
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

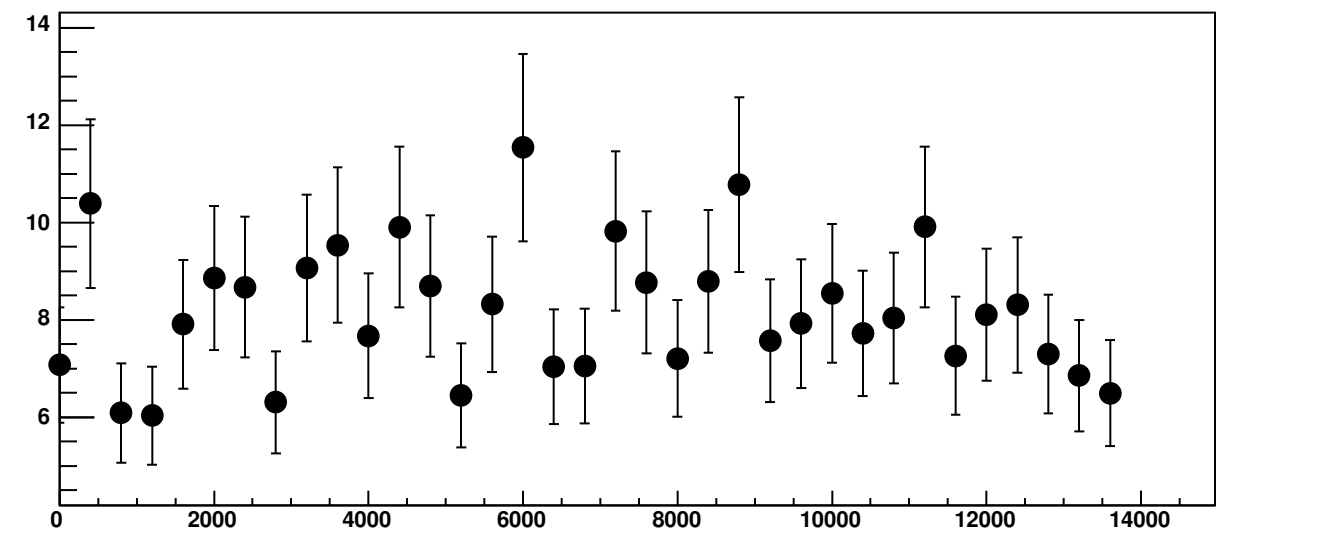

Chip 6, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

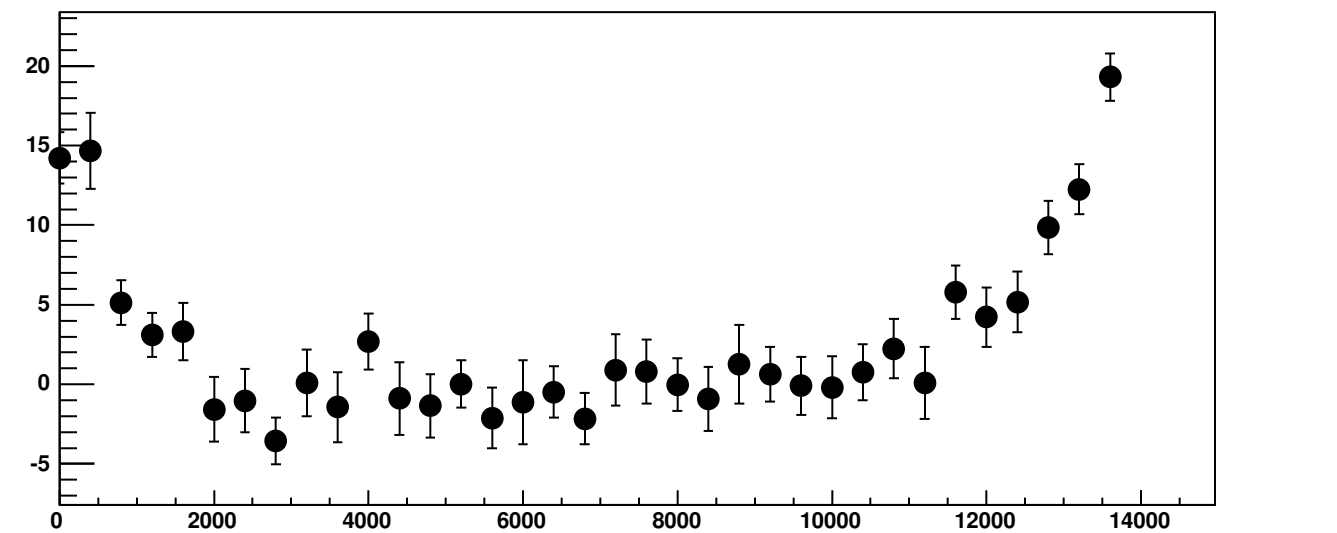
Chip 6, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

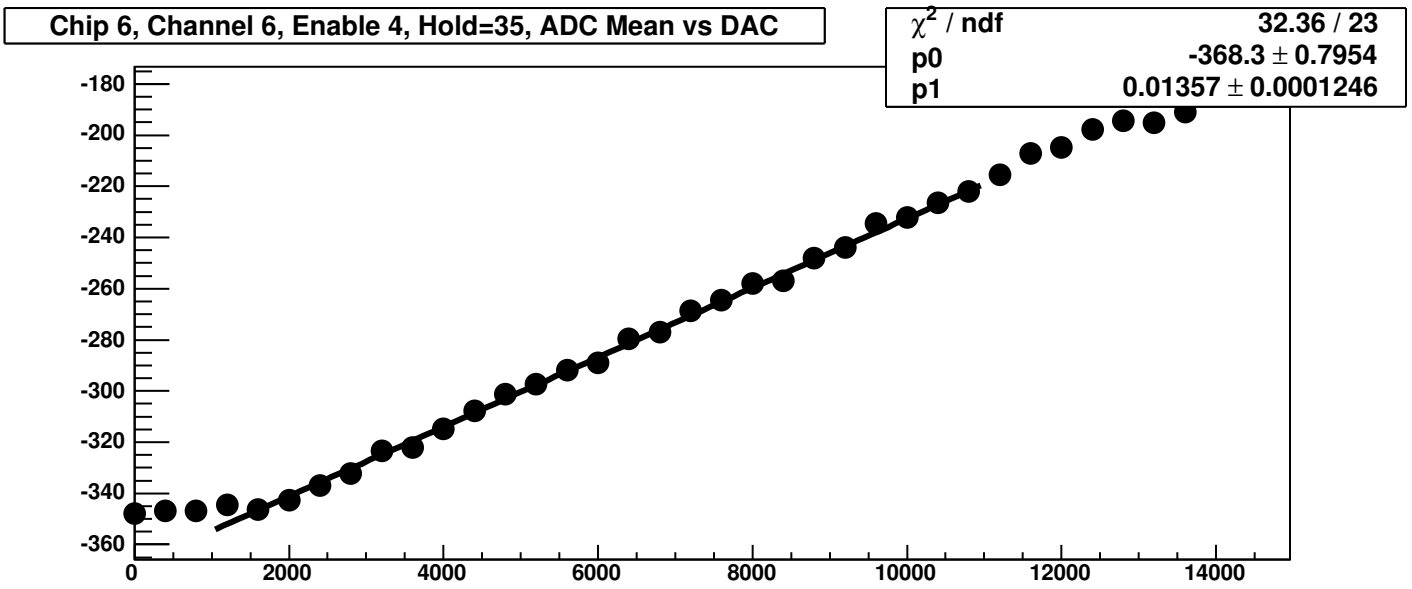


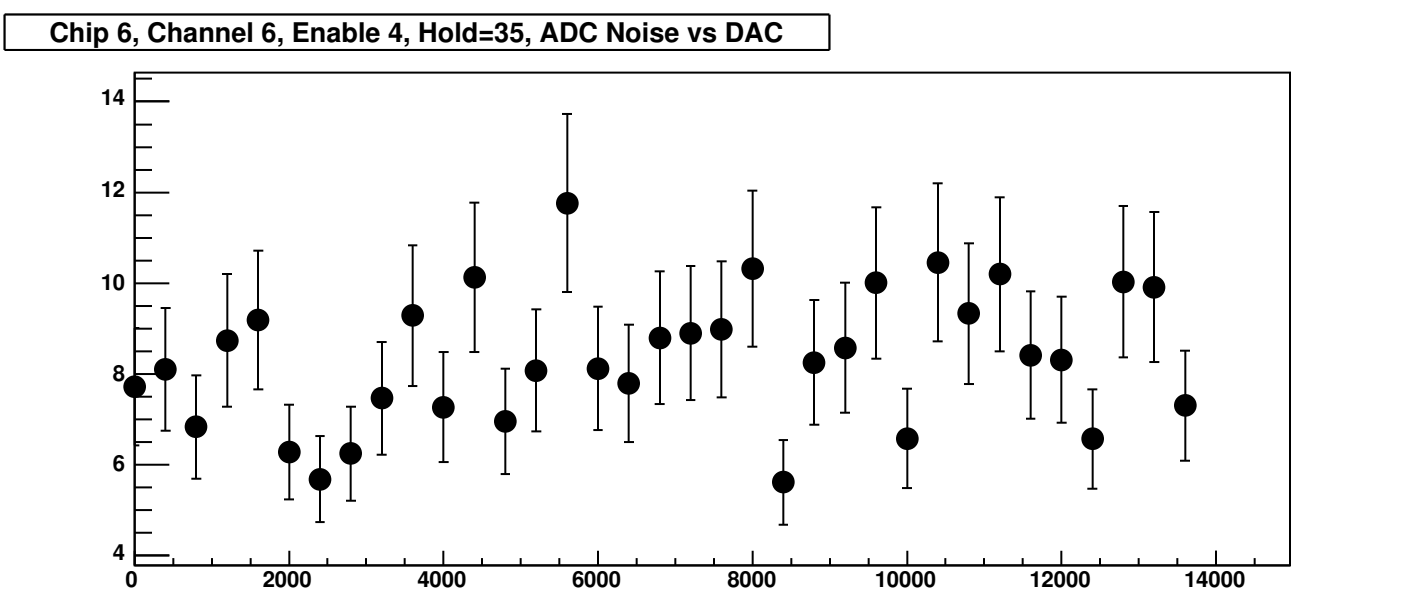
Chip 6, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC

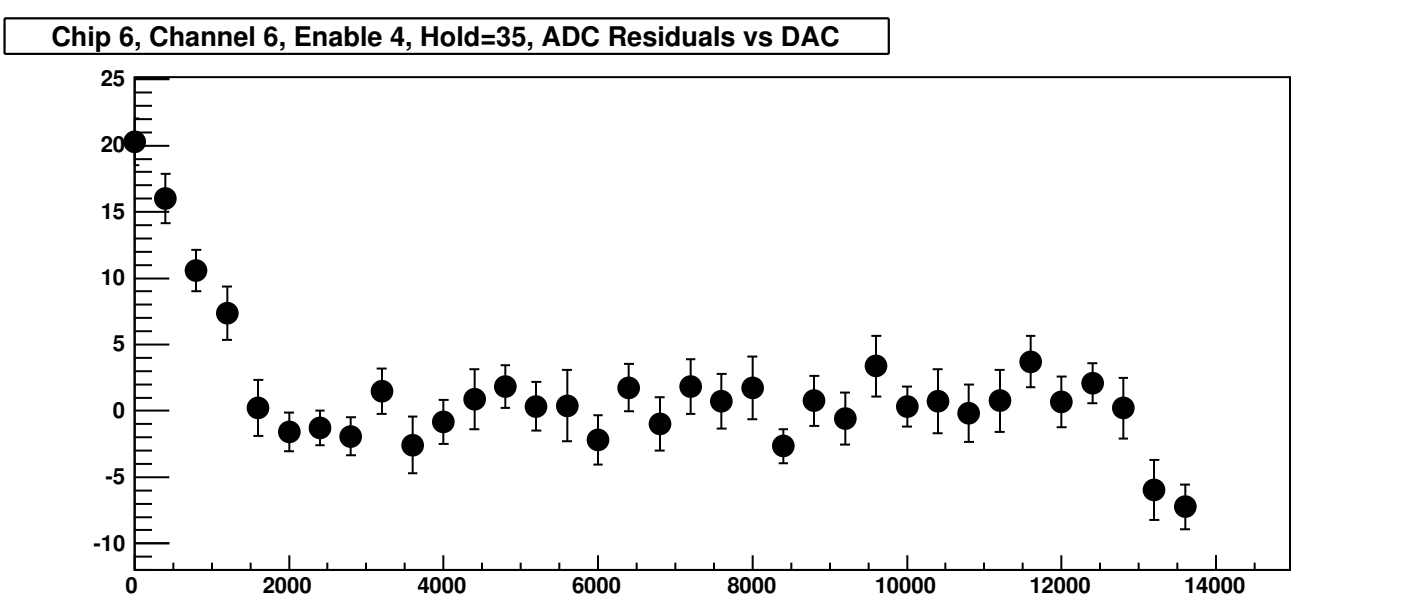


Chip 6, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

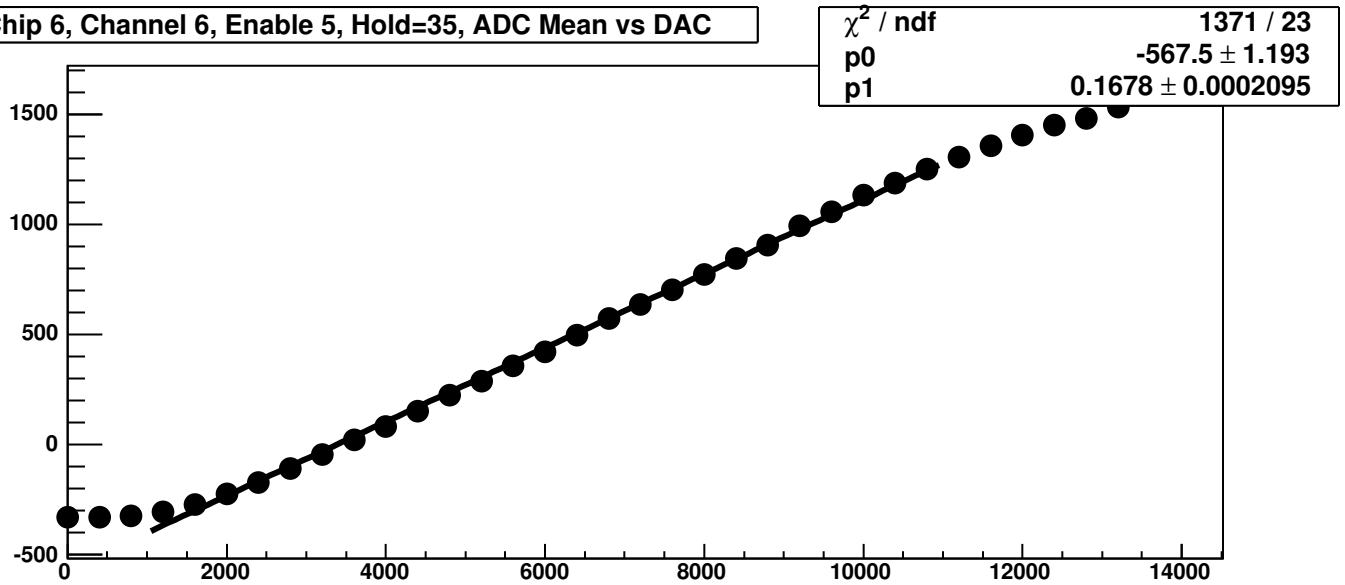



Chip 6, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 32.36 / 23
p0 -368.3 ± 0.7954
p1 0.01357 ± 0.0001246
-180
-200
-220
-240
-260
-280
-300
-320
-340
-360
0
2000
4000
6000
8000
10000
12000
14000

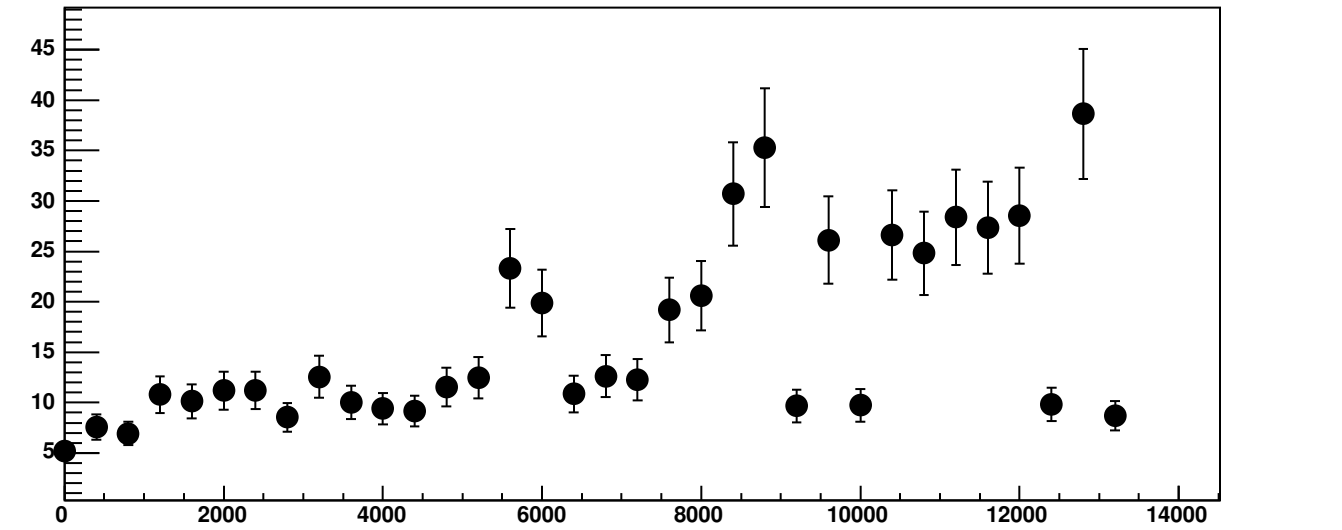

Chip 6, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

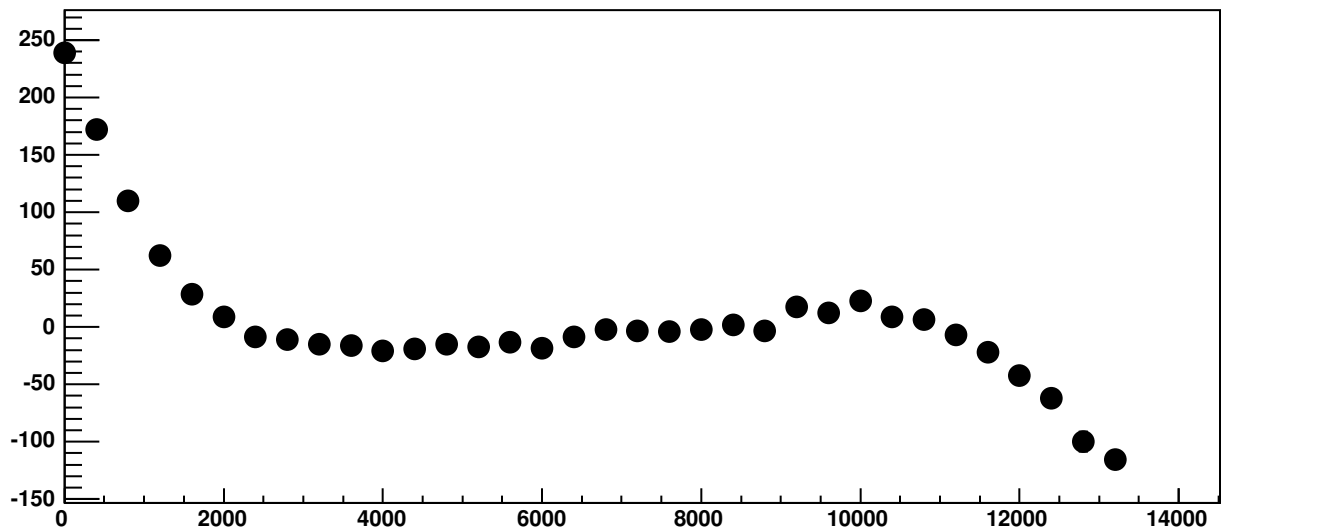
Chip 6, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC



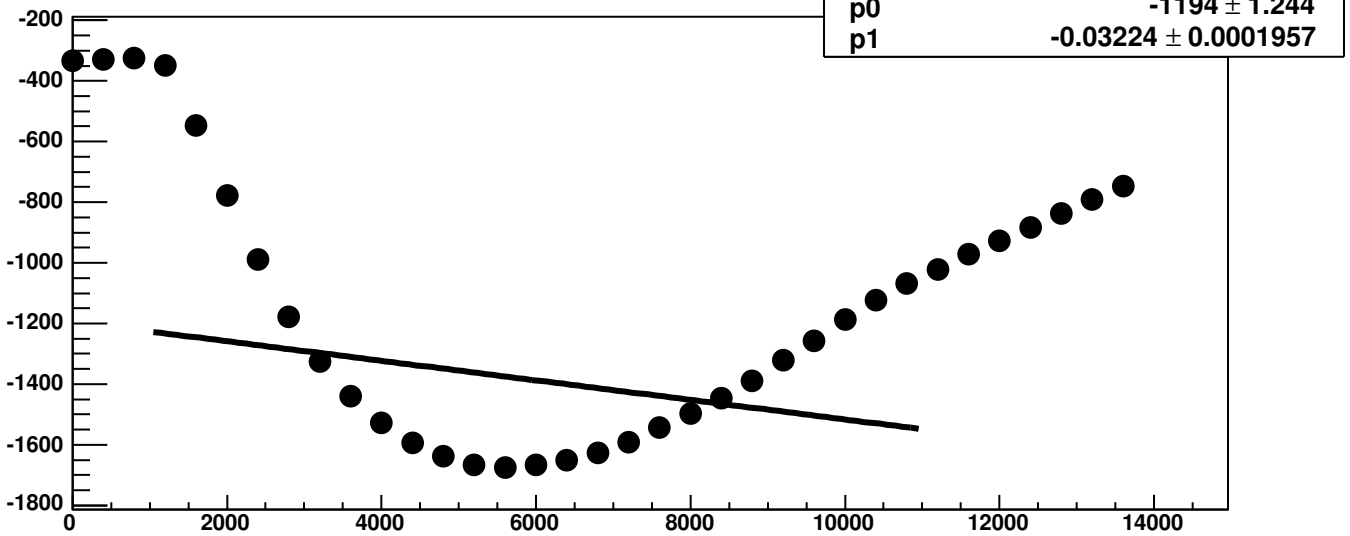
Chip 6, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



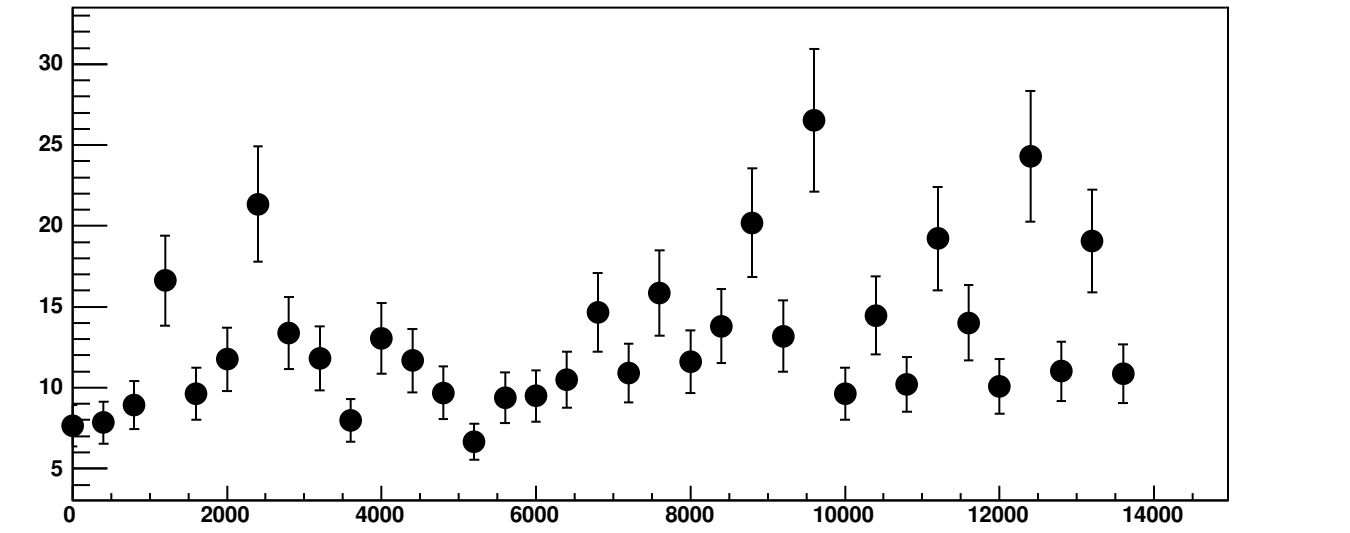
Chip 6, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC



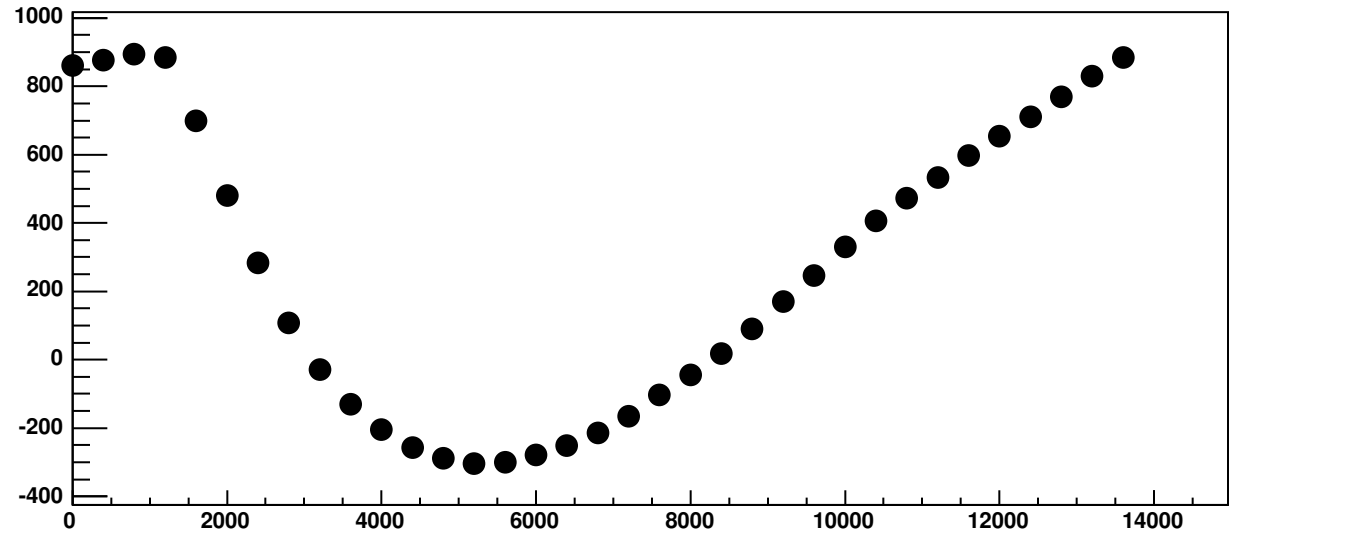
Chip 6, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



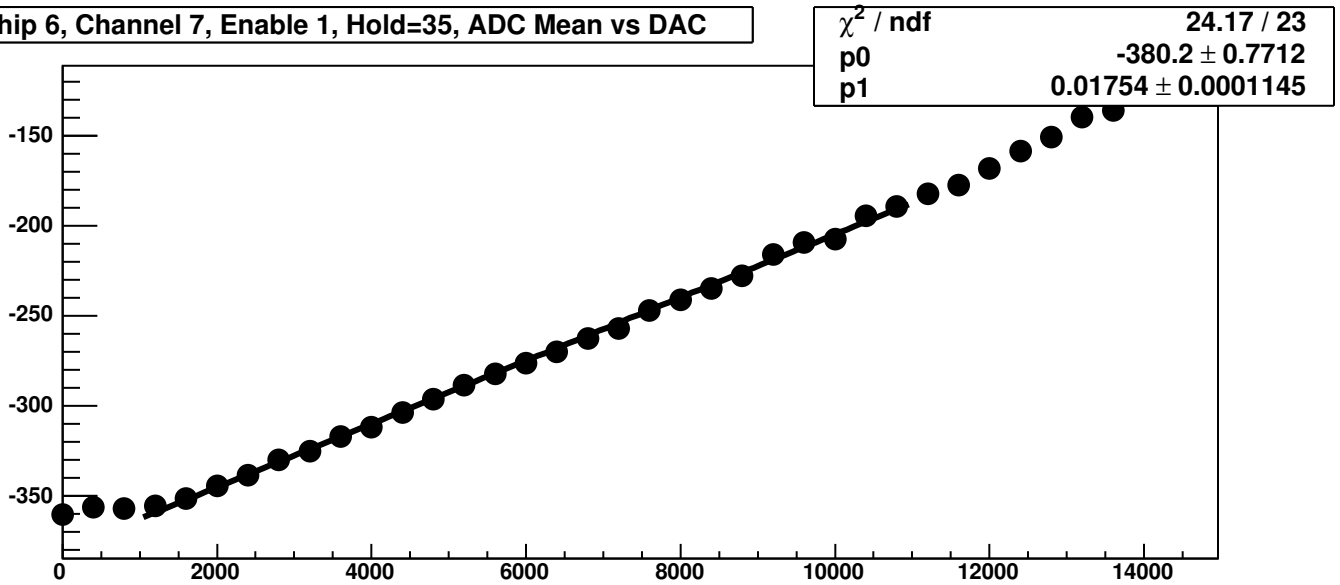
Chip 6, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



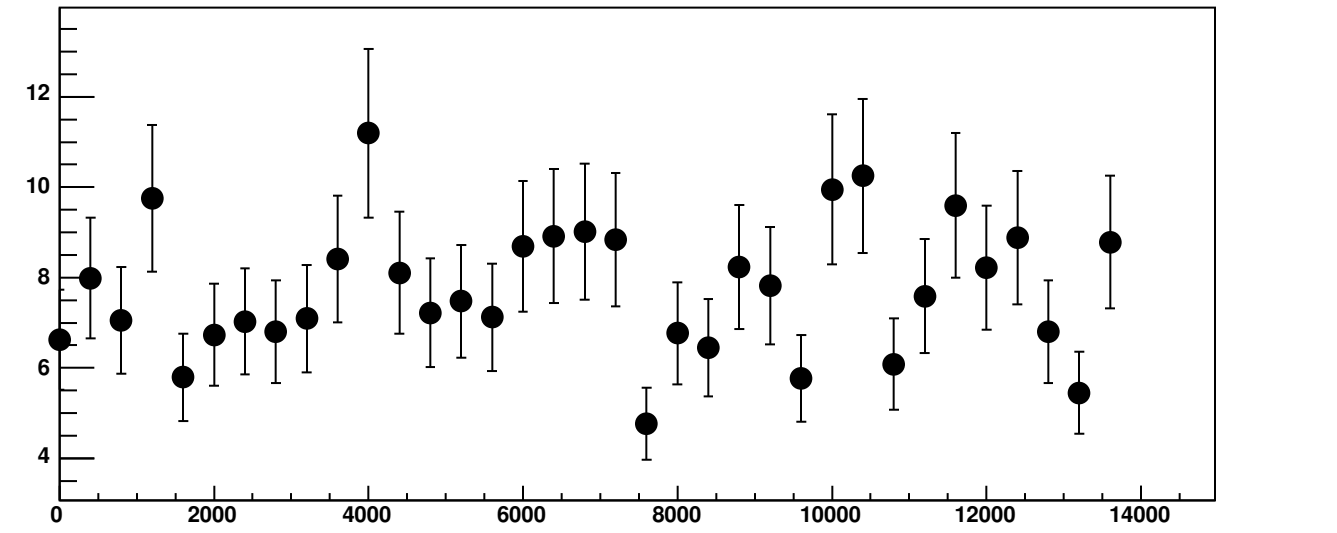
Chip 6, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



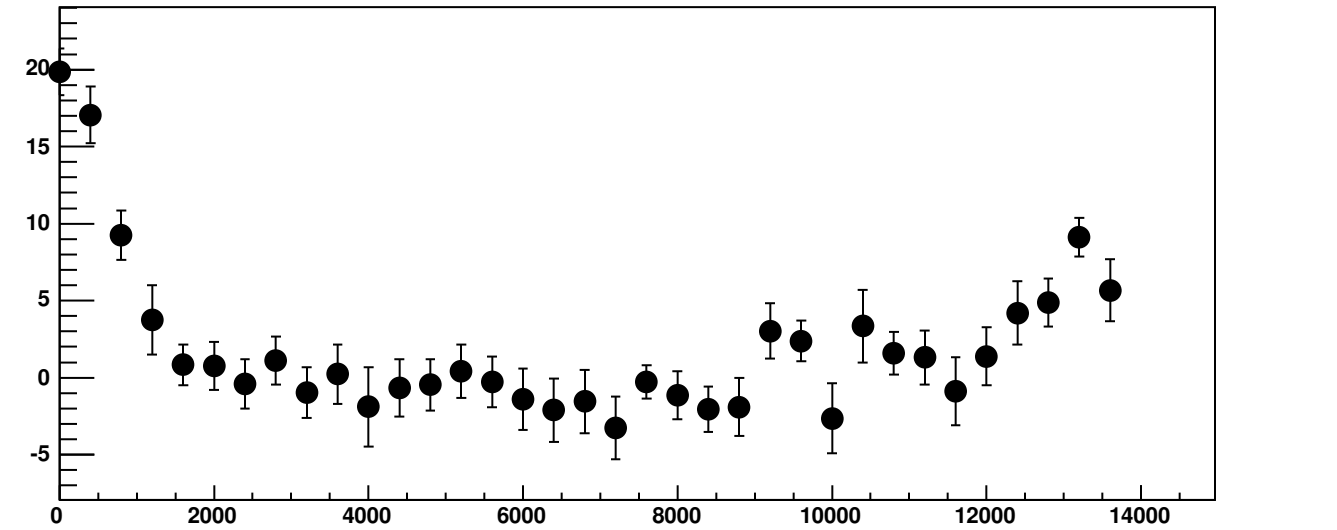
Chip 6, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

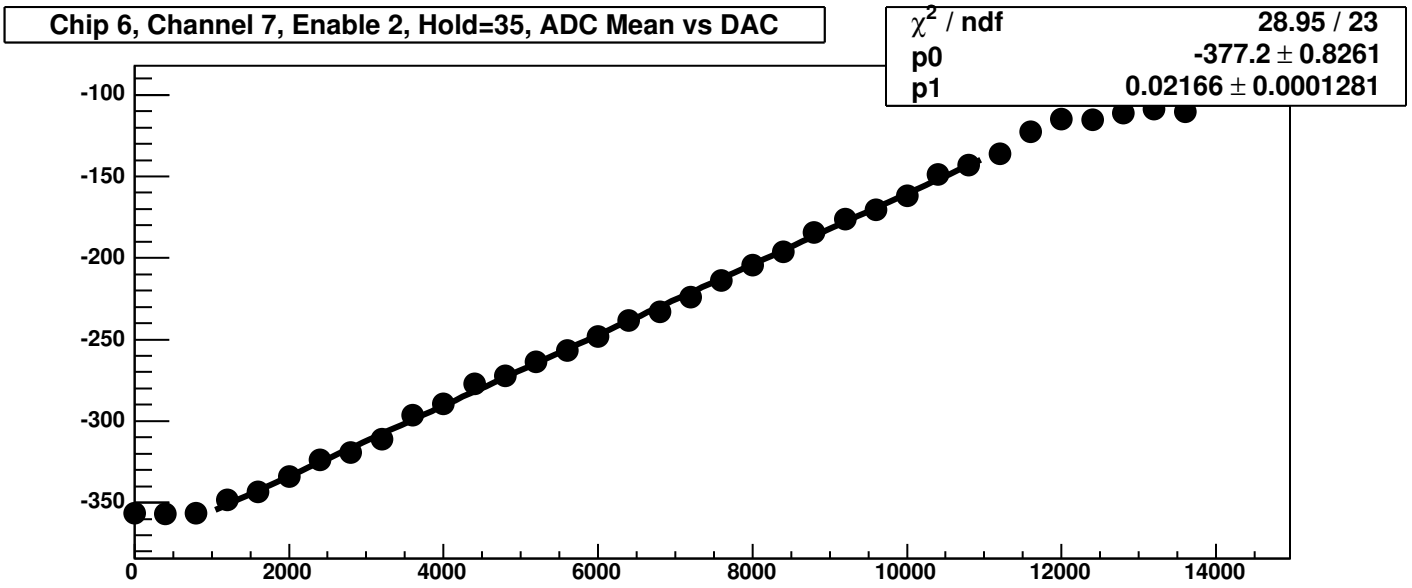


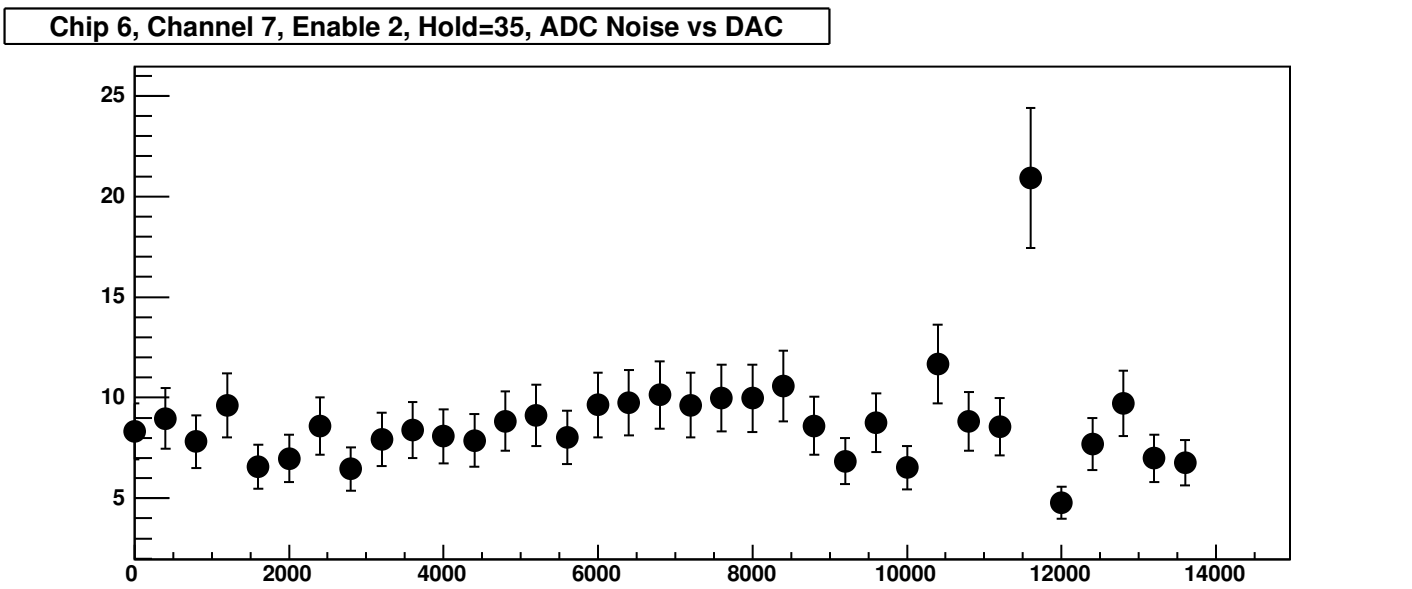
Chip 6, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC

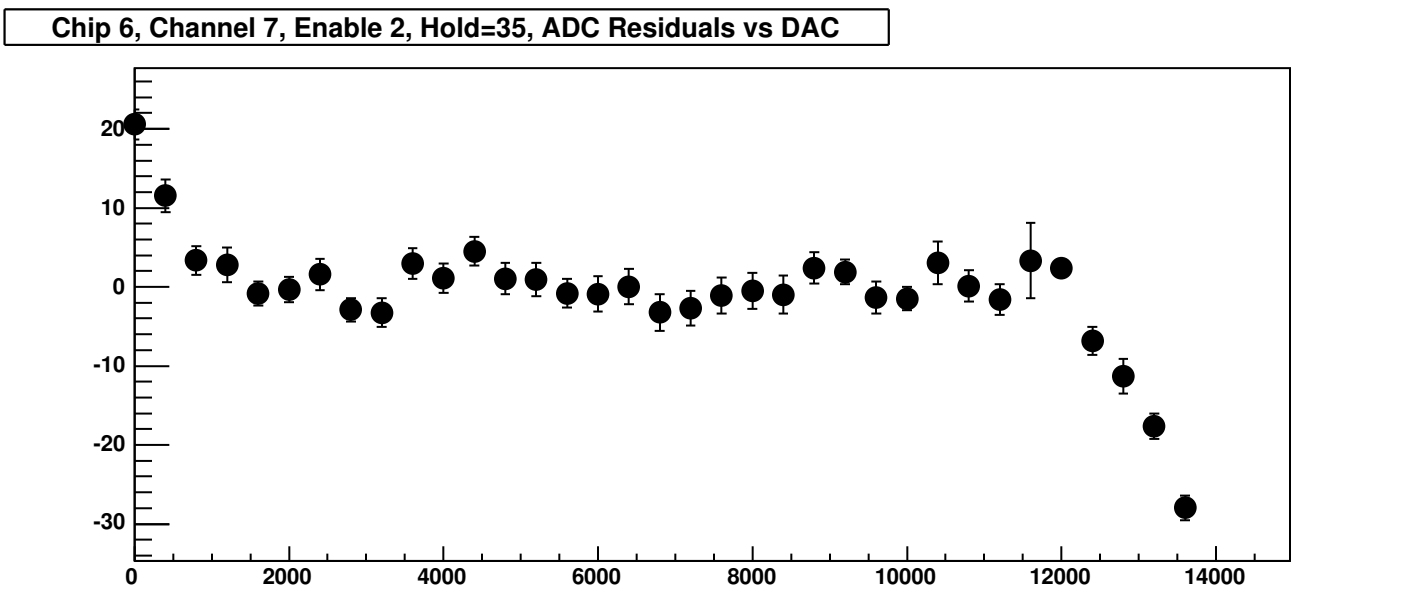


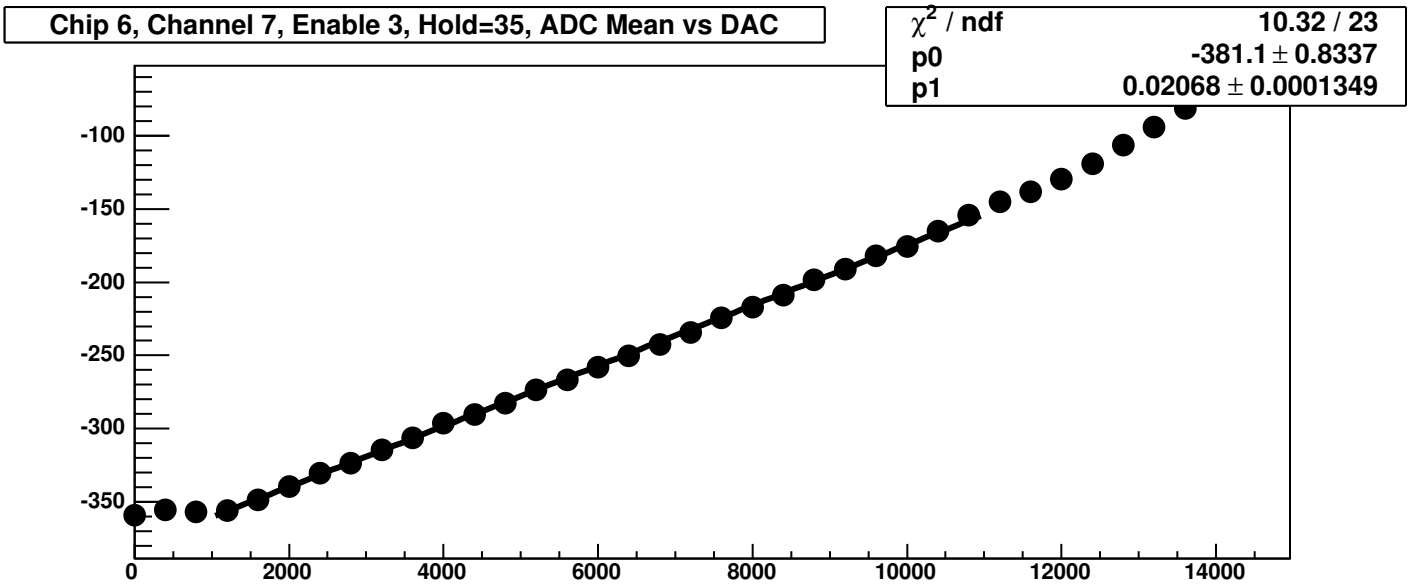
Chip 6, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

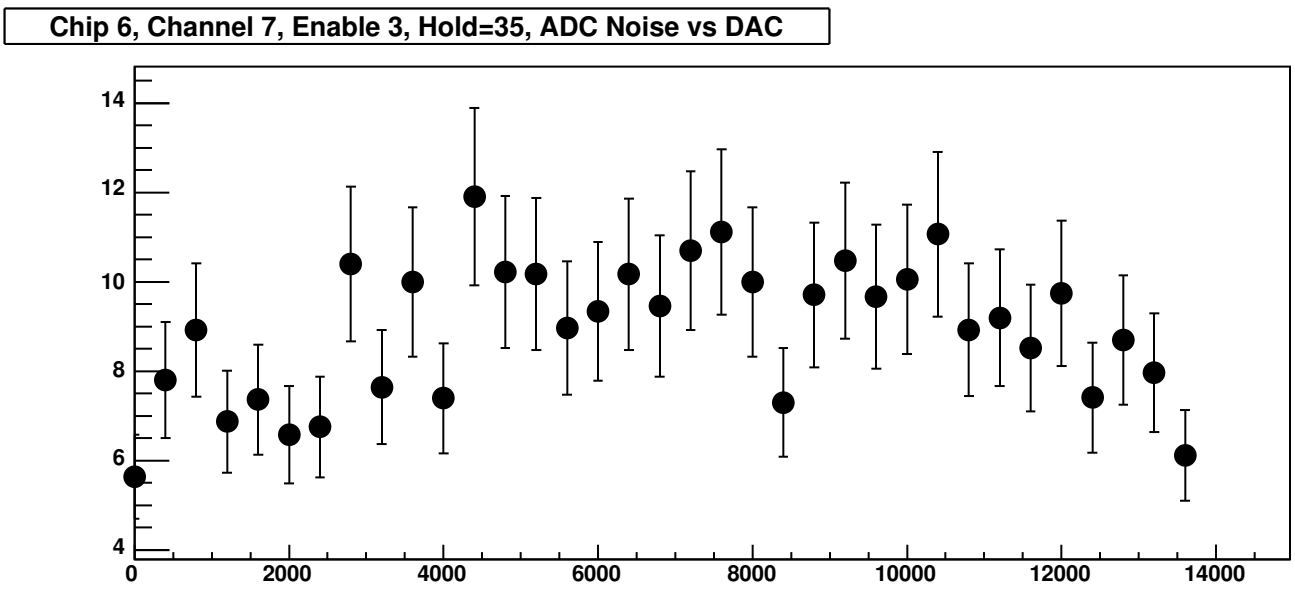


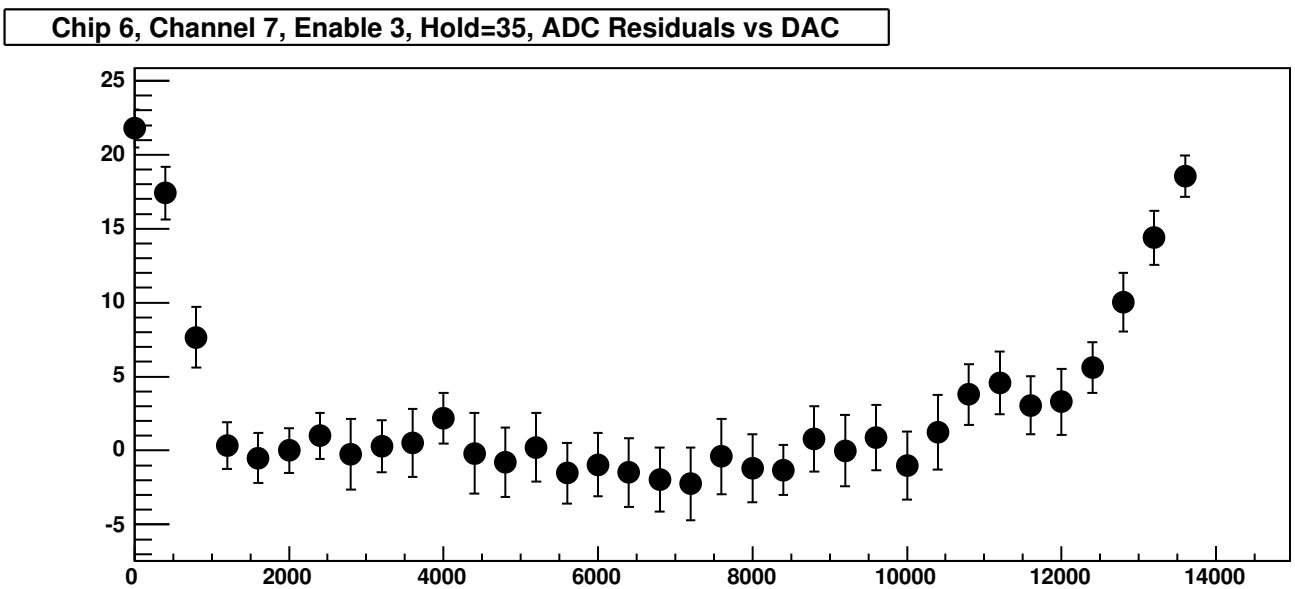

Chip 6, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 28.95 / 23
p0 -377.2 ± 0.8261
p1 0.02166 ± 0.0001281
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

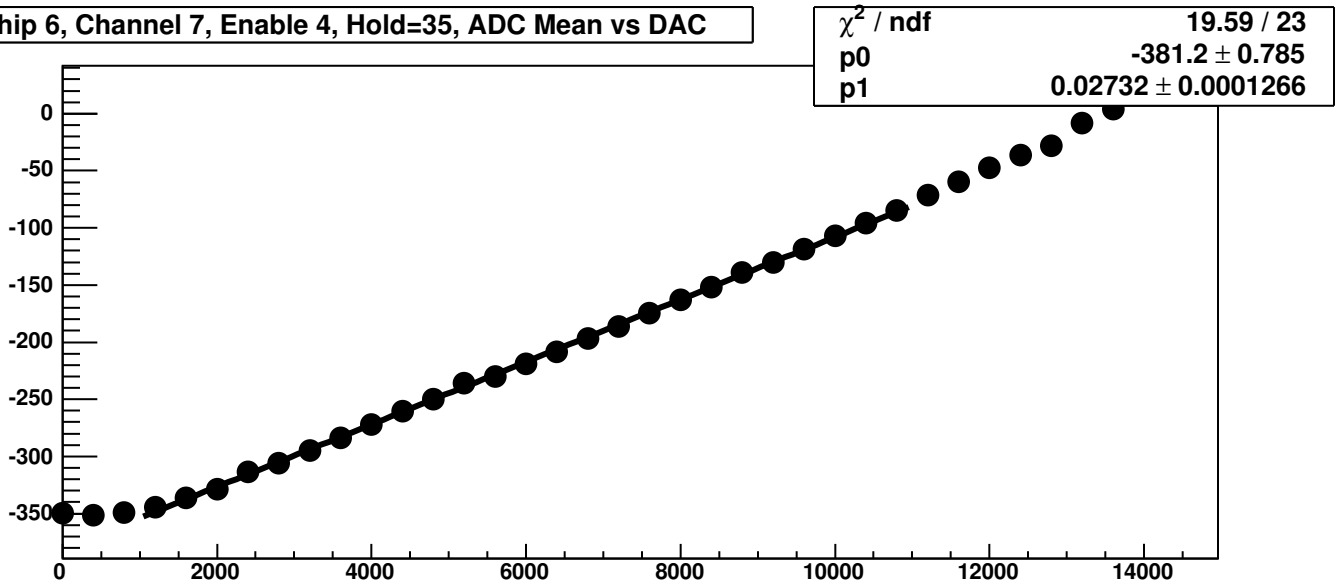

Chip 6, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 10.32 / 23
p0 -381.1 ± 0.8337
p1 0.02068 ± 0.0001349
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

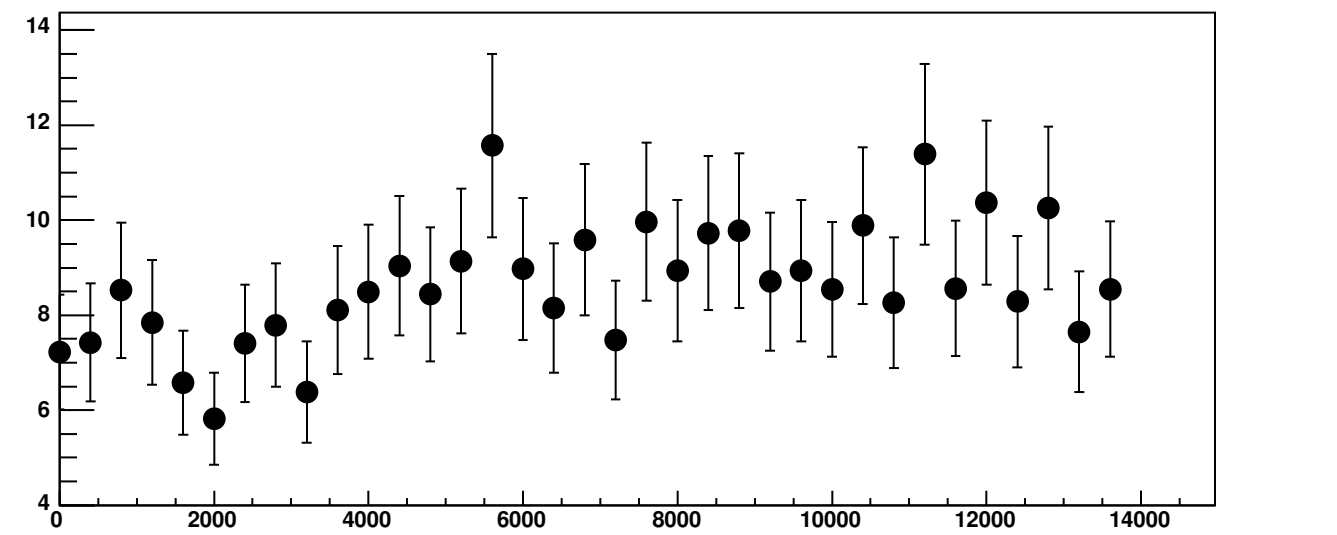

Chip 6, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

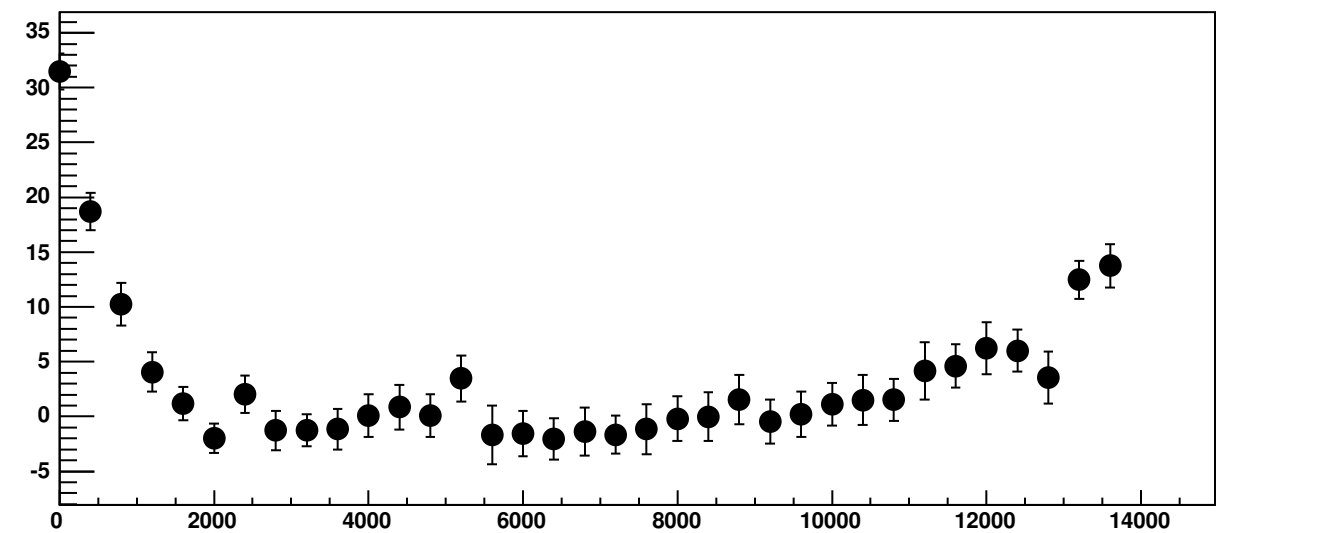
Chip 6, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

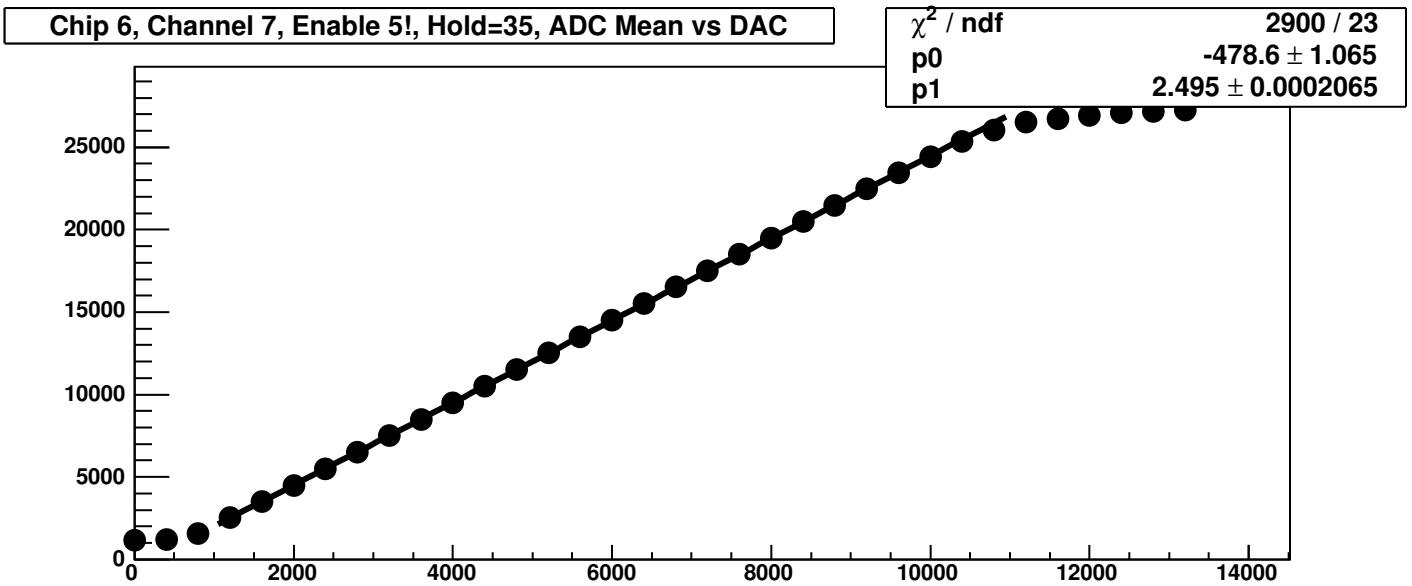


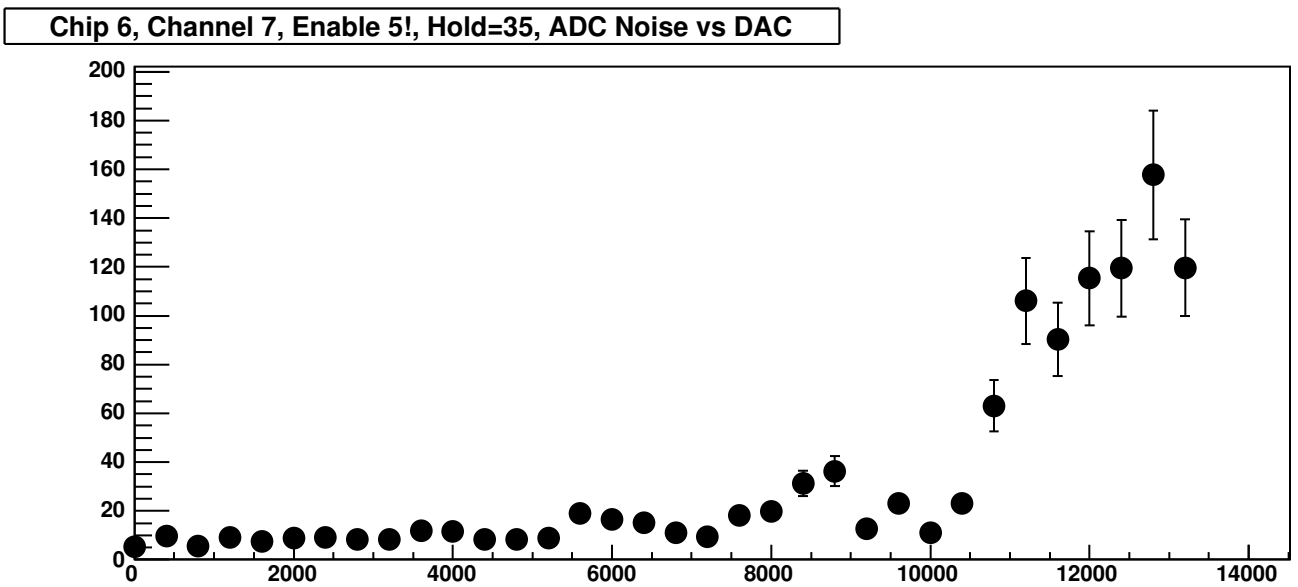
Chip 6, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

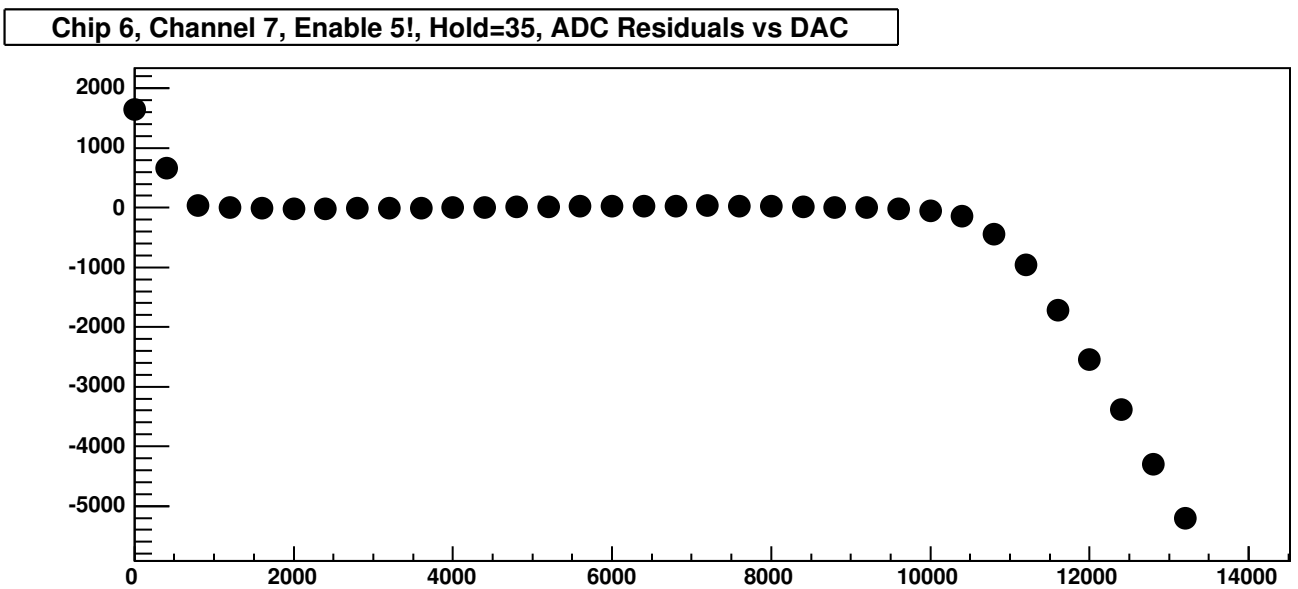


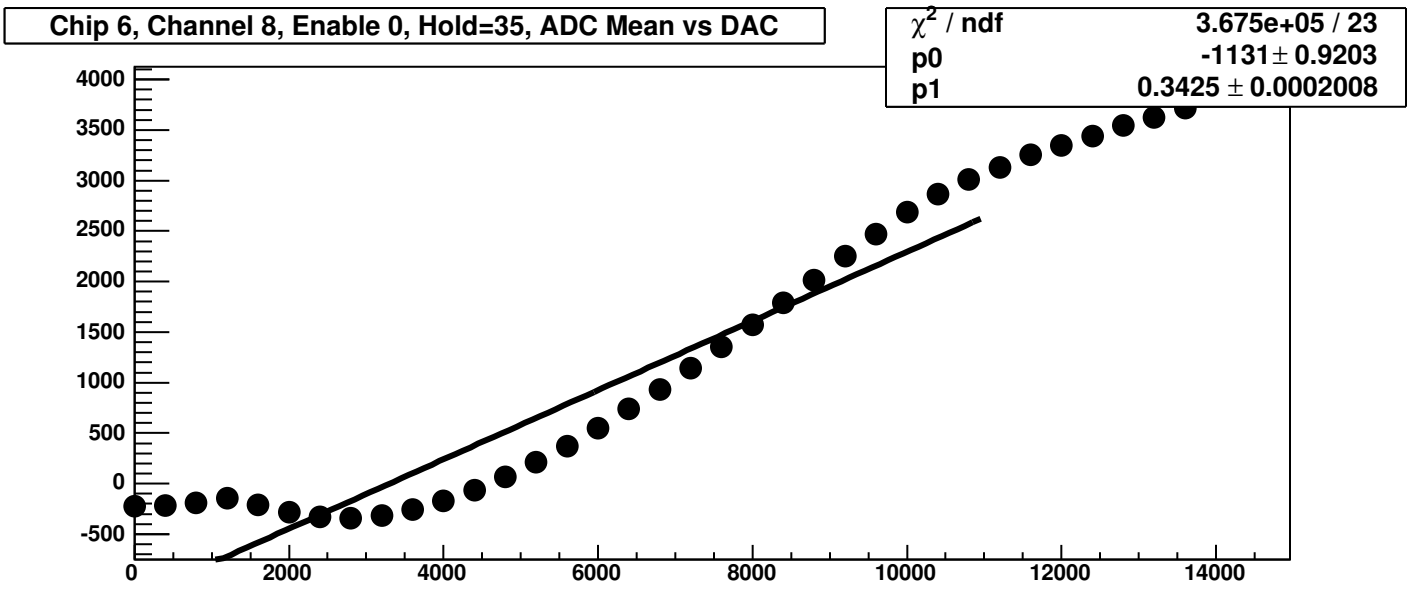
Chip 6, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

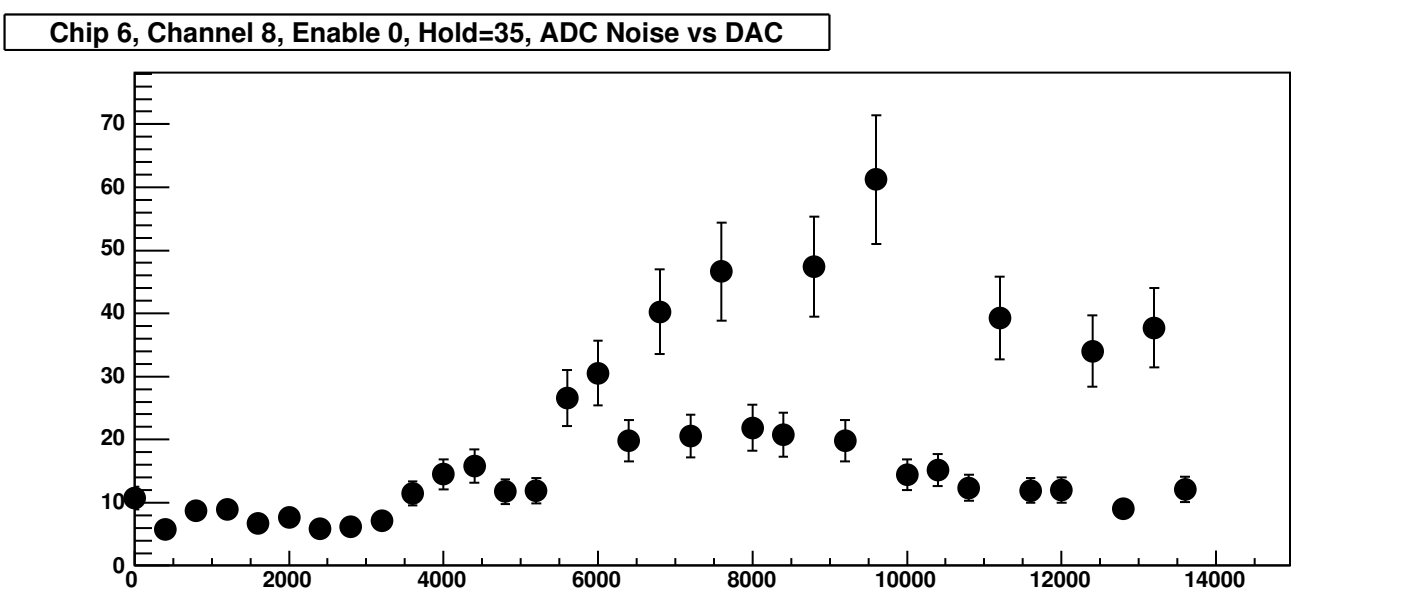


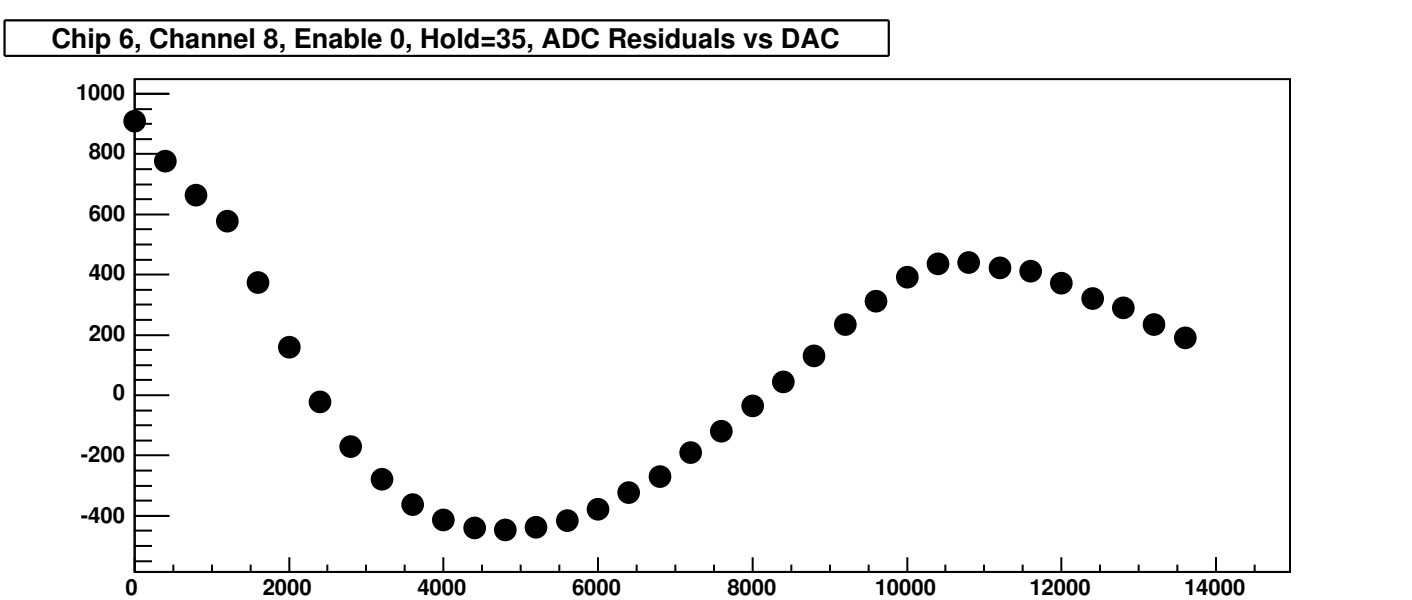

Chip 6, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2900 / 23
p0 -478.6 ± 1.065
p1 2.495 ± 0.0002065
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

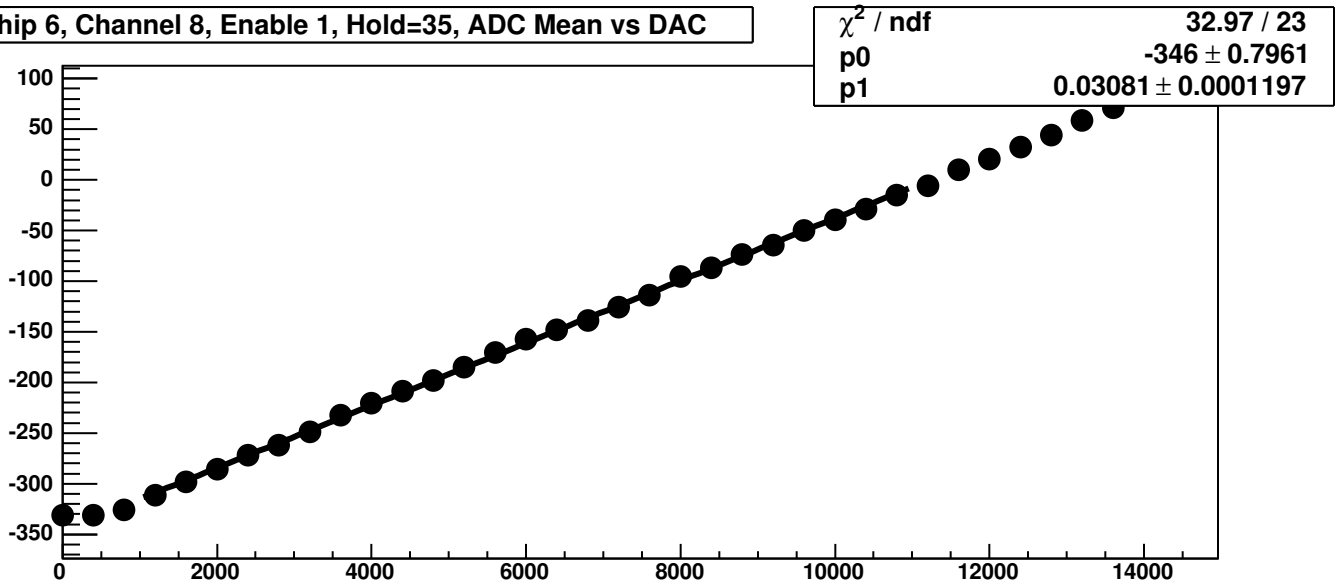

Chip 6, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.675e+05 / 23
p0 -1131 ± 0.9203
p1 0.3425 ± 0.0002008
4000
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

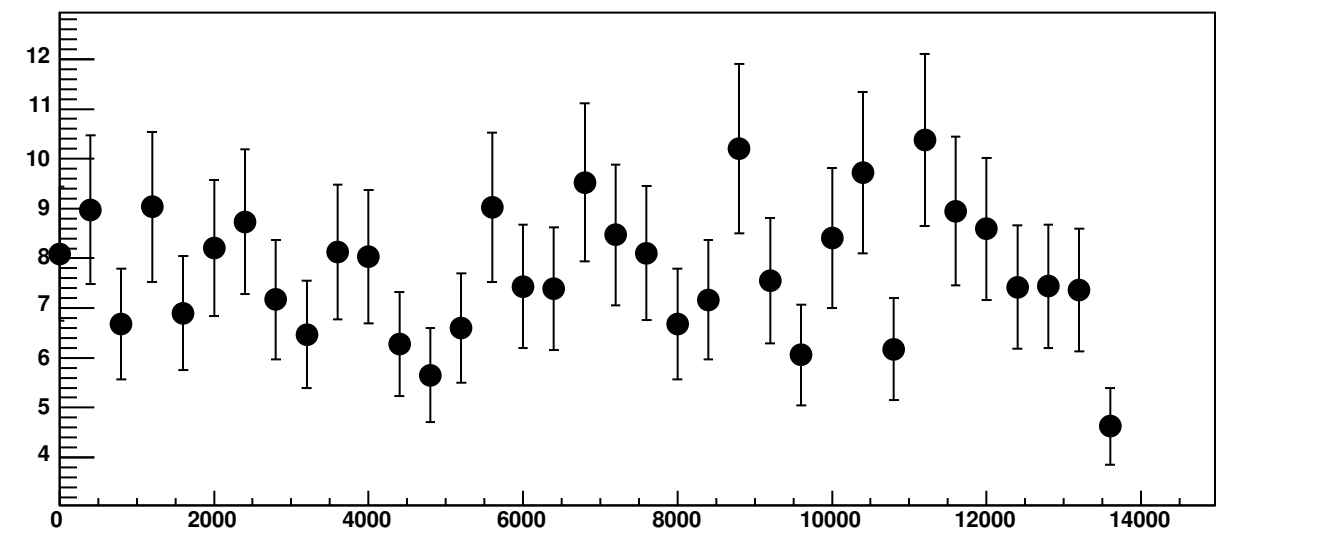

Chip 6, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

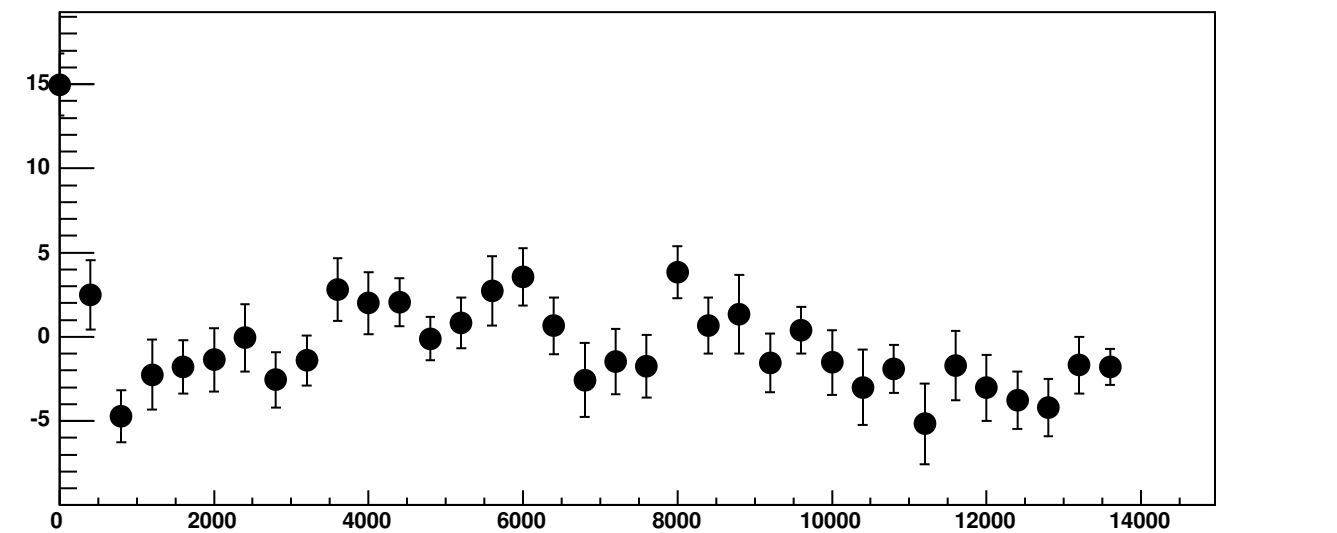
Chip 6, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

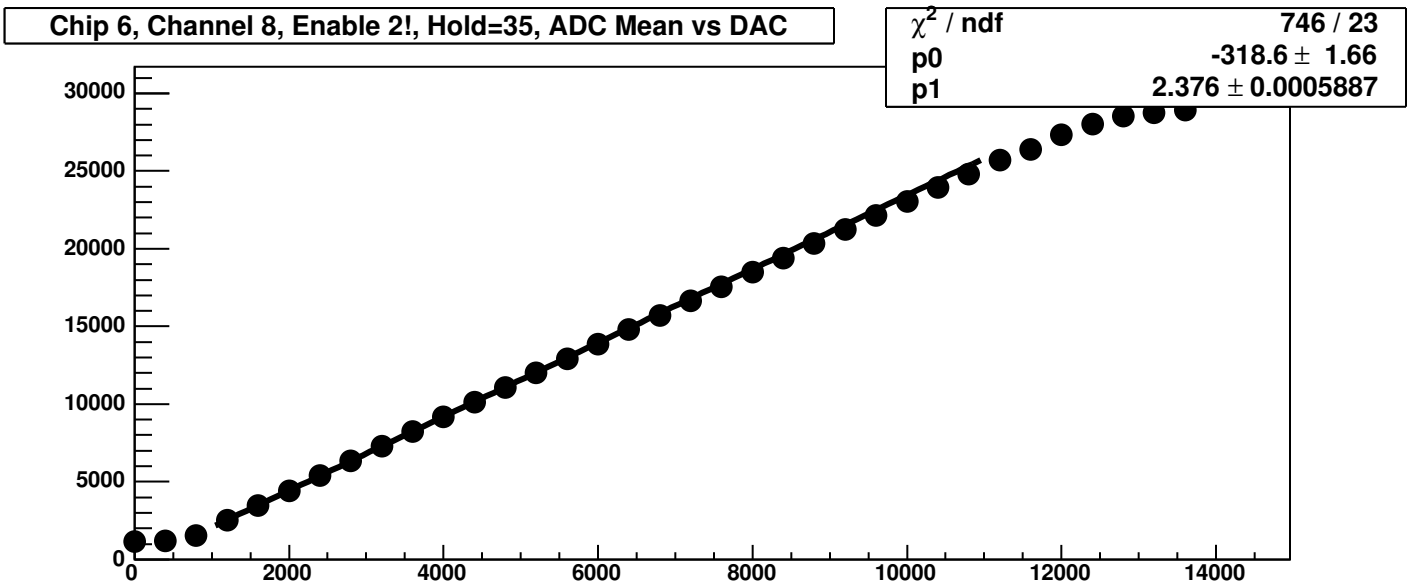


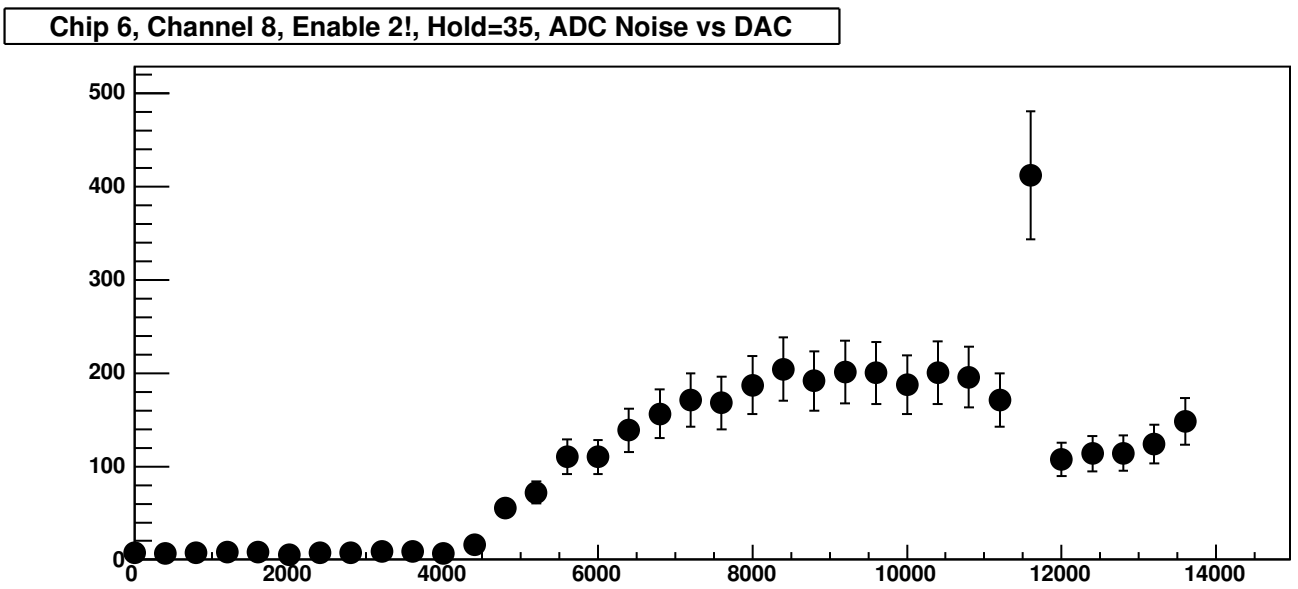
Chip 6, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC

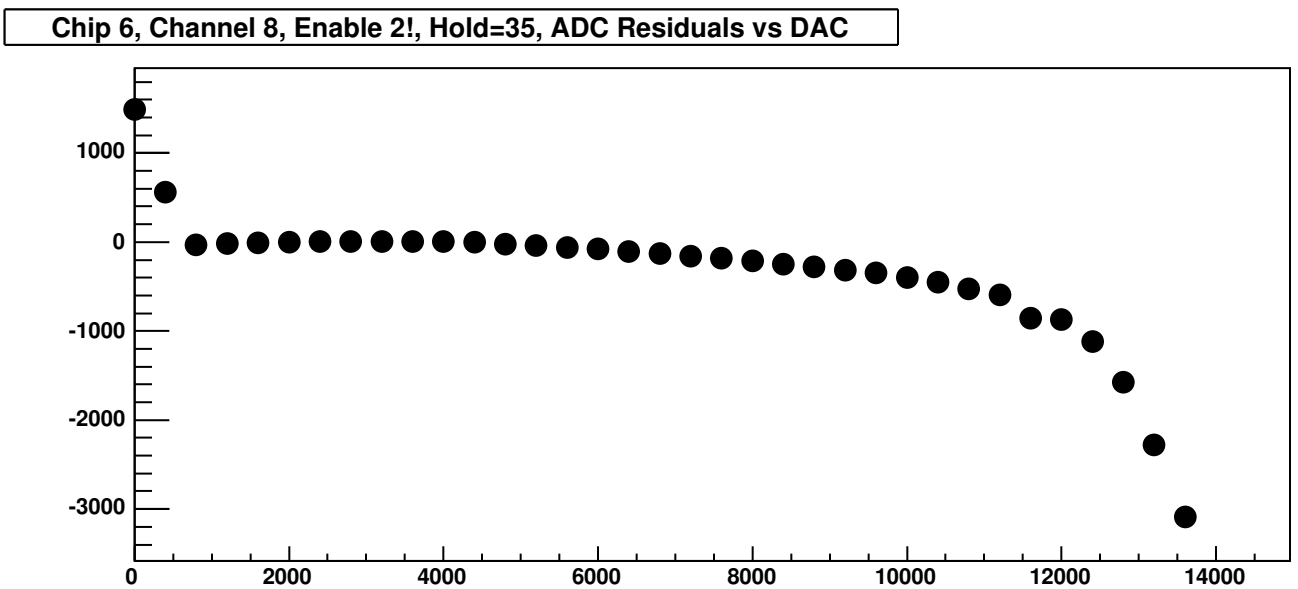


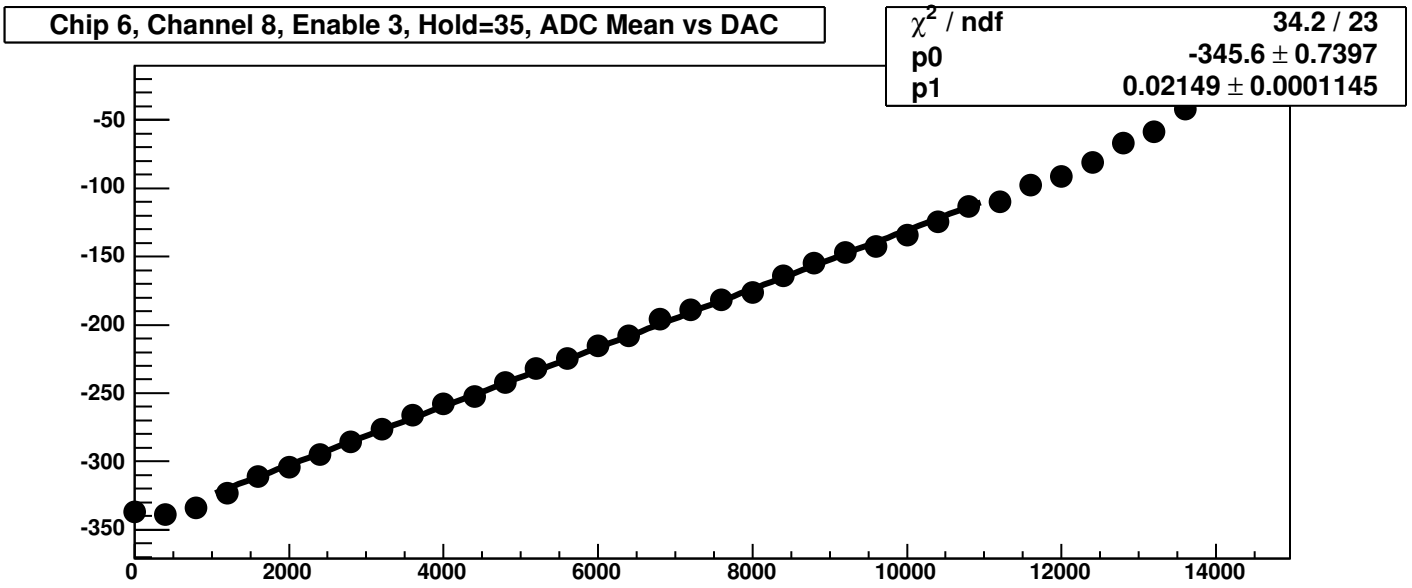
Chip 6, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

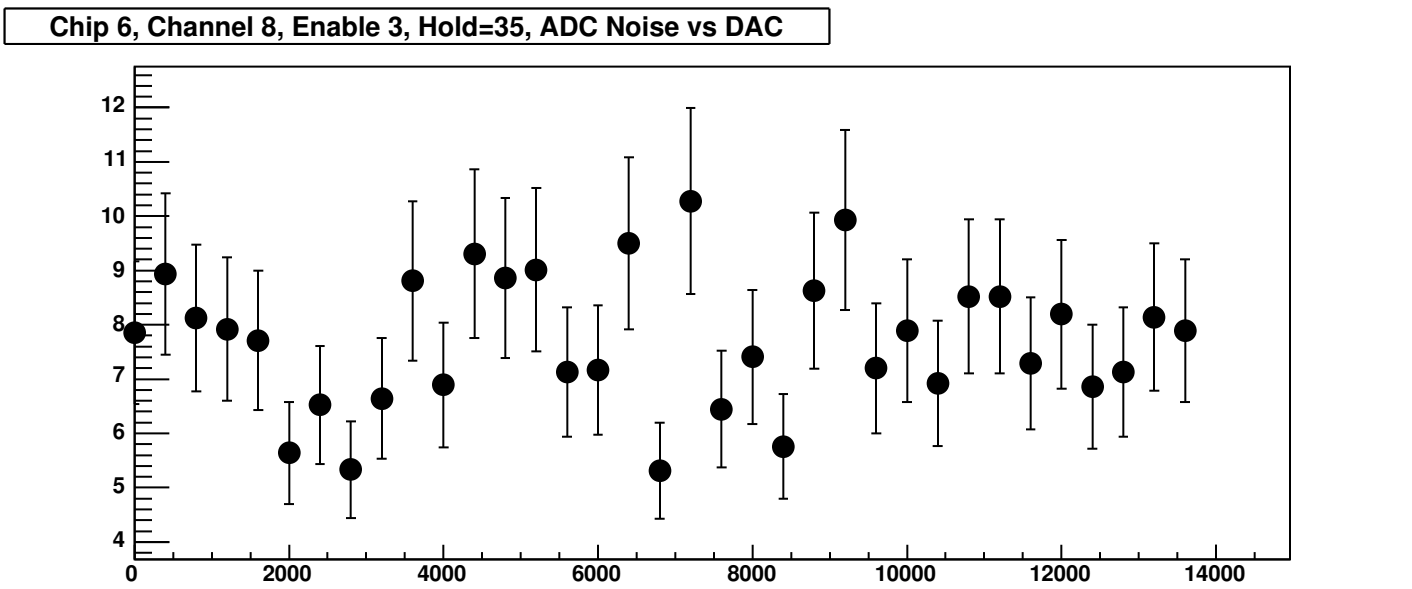


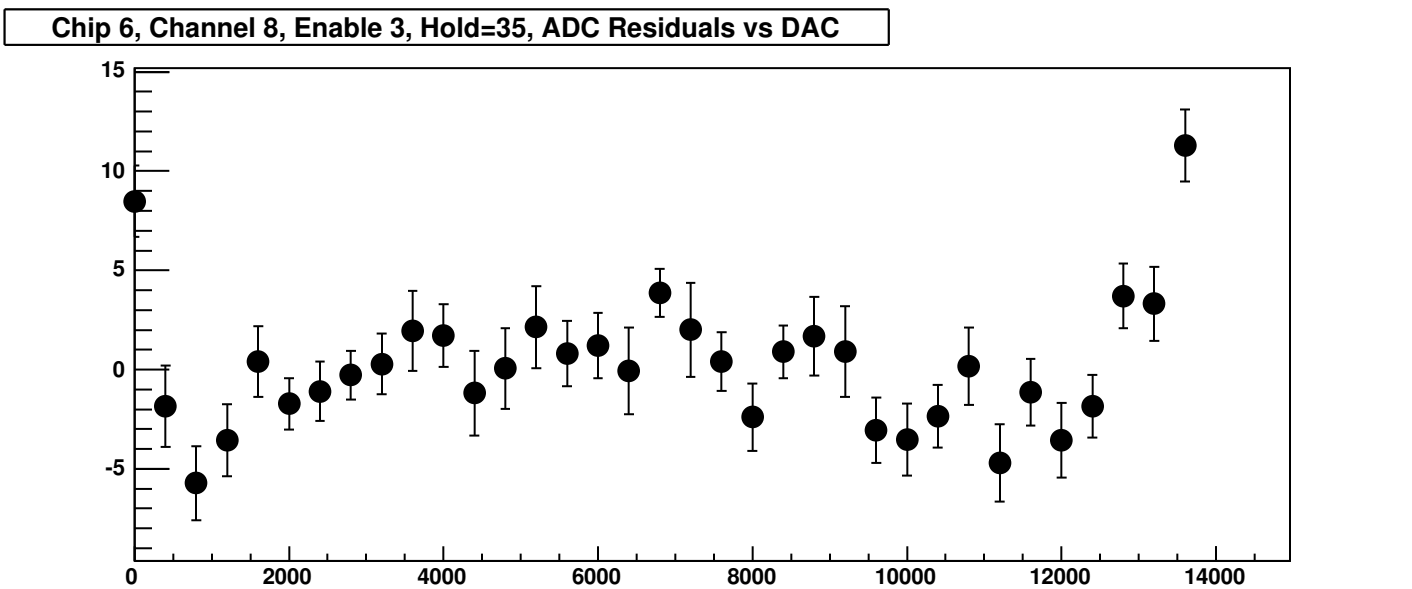

Chip 6, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 746 / 23
p0 -318.6 ± 1.66
p1 2.376 ± 0.0005887
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

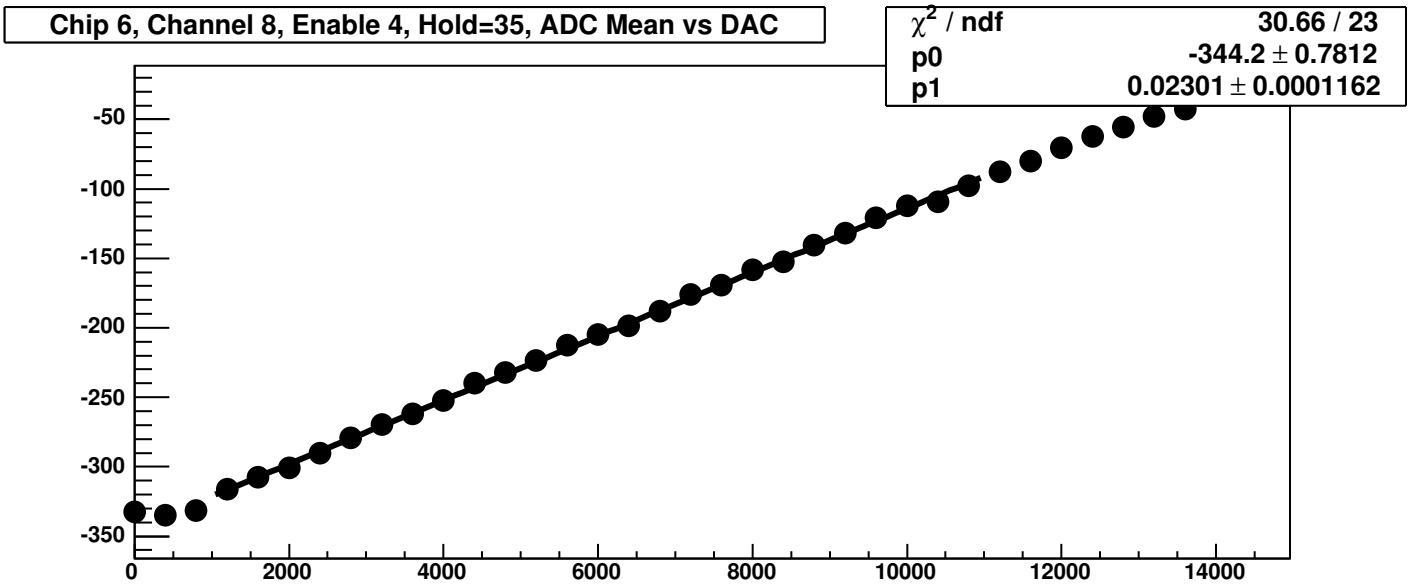

Chip 6, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

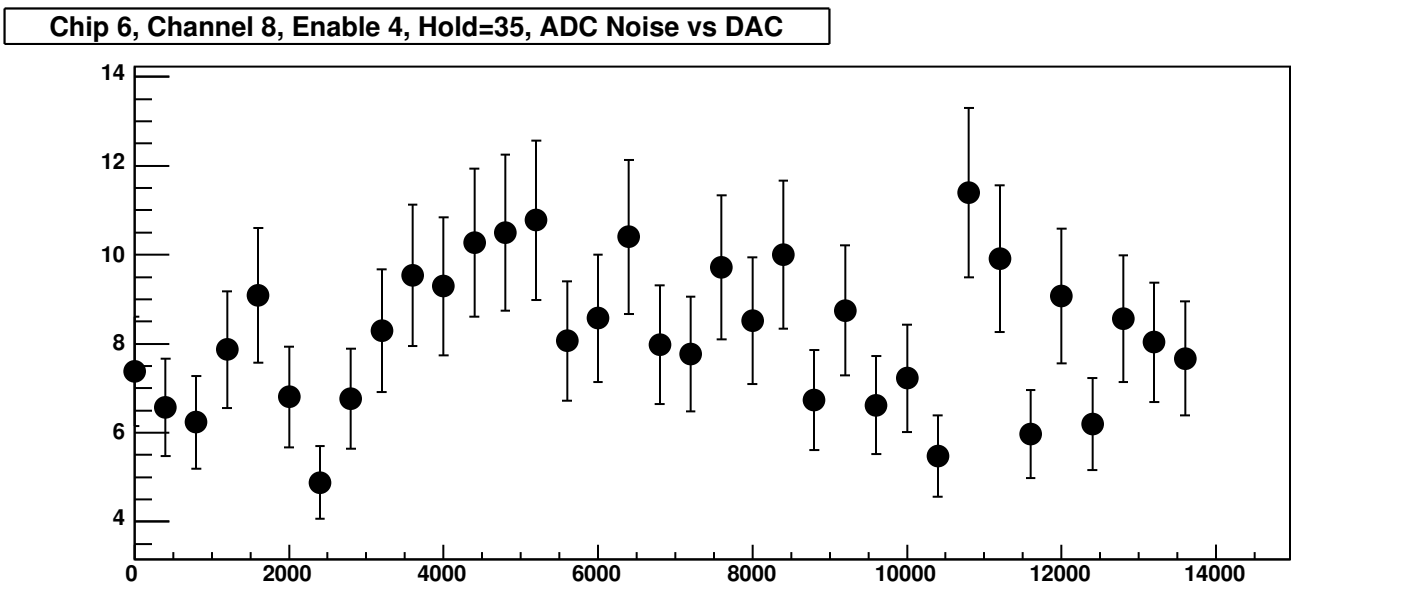

Chip 6, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

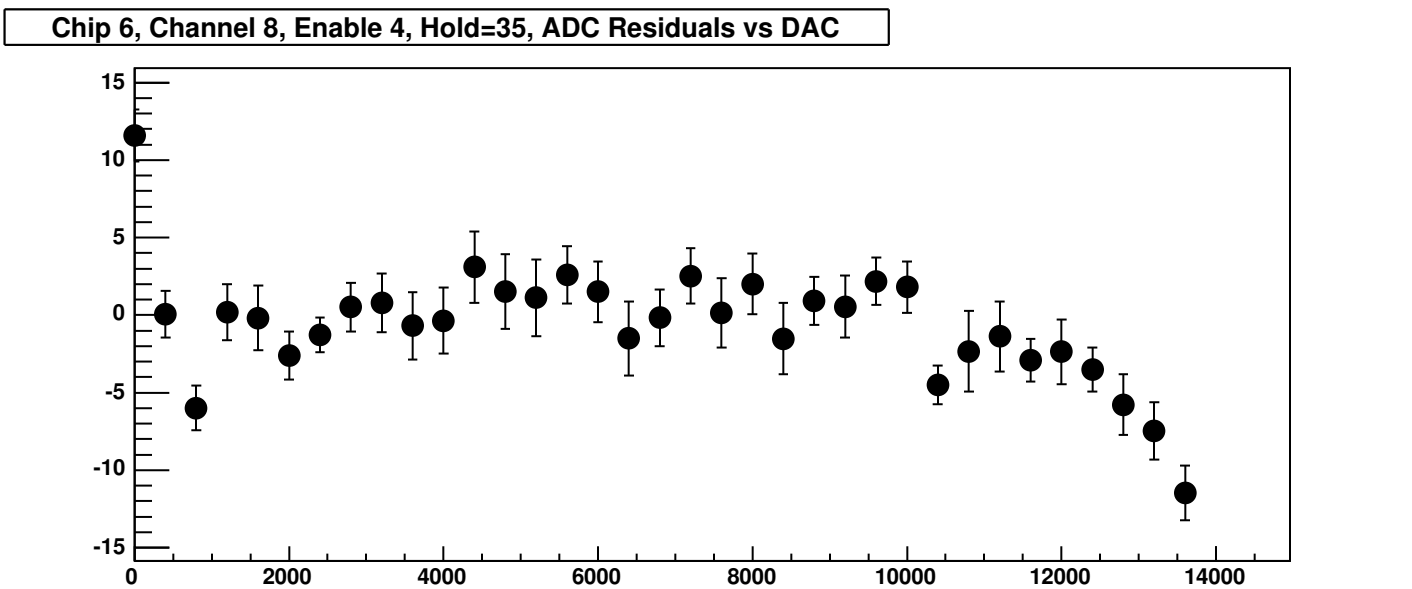

Chip 6, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 34.2 / 23
p0 -345.6 ± 0.7397
p1 0.02149 ± 0.0001145
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

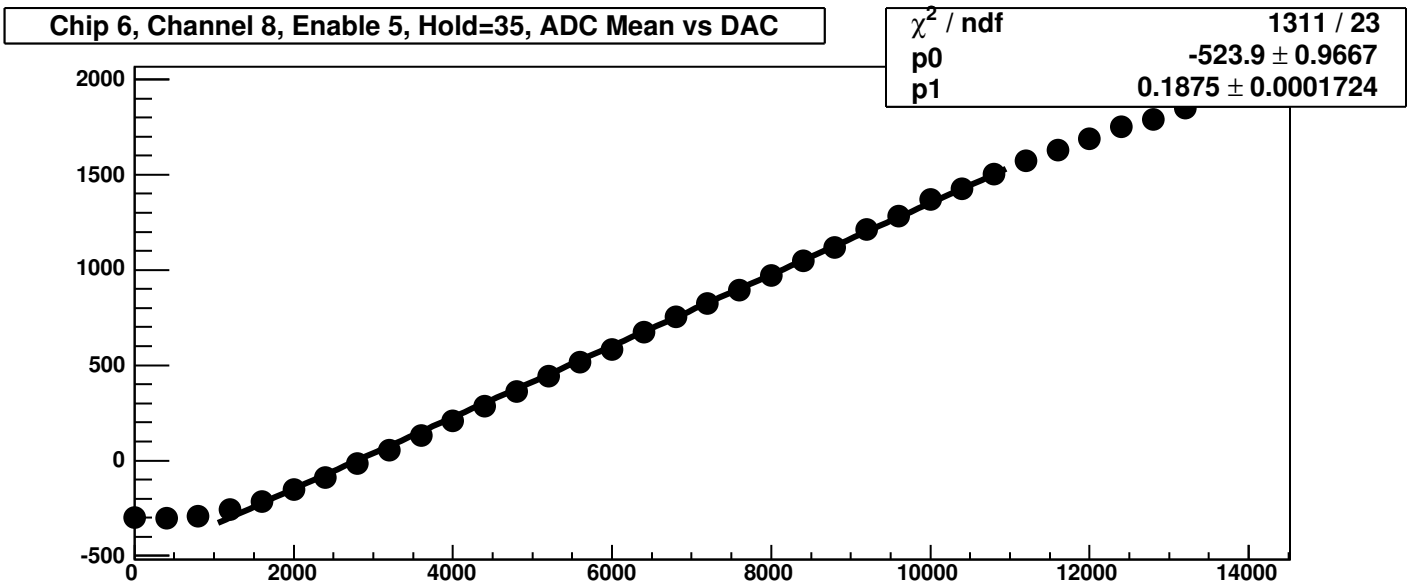

Chip 6, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

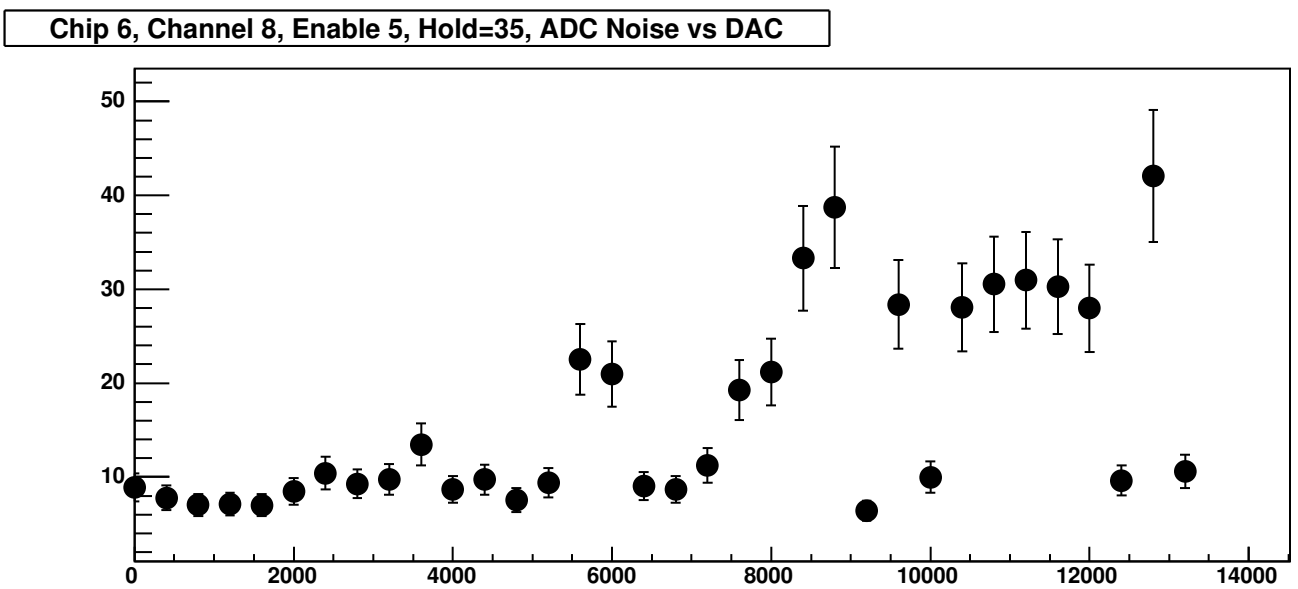

Chip 6, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

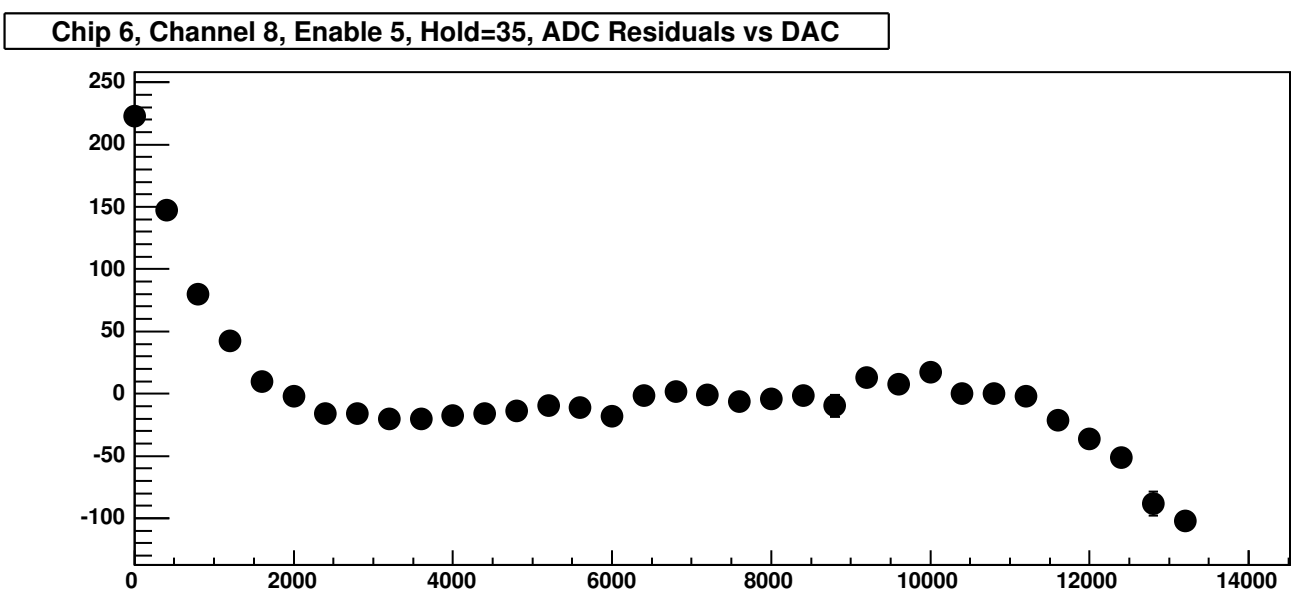

Chip 6, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.66 / 23
p0 -344.2 ± 0.7812
p1 0.02301 ± 0.0001162
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

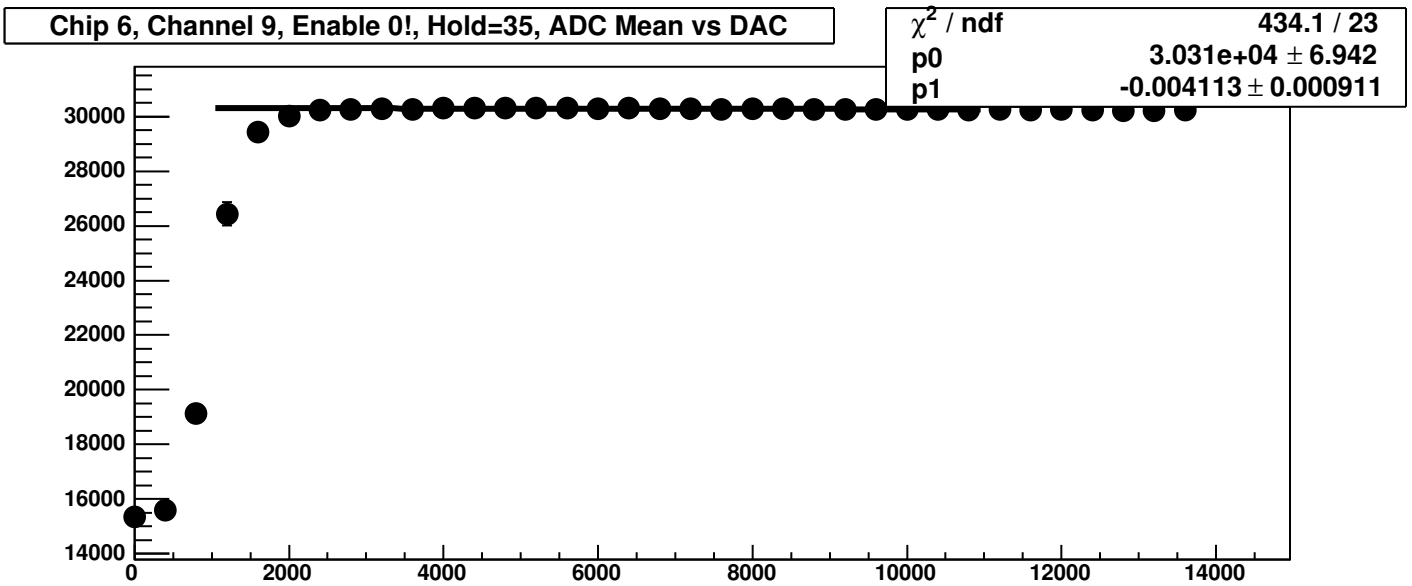

Chip 6, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

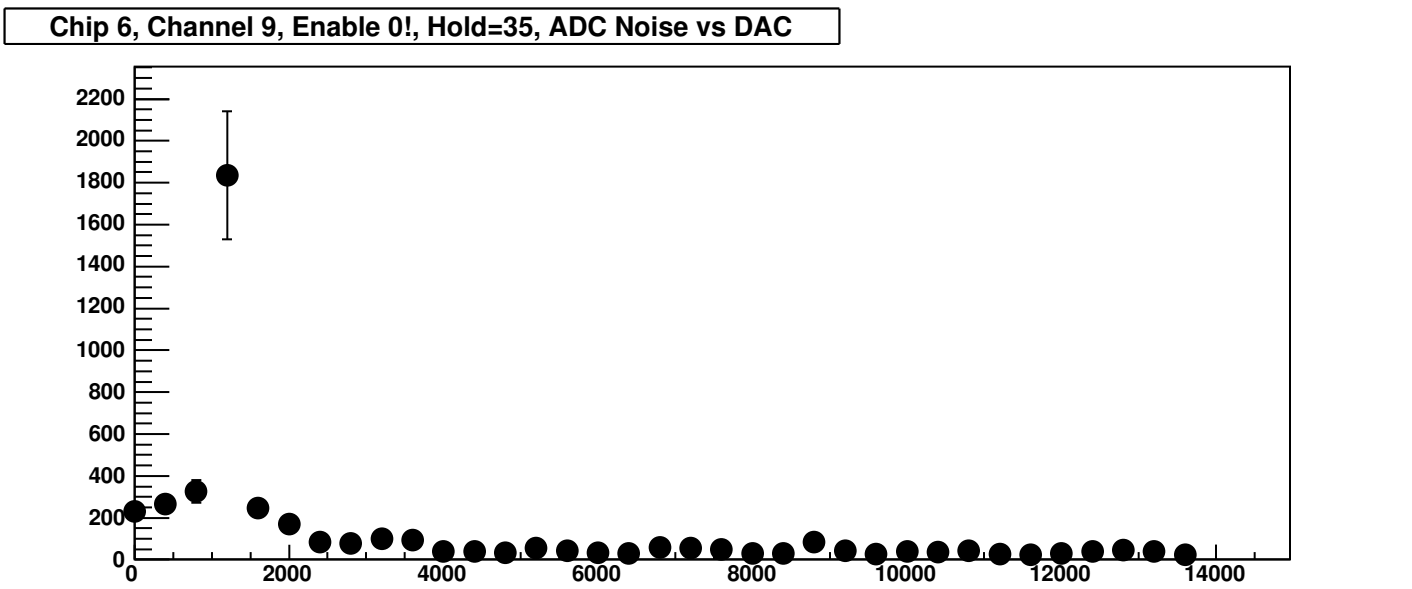

Chip 6, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

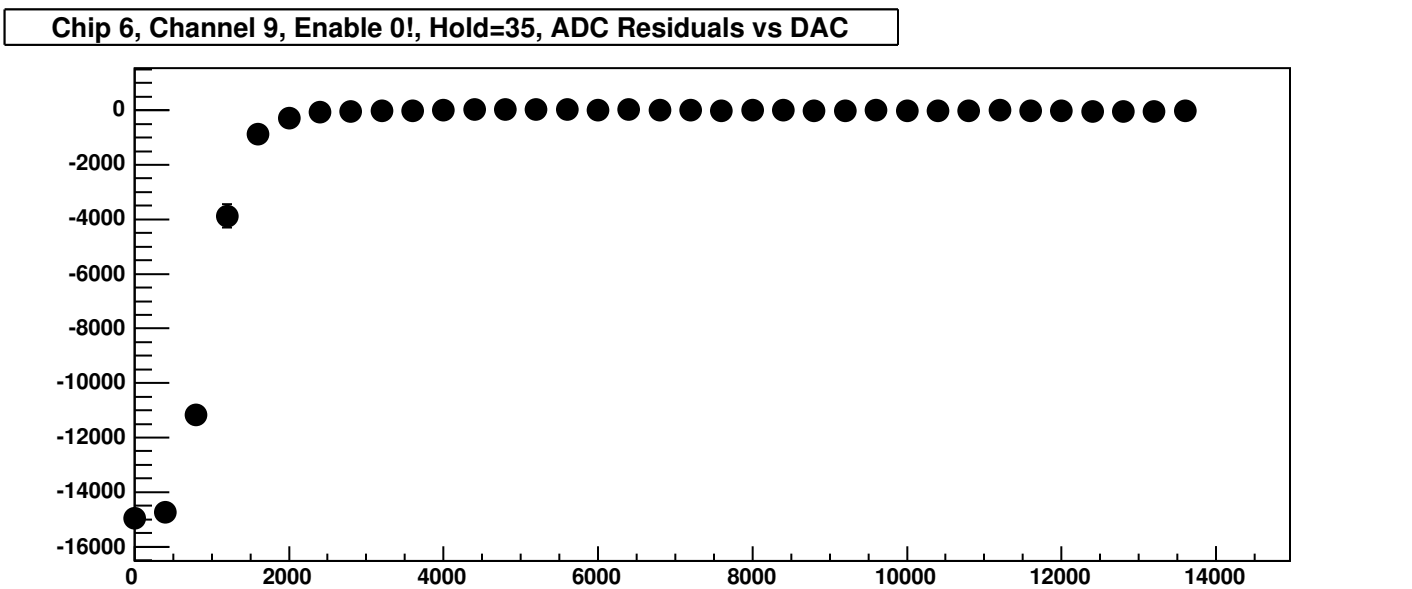

Chip 6, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1311 / 23
p0 -523.9 ± 0.9667
p1 0.1875 ± 0.0001724
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

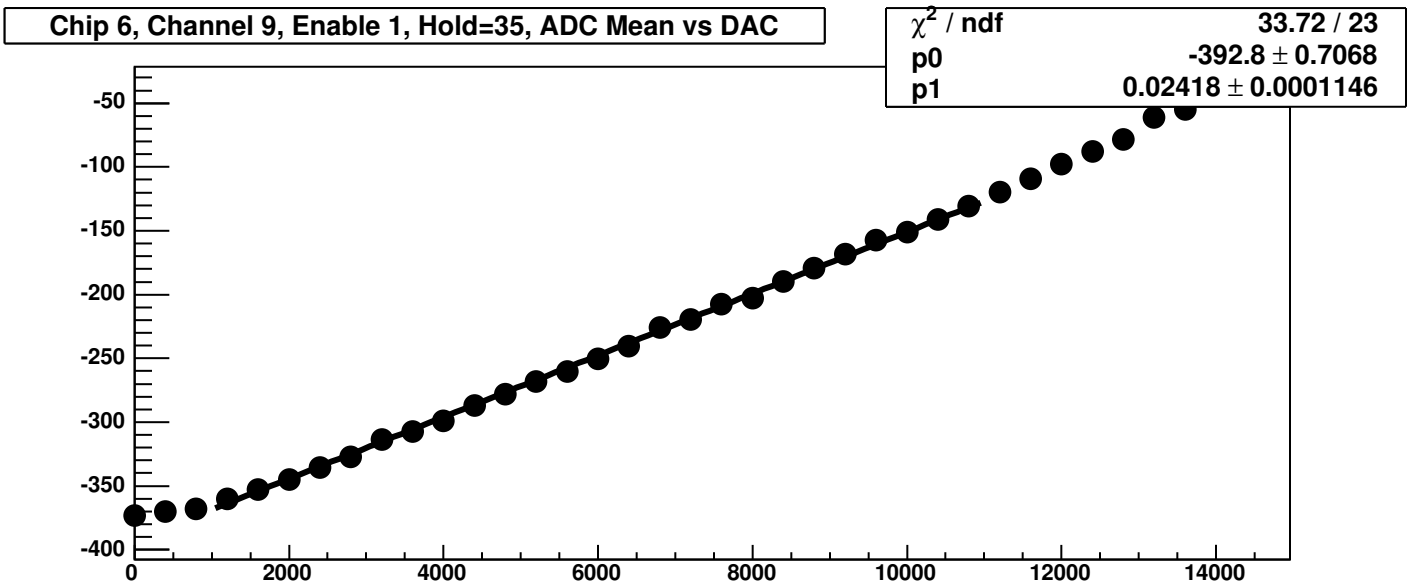

Chip 6, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

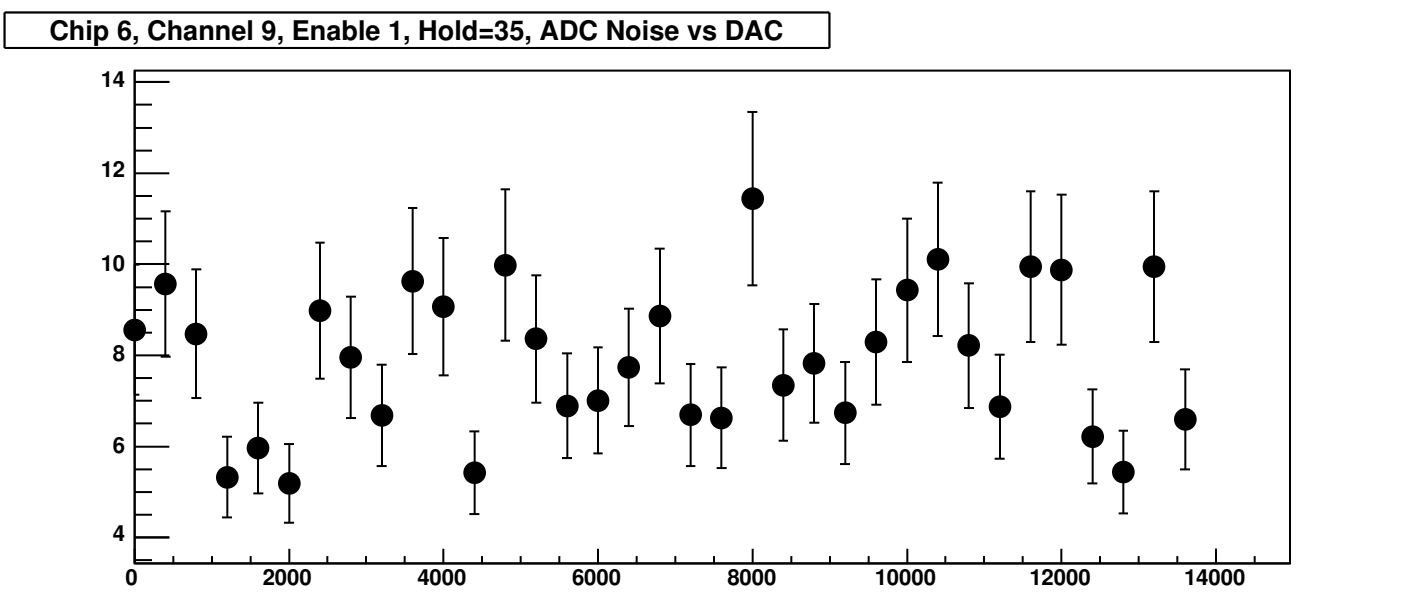

Chip 6, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

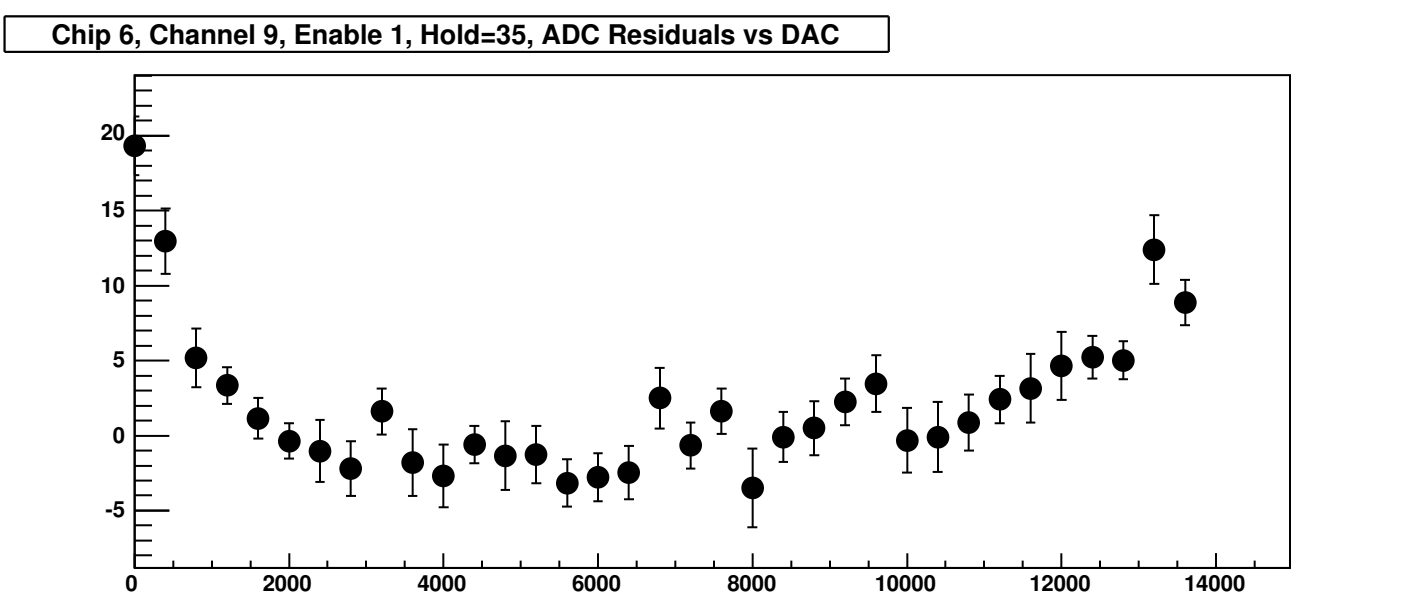

Chip 6, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 434.1 / 23
p0 3.031e+04 ± 6.942
p1 -0.004113 ± 0.000911
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

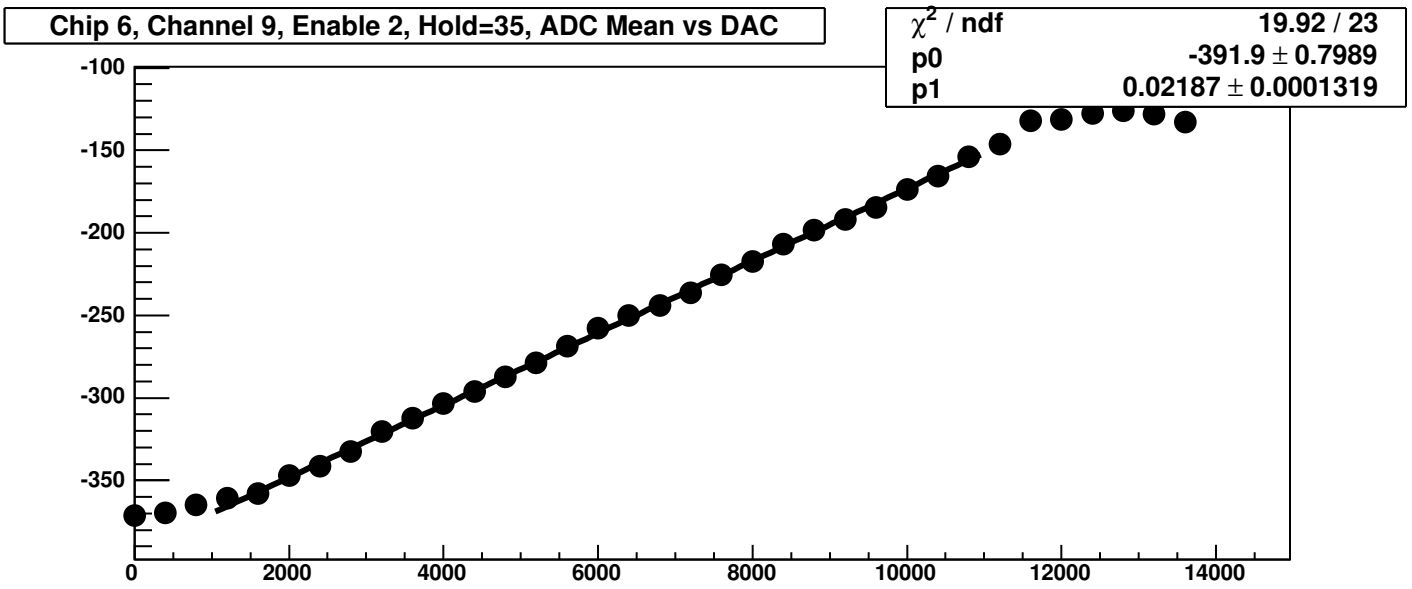

Chip 6, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

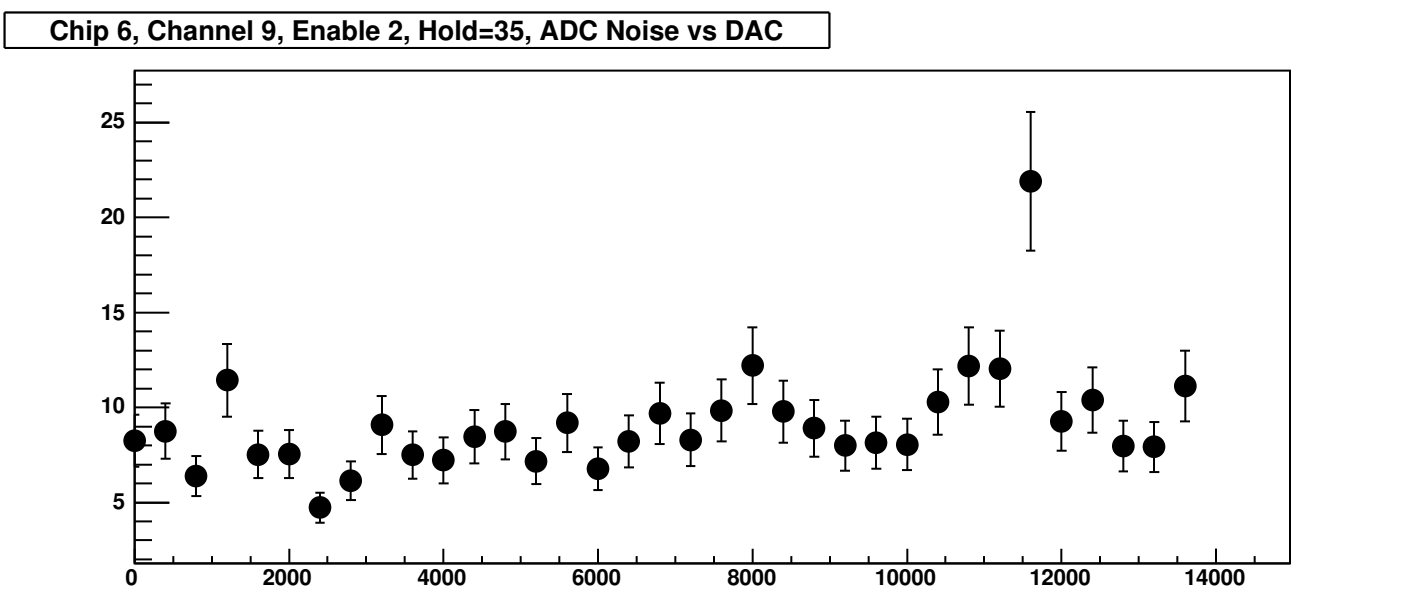

Chip 6, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

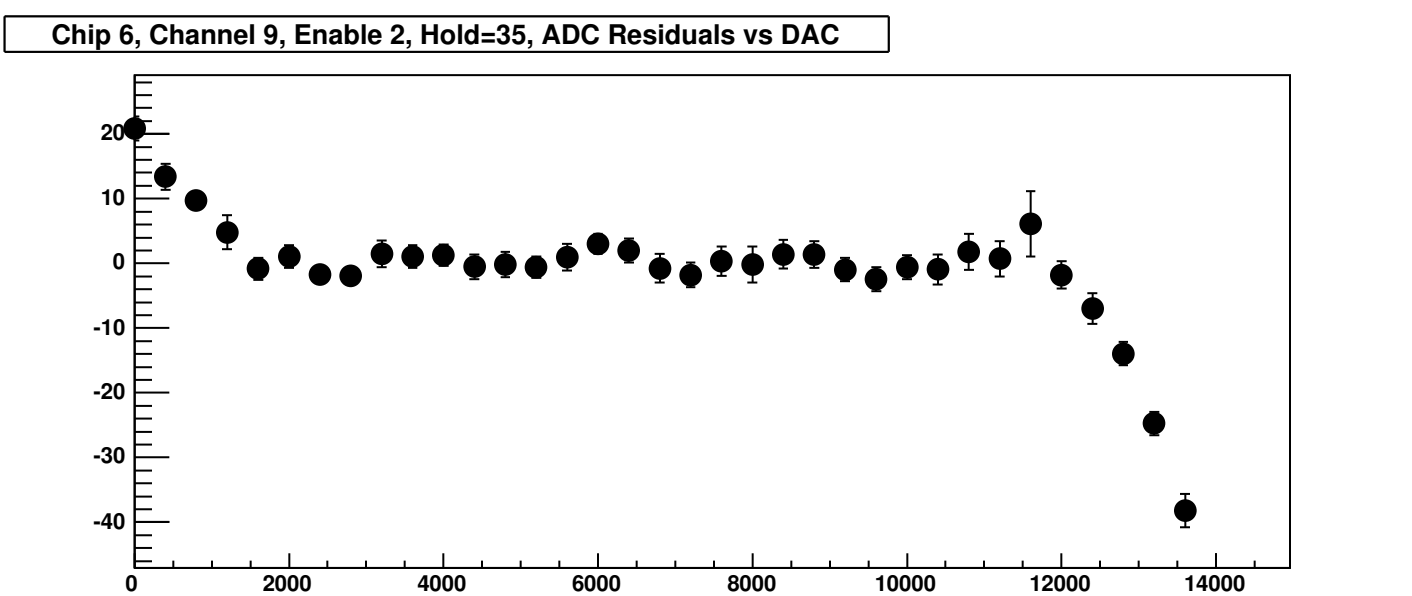

Chip 6, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.72 / 23
p0 -392.8 ± 0.7068
p1 0.02418 ± 0.0001146
-50
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

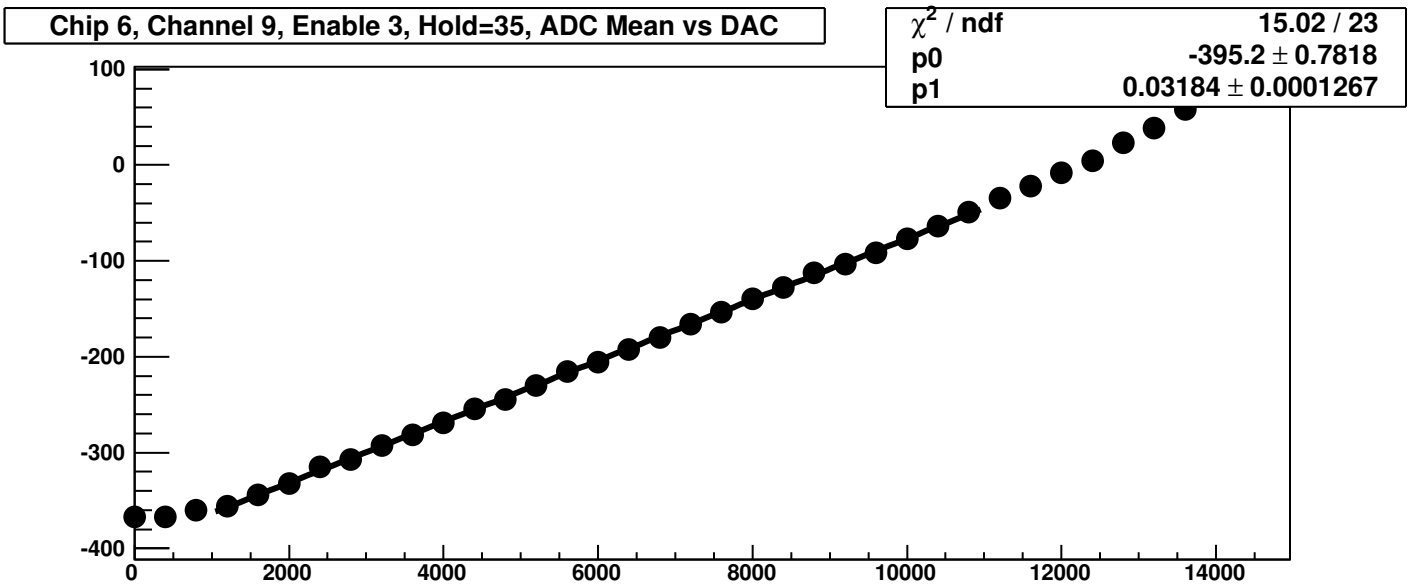

Chip 6, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

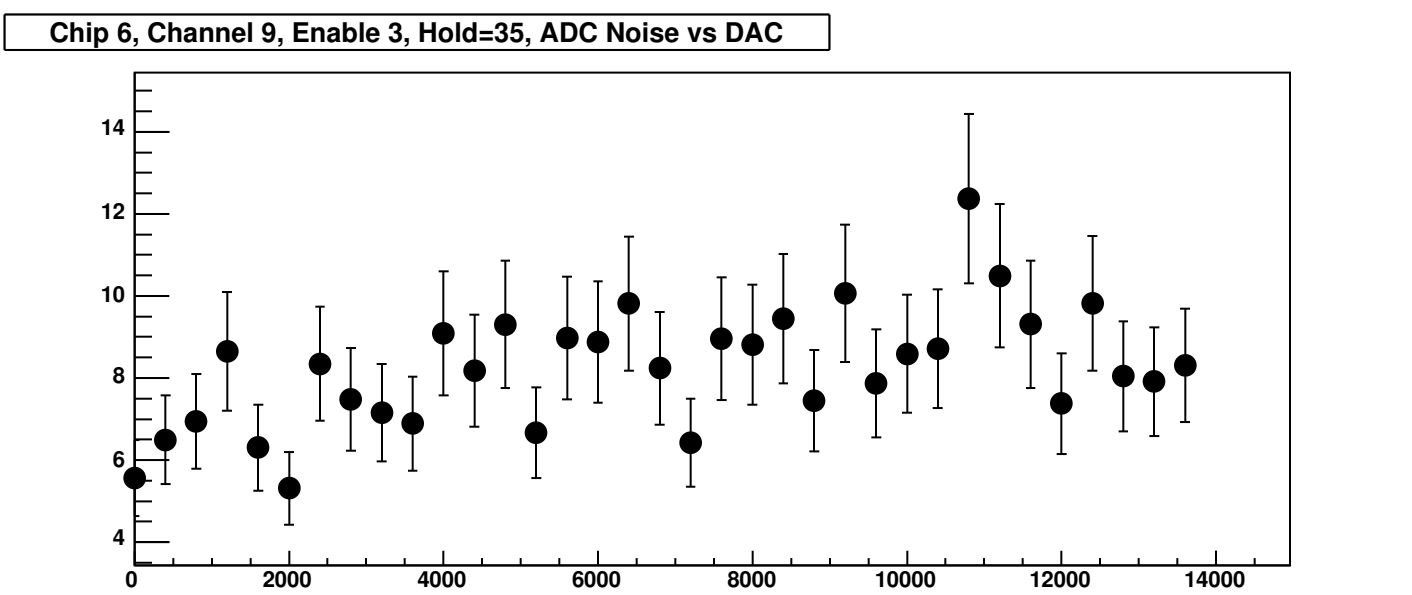

Chip 6, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

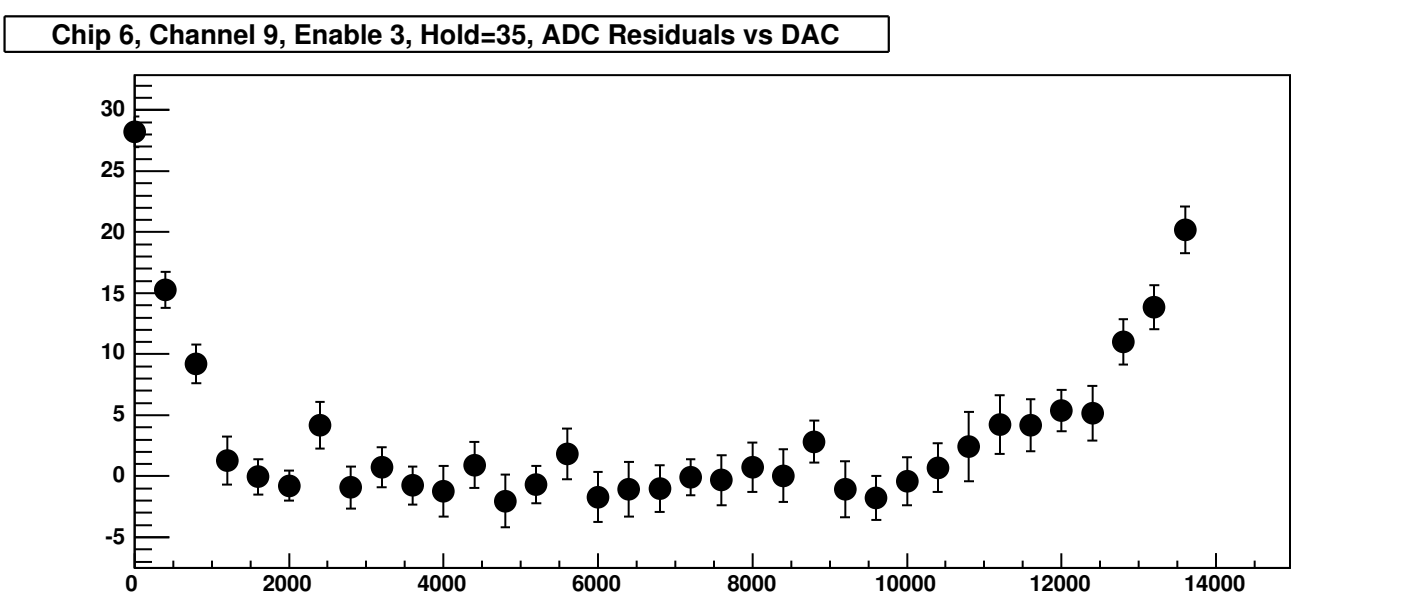

Chip 6, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 19.92 / 23
p0 -391.9 ± 0.7989
p1 0.02187 ± 0.0001319
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

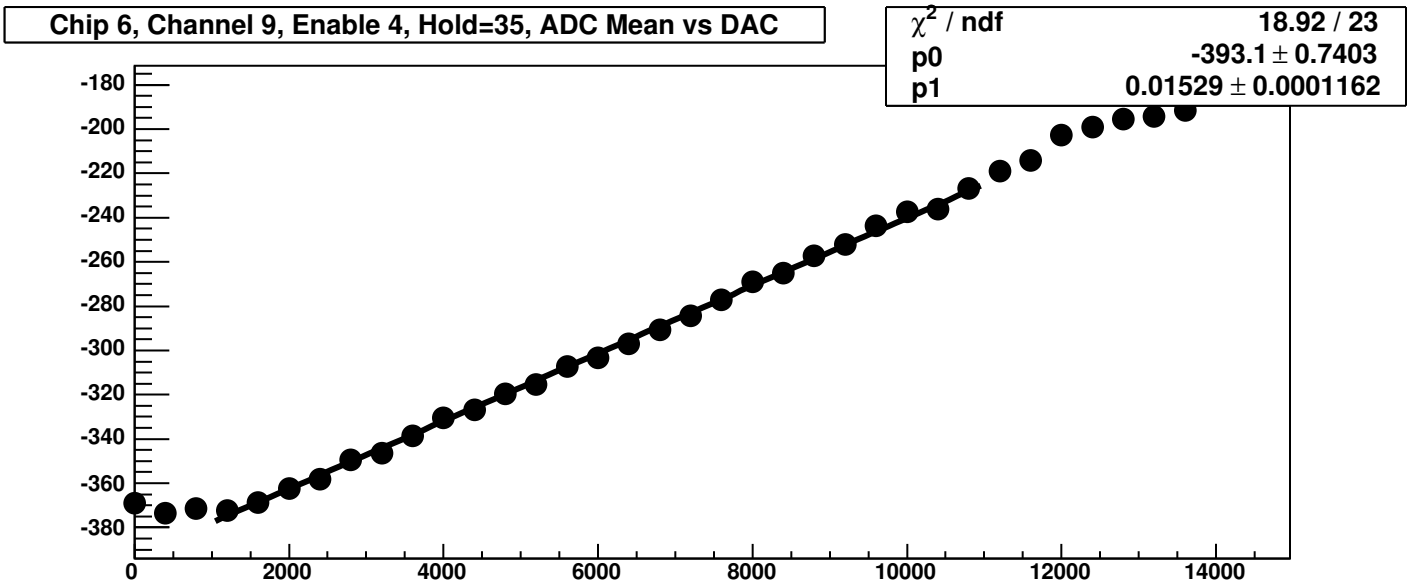

Chip 6, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

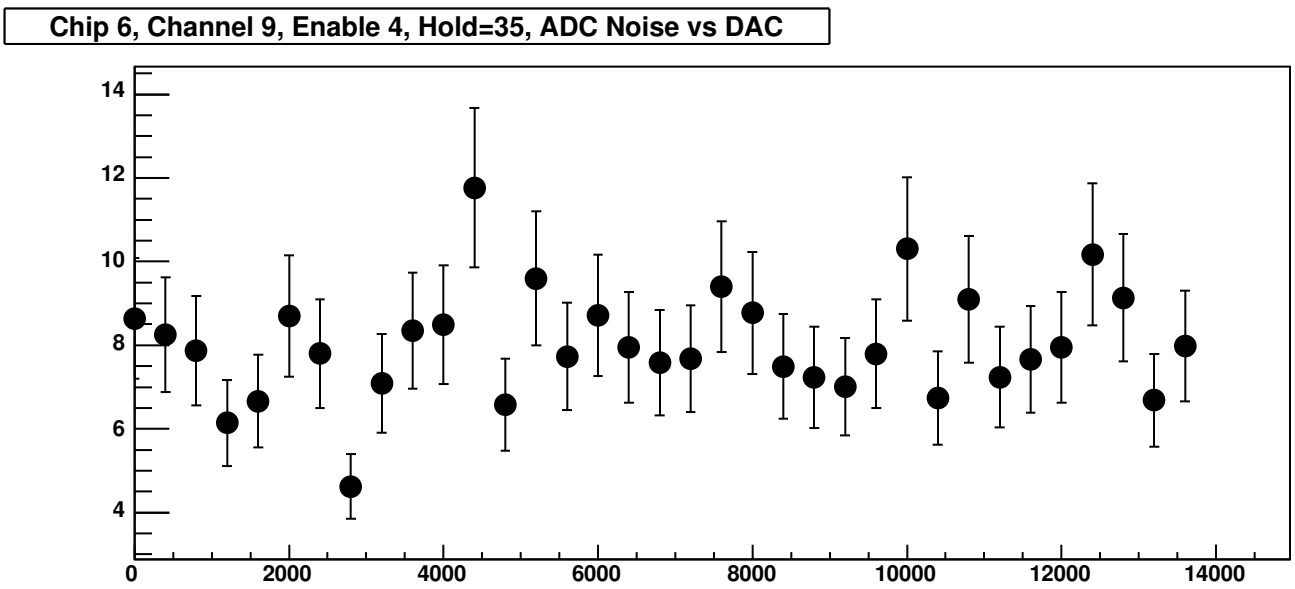

Chip 6, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

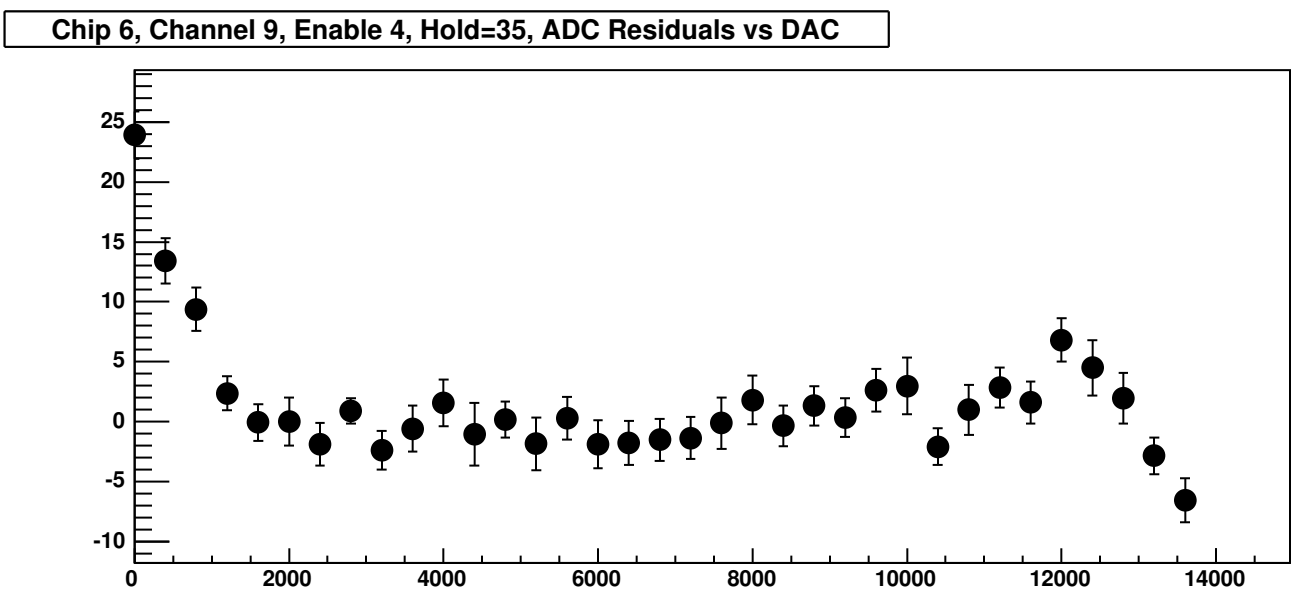

Chip 6, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 15.02 / 23
p0 -395.2 ± 0.7818
p1 0.03184 ± 0.0001267
100
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

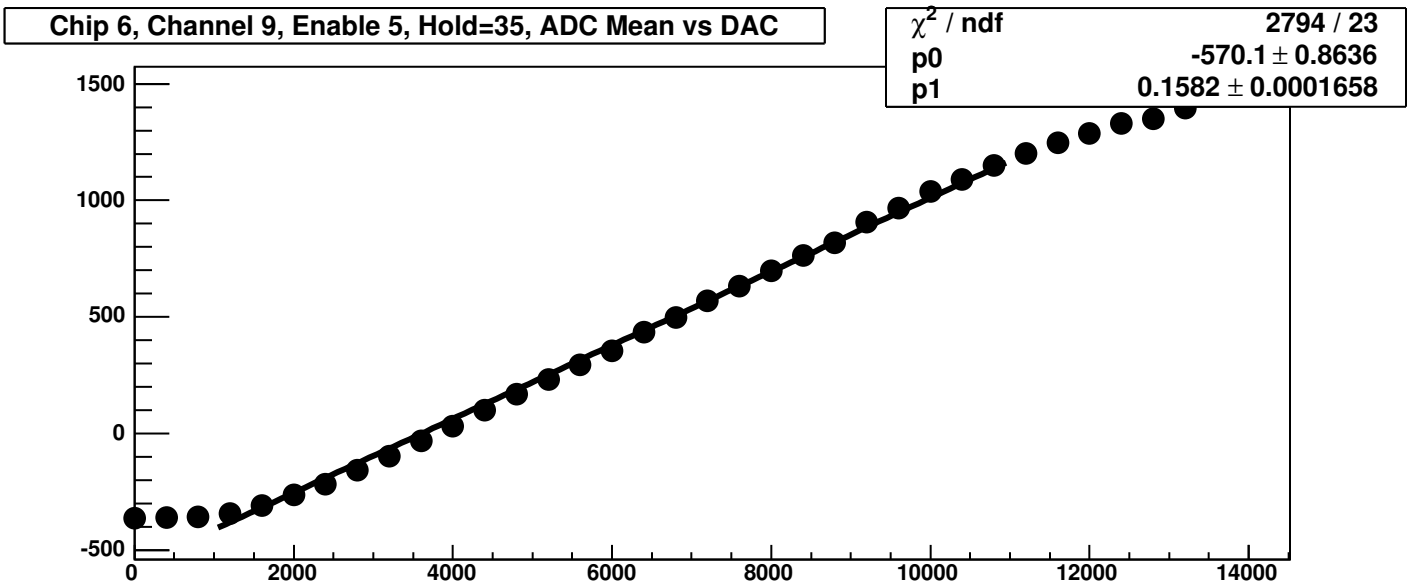

Chip 6, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

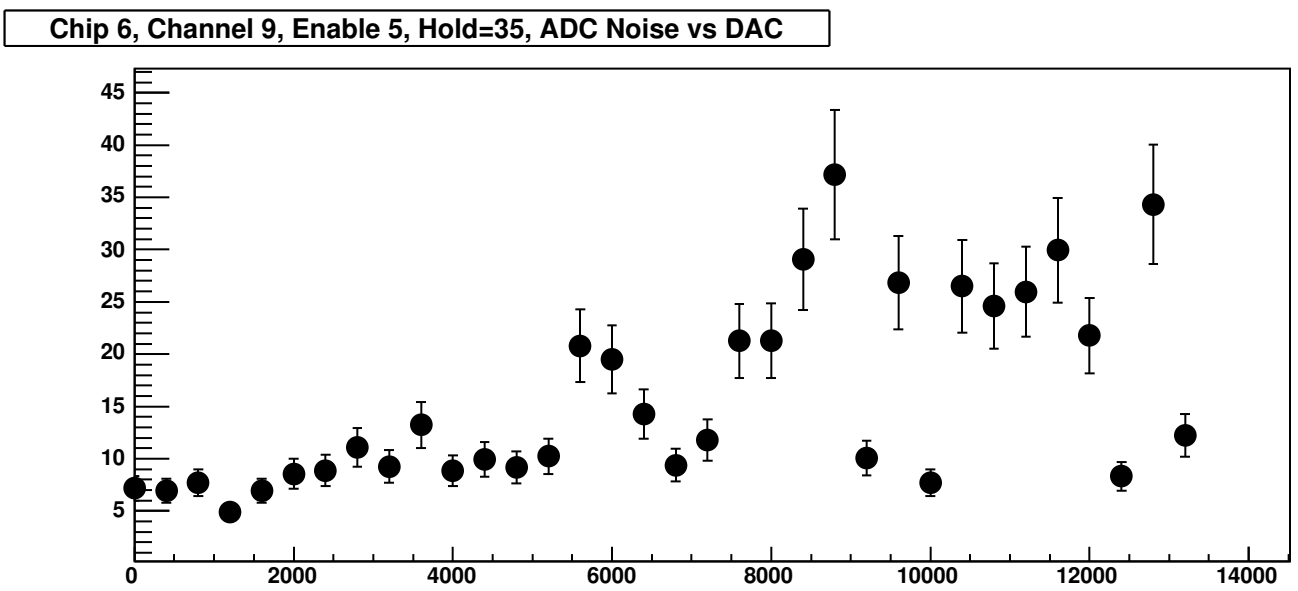

Chip 6, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

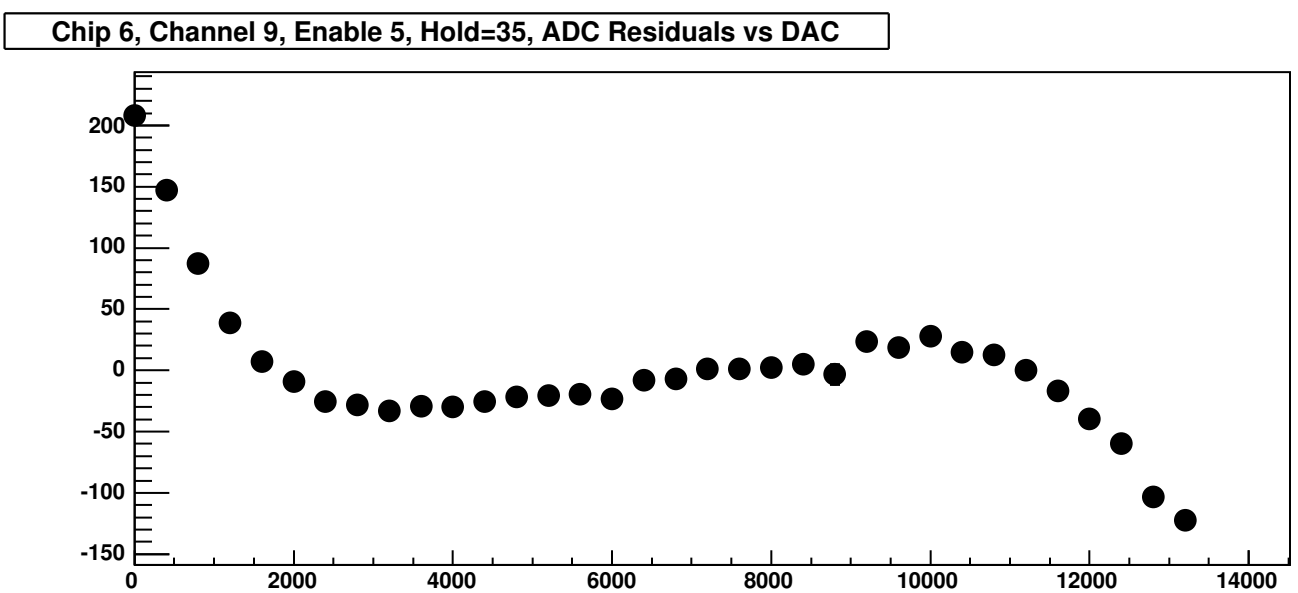

Chip 6, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 18.92 / 23
p0 -393.1 ± 0.7403
p1 0.01529 ± 0.0001162
-180
-200
-220
-240
-260
-280
-300
-320
-340
-360
-380
0
2000
4000
6000
8000
10000
12000
14000

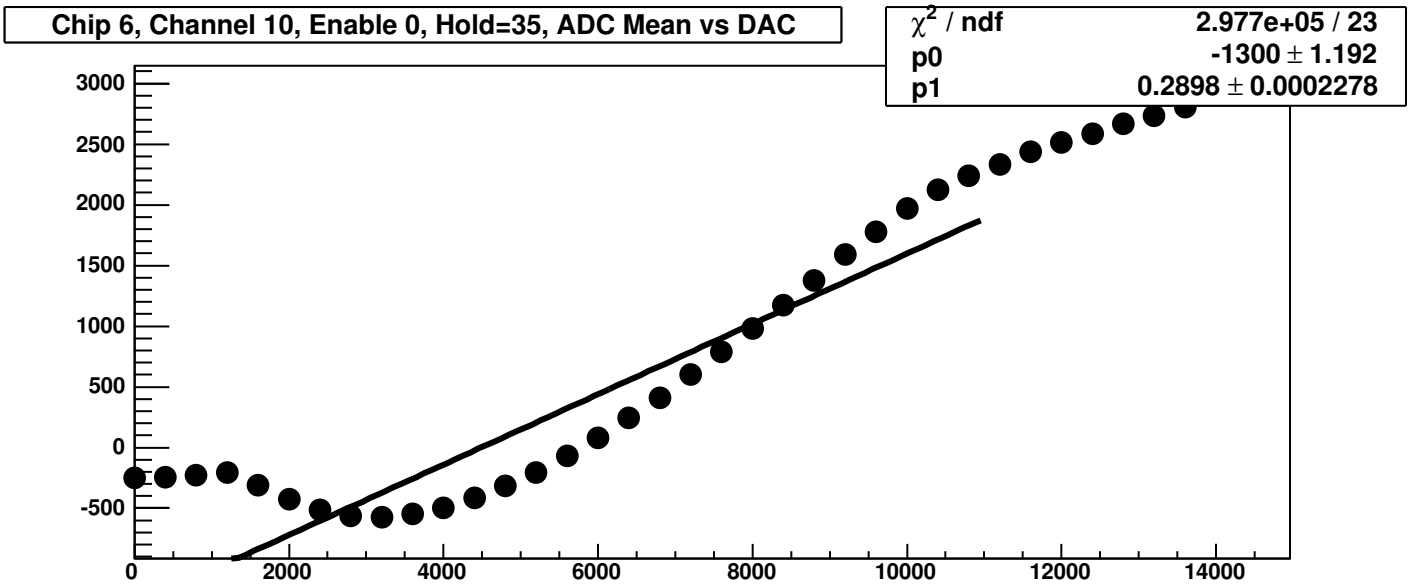

Chip 6, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

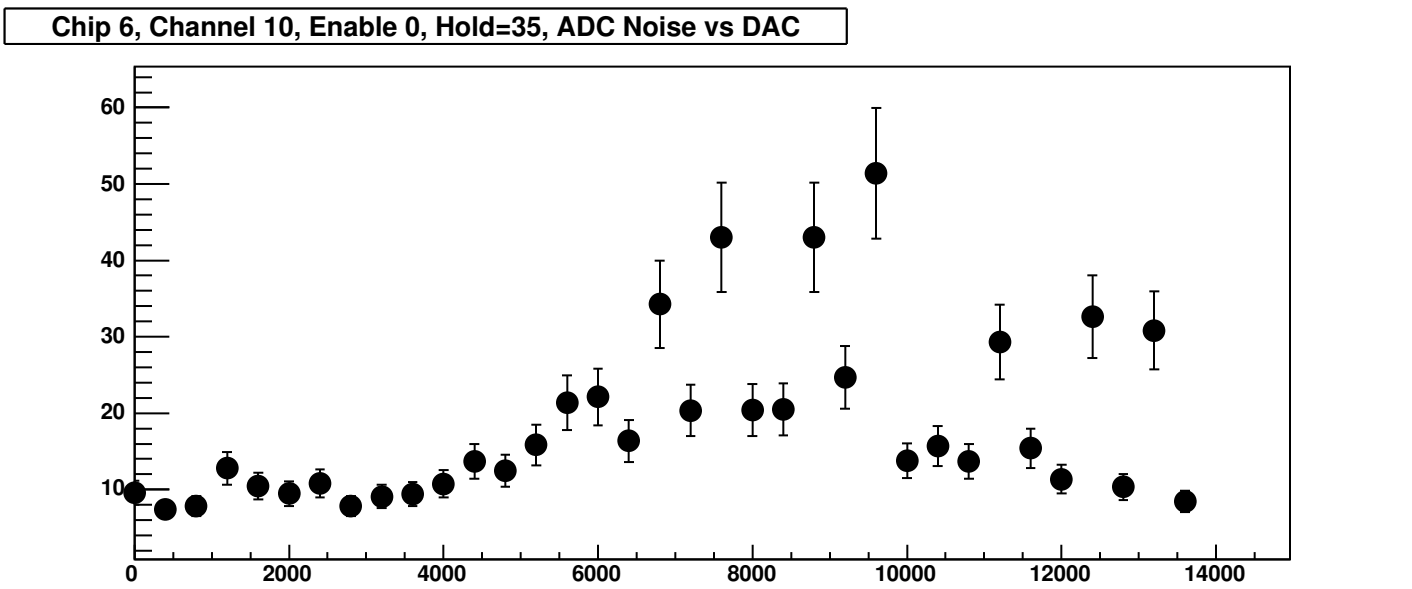

Chip 6, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

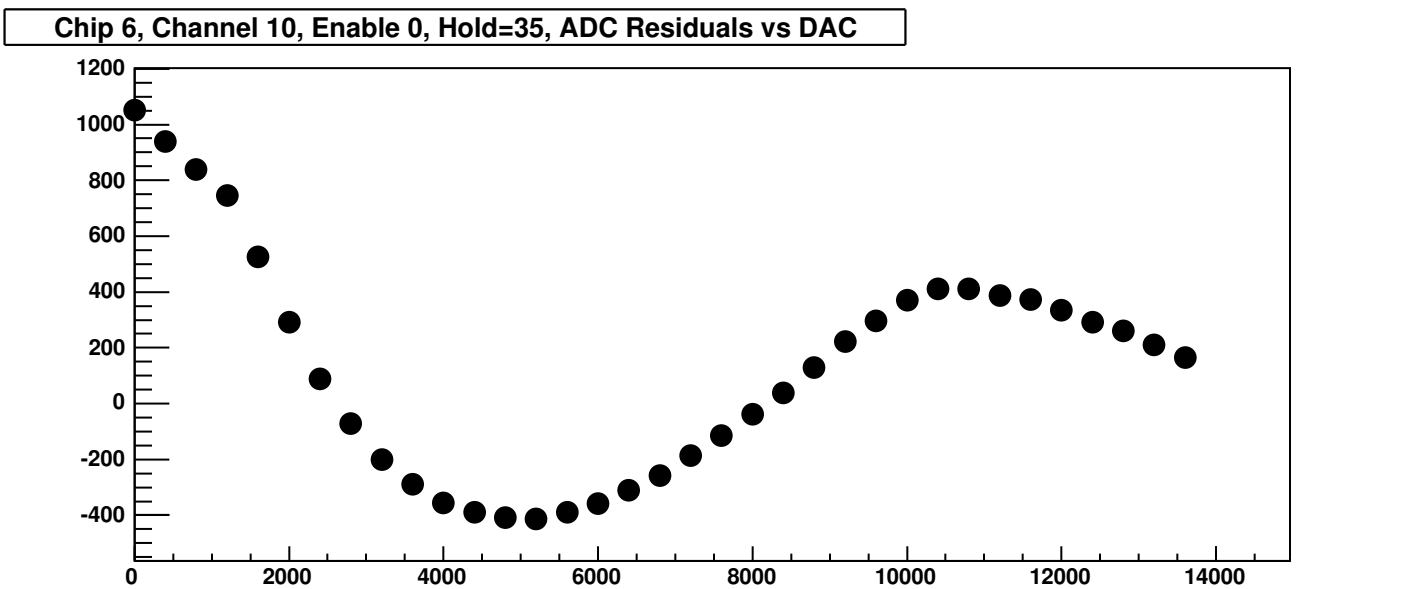

Chip 6, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2794 / 23
p0 -570.1 ± 0.8636
p1 0.1582 ± 0.0001658
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

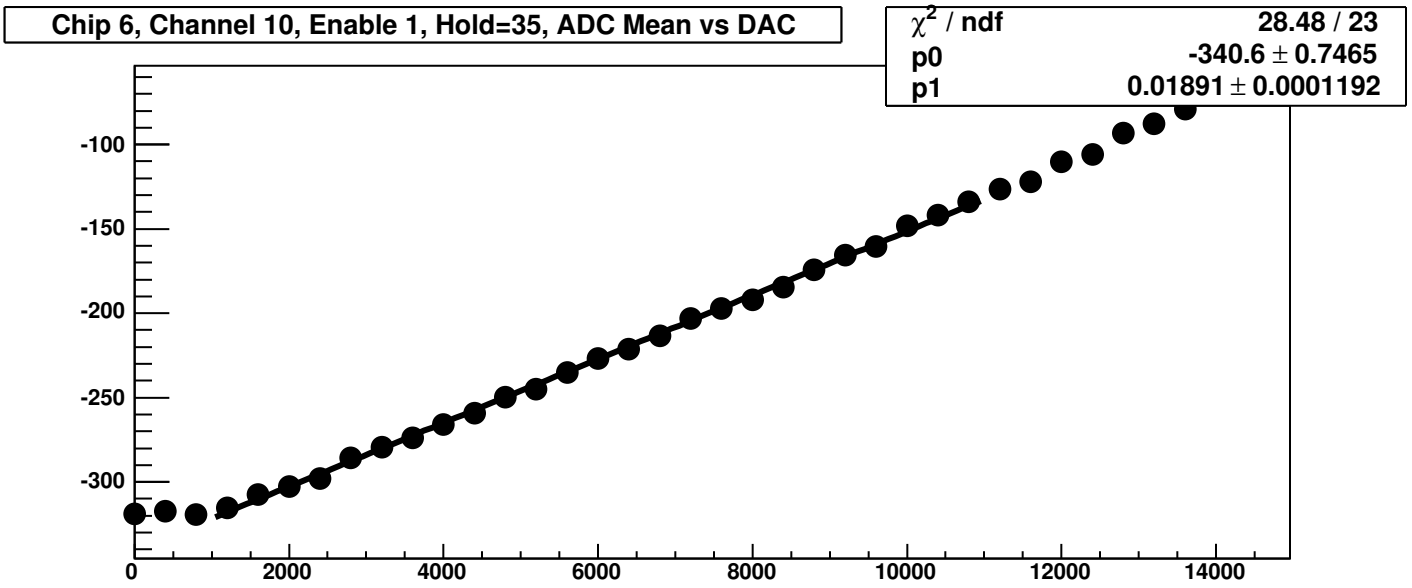

Chip 6, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

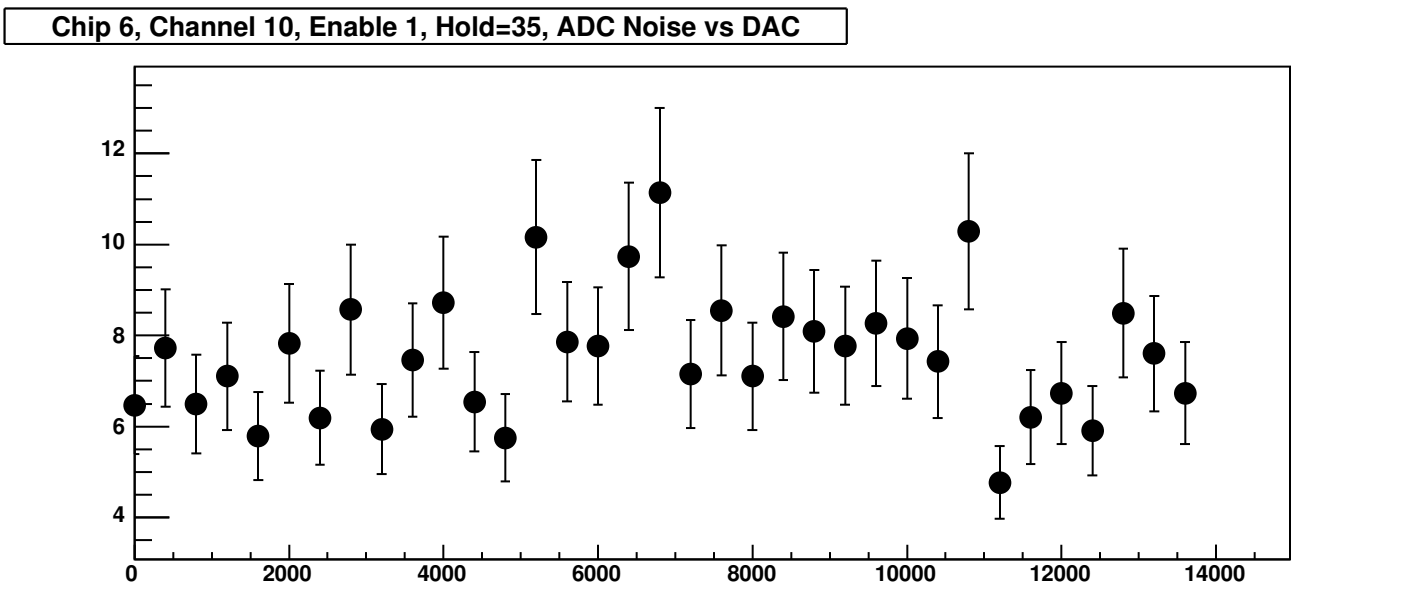

Chip 6, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

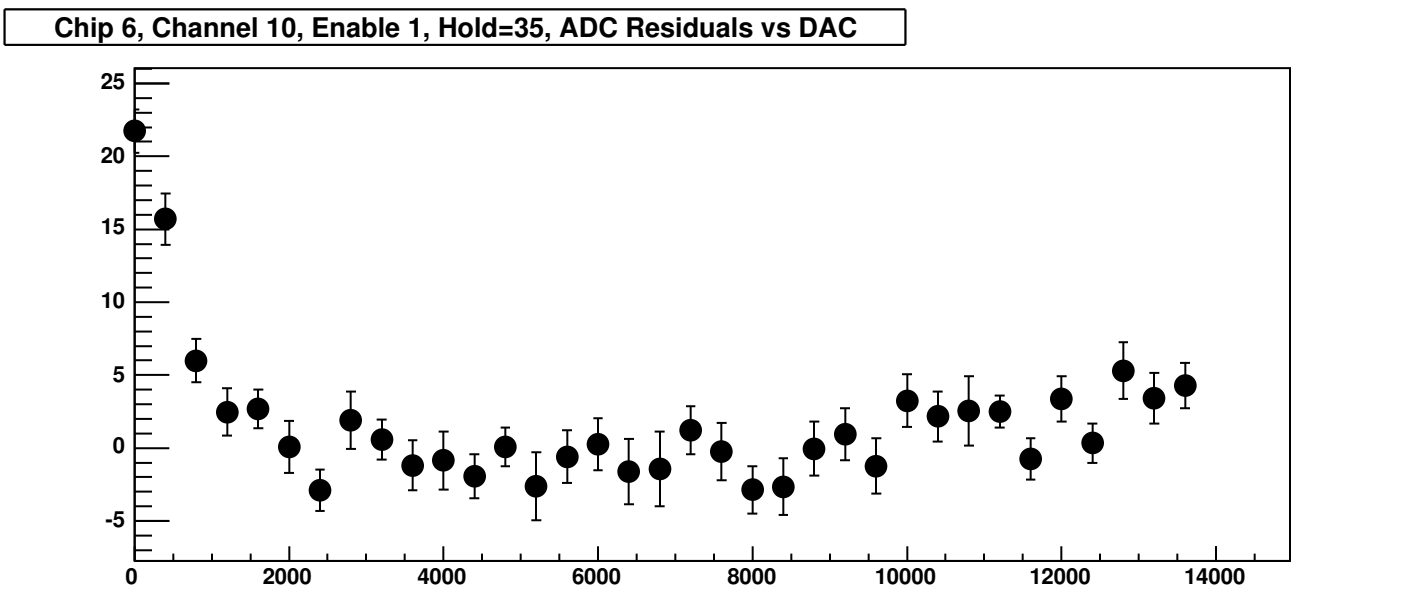

Chip 6, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.977e+05 / 23
p0 -1300 ± 1.192
p1 0.2898 ± 0.0002278
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

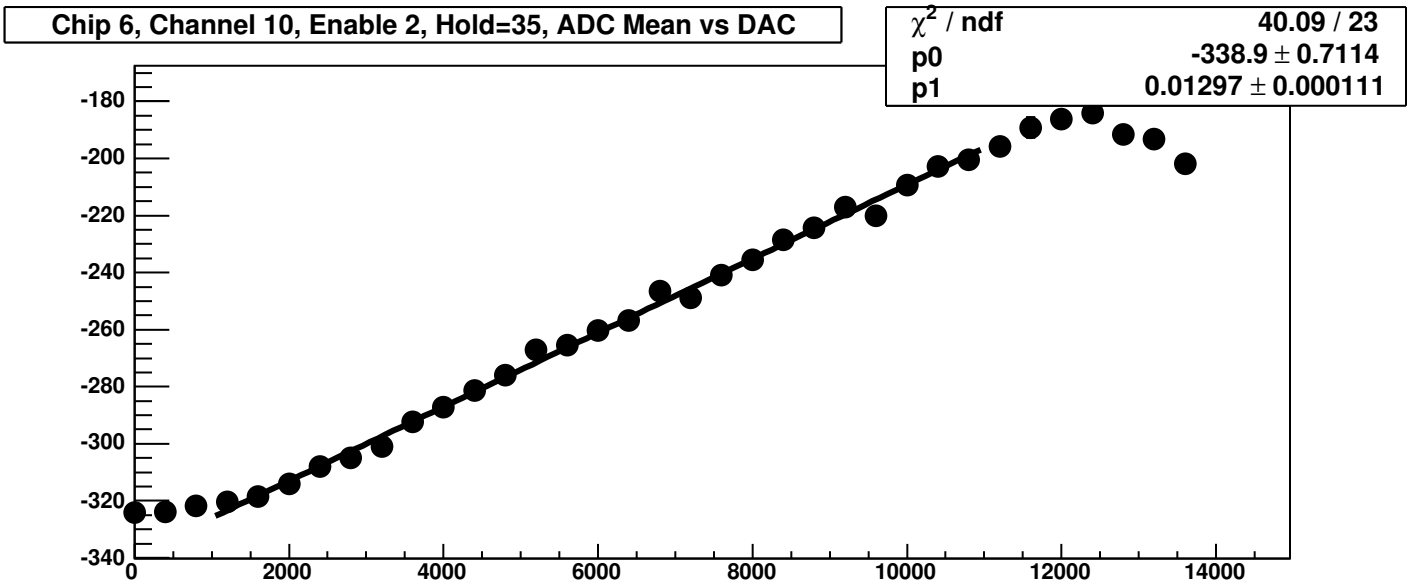

Chip 6, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

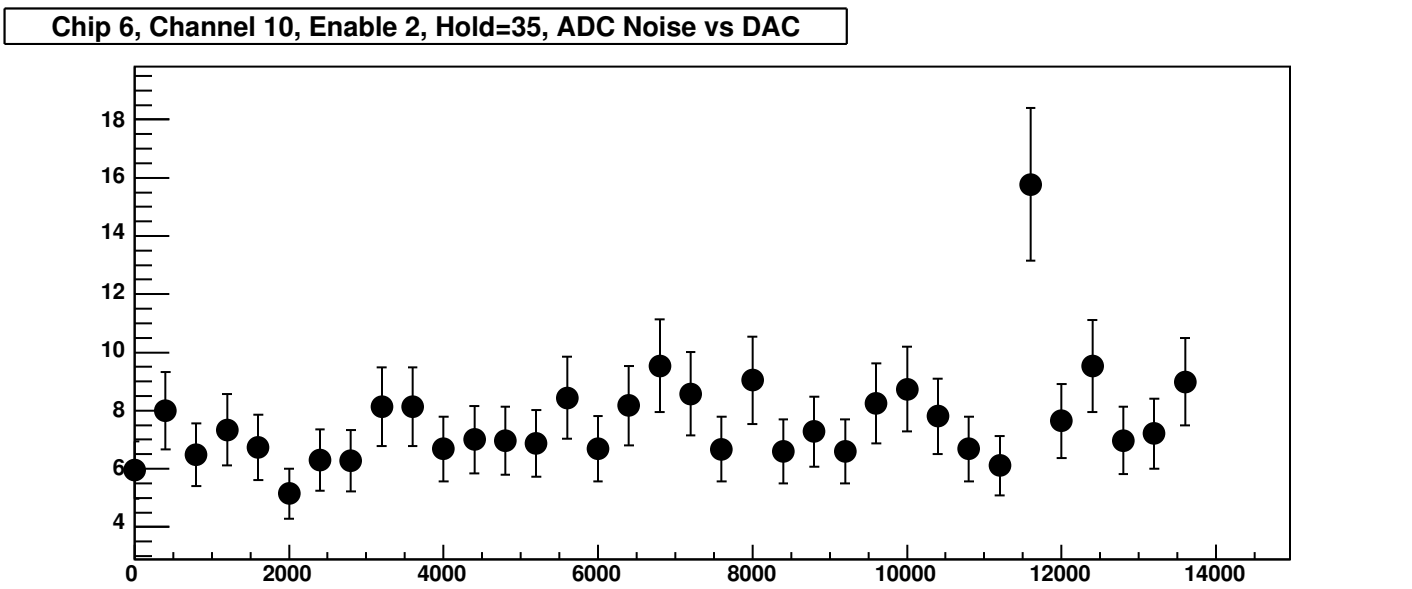

Chip 6, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

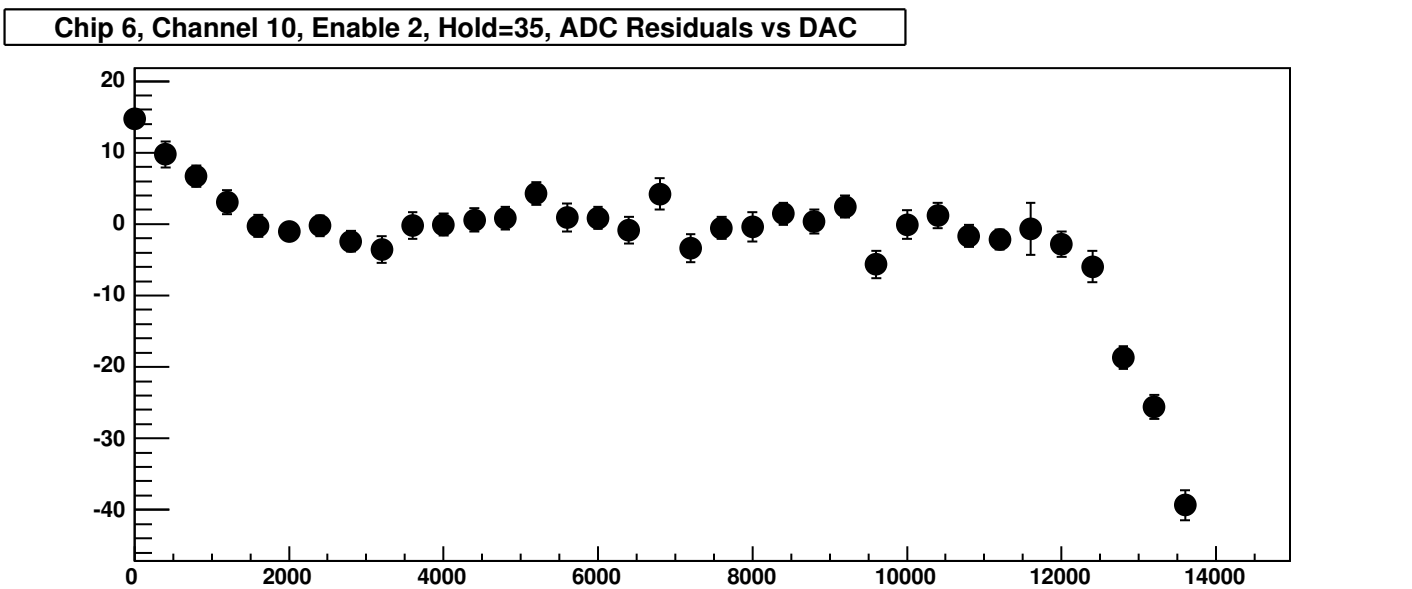

Chip 6, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 28.48 / 23
p0 -340.6 ± 0.7465
p1 0.01891 ± 0.0001192
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

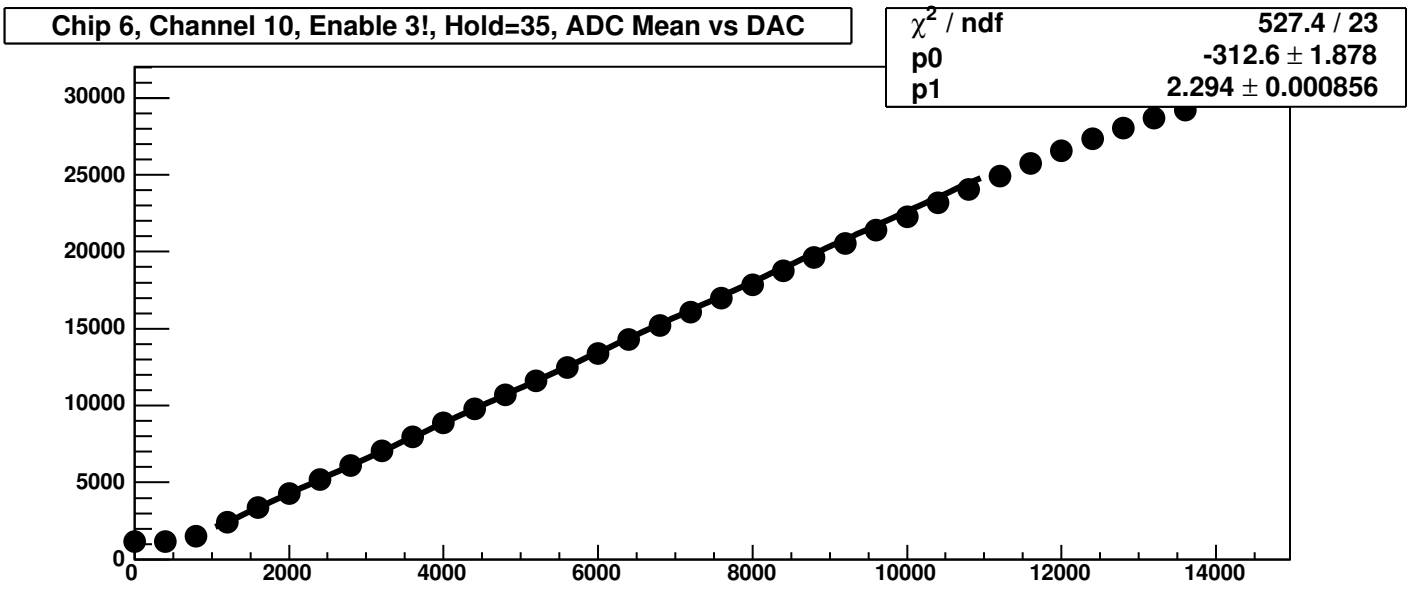

Chip 6, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

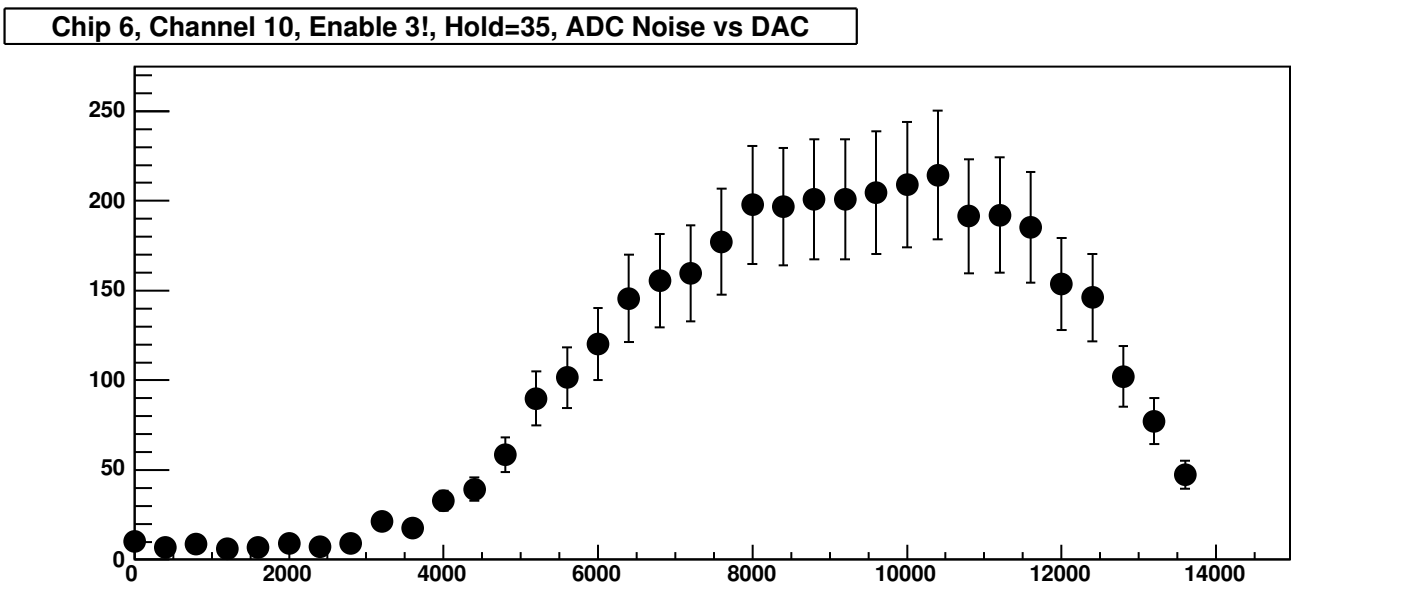

Chip 6, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

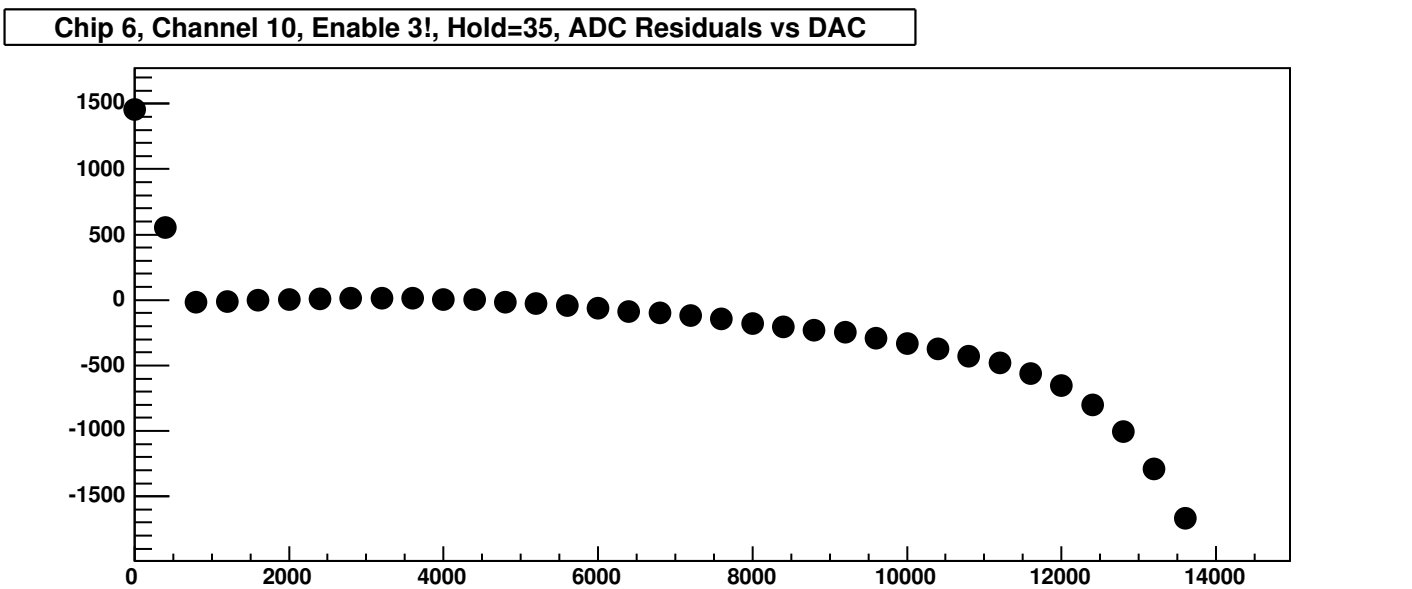

Chip 6, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 40.09 / 23
p0 -338.9 ± 0.7114
p1 0.01297 ± 0.000111
-180
-200
-220
-240
-260
-280
-300
-320
-340
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

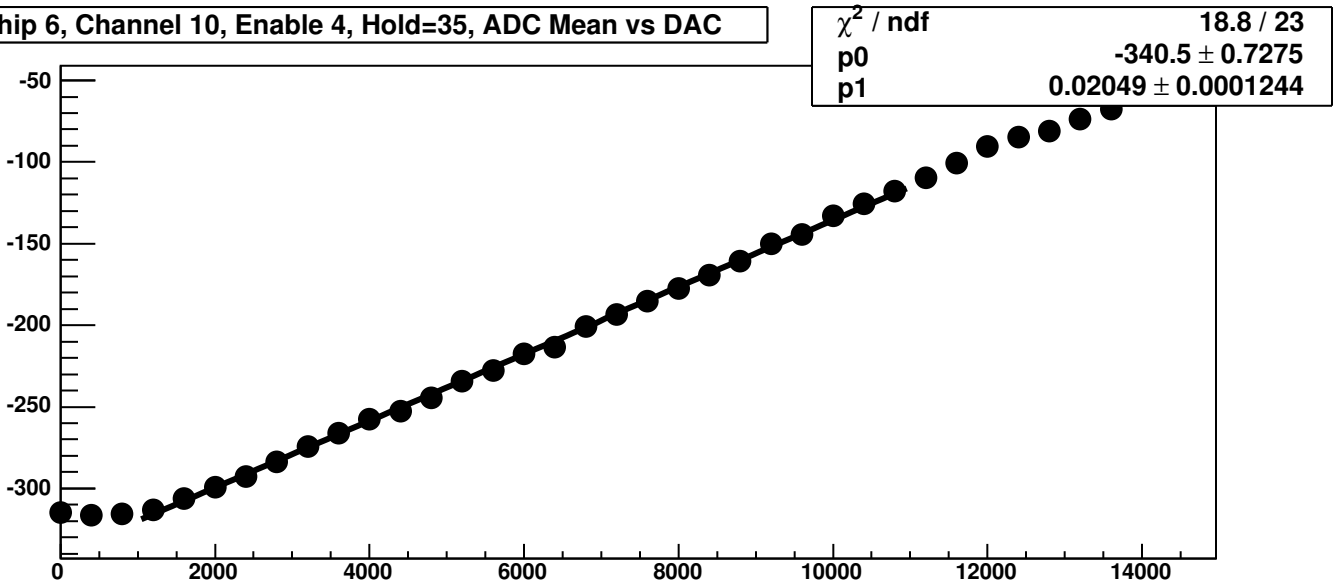

Chip 6, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 527.4 / 23
p0 -312.6 ± 1.878
p1 2.294 ± 0.000856
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

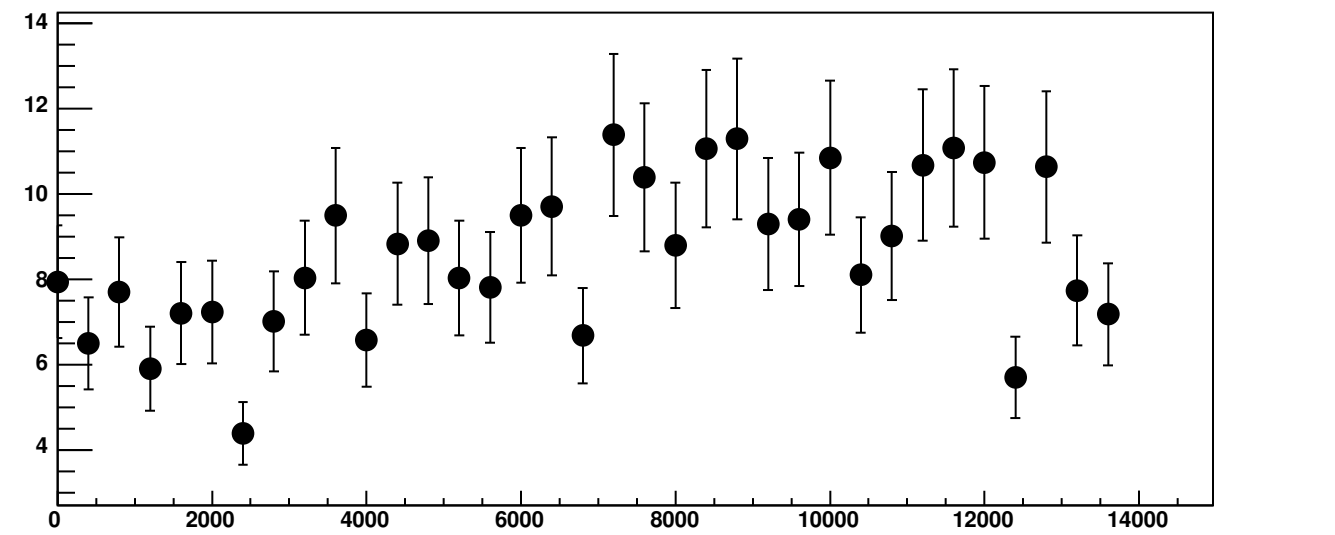

Chip 6, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

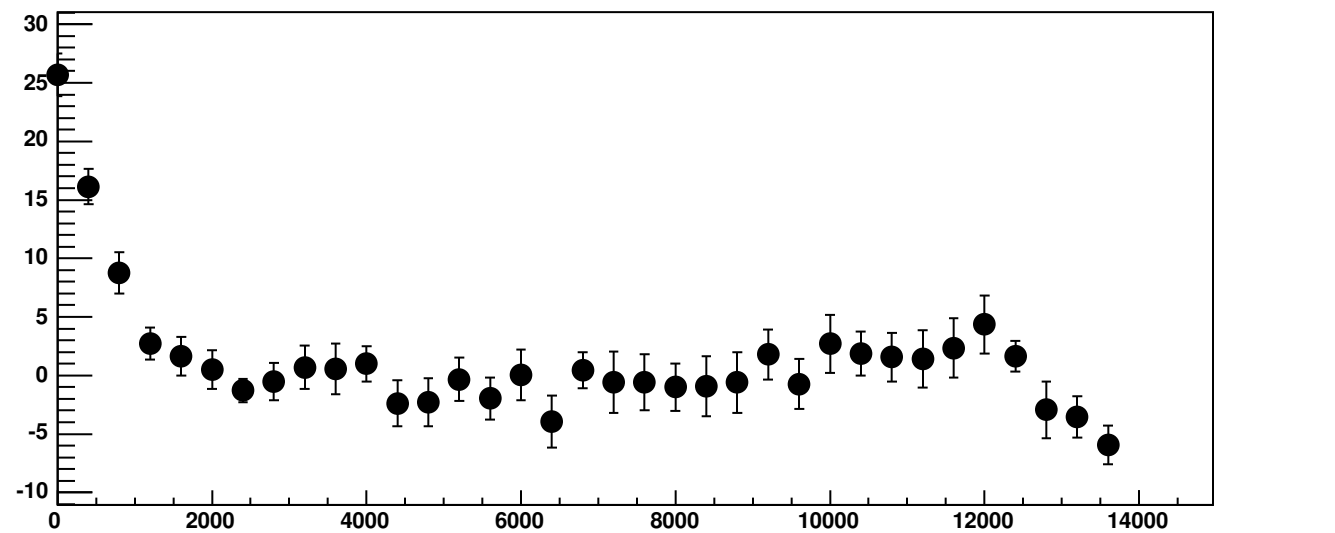
Chip 6, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

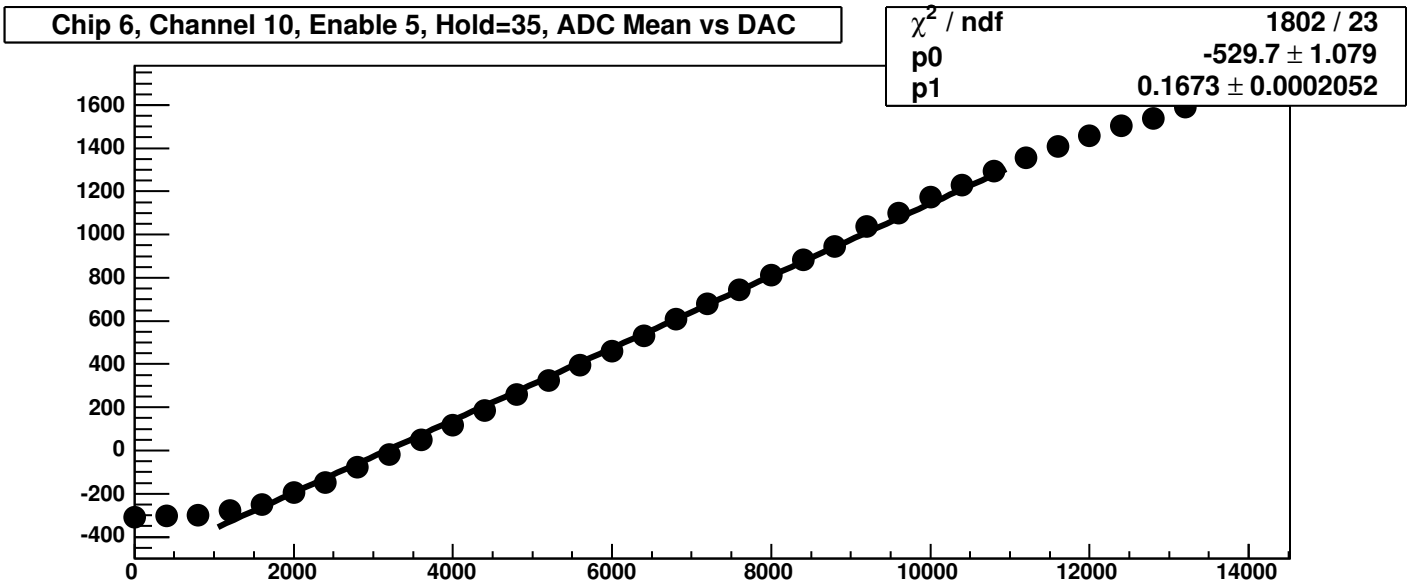


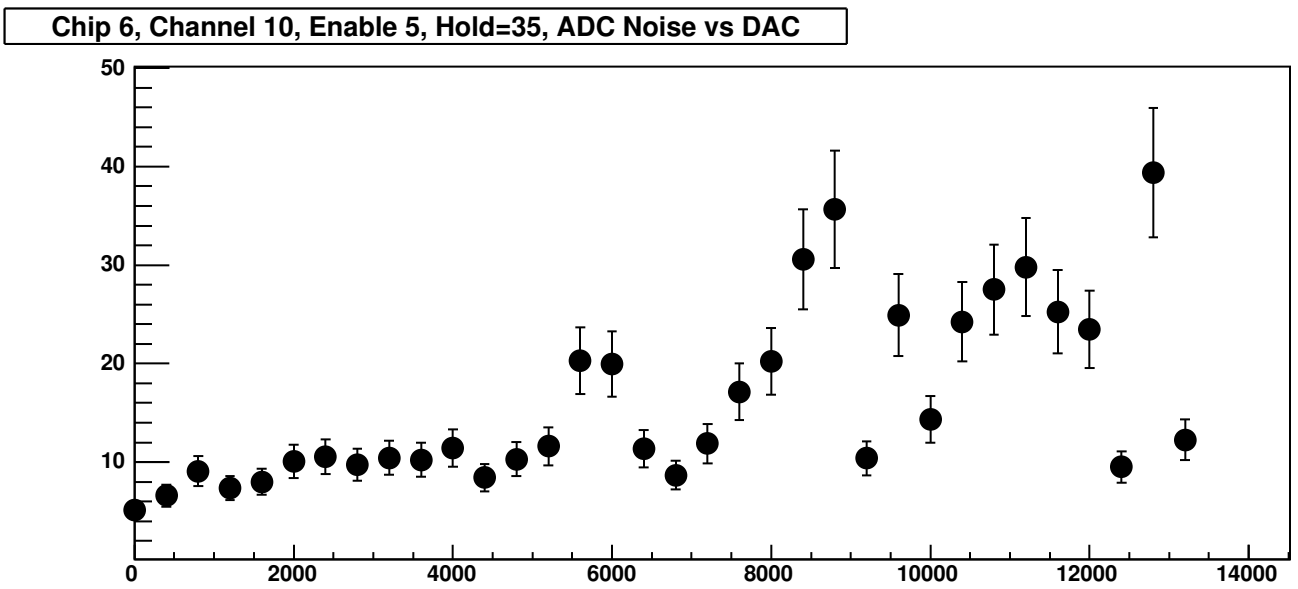
Chip 6, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

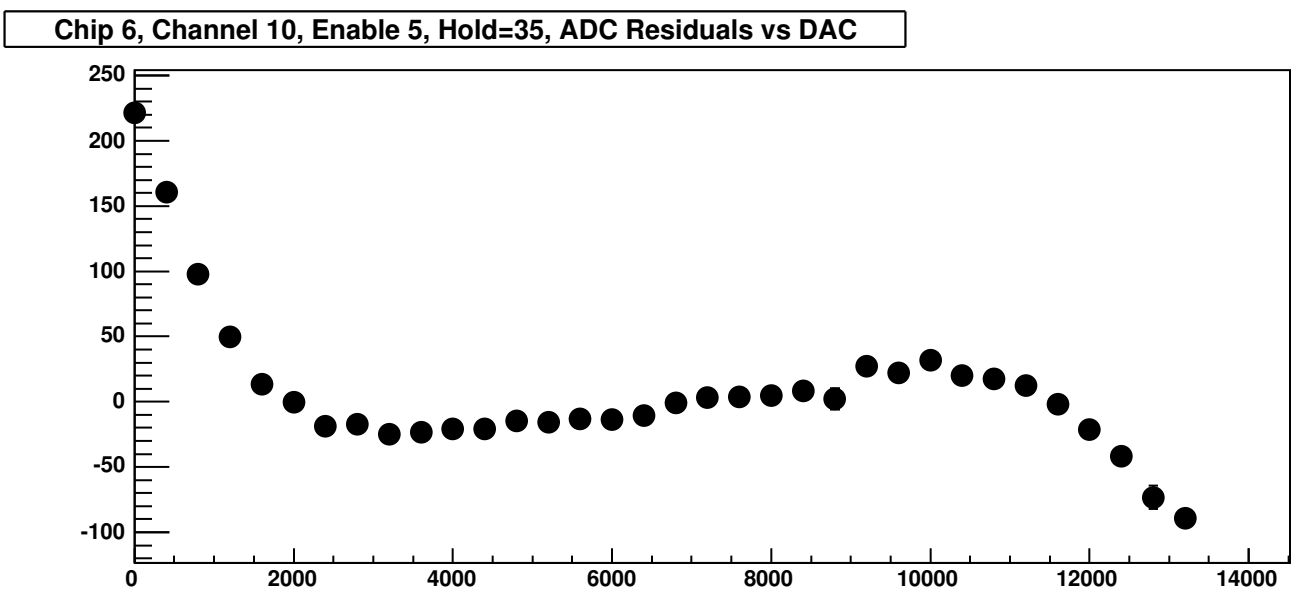


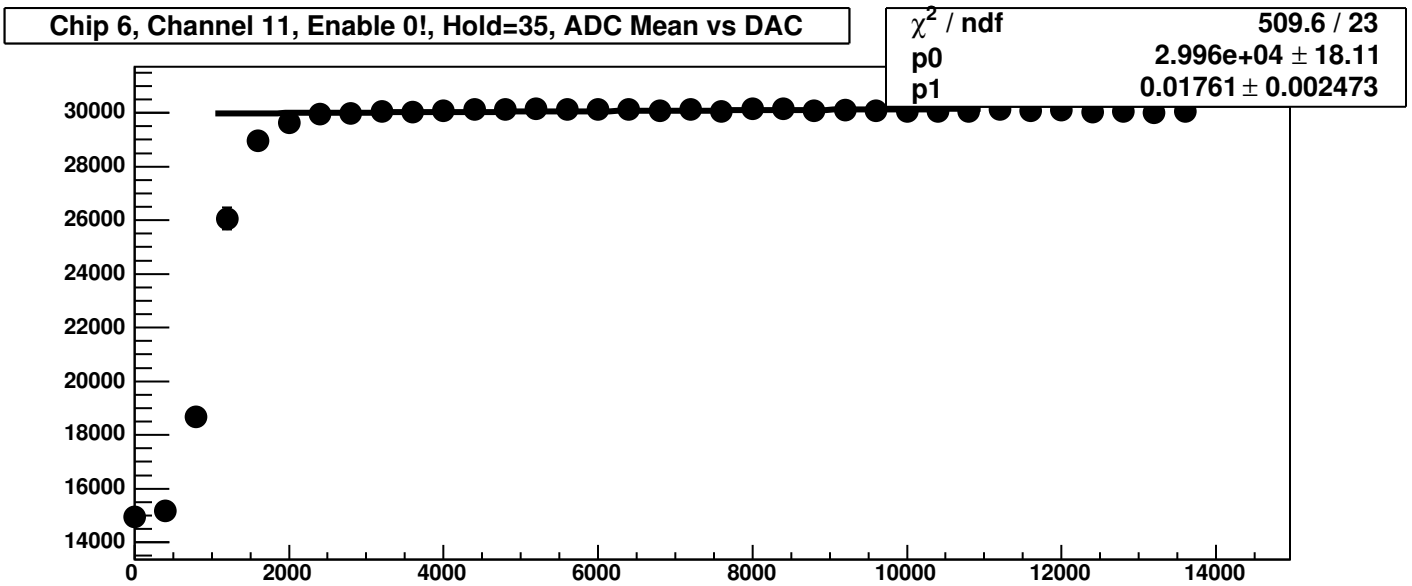
Chip 6, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

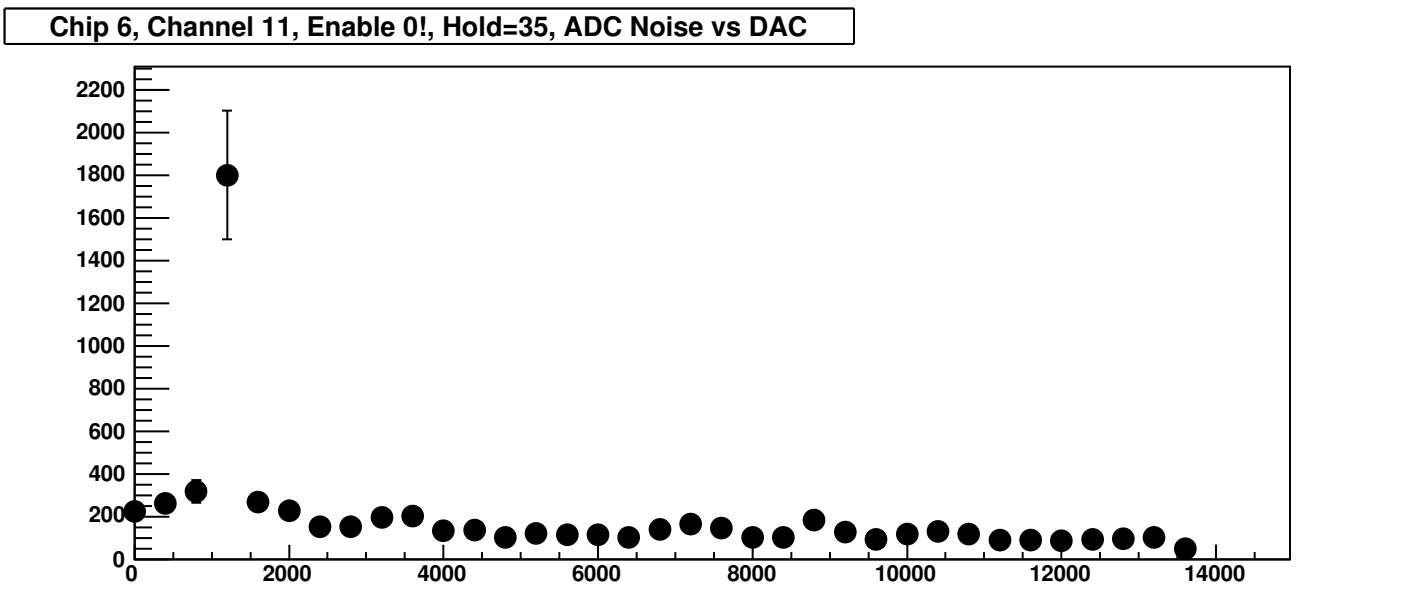


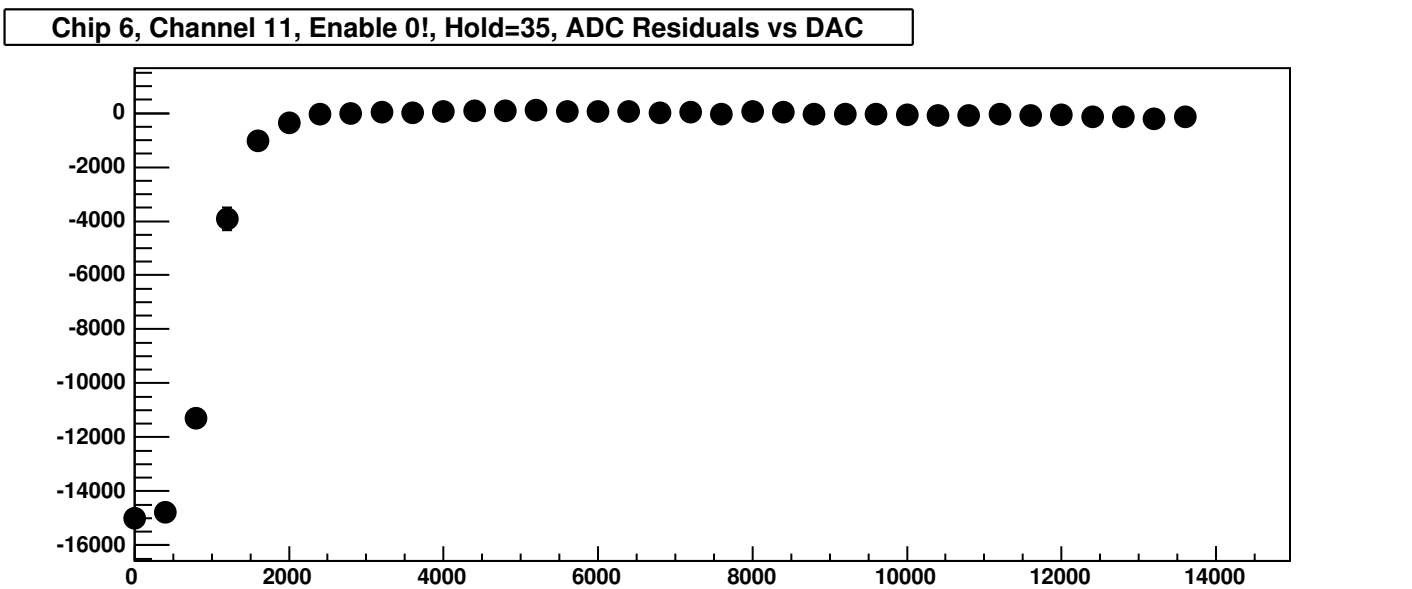

Chip 6, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1802 / 23
p0 -529.7 ± 1.079
p1 0.1673 ± 0.0002052
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

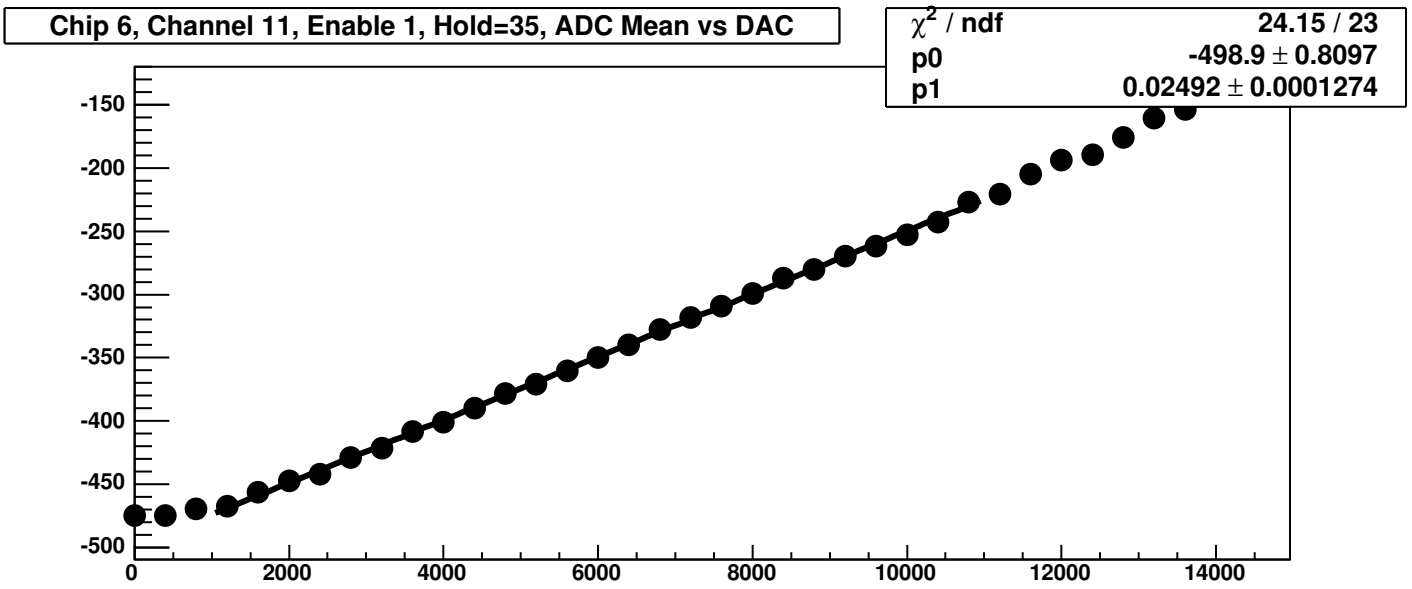

Chip 6, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

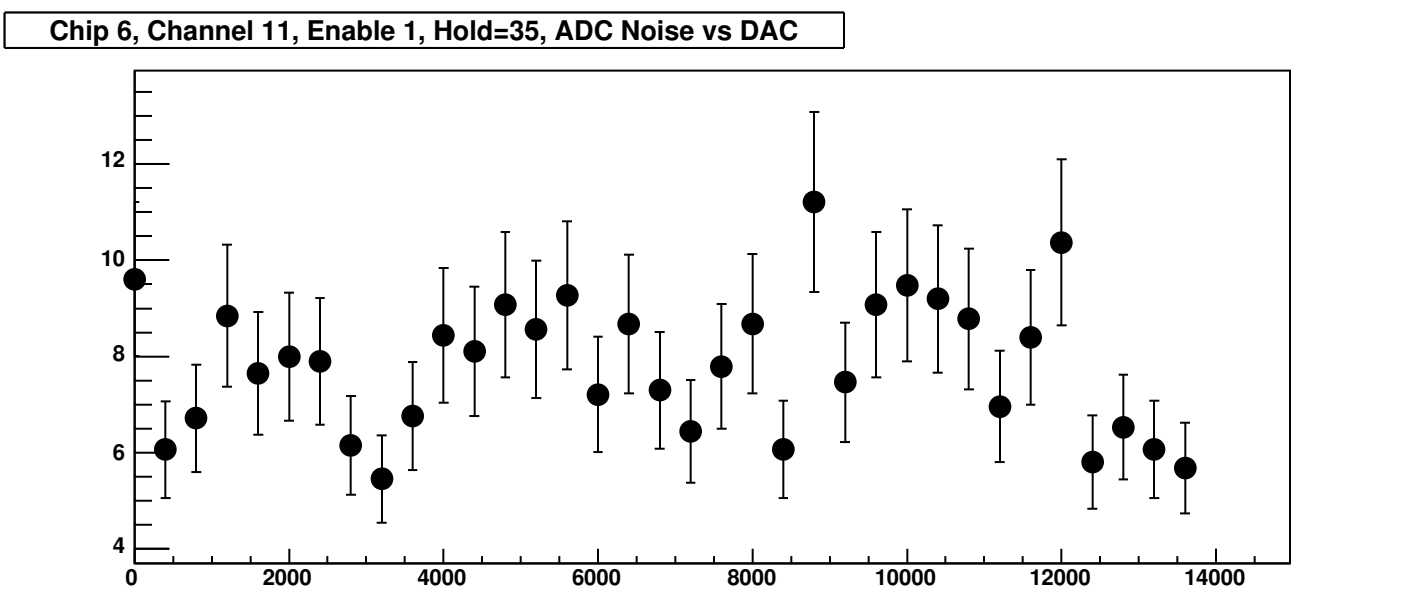

Chip 6, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

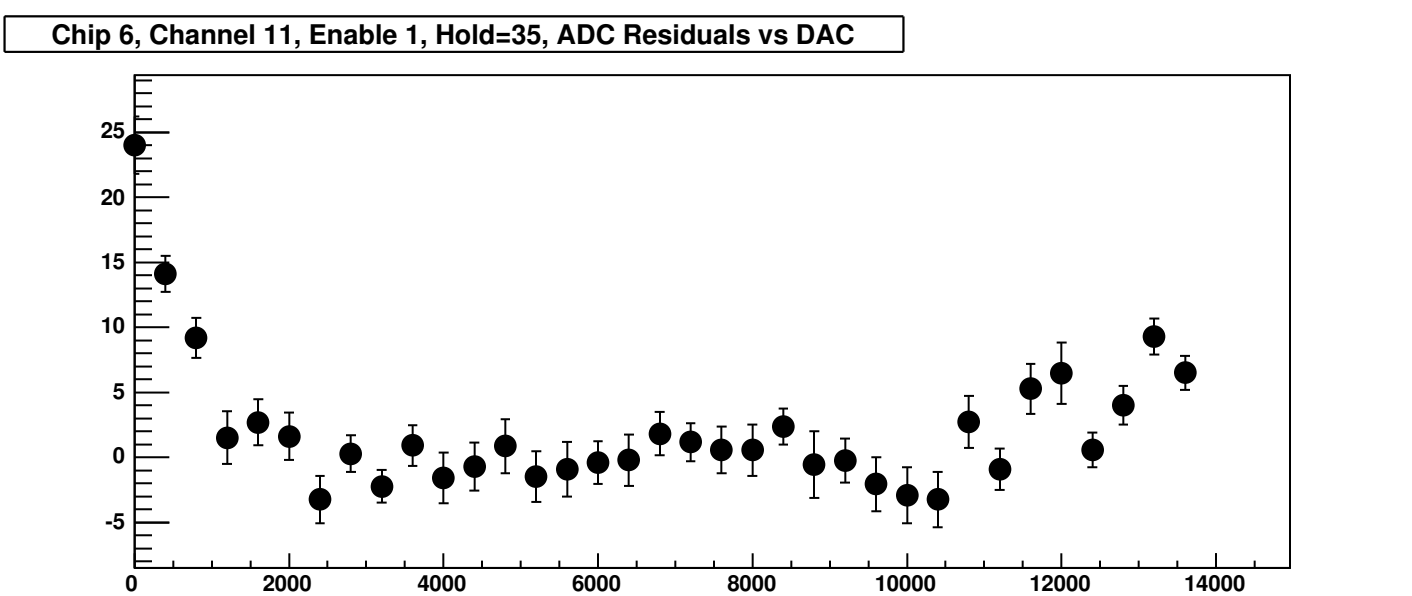

Chip 6, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 509.6 / 23
p0 2.996e+04 ± 18.11
p1 0.01761 ± 0.002473
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

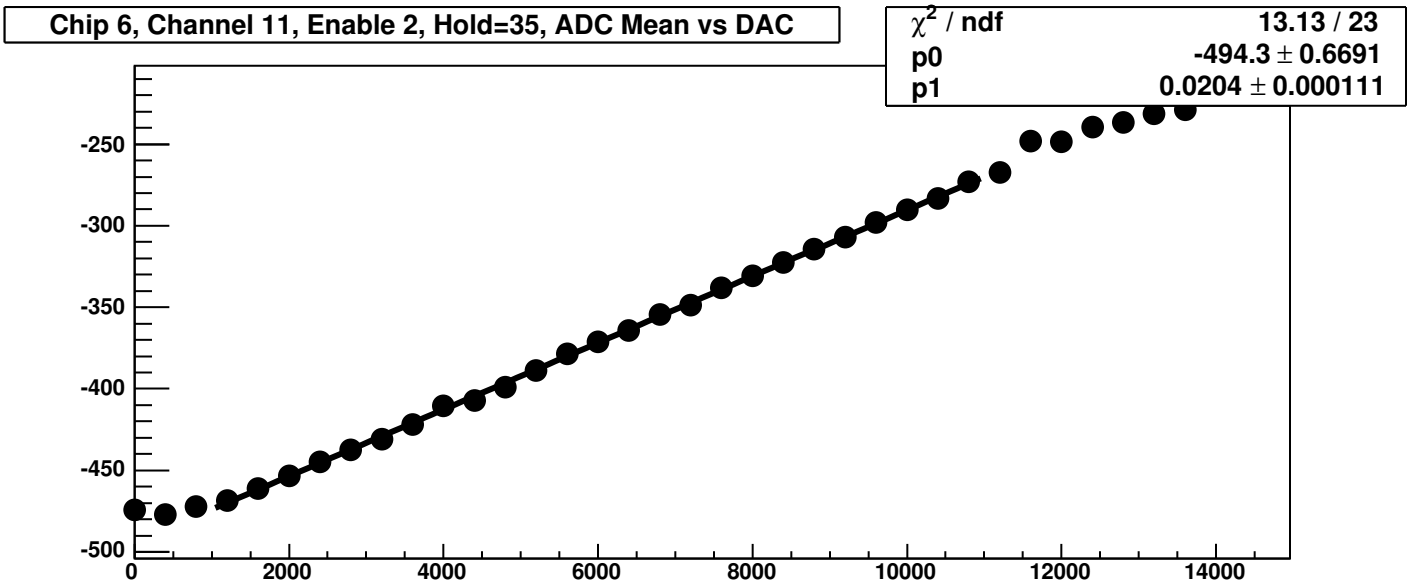

Chip 6, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

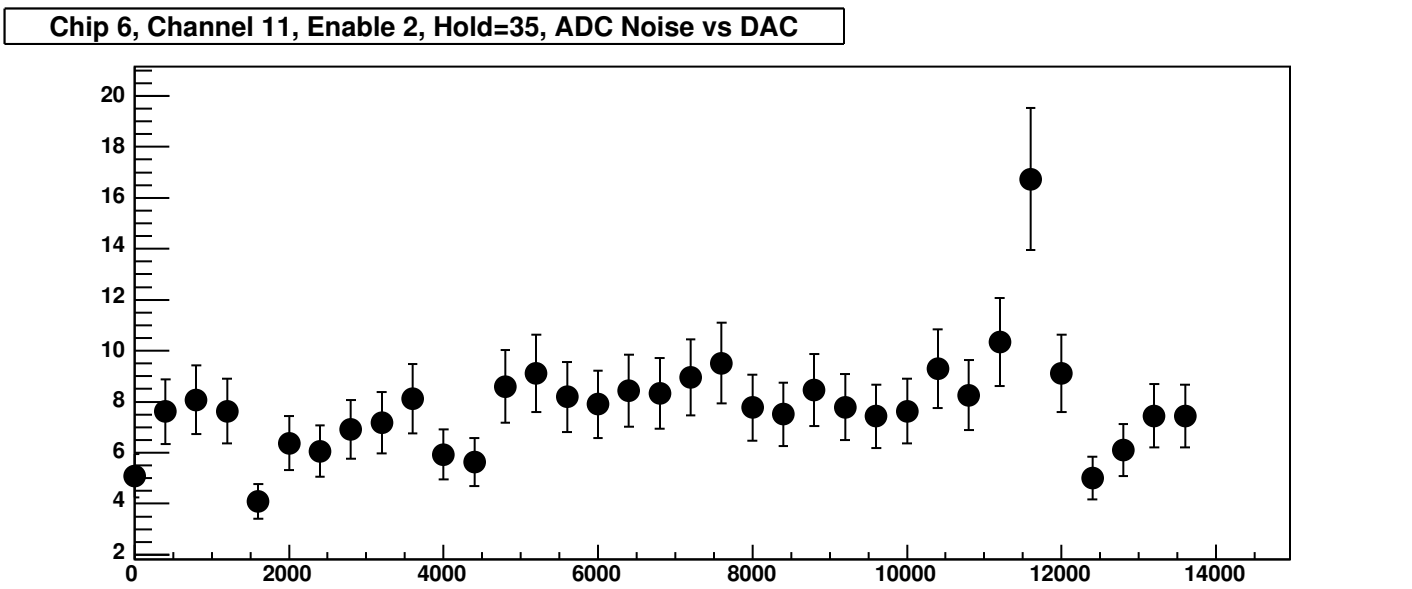

Chip 6, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

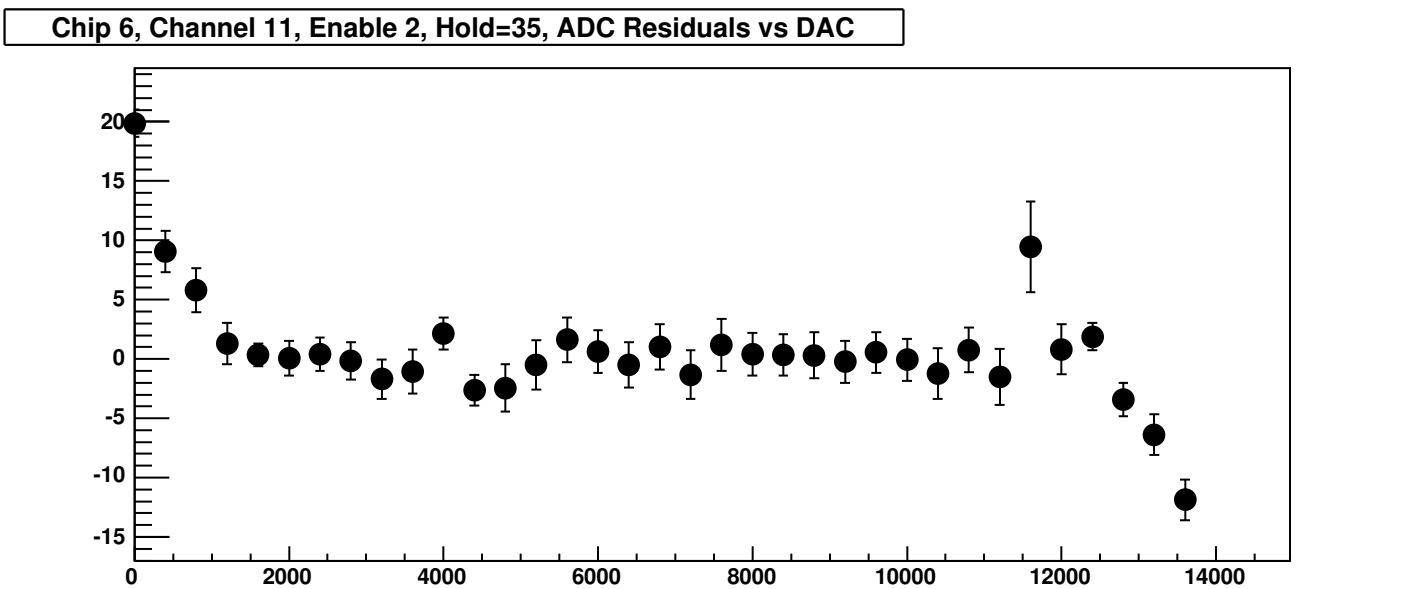

Chip 6, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 24.15 / 23
p0 -498.9 ± 0.8097
p1 0.02492 ± 0.0001274
-150
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

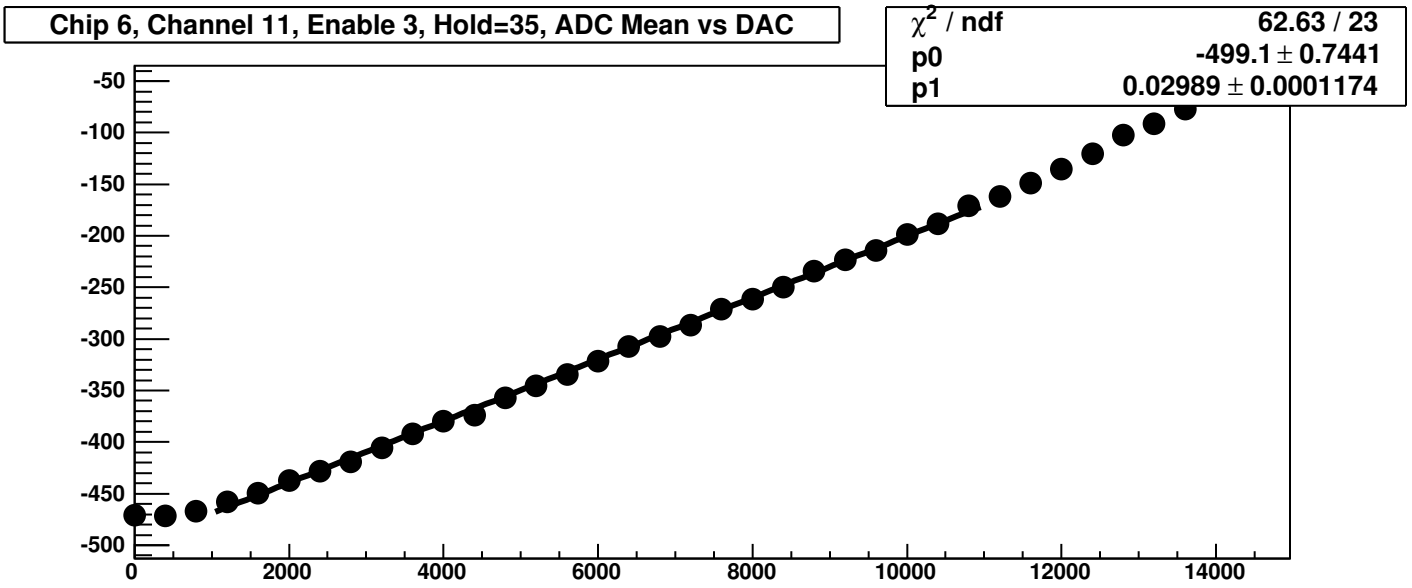

Chip 6, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

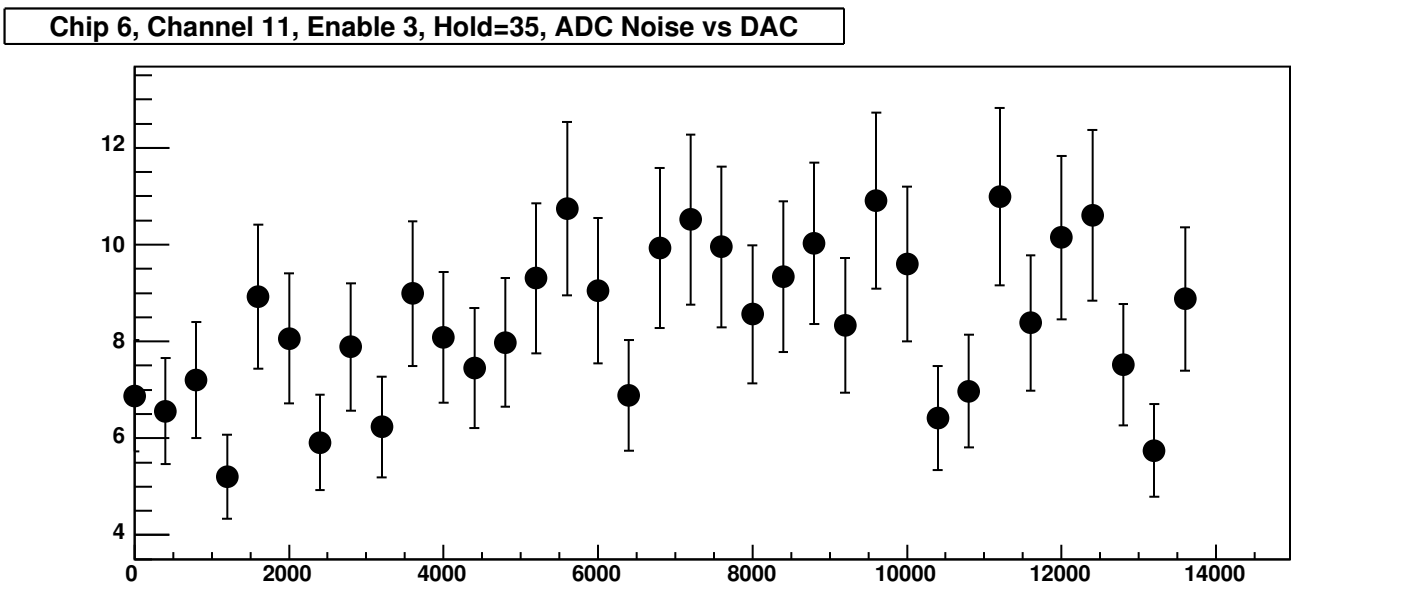

Chip 6, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

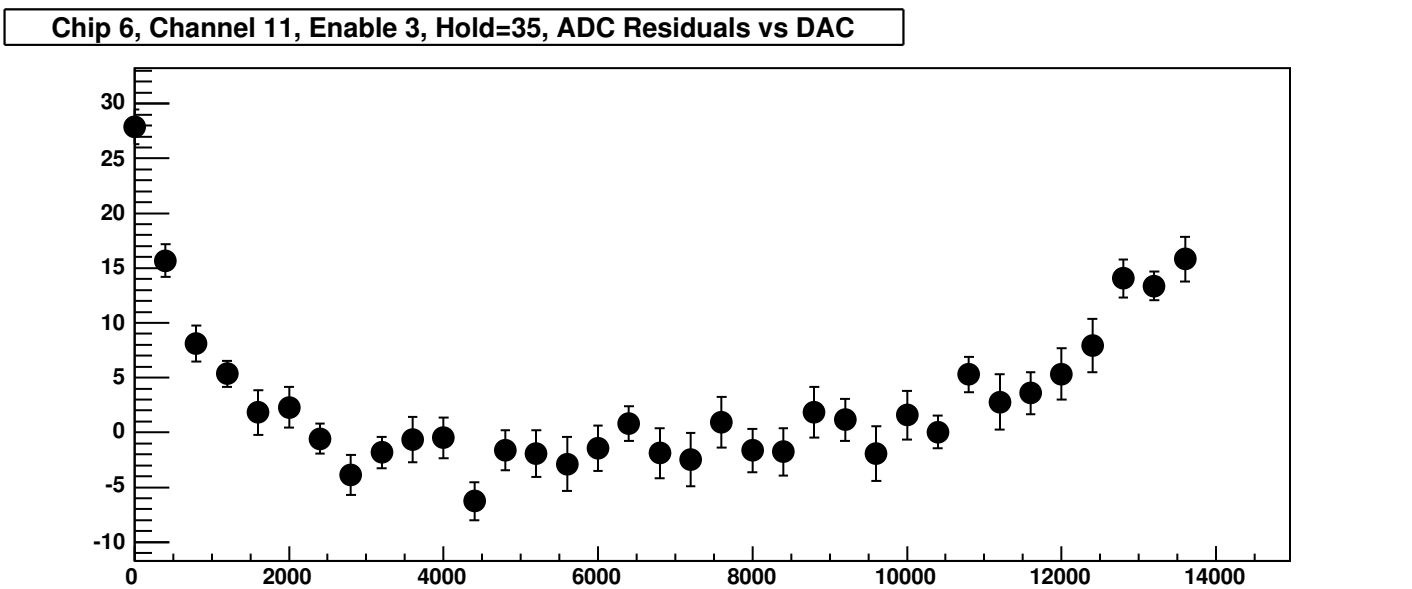

Chip 6, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 13.13 / 23
p0 -494.3 ± 0.6691
p1 0.0204 ± 0.000111
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

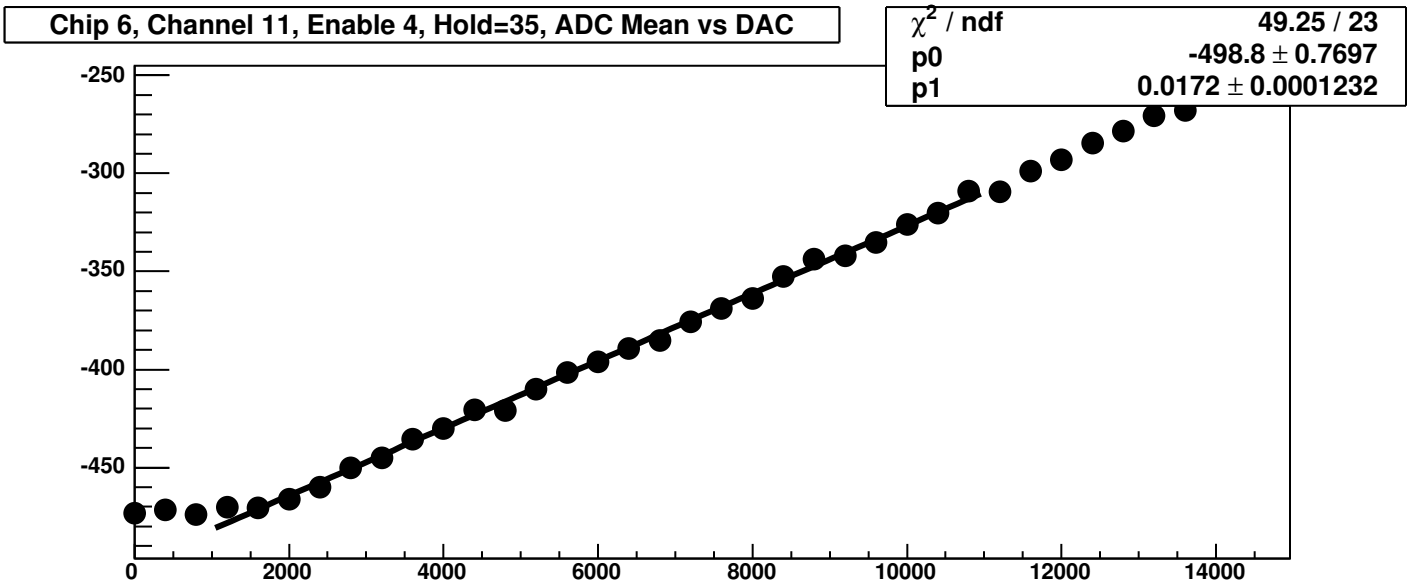

Chip 6, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
20
18
16
14
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

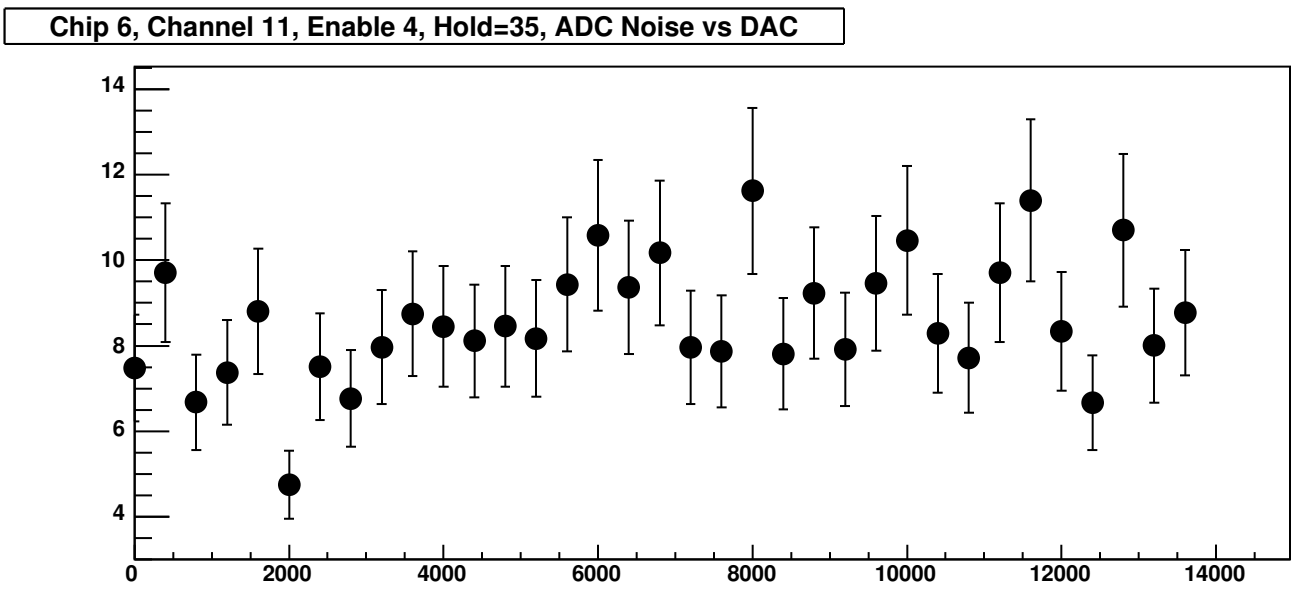

Chip 6, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

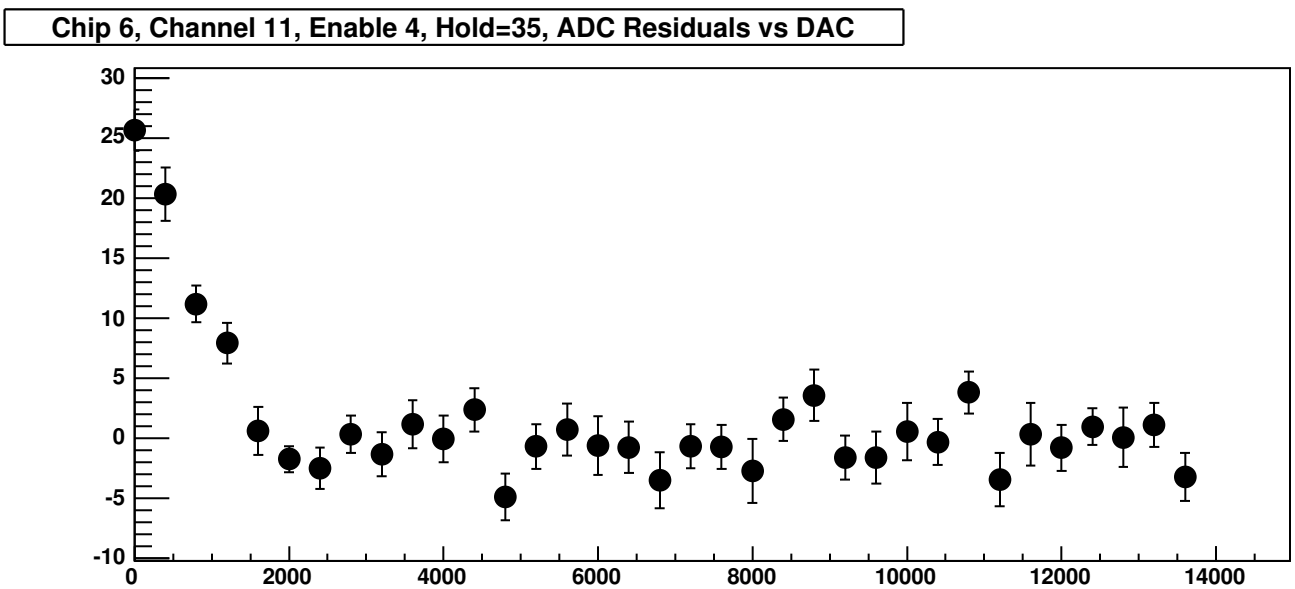

Chip 6, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 62.63 / 23
p0 -499.1 ± 0.7441
p1 0.02989 ± 0.0001174
-50
-100
-150
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

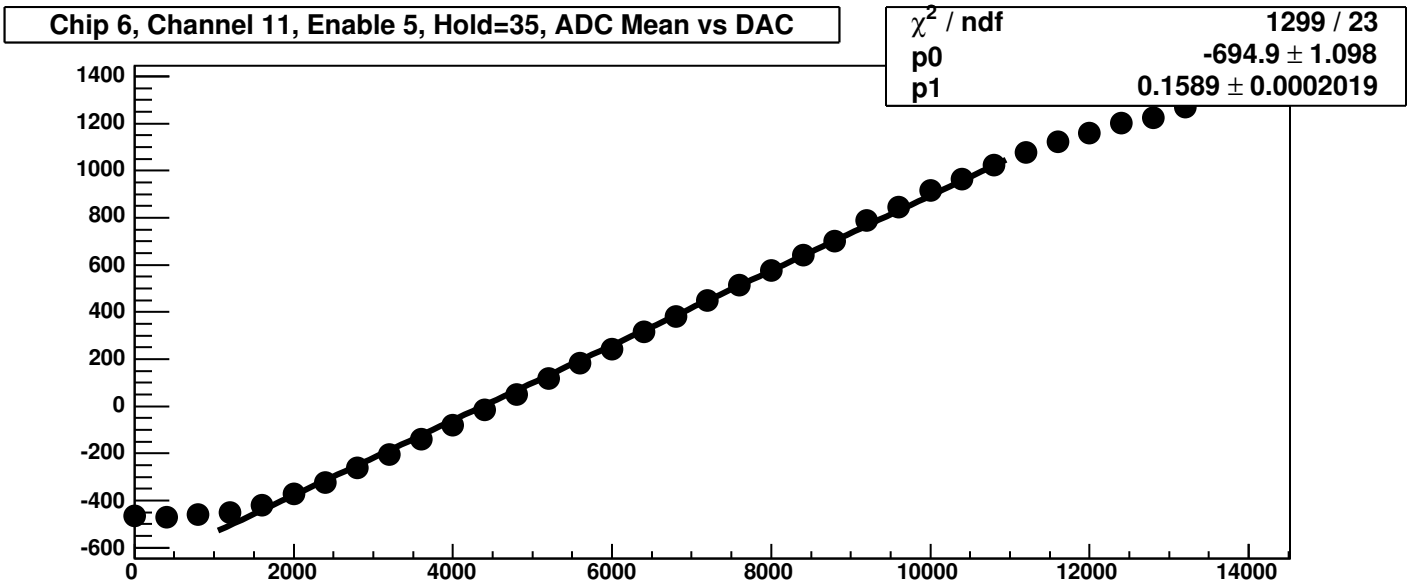

Chip 6, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

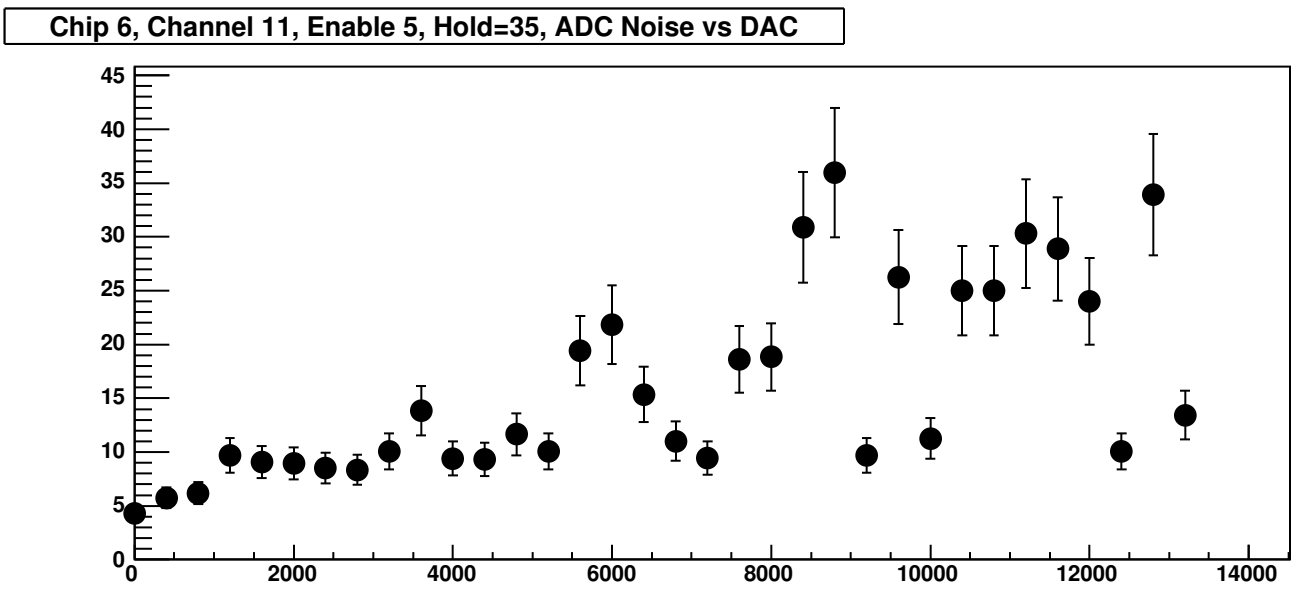

Chip 6, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

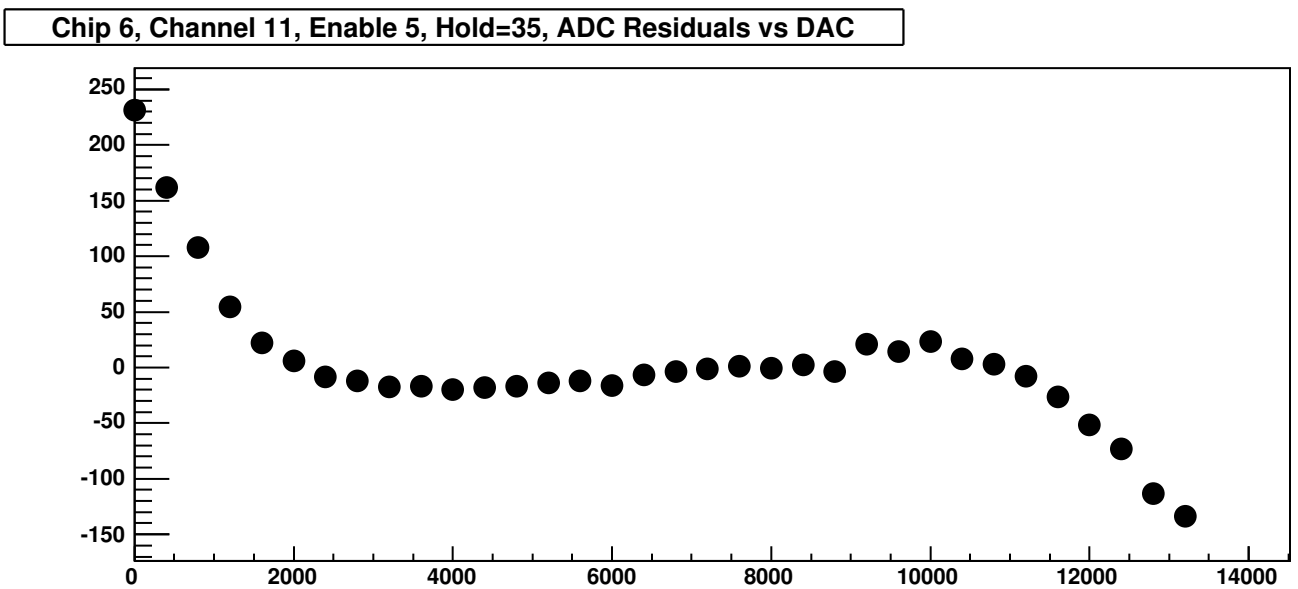

Chip 6, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 49.25 / 23
p0 -498.8 ± 0.7697
p1 0.0172 ± 0.0001232
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

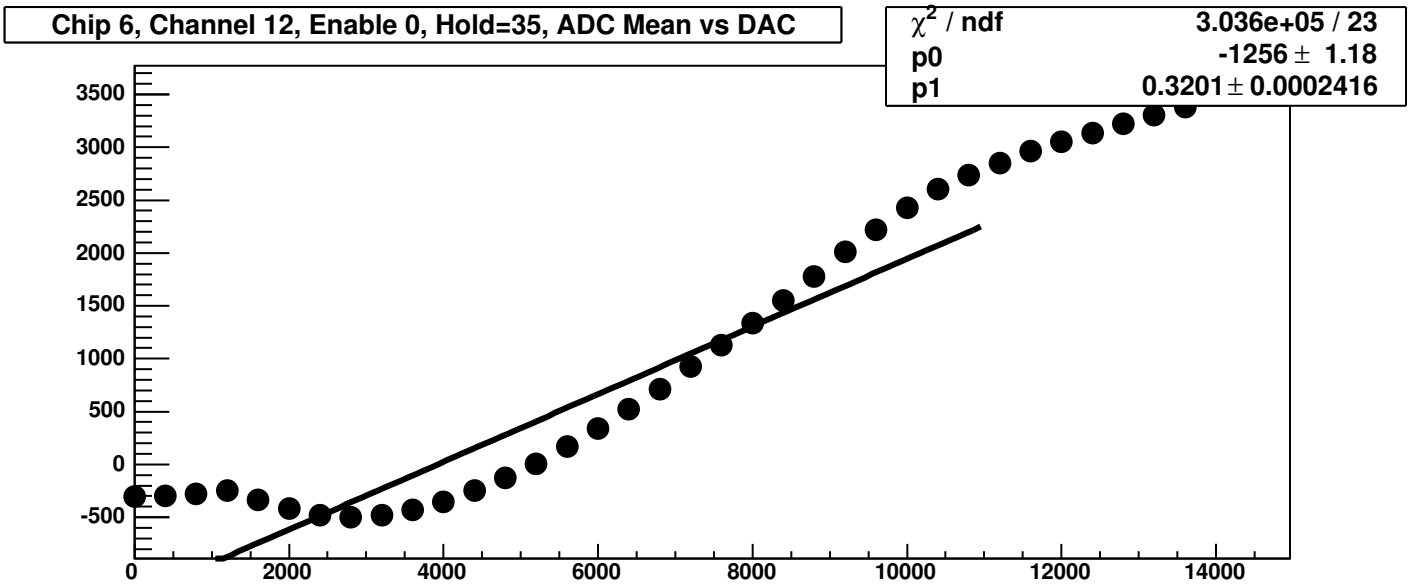

Chip 6, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

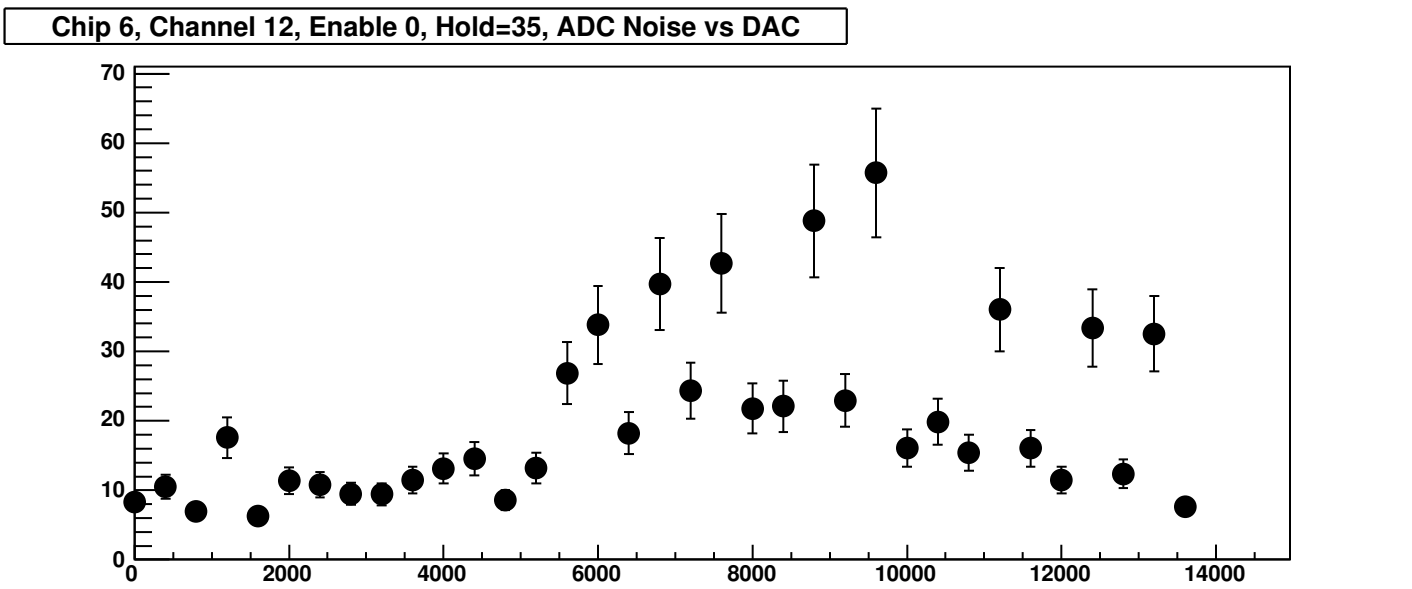

Chip 6, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

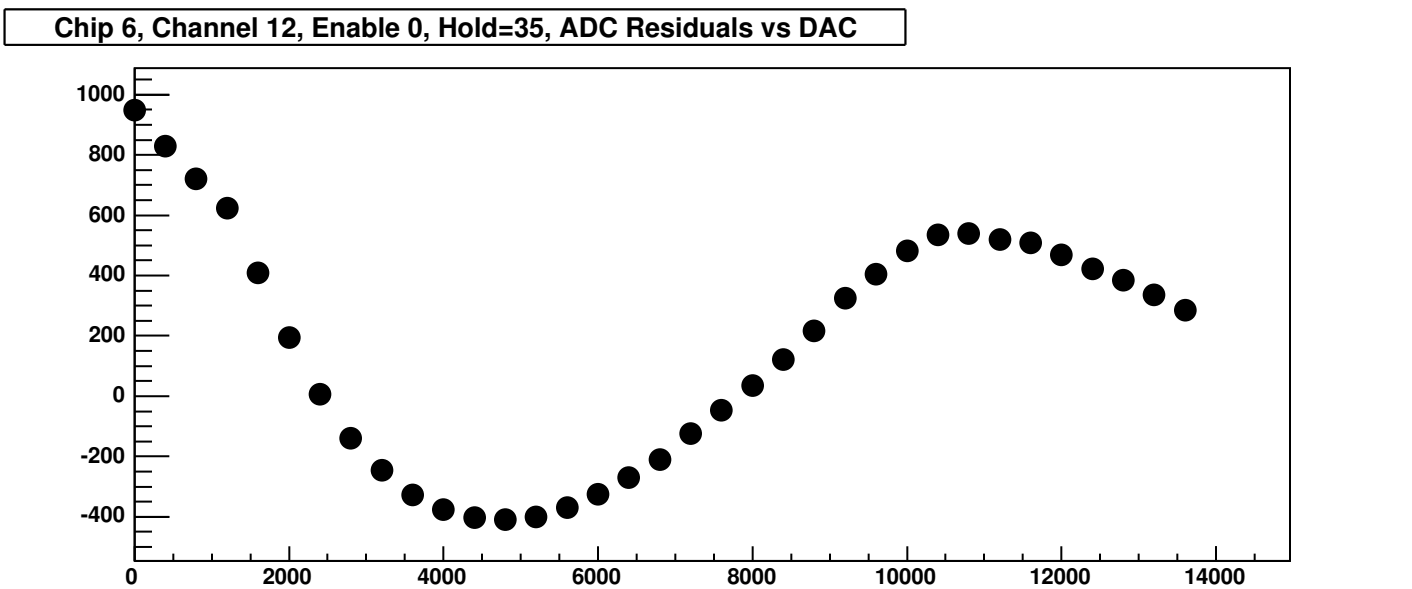

Chip 6, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1299 / 23
p0 -694.9 ± 1.098
p1 0.1589 ± 0.0002019
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

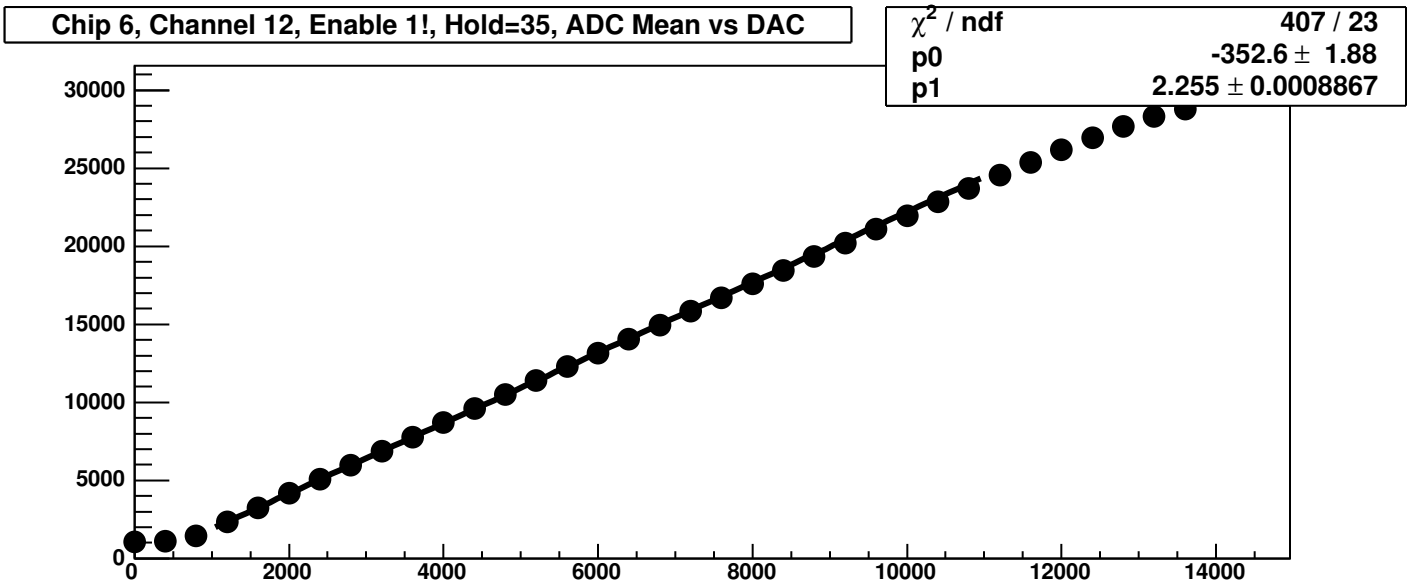

Chip 6, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
0
2000
4000
6000
8000
10000
12000
14000

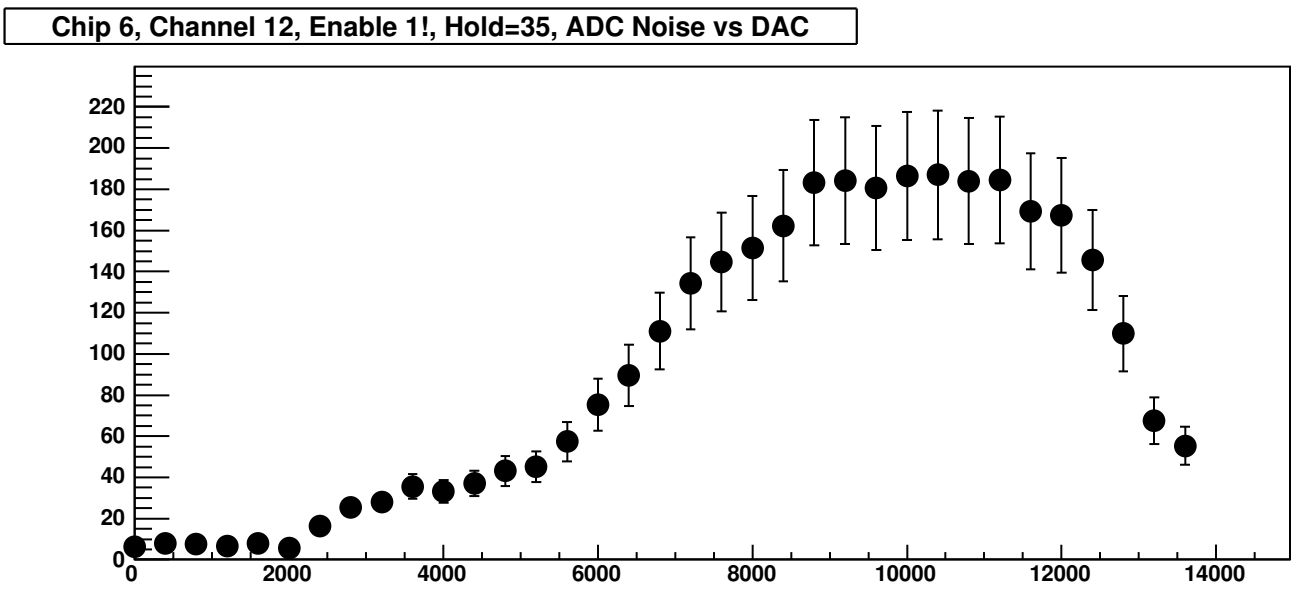

Chip 6, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

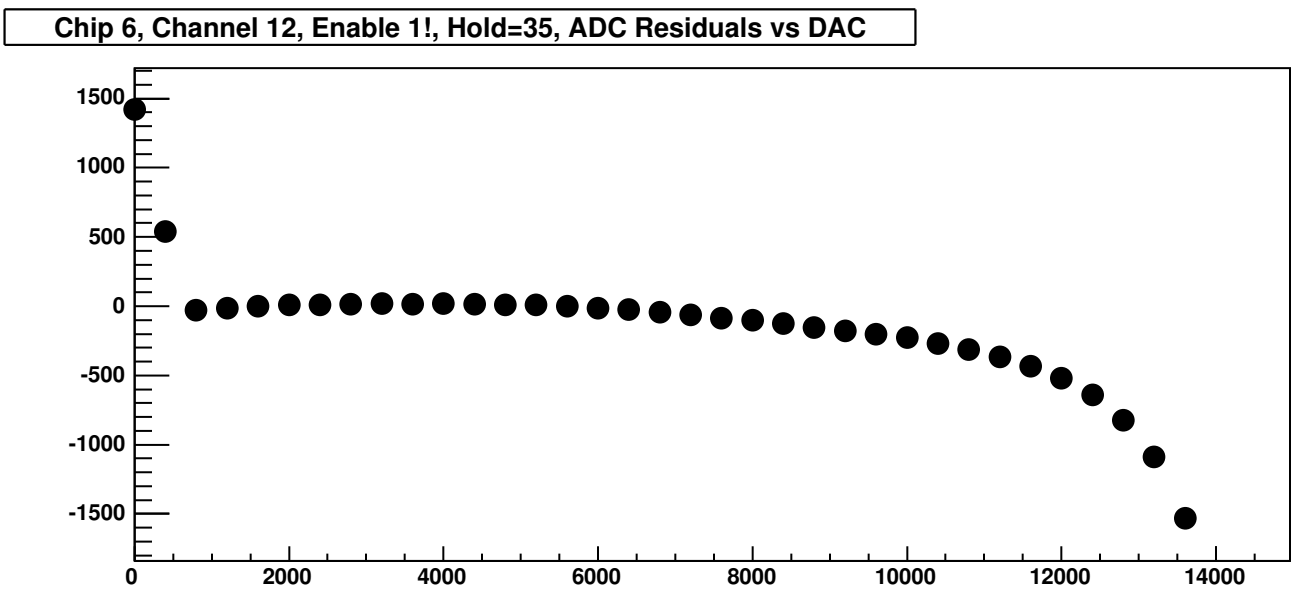

Chip 6, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.036e+05 / 23
p0 -1256 ± 1.18
p1 0.3201 ± 0.0002416
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

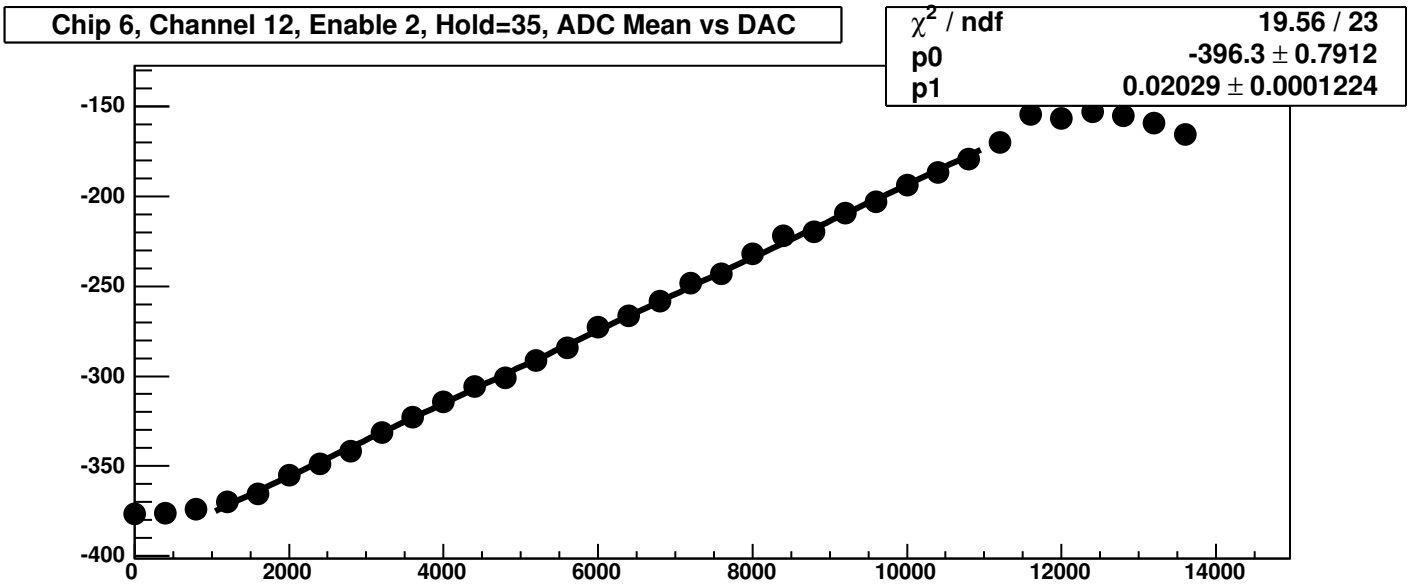

Chip 6, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

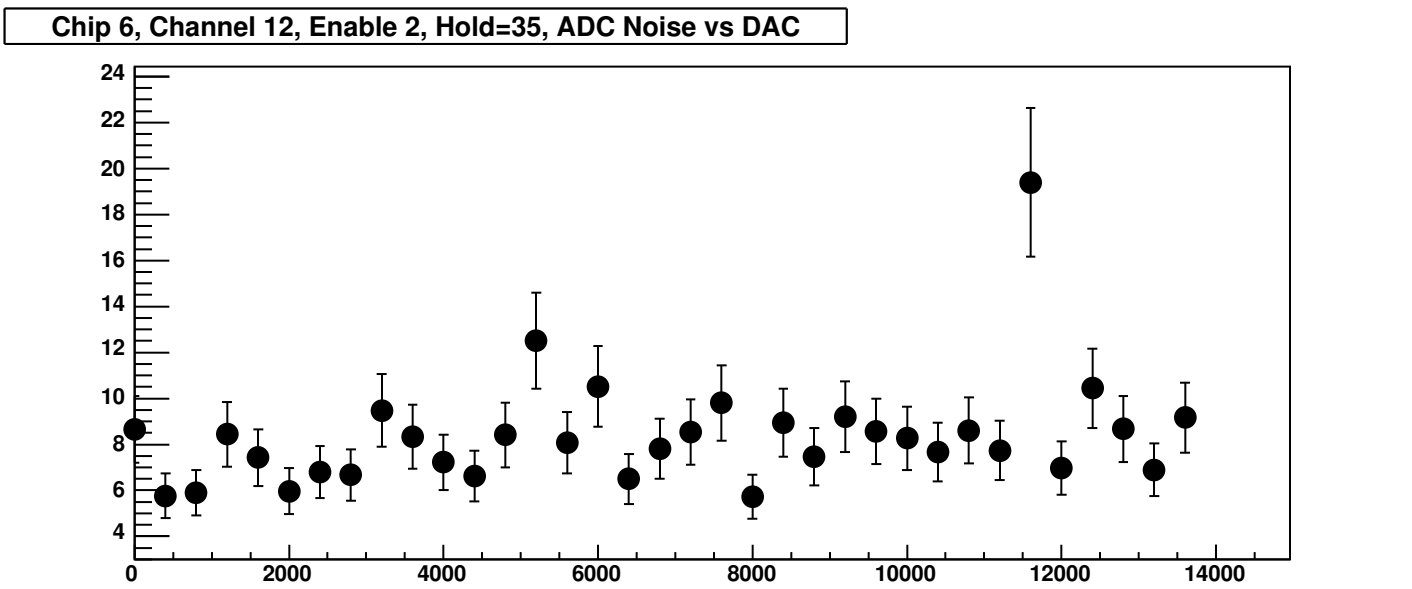

Chip 6, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

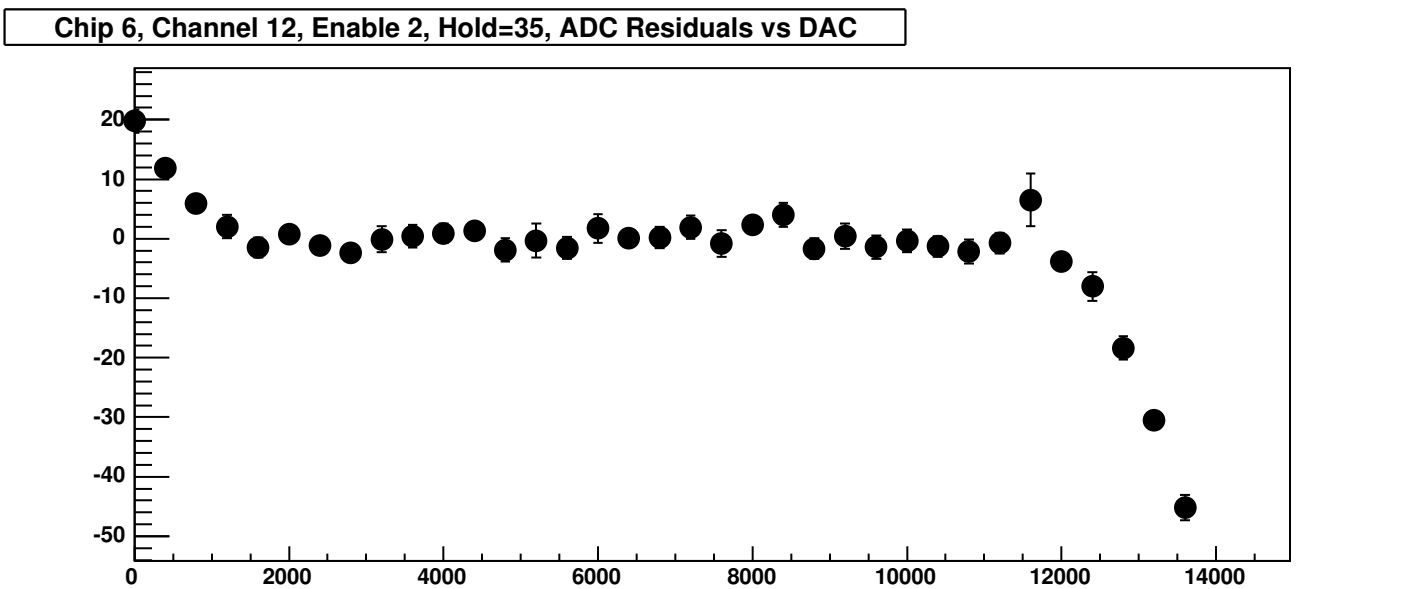

Chip 6, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 407 / 23
p0 -352.6 ± 1.88
p1 2.255 ± 0.0008867
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

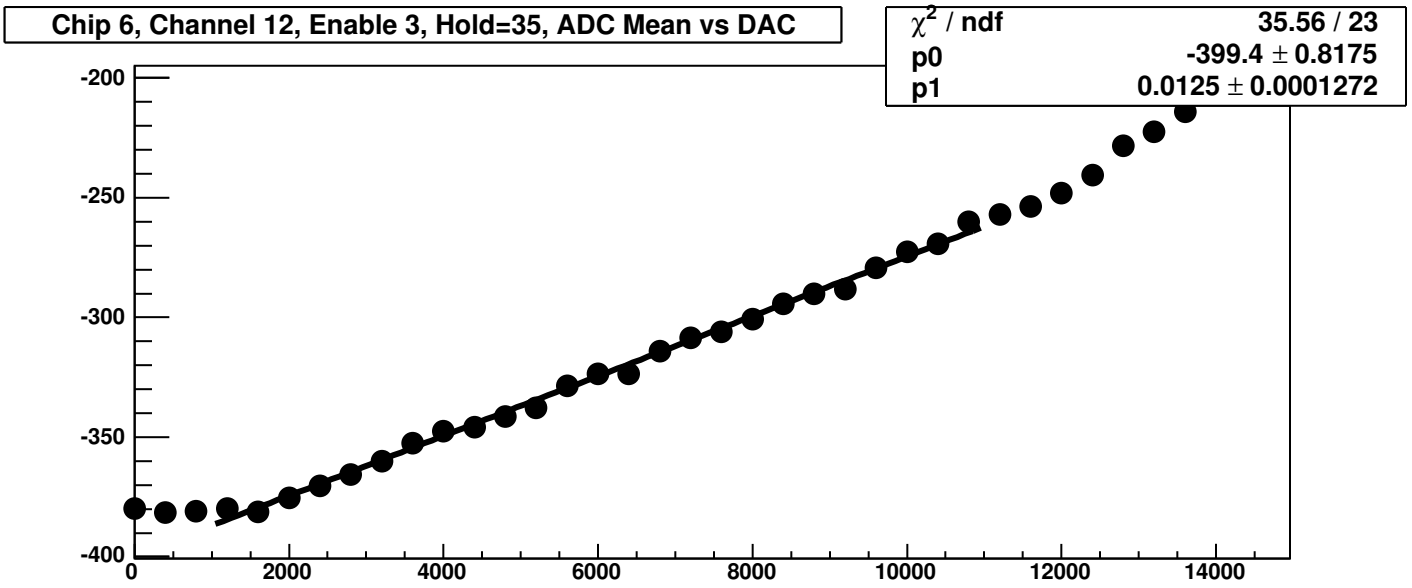

Chip 6, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

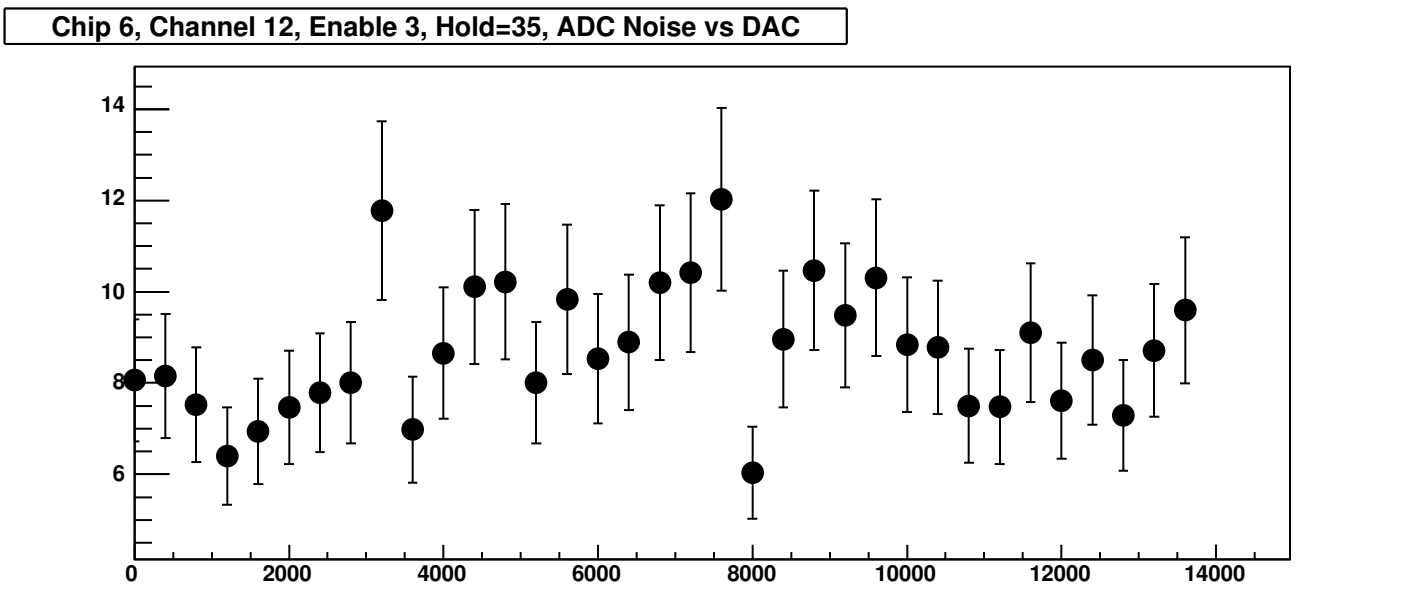

Chip 6, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

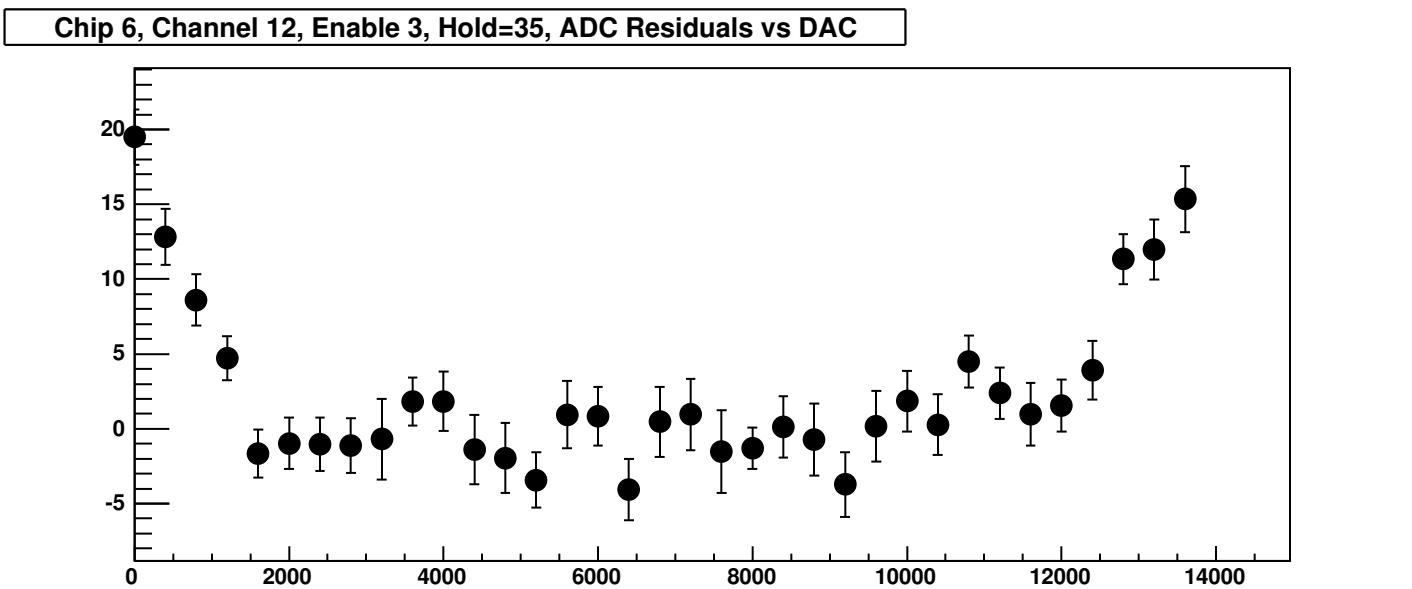

Chip 6, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 19.56 / 23
p0 -396.3 ± 0.7912
p1 0.02029 ± 0.0001224
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

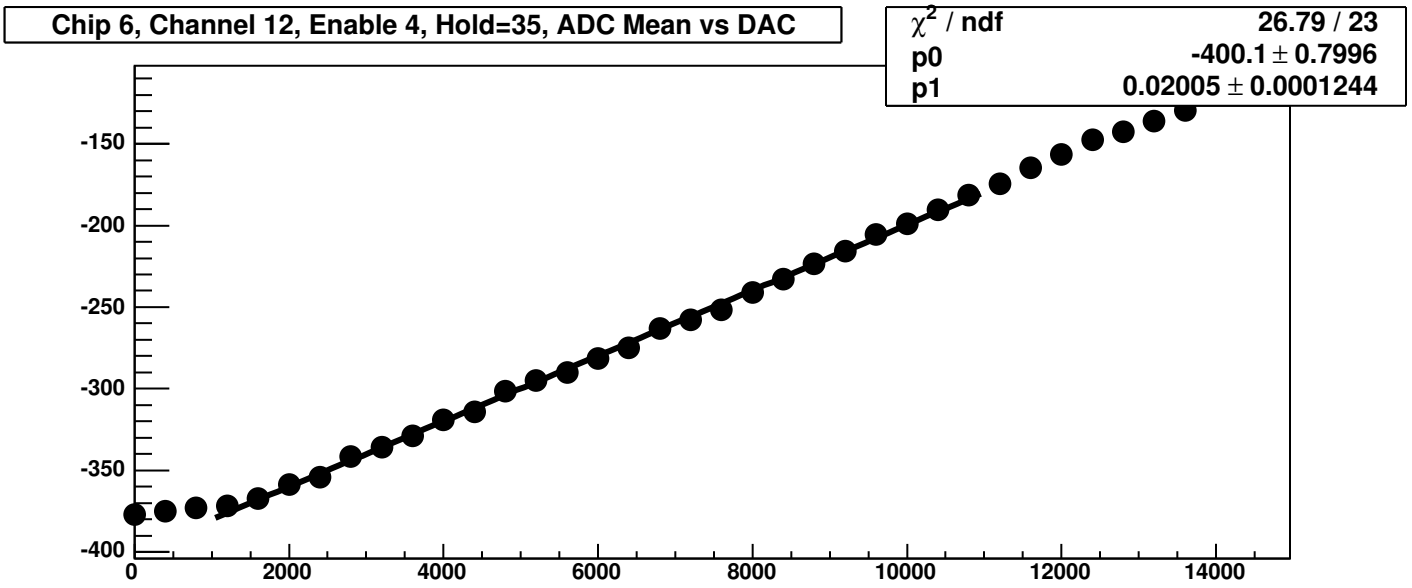

Chip 6, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
24
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

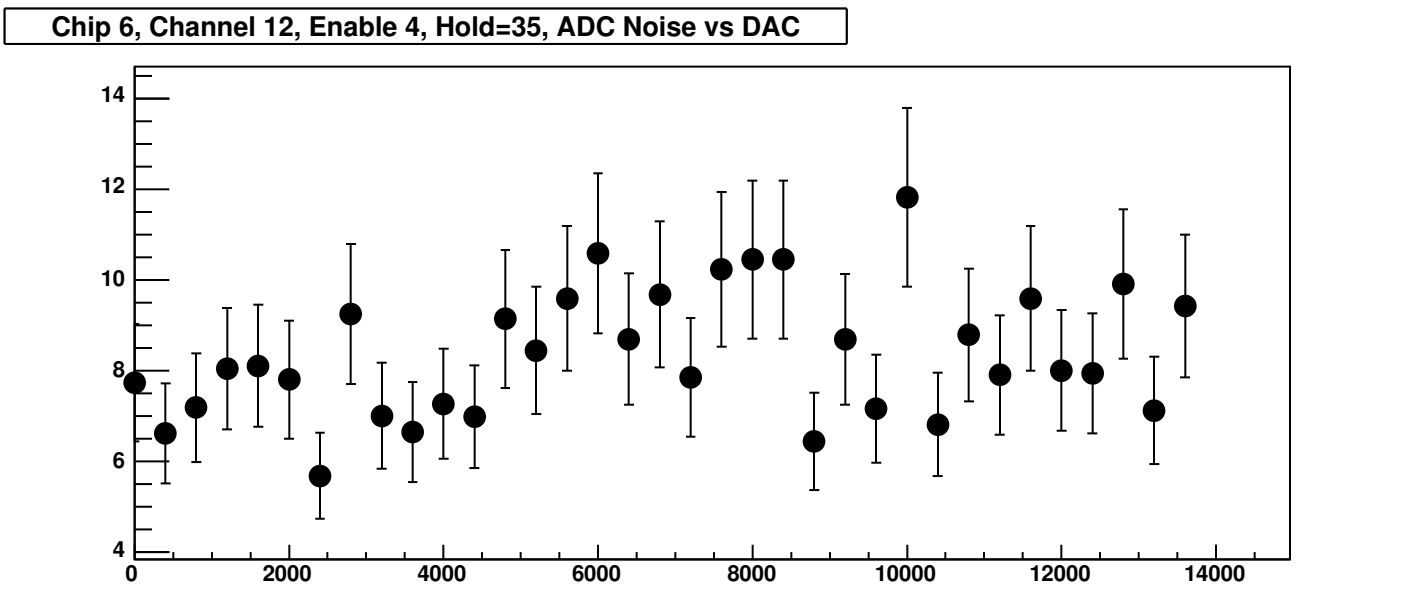

Chip 6, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

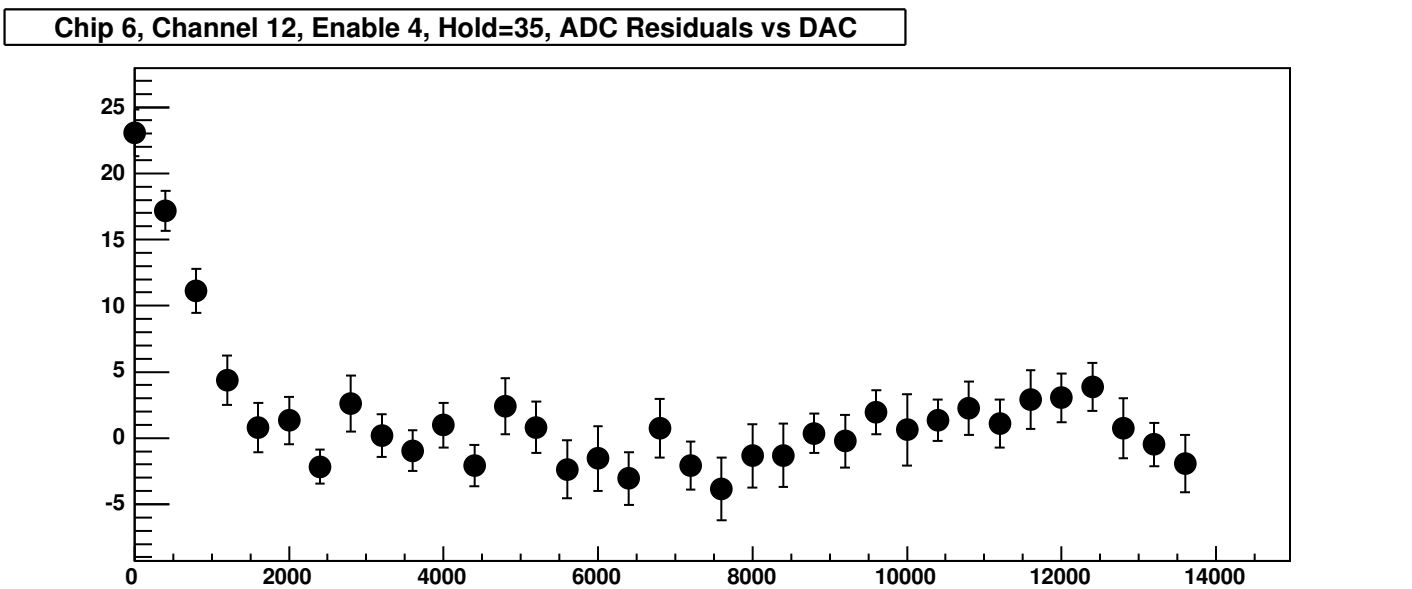

Chip 6, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 35.56 / 23
p0 -399.4 ± 0.8175
p1 0.0125 ± 0.0001272
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

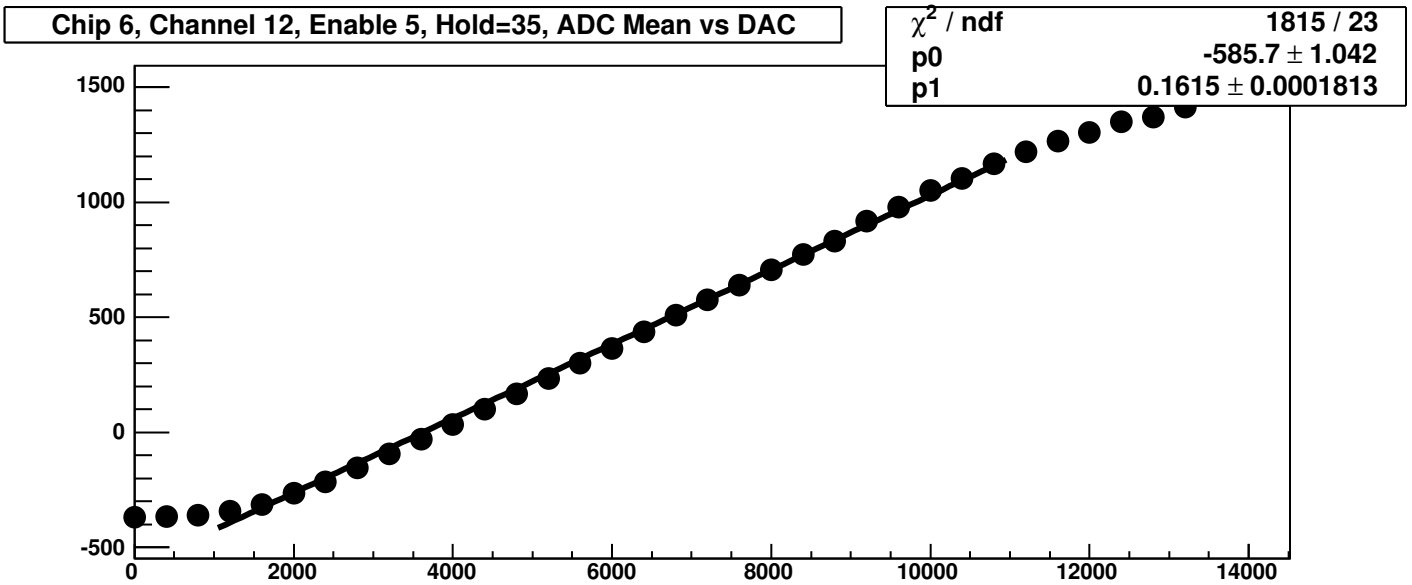

Chip 6, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

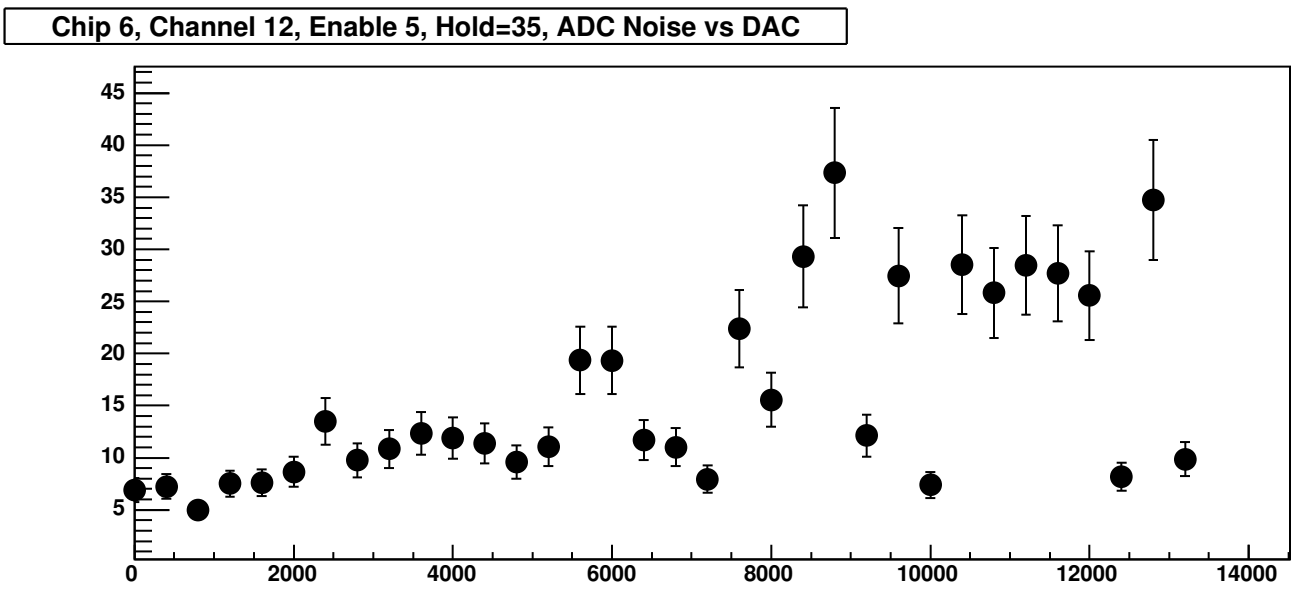

Chip 6, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

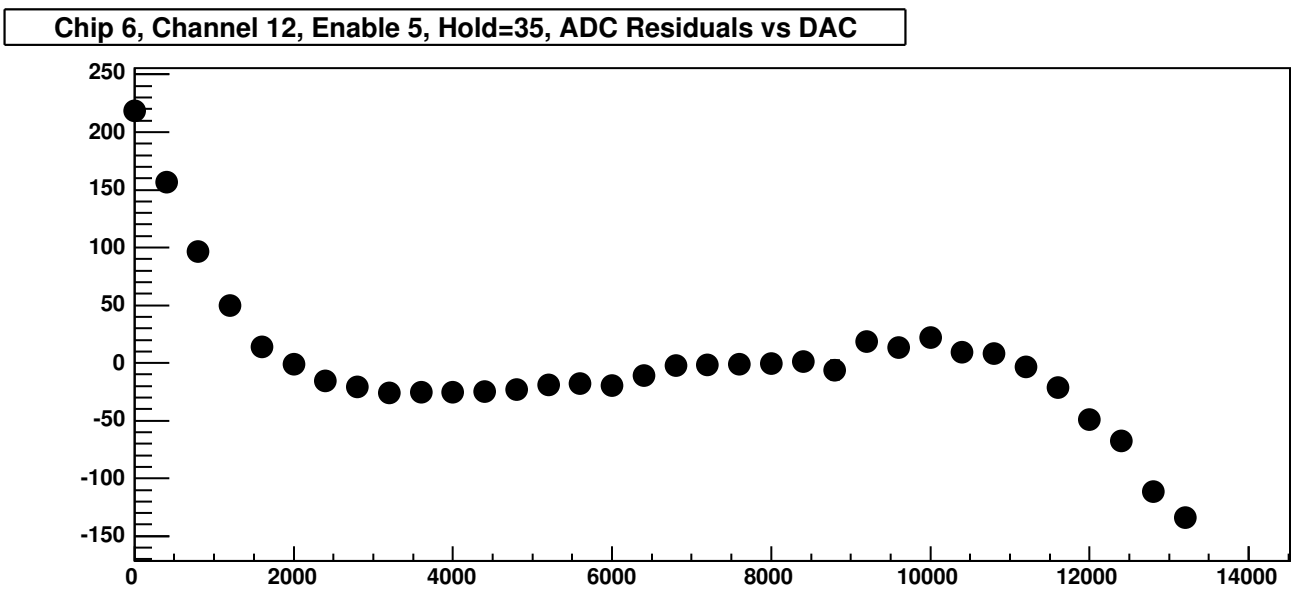

Chip 6, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.79 / 23
p0 -400.1 ± 0.7996
p1 0.02005 ± 0.0001244
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

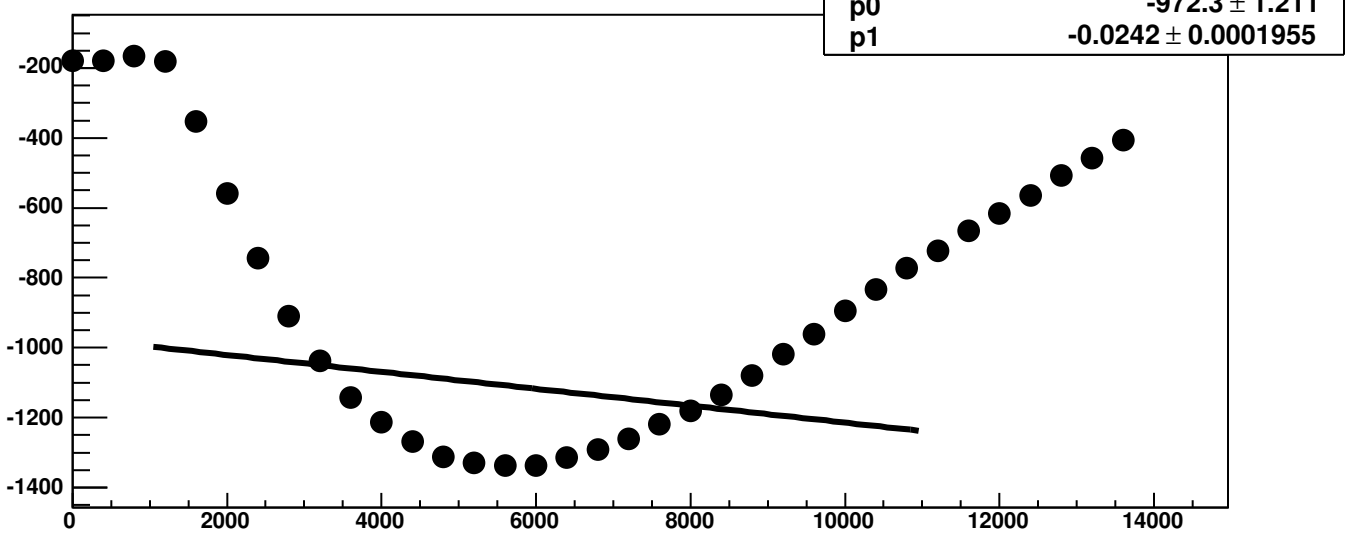

Chip 6, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1815 / 23
p0 -585.7 ± 1.042
p1 0.1615 ± 0.0001813
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

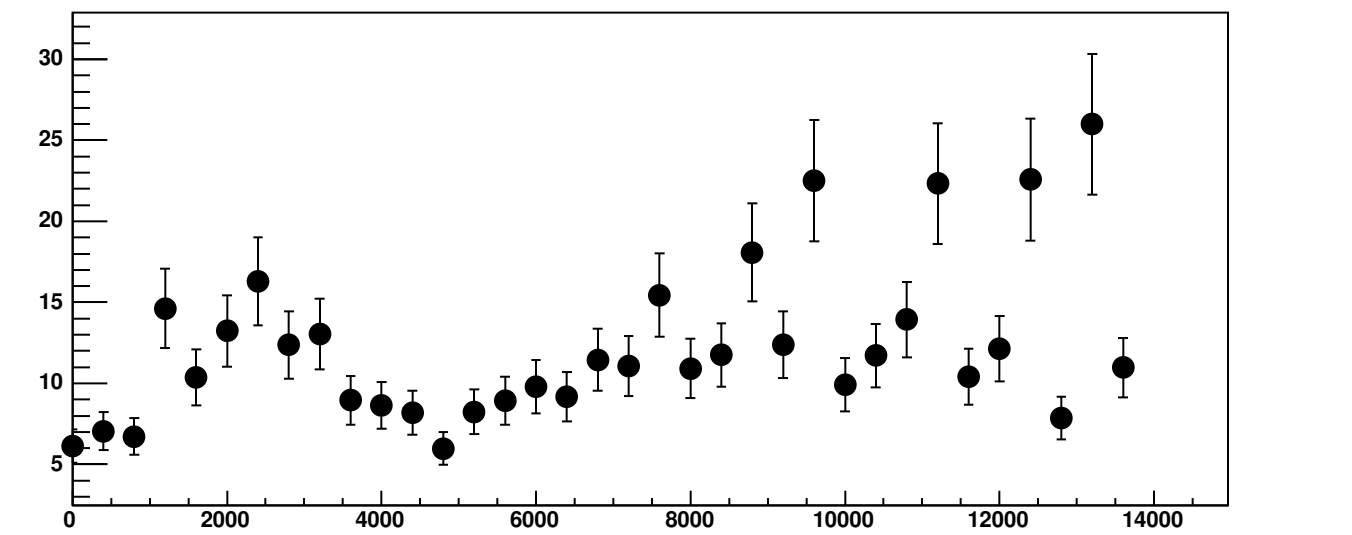

Chip 6, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

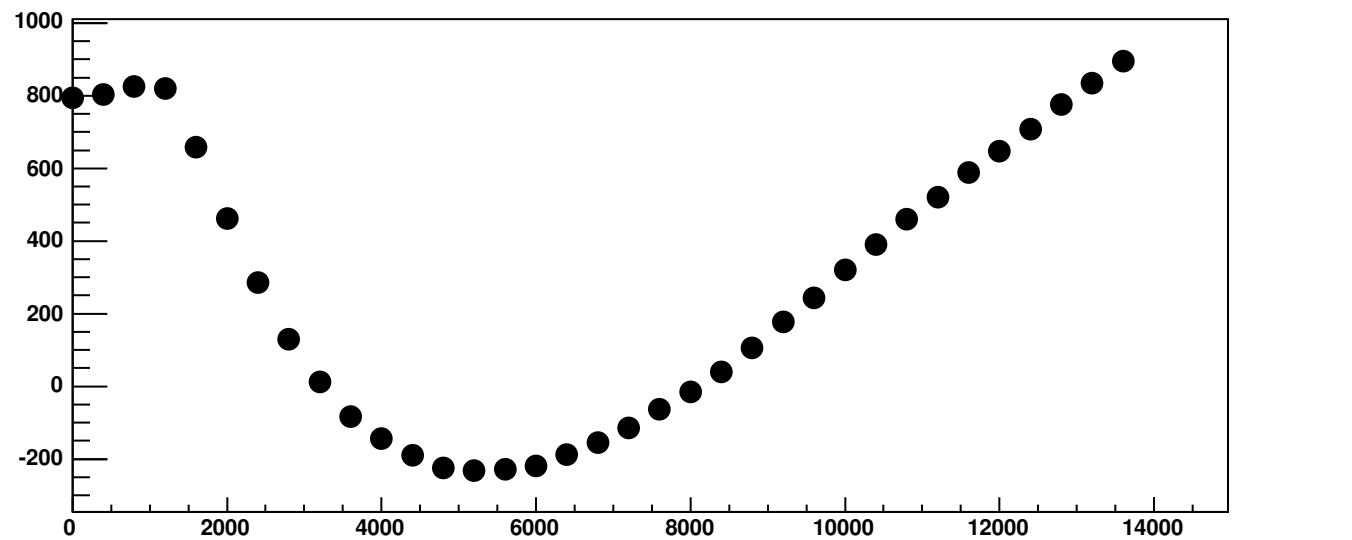
Chip 6, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

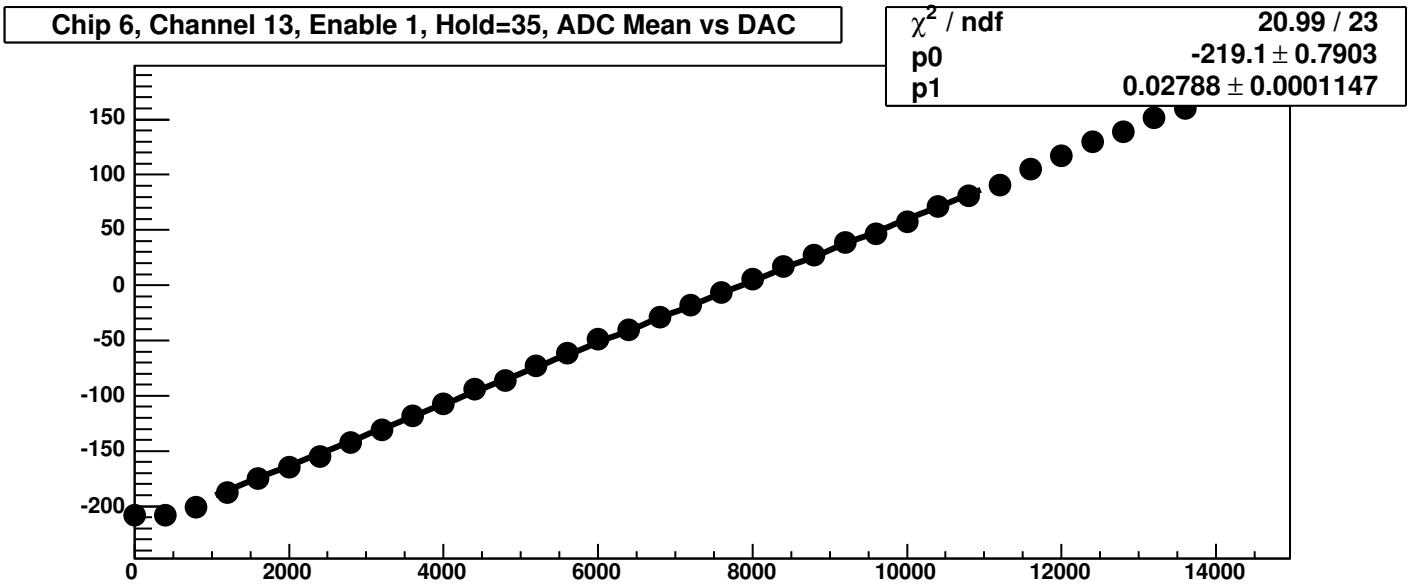


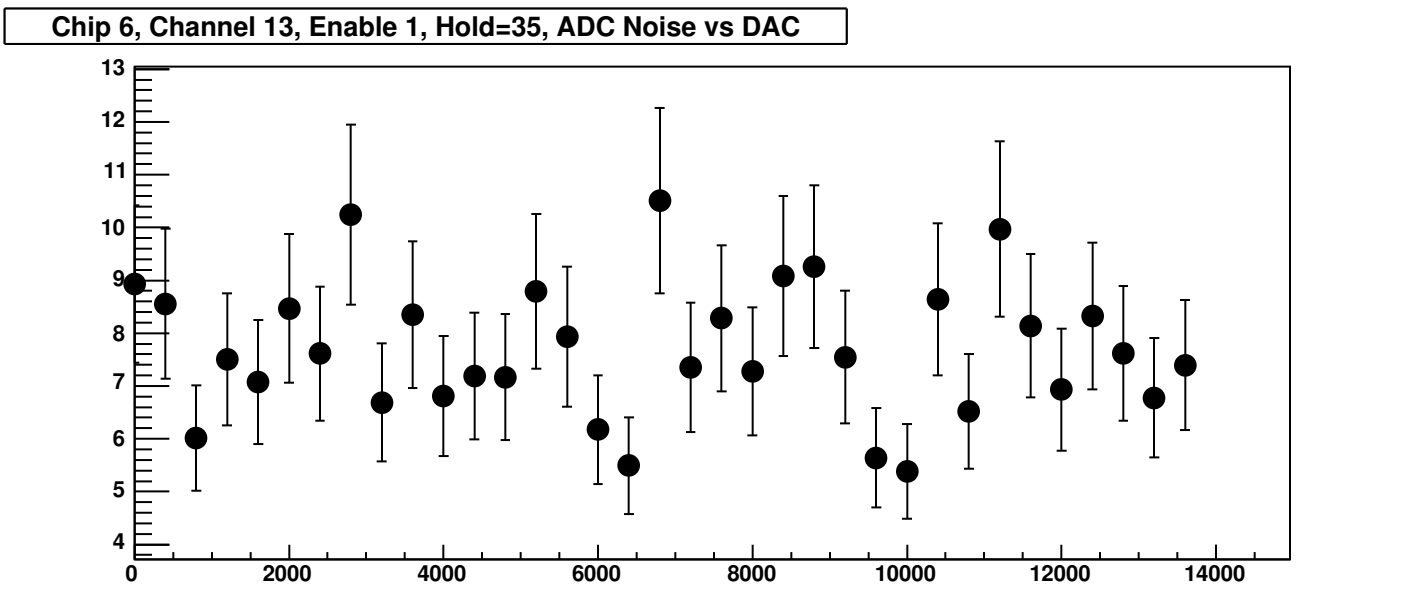
Chip 6, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

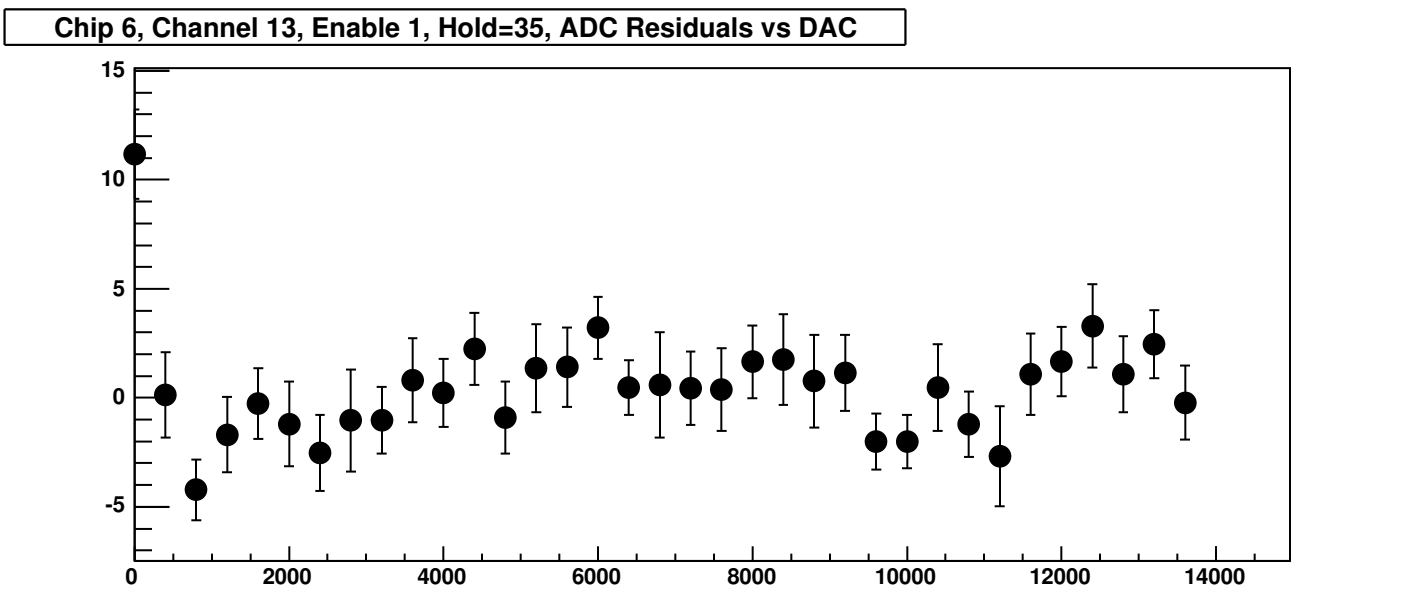


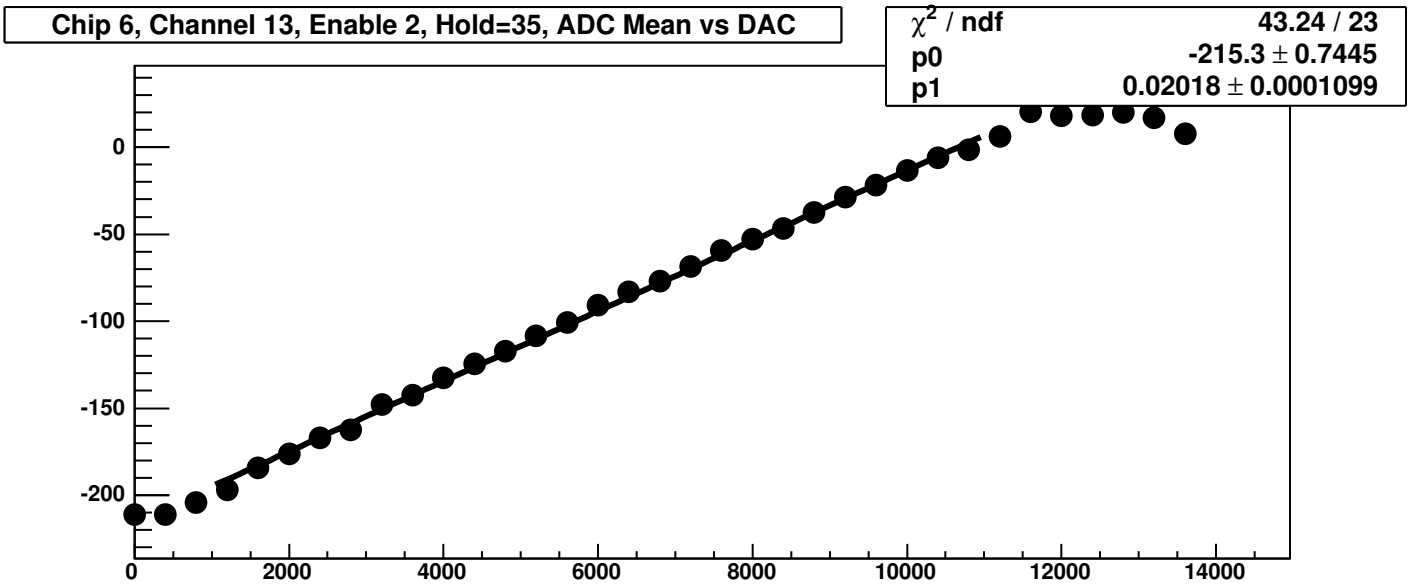
Chip 6, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

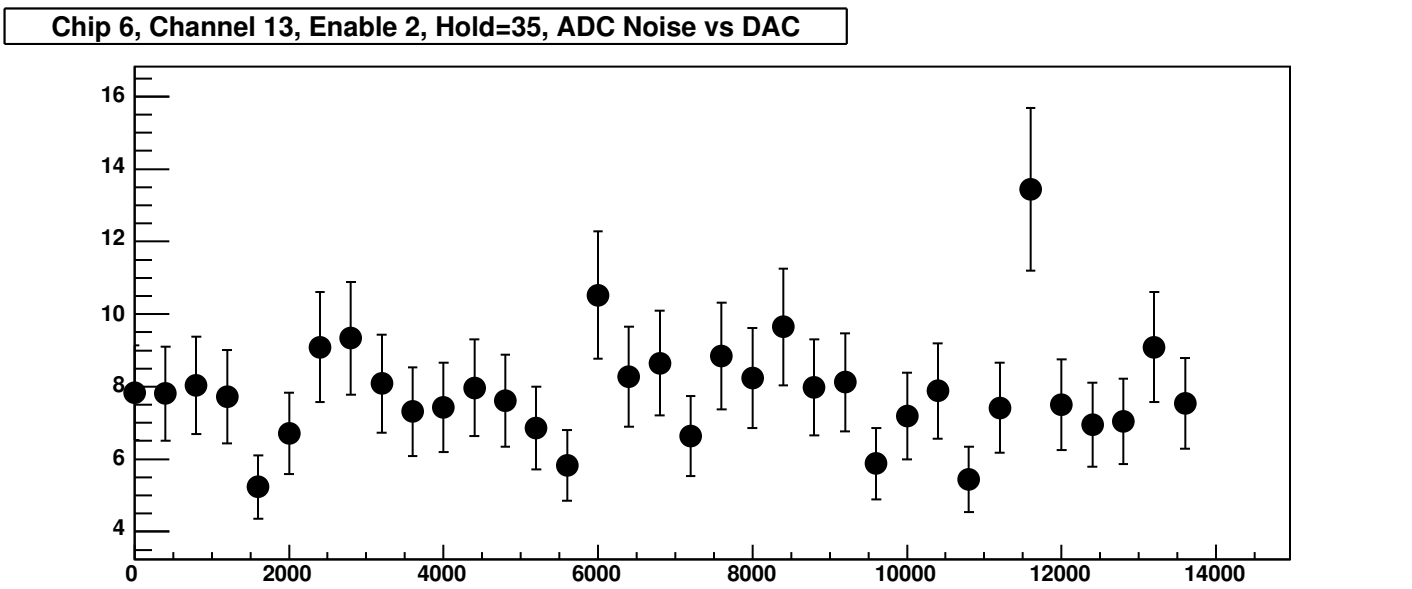


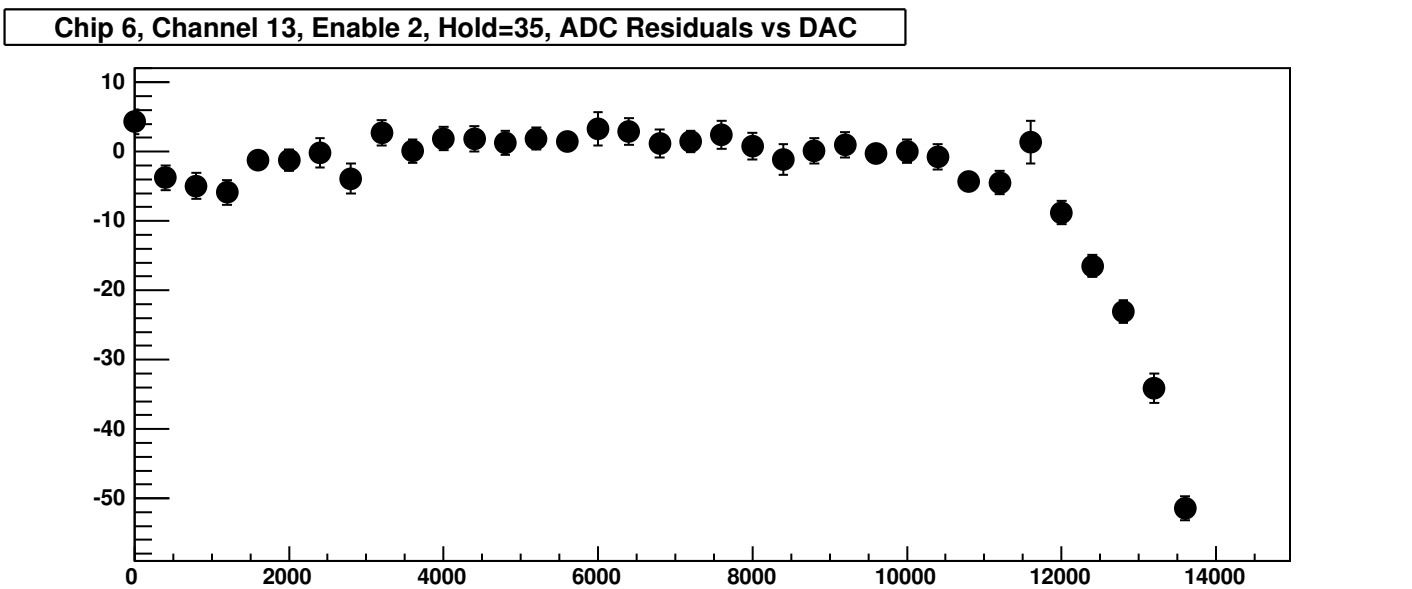

Chip 6, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 20.99 / 23
p0 -219.1 ± 0.7903
p1 0.02788 ± 0.0001147
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

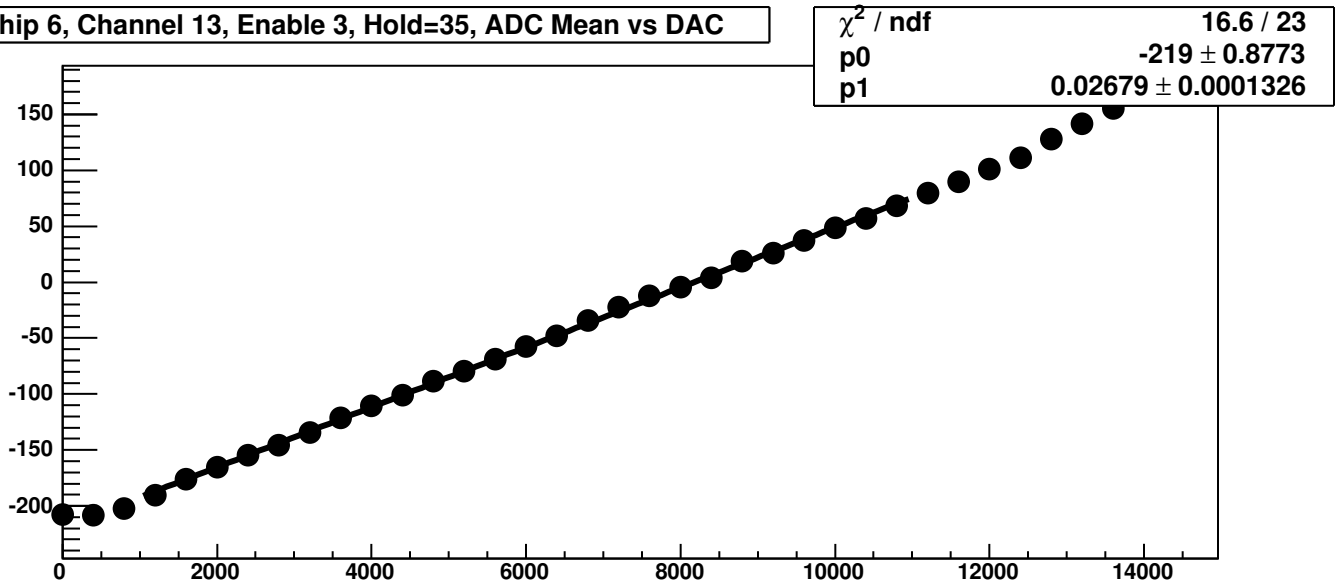

Chip 6, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 43.24 / 23
p0 -215.3 ± 0.7445
p1 0.02018 ± 0.0001099
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

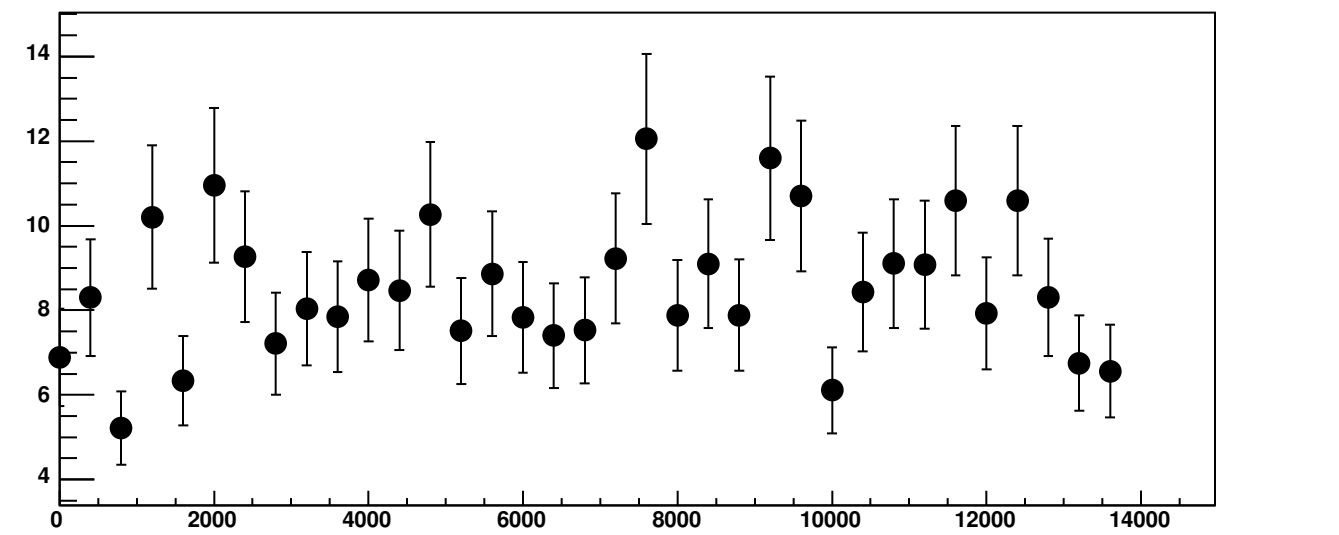

Chip 6, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

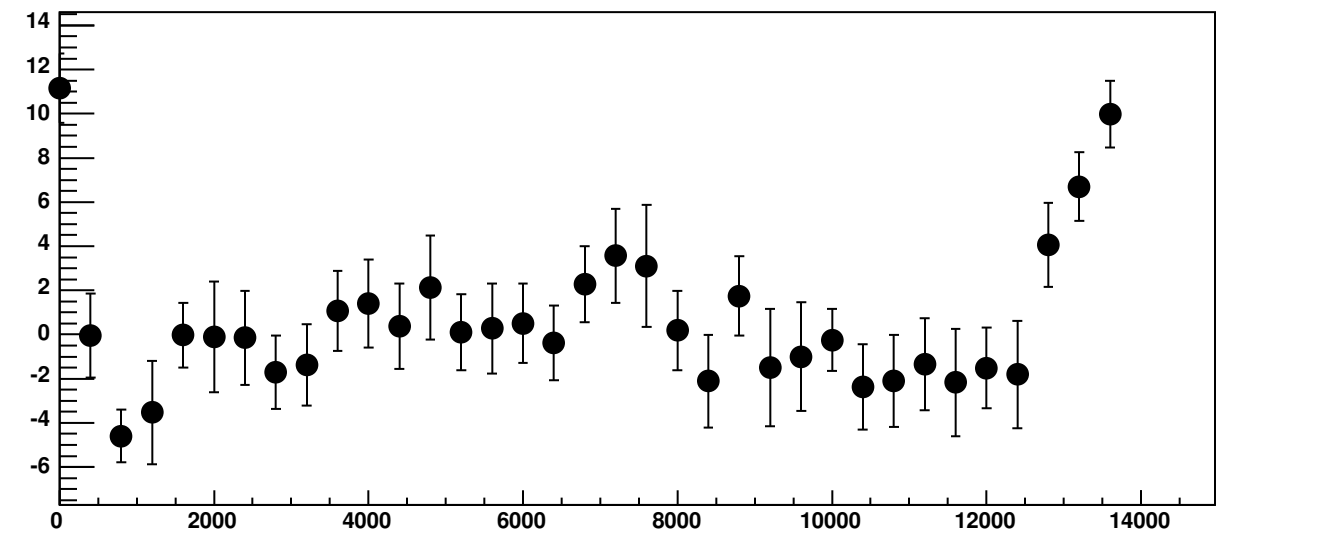
Chip 6, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

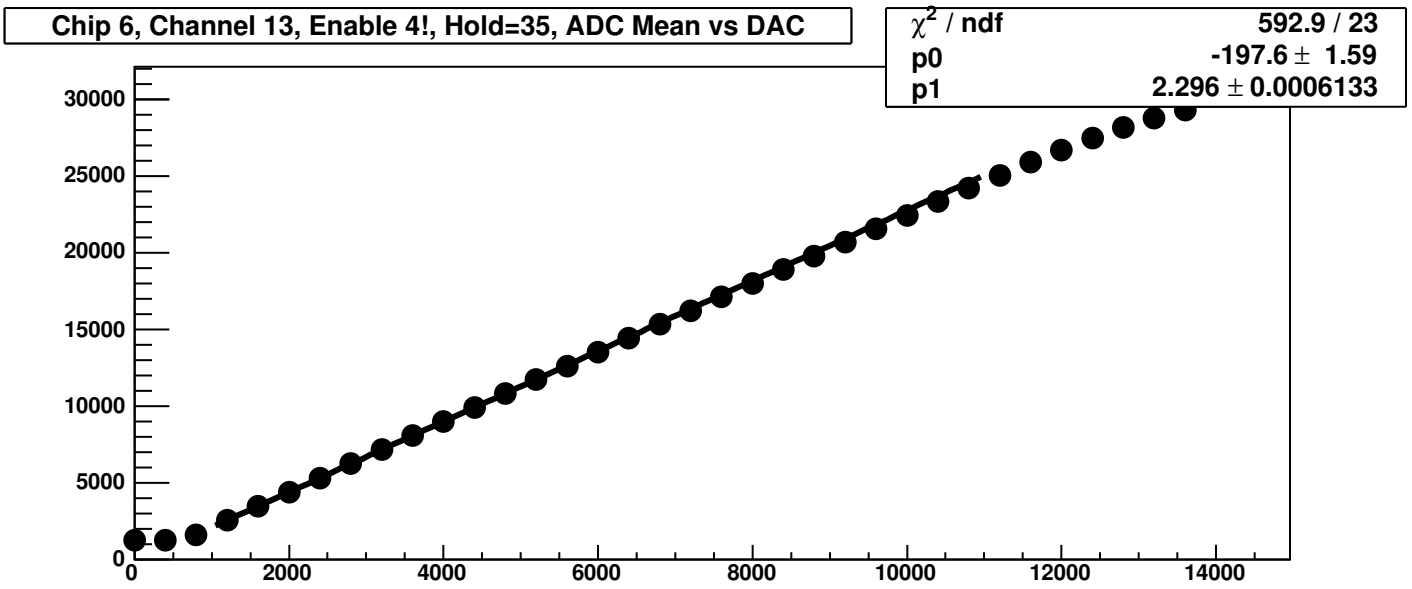


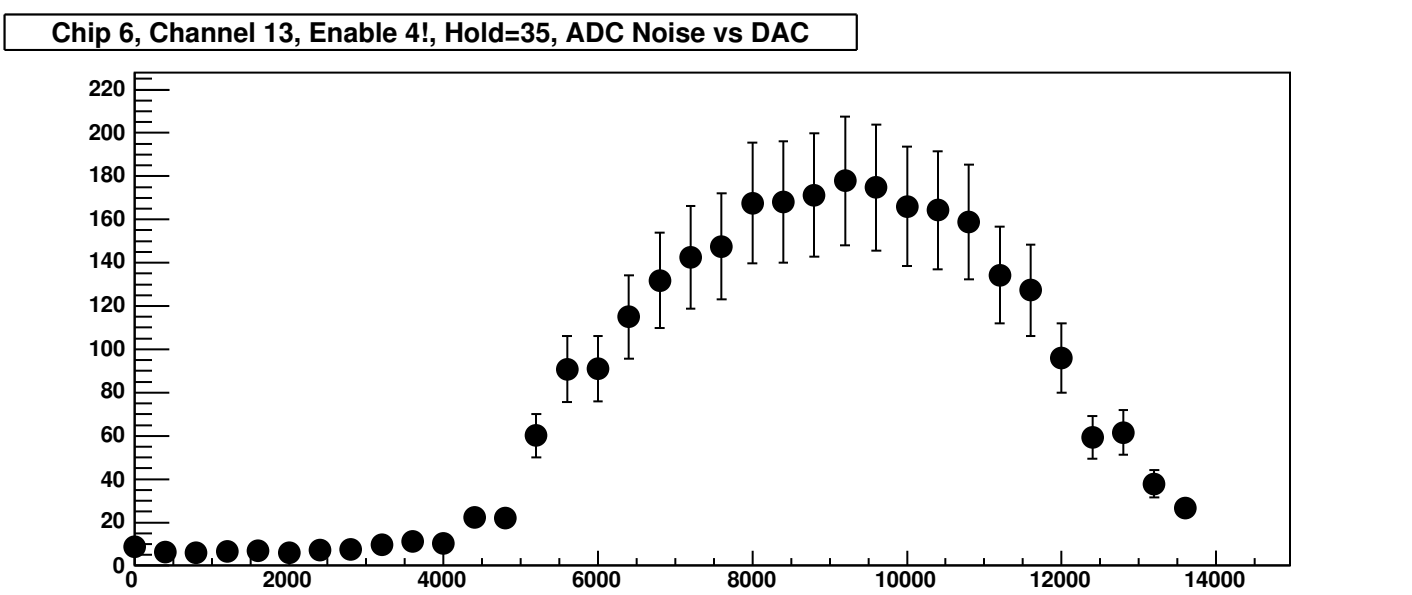
Chip 6, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

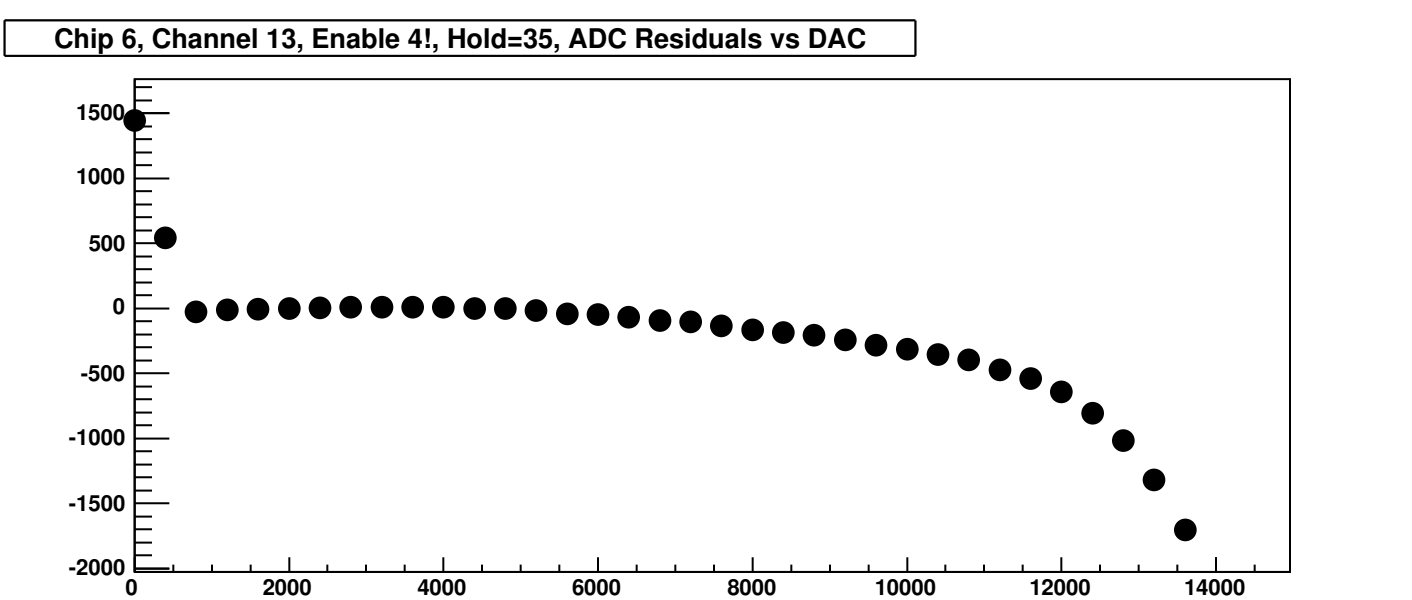


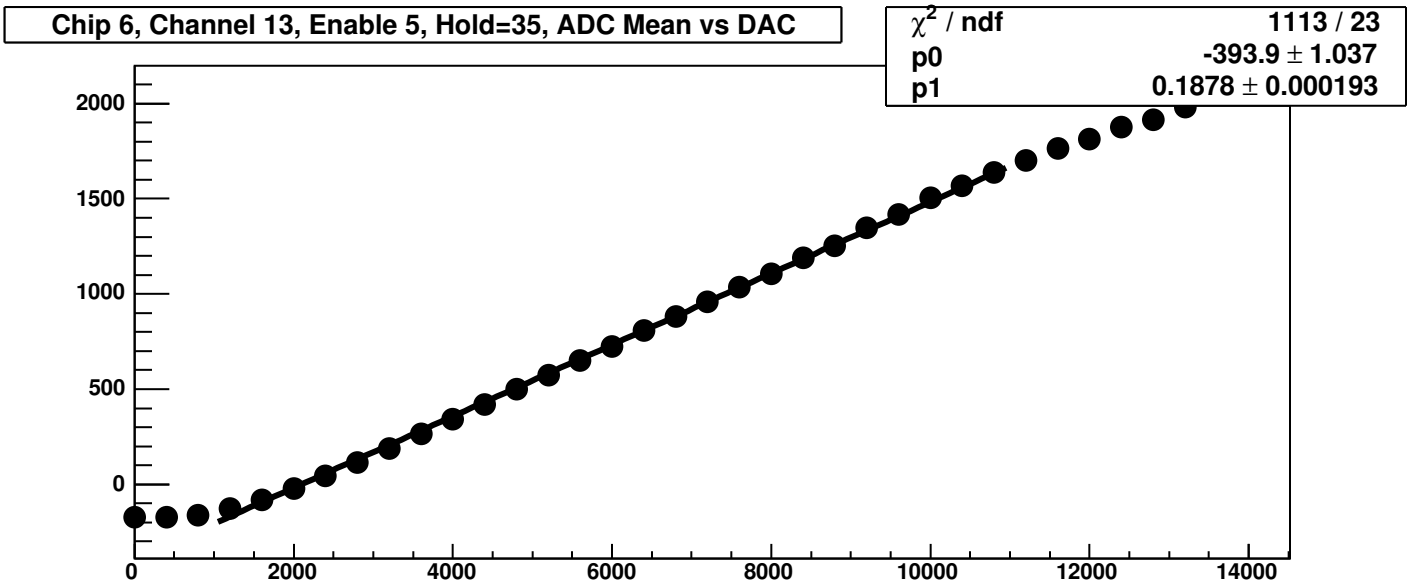
Chip 6, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

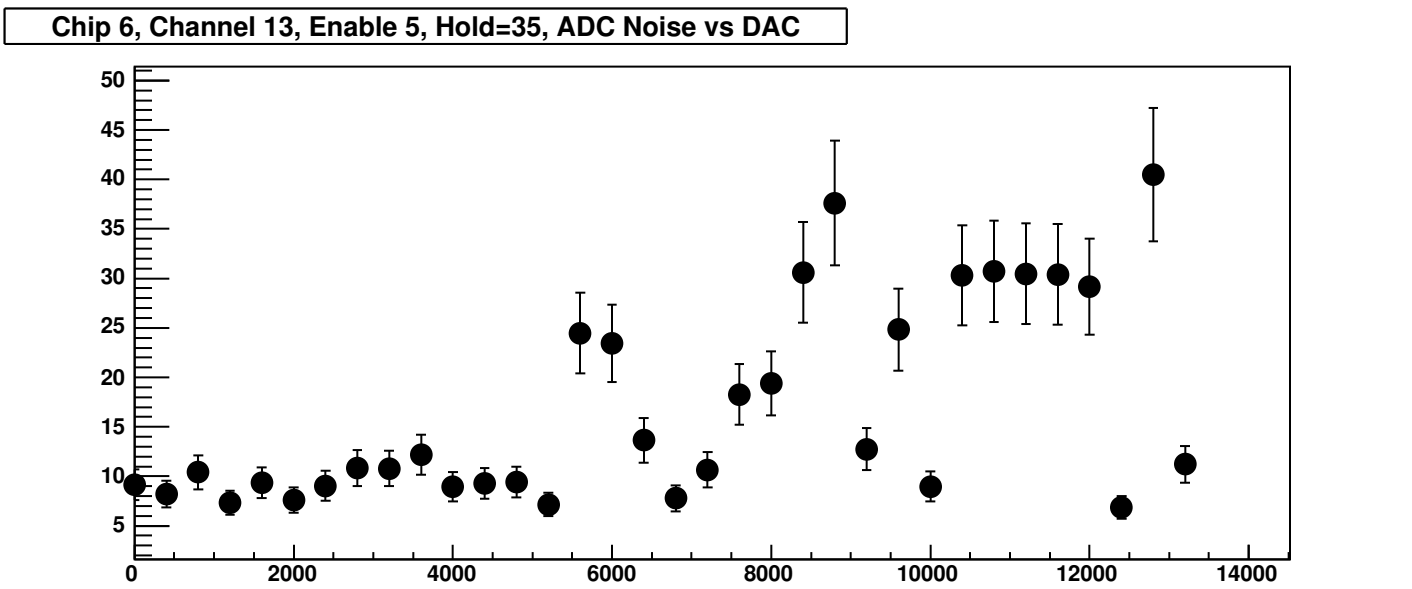


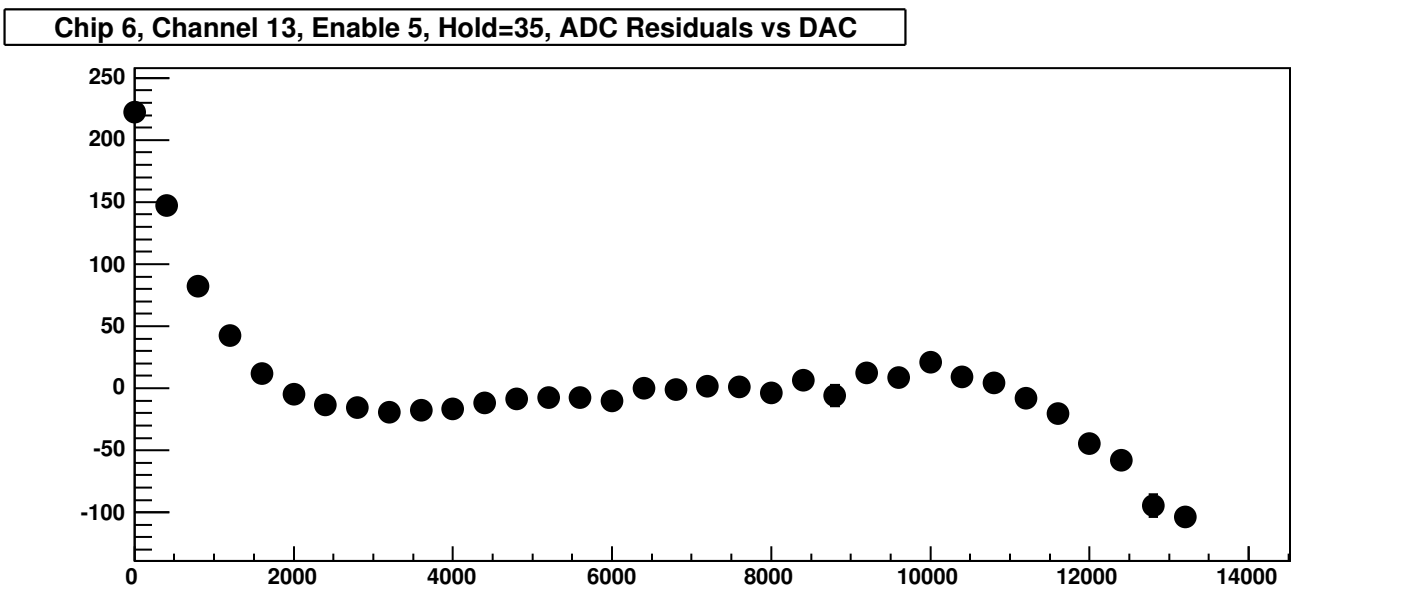

Chip 6, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 592.9 / 23
p0 -197.6 ± 1.59
p1 2.296 ± 0.0006133
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

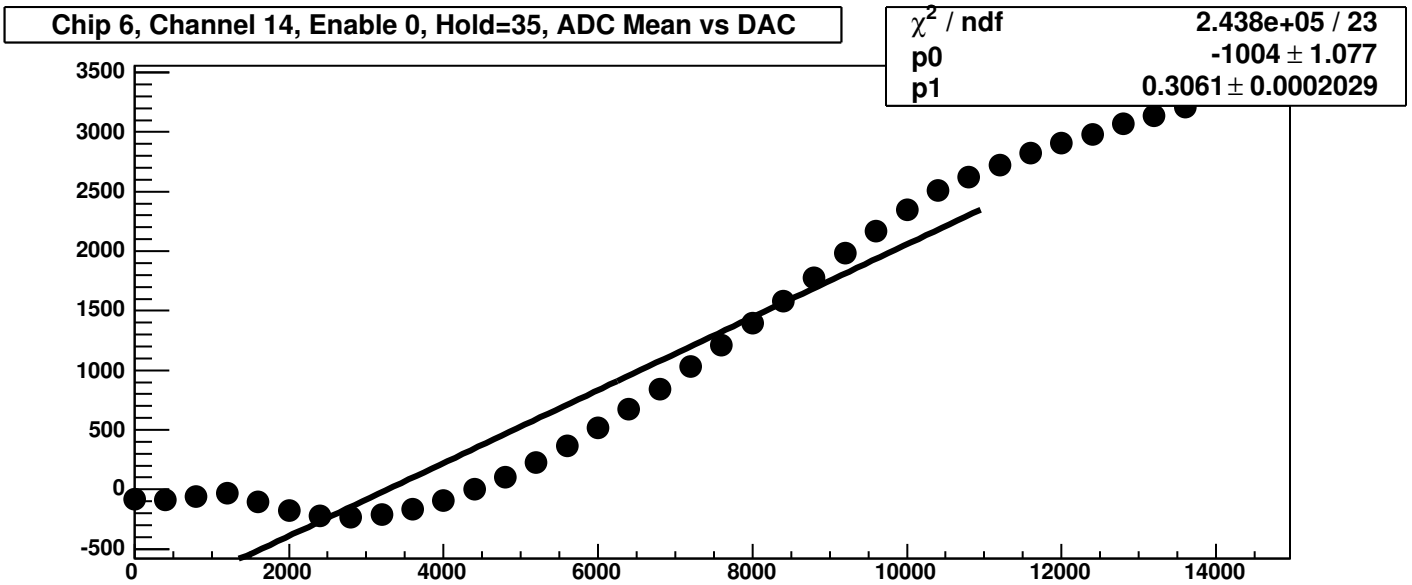

Chip 6, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

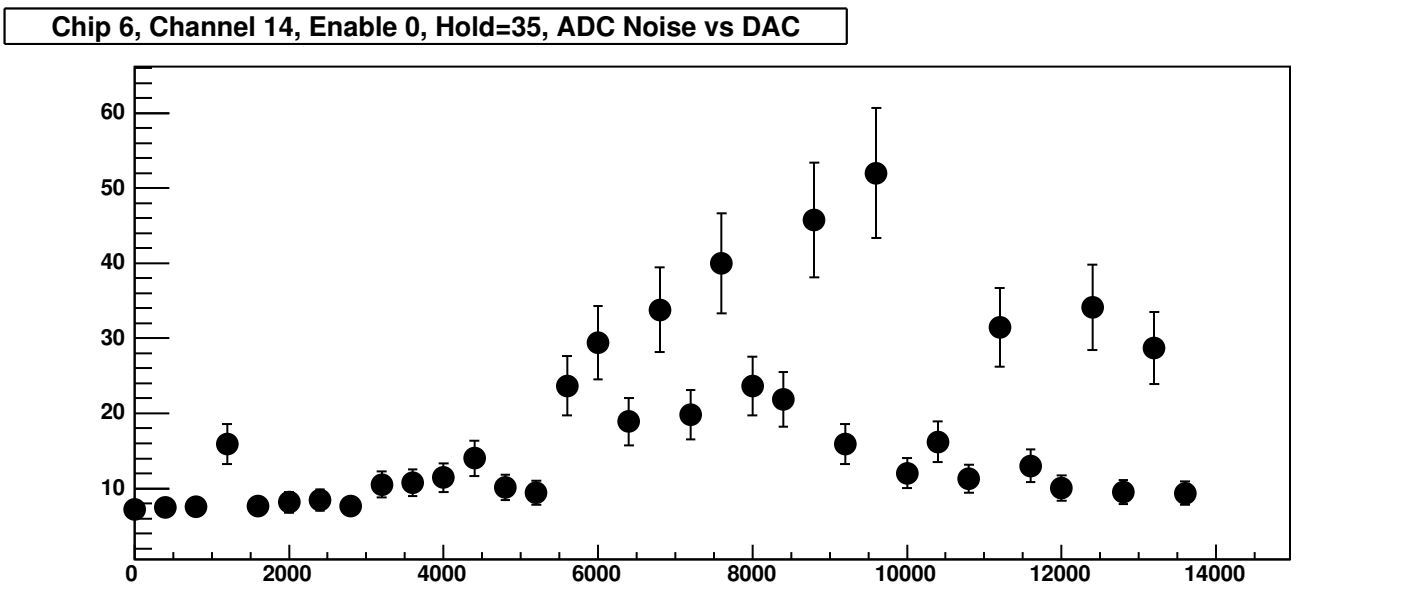

Chip 6, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

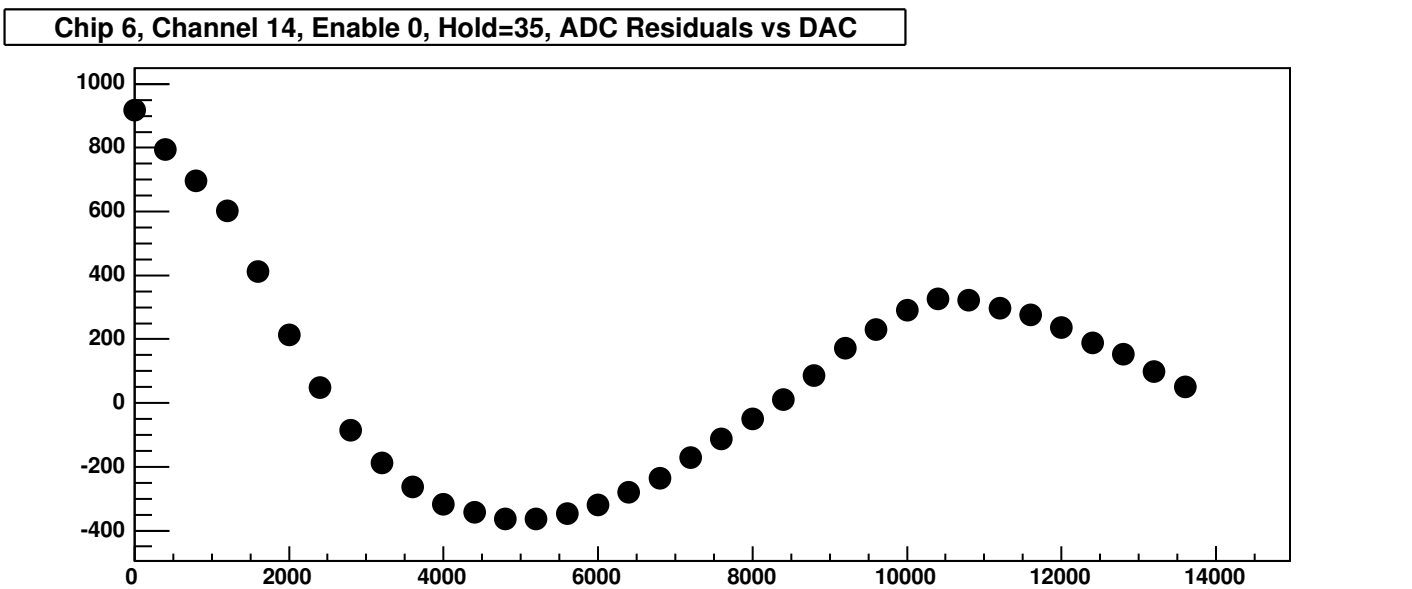

Chip 6, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1113 / 23
p0 -393.9 ± 1.037
p1 0.1878 ± 0.000193
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

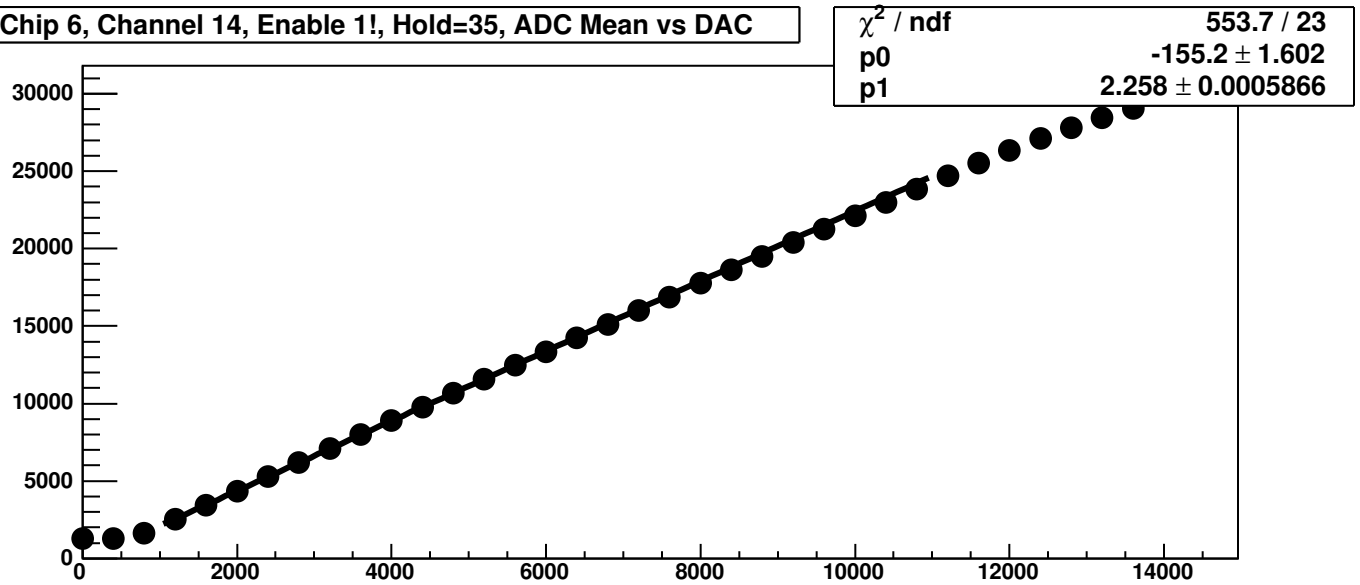

Chip 6, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.438e+05 / 23
p0 -1004 ± 1.077
p1 0.3061 ± 0.0002029
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

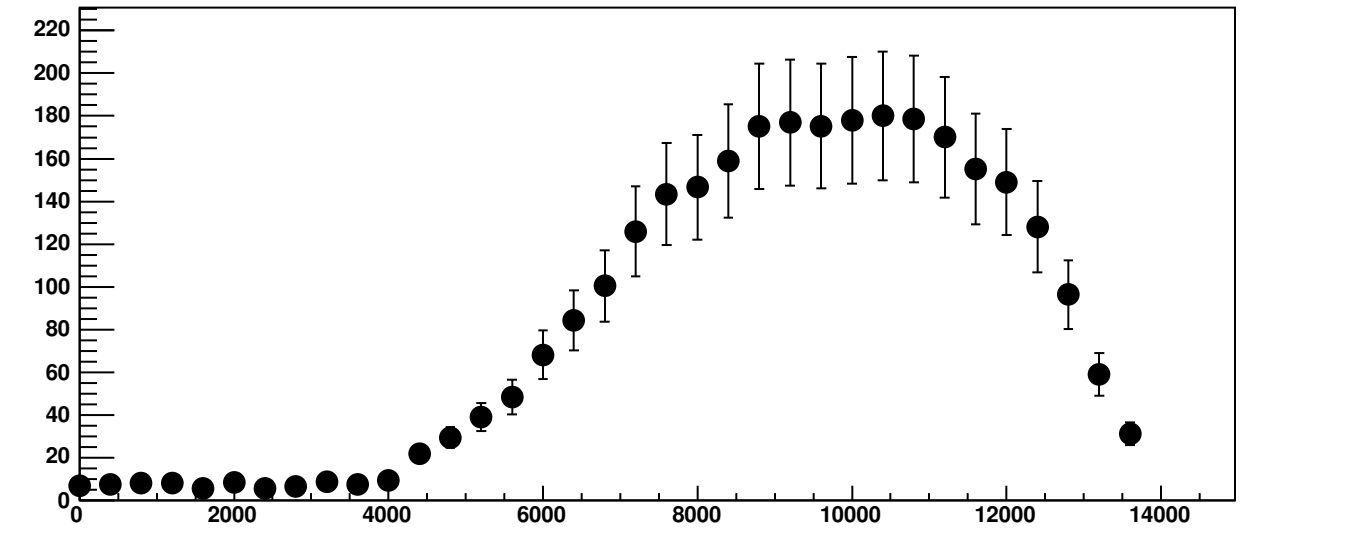

Chip 6, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

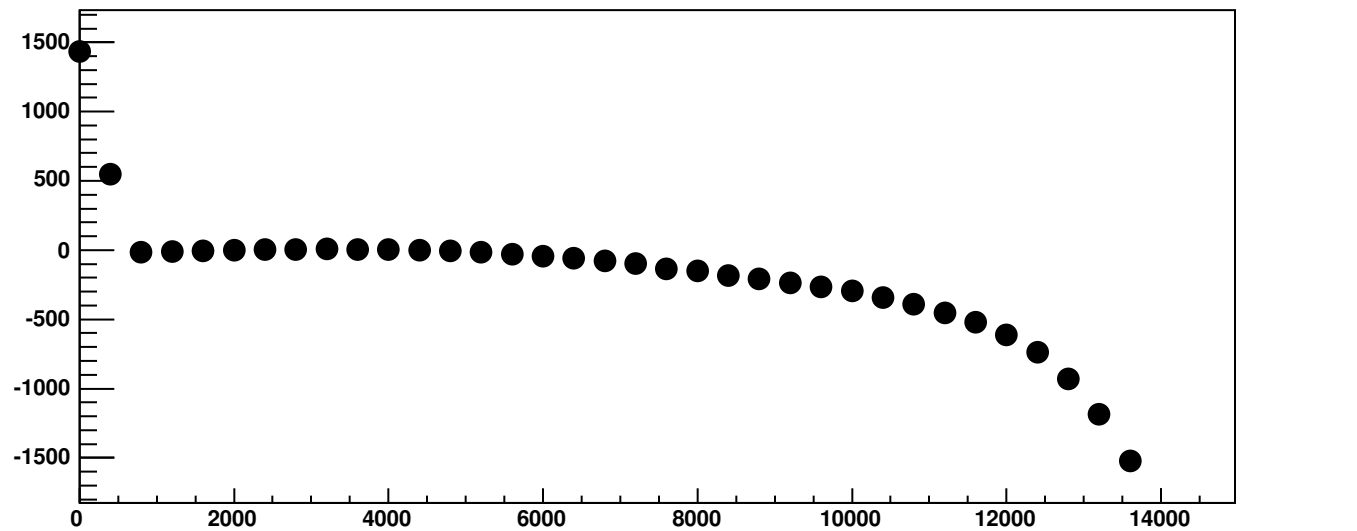
Chip 6, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC



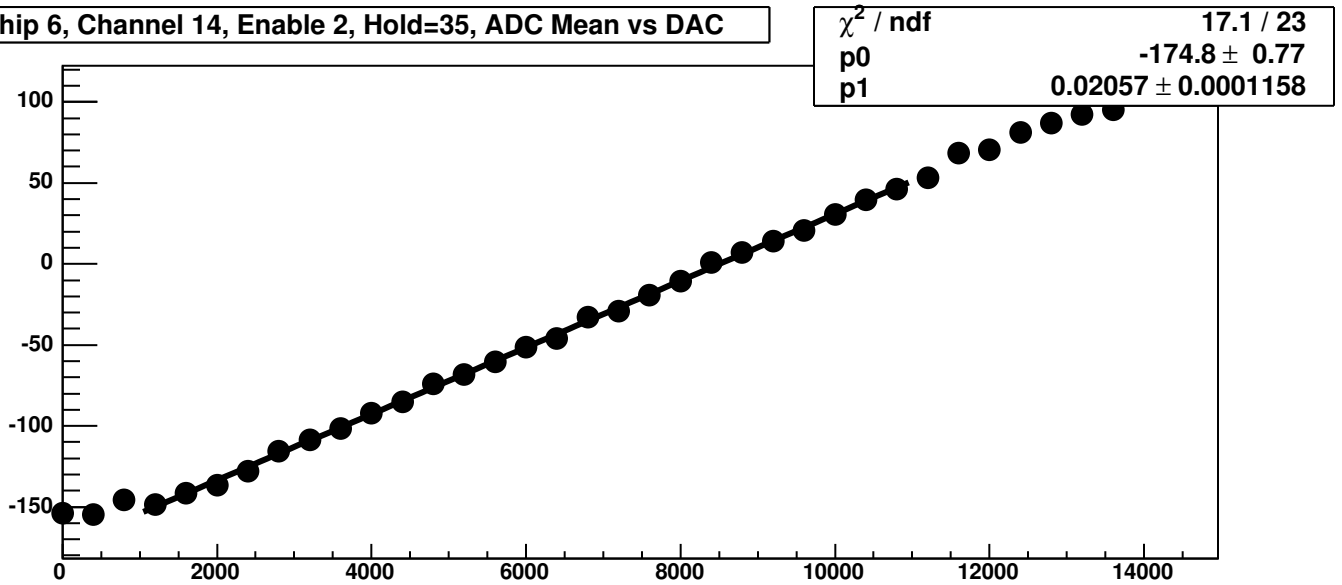
Chip 6, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC



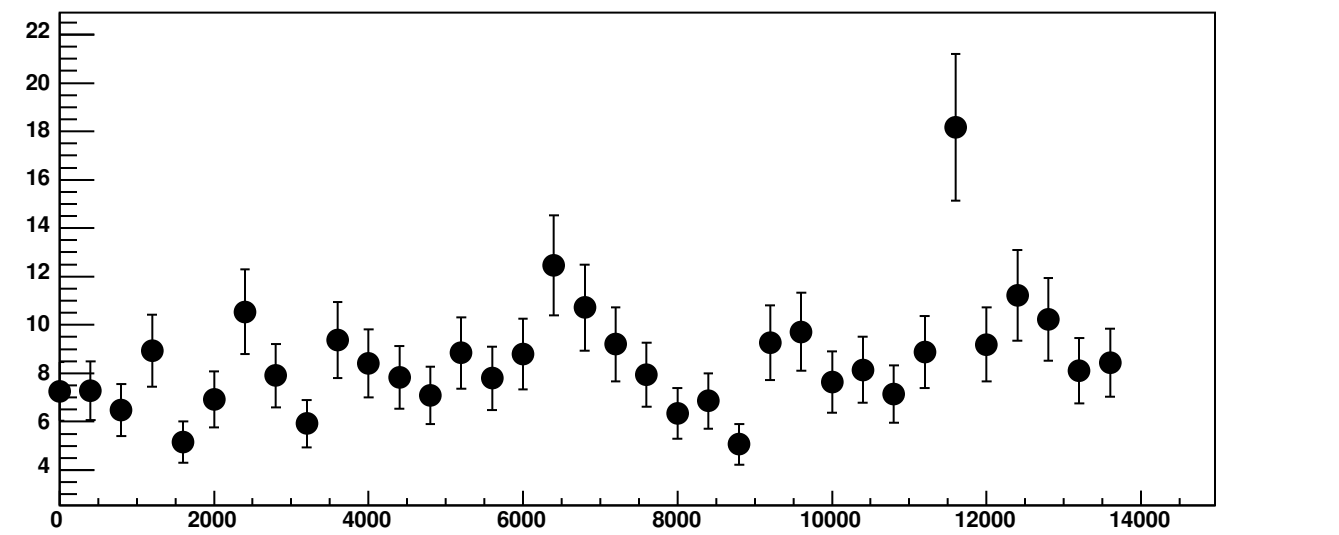
Chip 6, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC



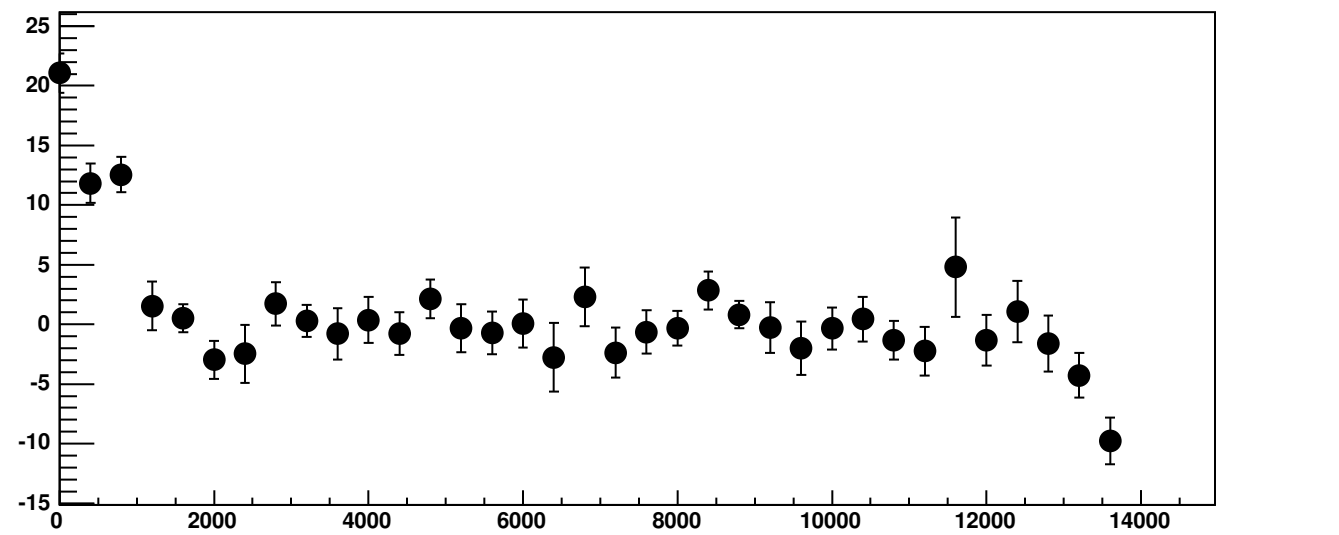
Chip 6, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC



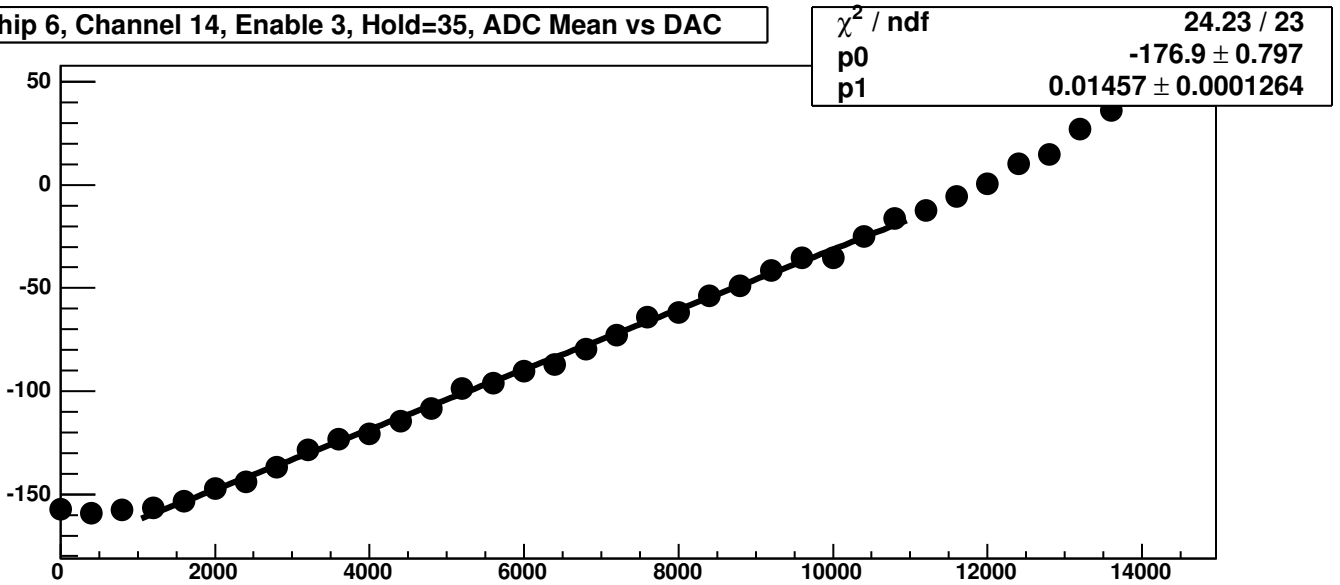
Chip 6, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC



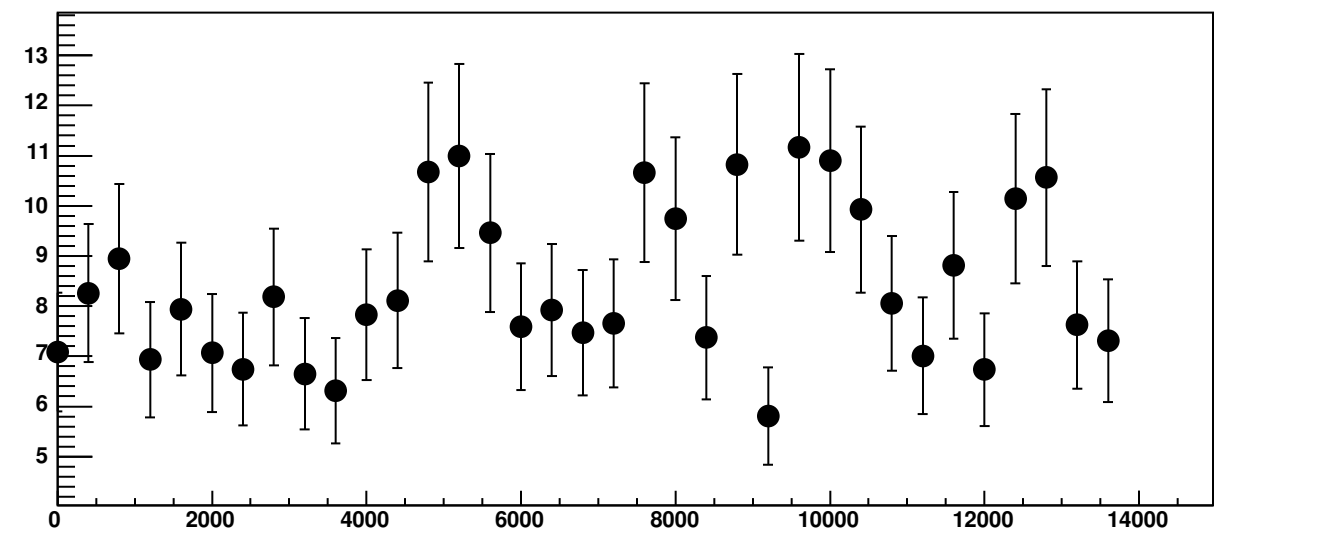
Chip 6, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC



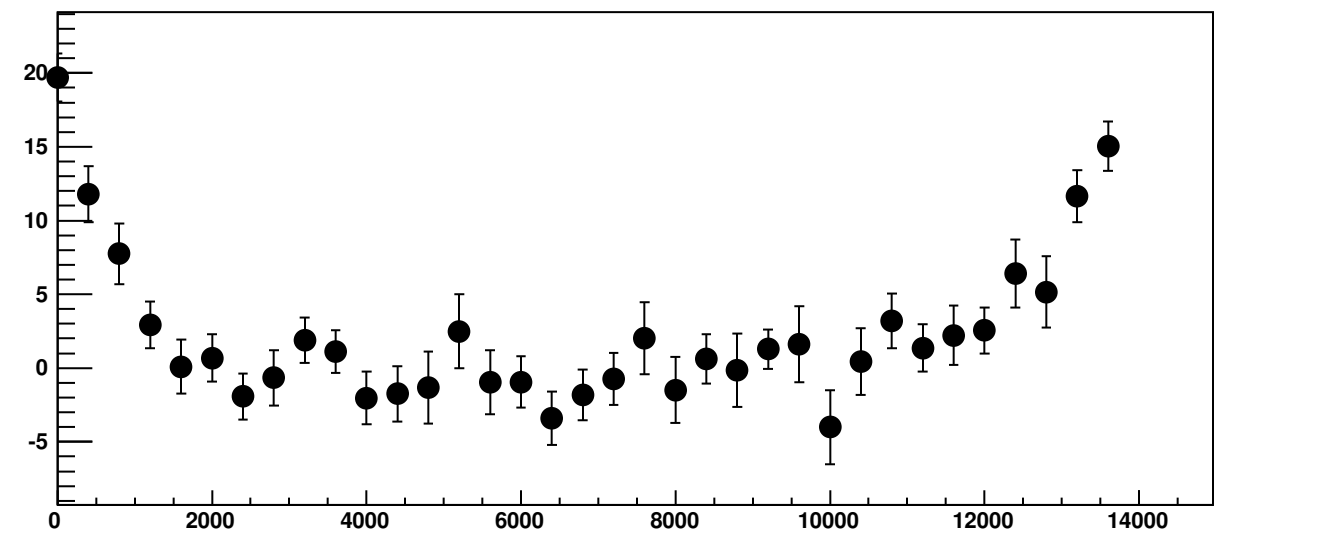
Chip 6, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC



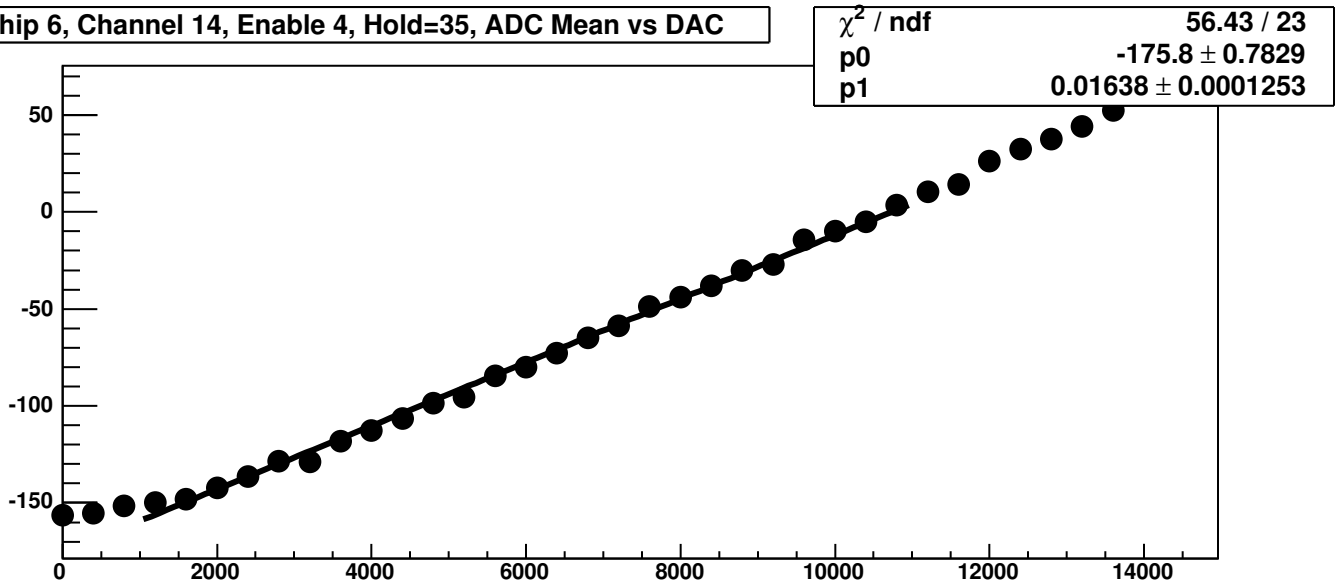
Chip 6, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC



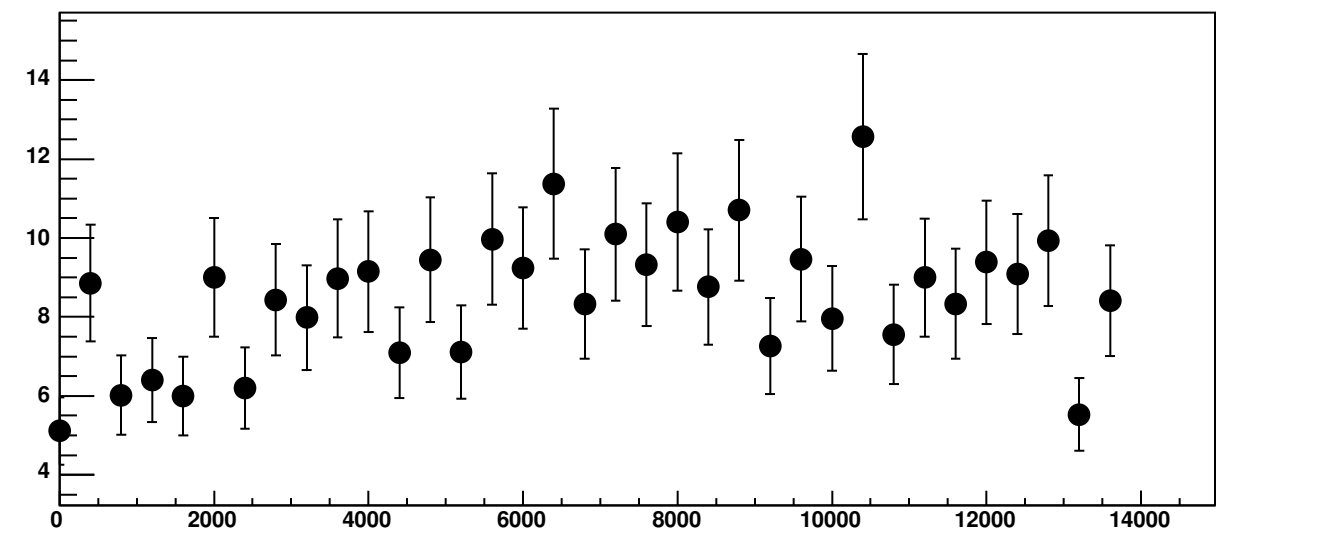
Chip 6, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC



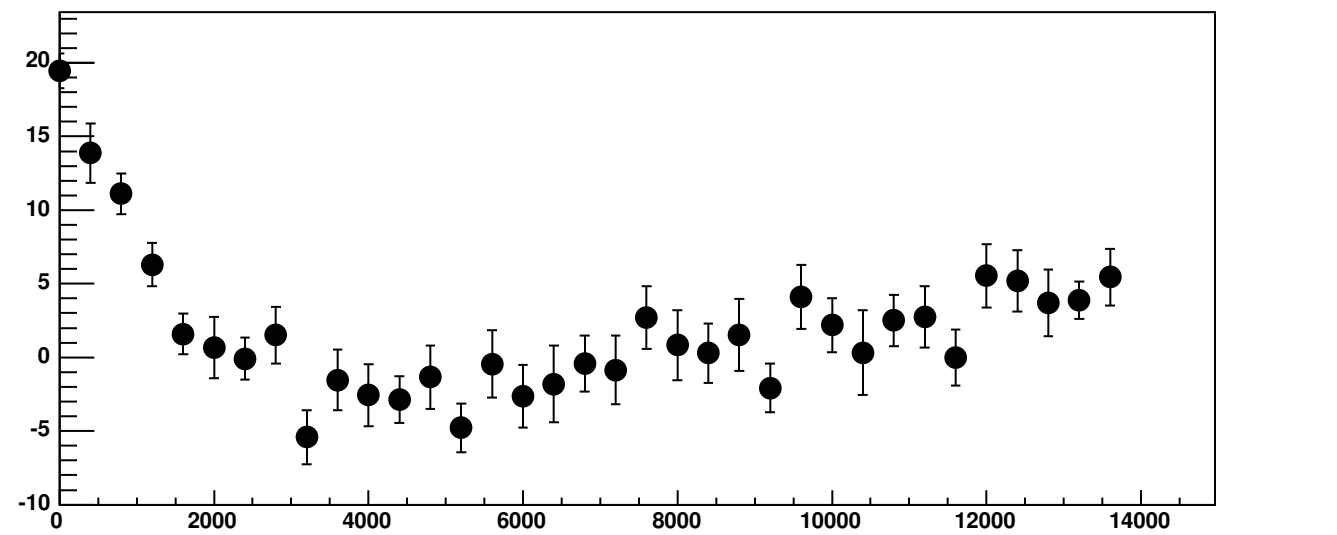
Chip 6, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC

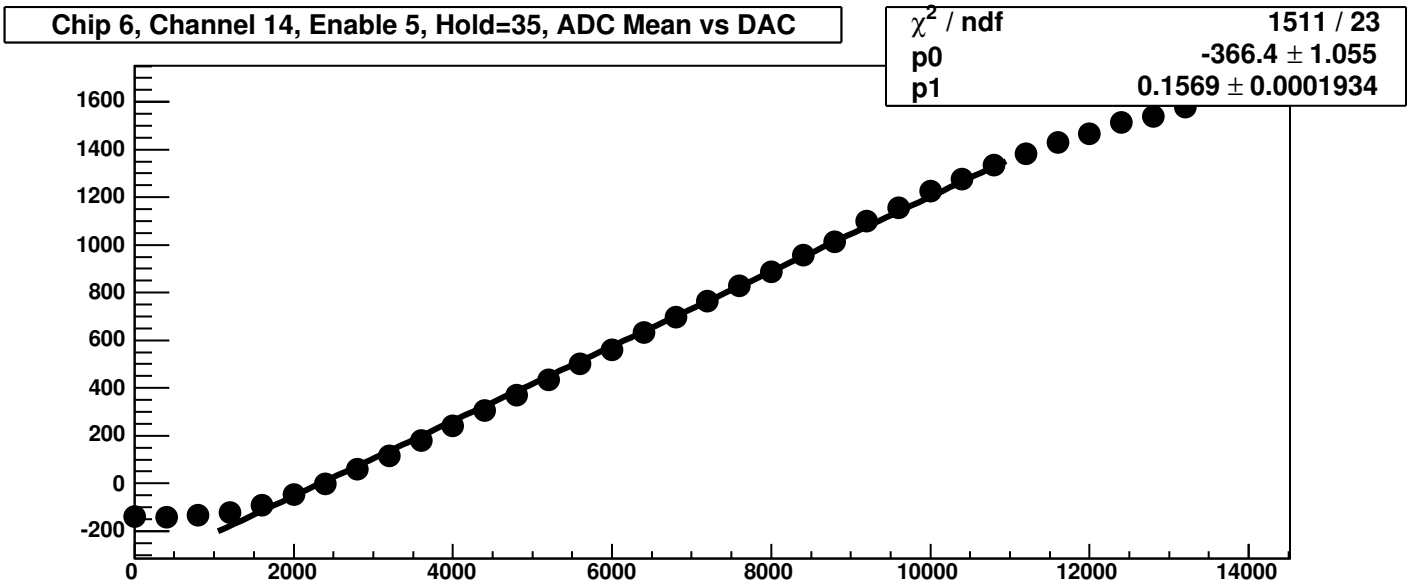


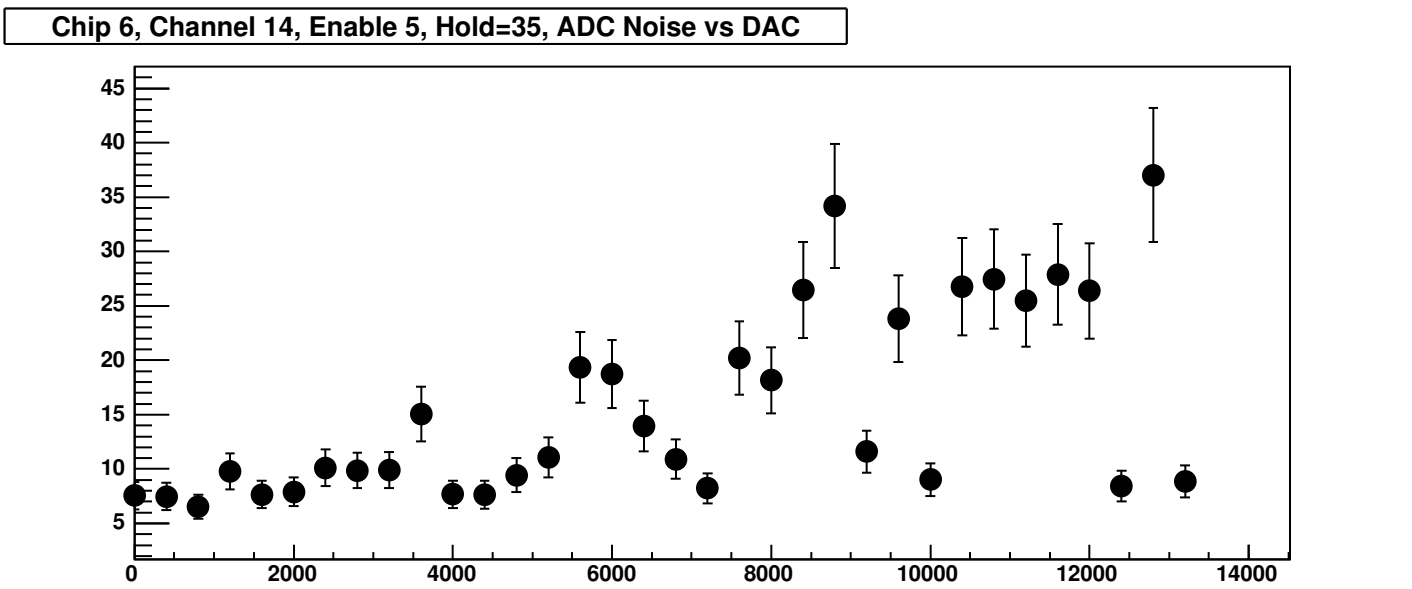
Chip 6, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC

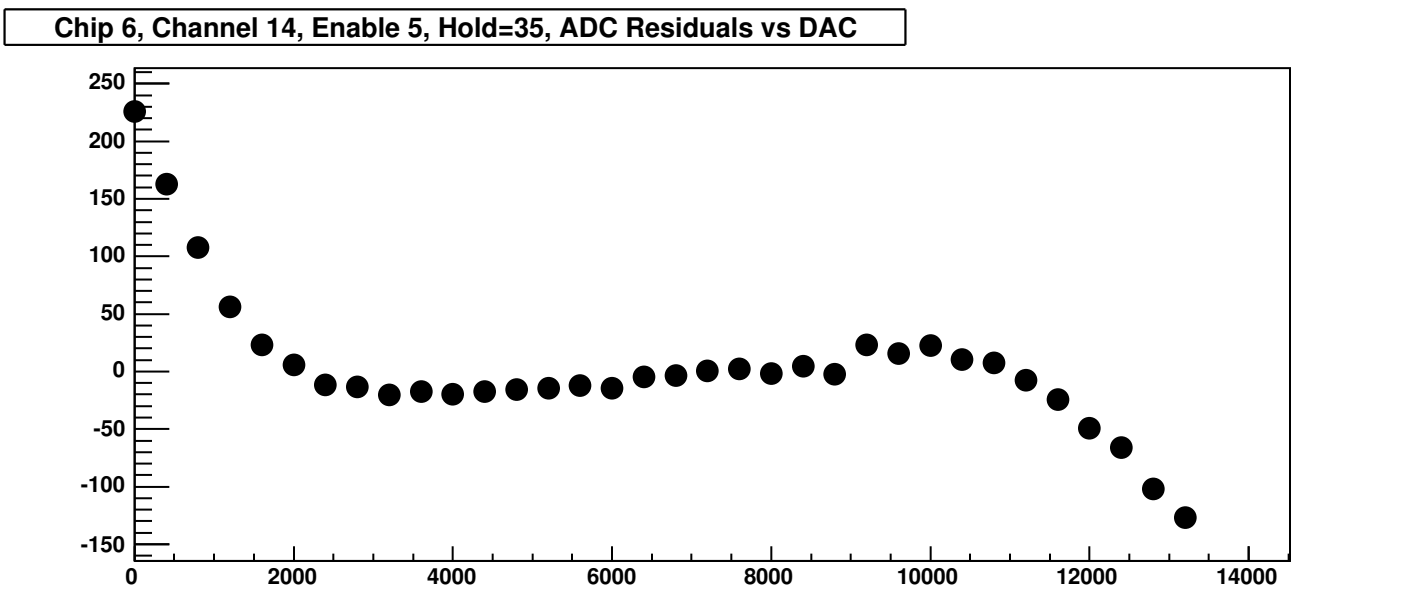


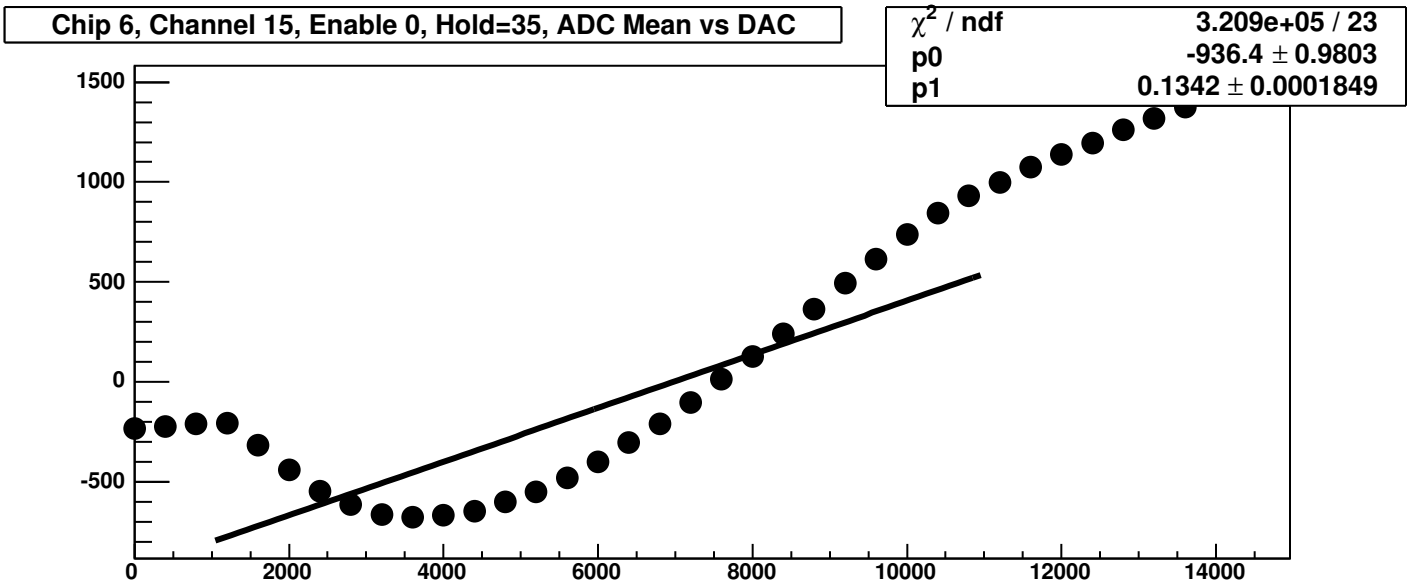
Chip 6, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC

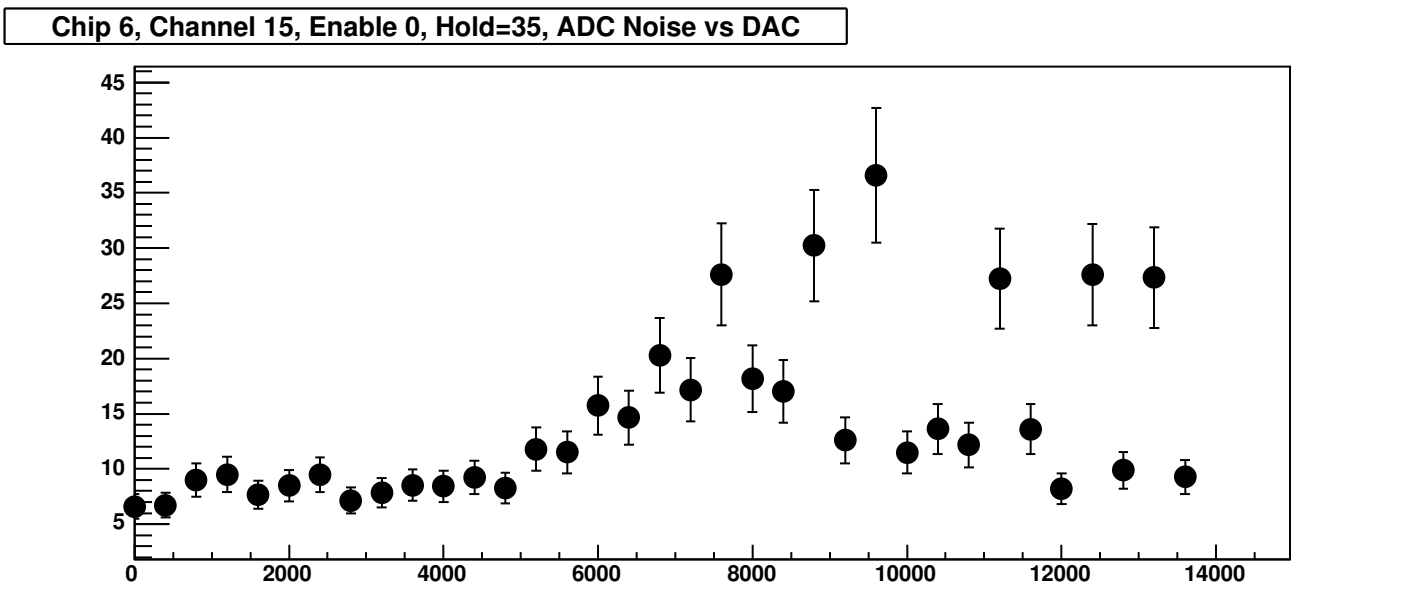


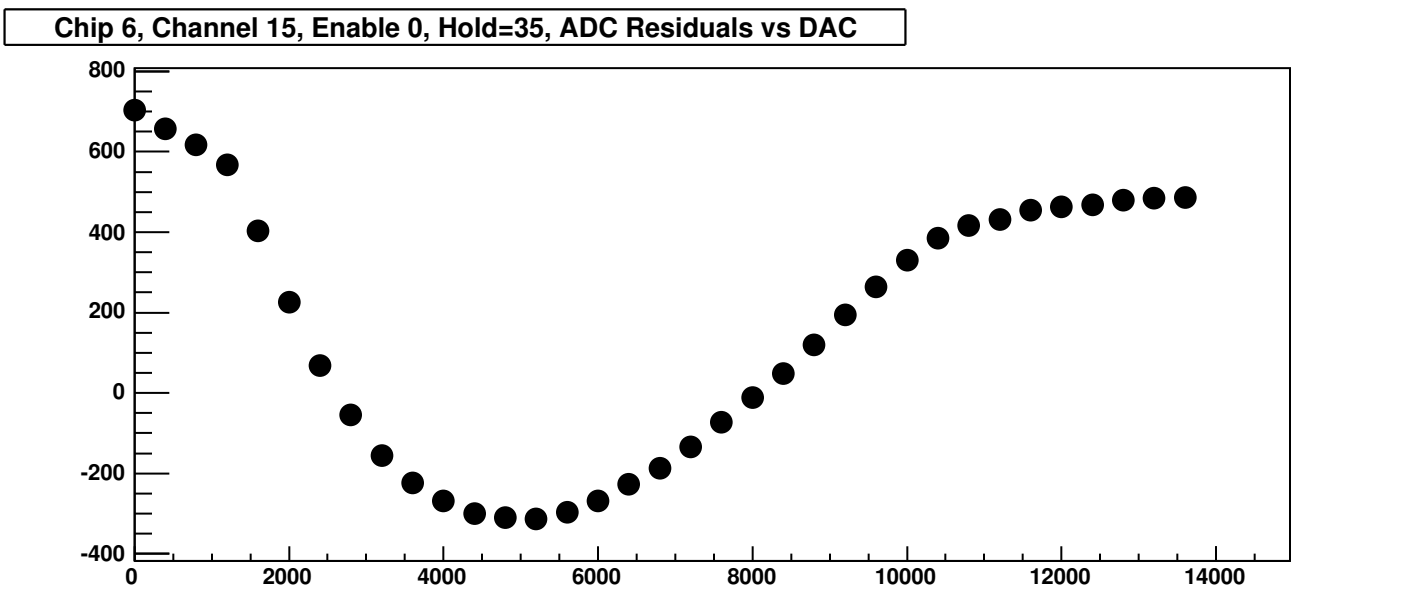

Chip 6, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1511 / 23
p0 -366.4 ± 1.055
p1 0.1569 ± 0.0001934
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

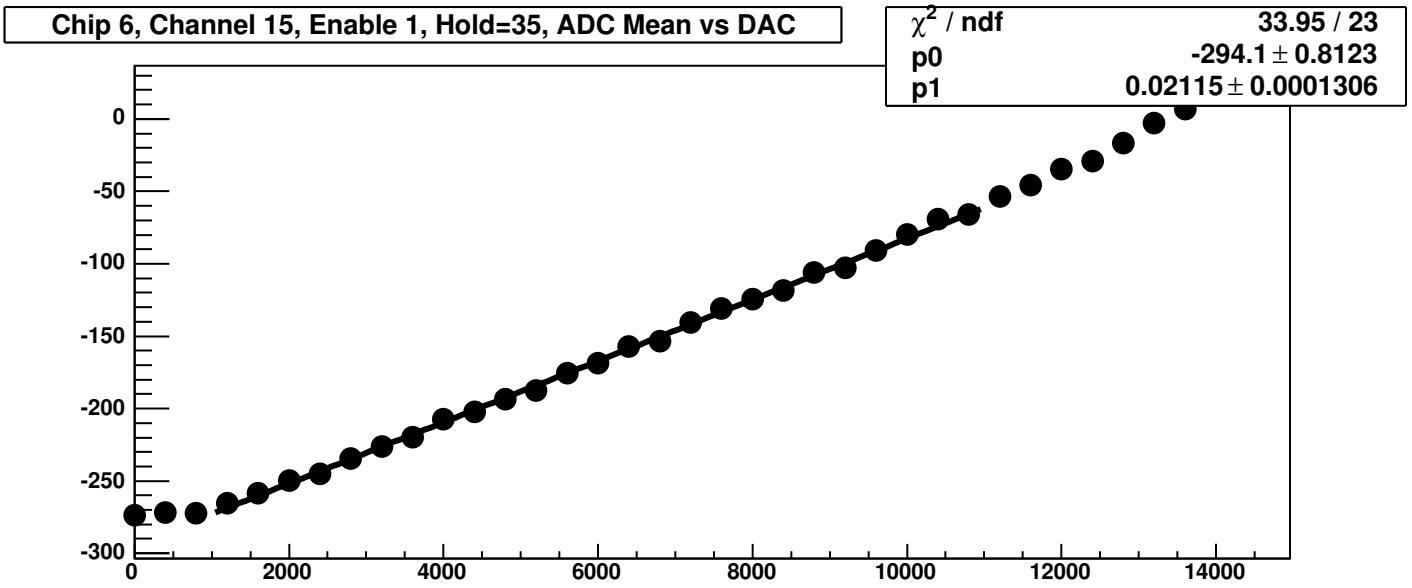

Chip 6, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

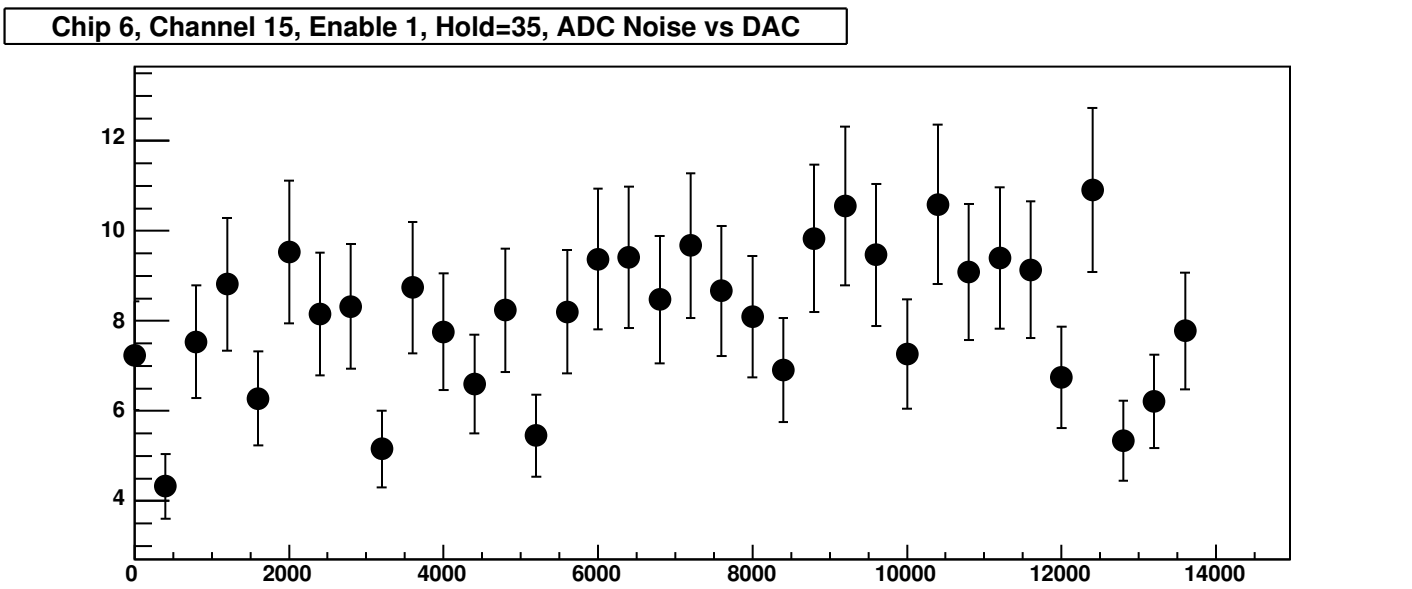

Chip 6, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

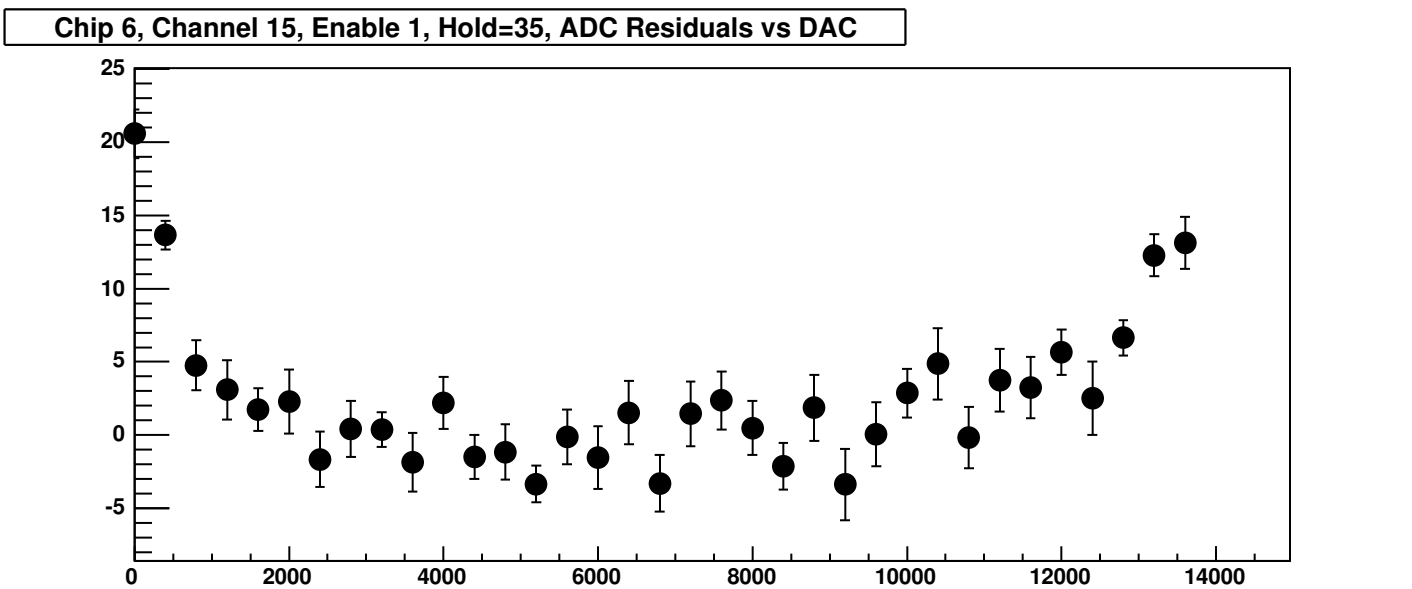

Chip 6, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.209e+05 / 23
p0 -936.4 ± 0.9803
p1 0.1342 ± 0.0001849
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

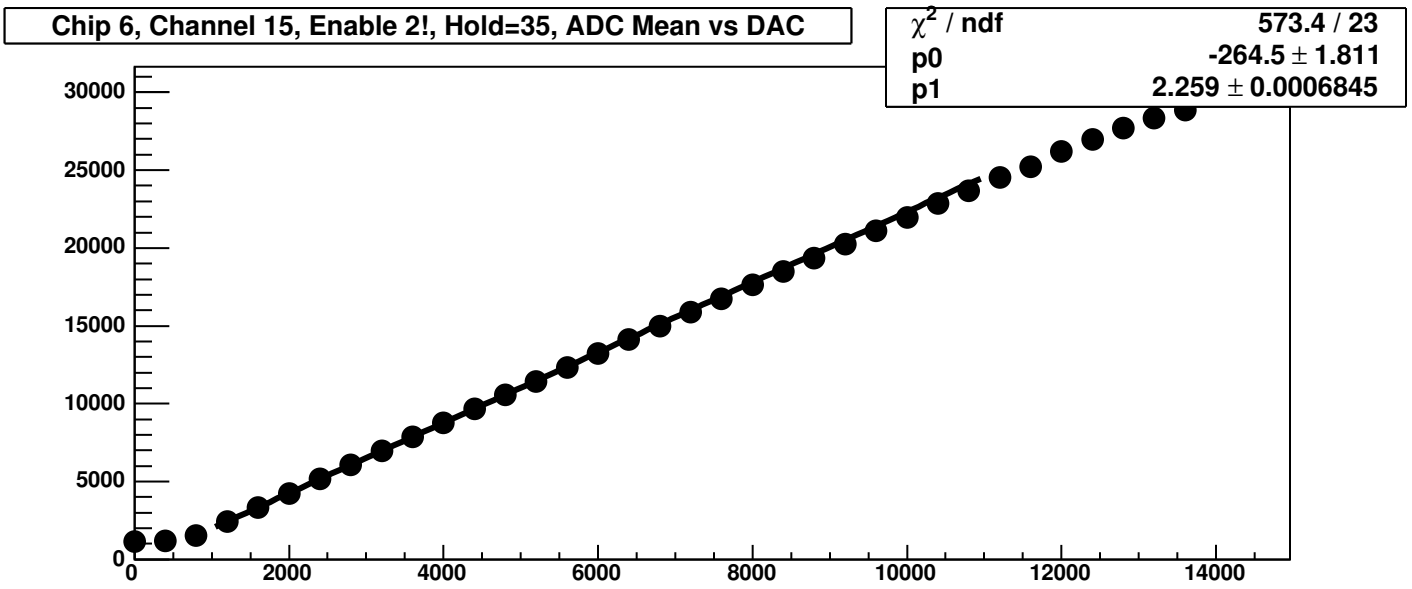

Chip 6, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

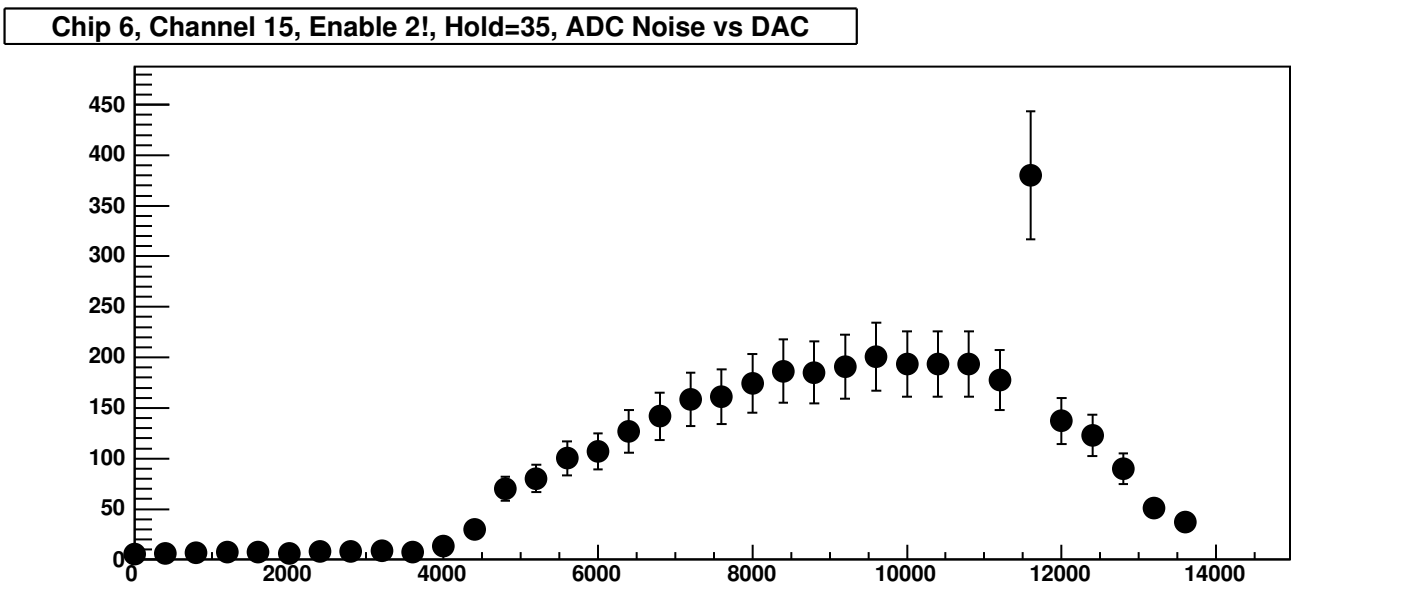

Chip 6, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

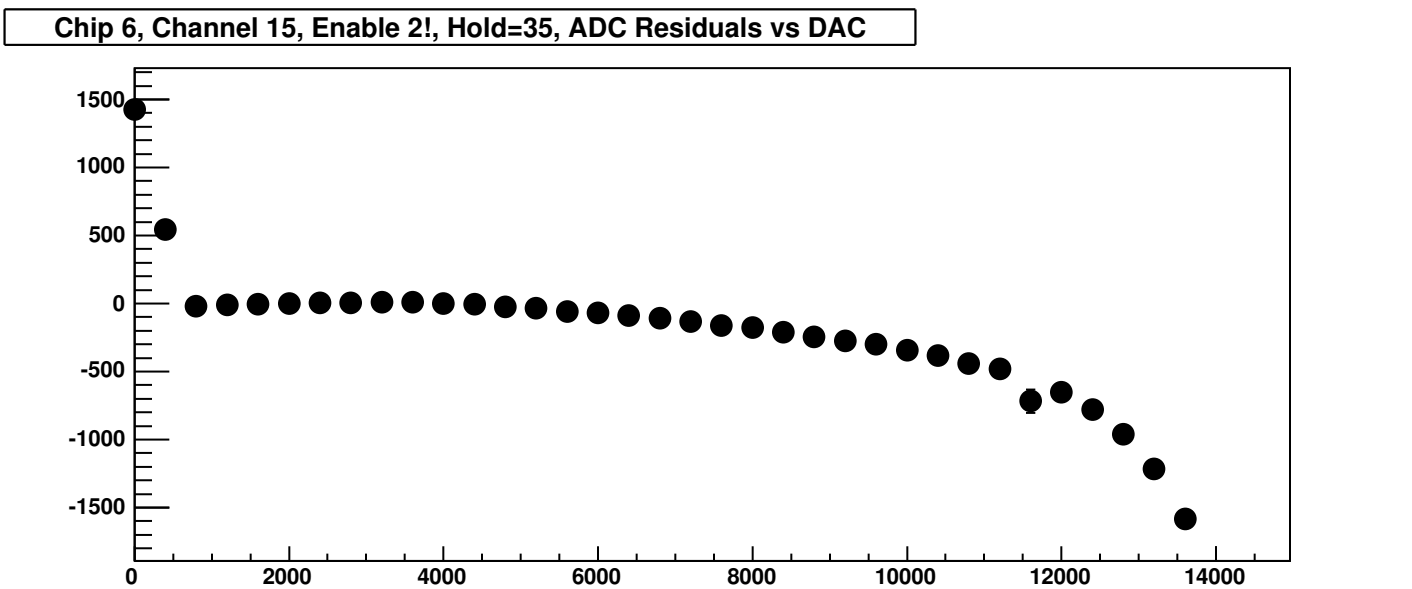

Chip 6, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.95 / 23
p0 -294.1 ± 0.8123
p1 0.02115 ± 0.0001306
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

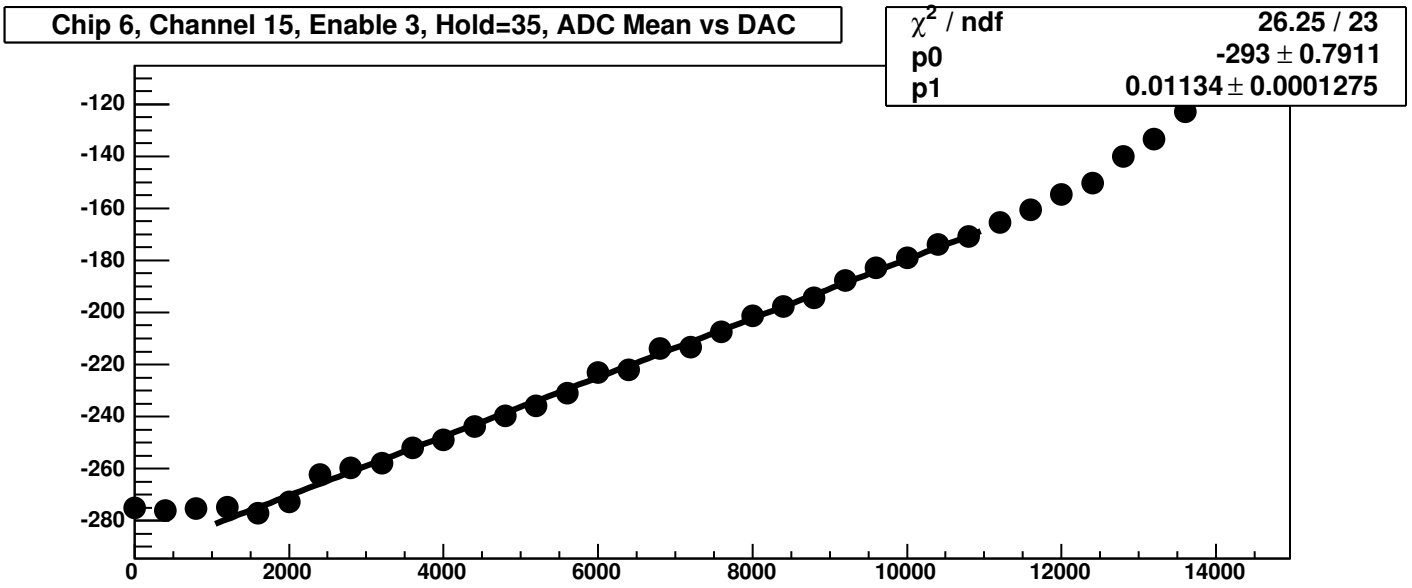

Chip 6, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

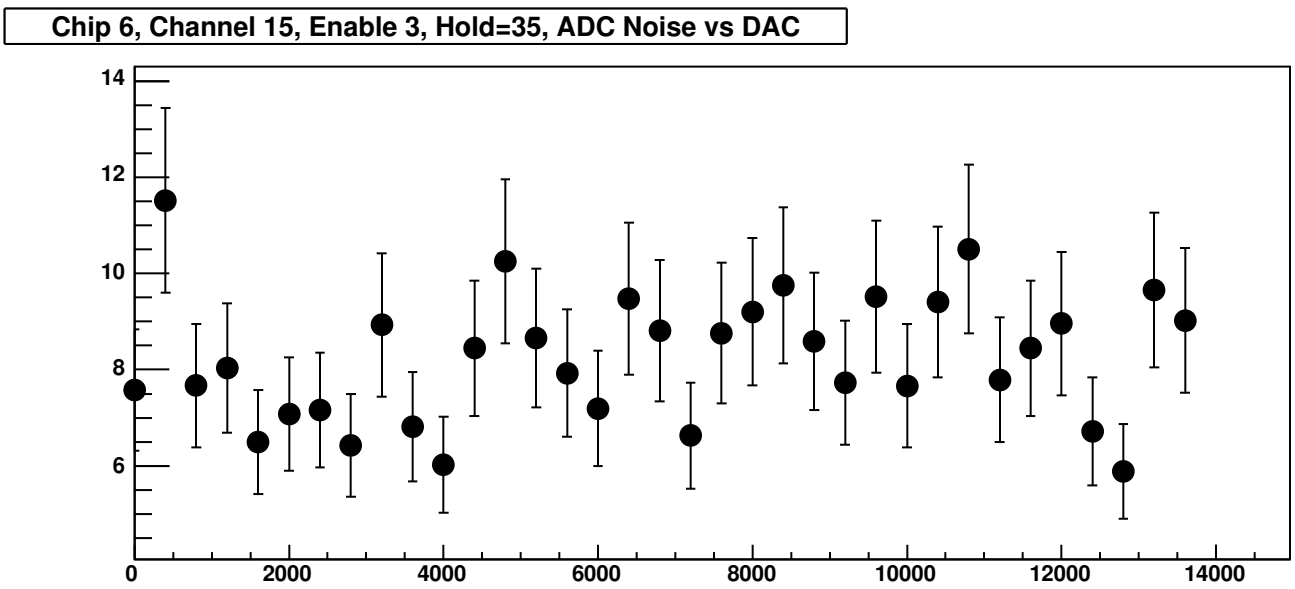

Chip 6, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

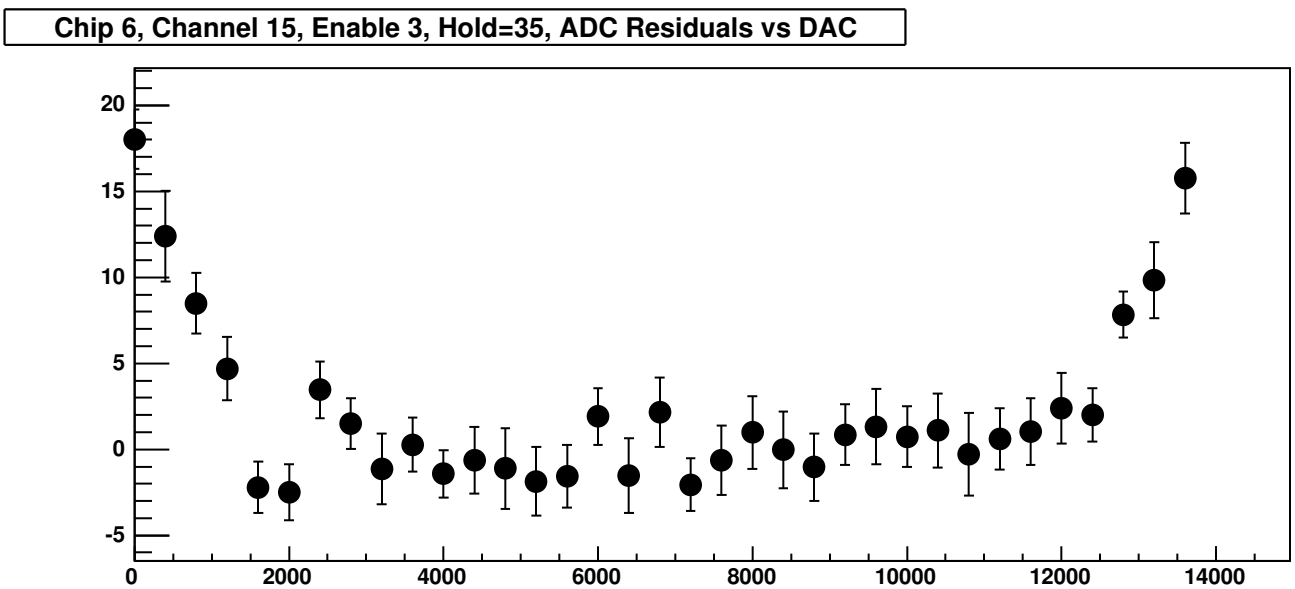

Chip 6, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 573.4 / 23
p0 -264.5 ± 1.811
p1 2.259 ± 0.0006845
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

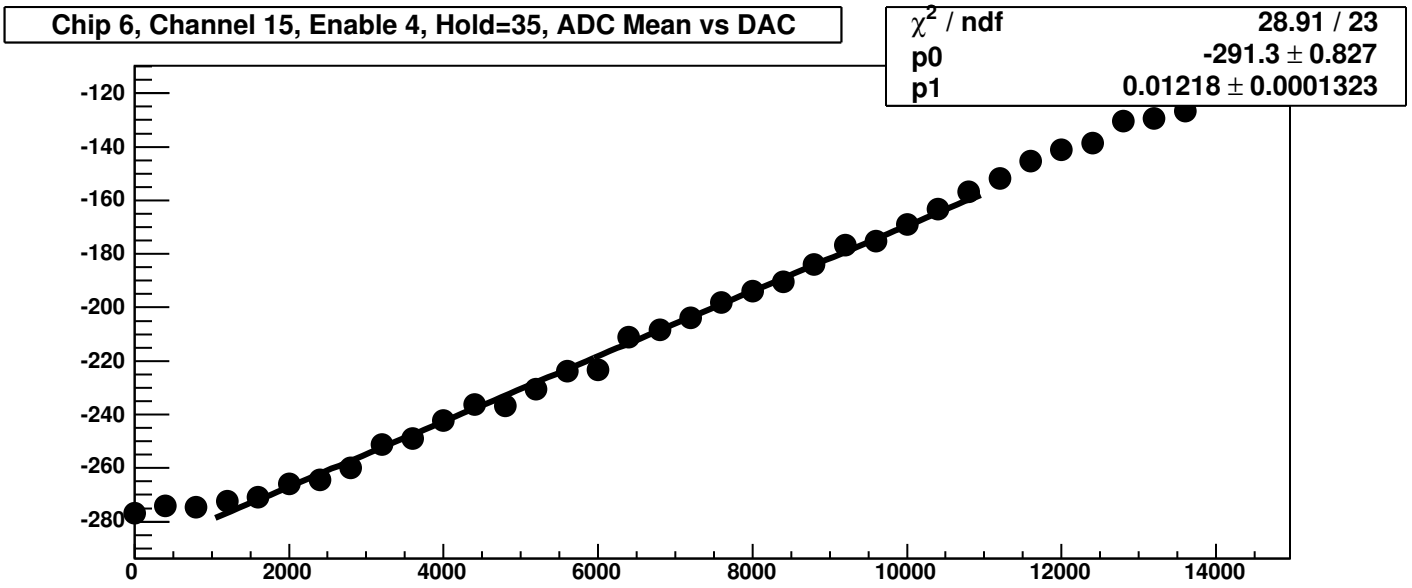

Chip 6, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

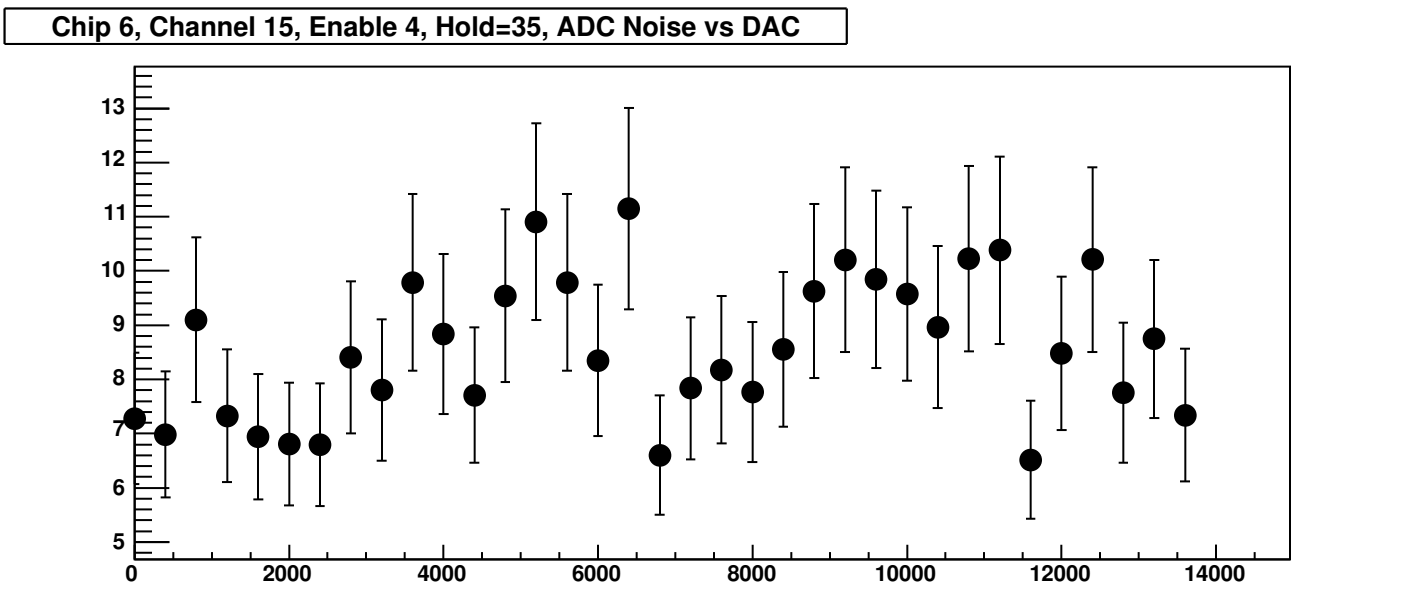

Chip 6, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

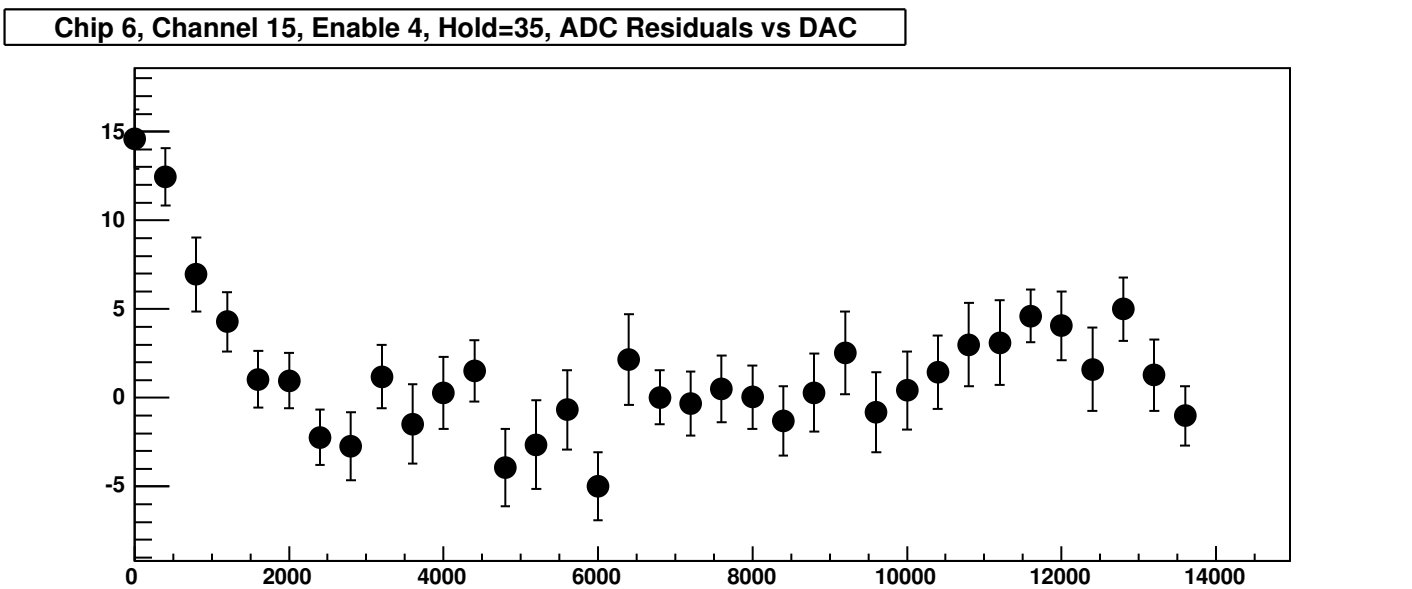

Chip 6, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.25 / 23
p0 -293 ± 0.7911
p1 0.01134 ± 0.0001275
-120
-140
-160
-180
-200
-220
-240
-260
-280
0
2000
4000
6000
8000
10000
12000
14000

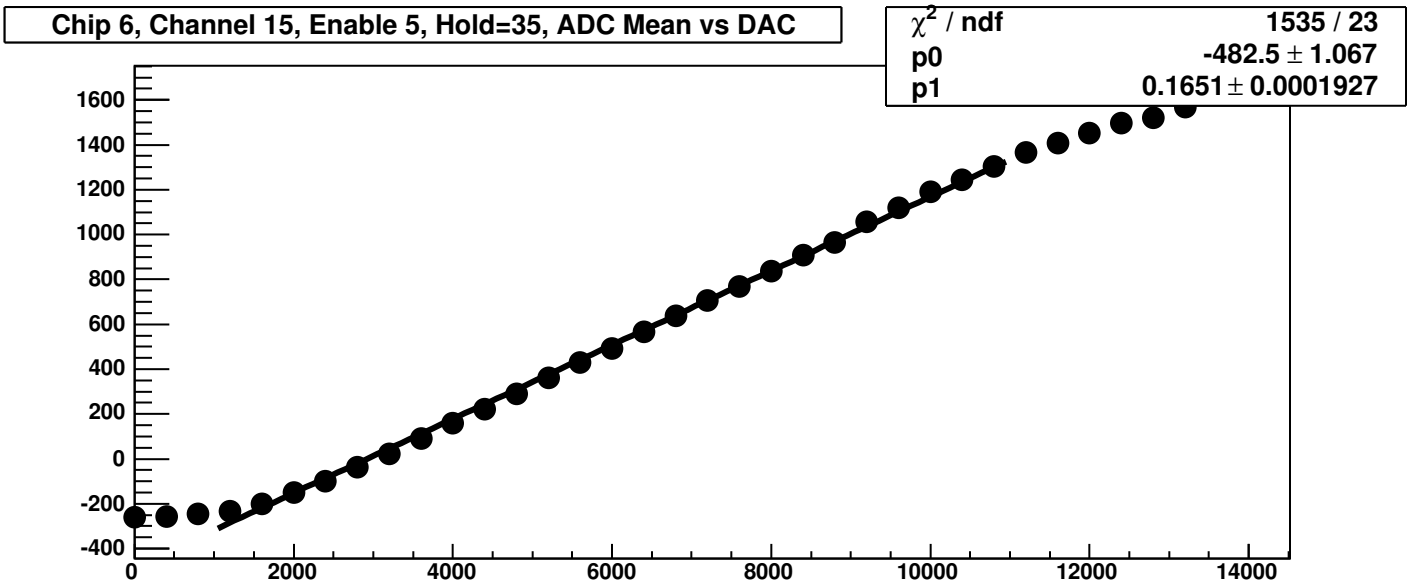

Chip 6, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

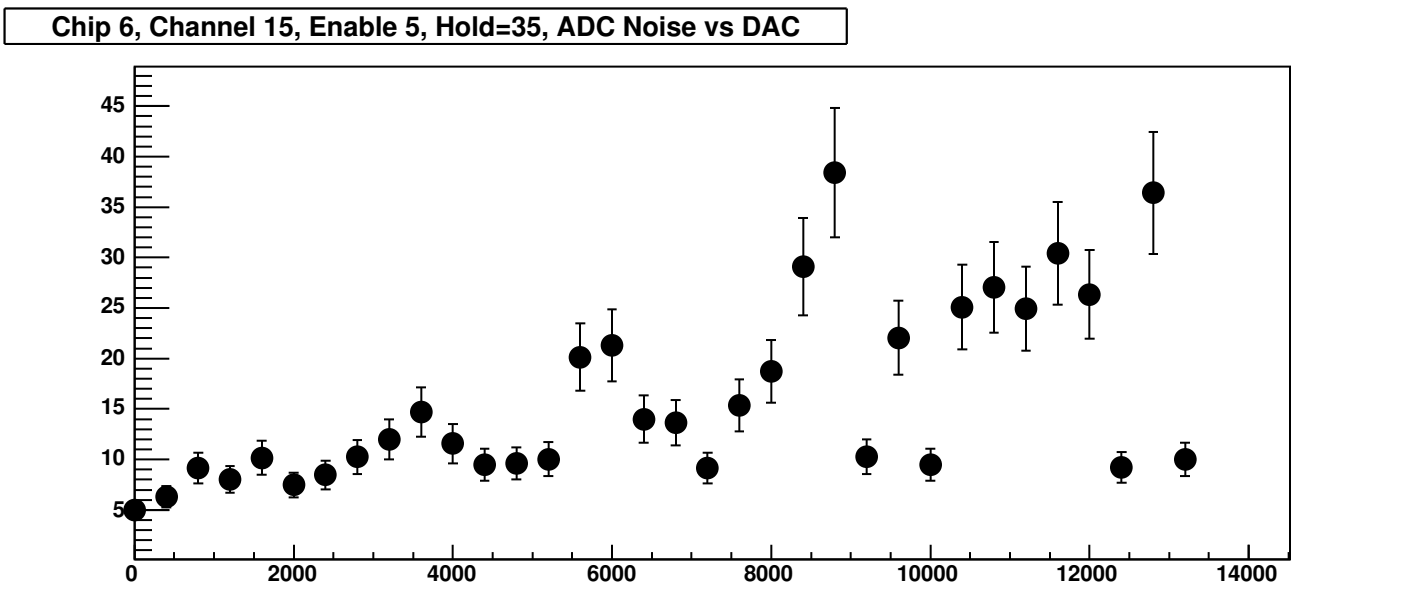

Chip 6, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

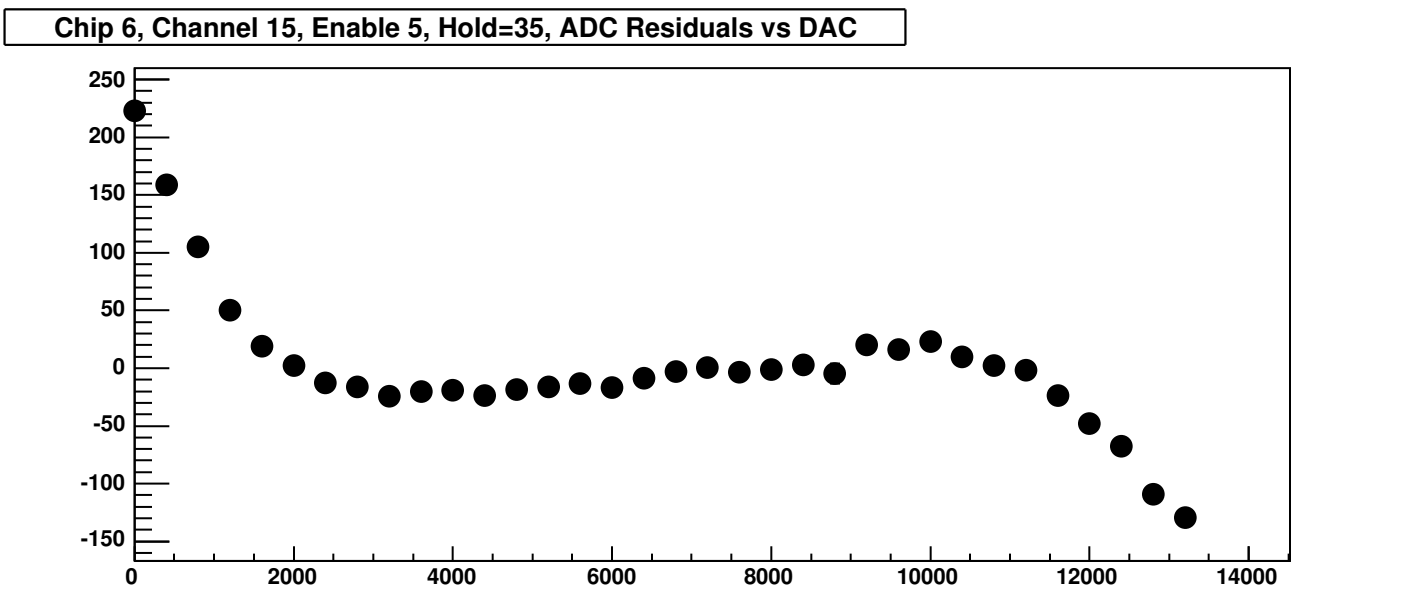

Chip 6, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 28.91 / 23
p0 -291.3 ± 0.827
p1 0.01218 ± 0.0001323
-120
-140
-160
-180
-200
-220
-240
-260
-280
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

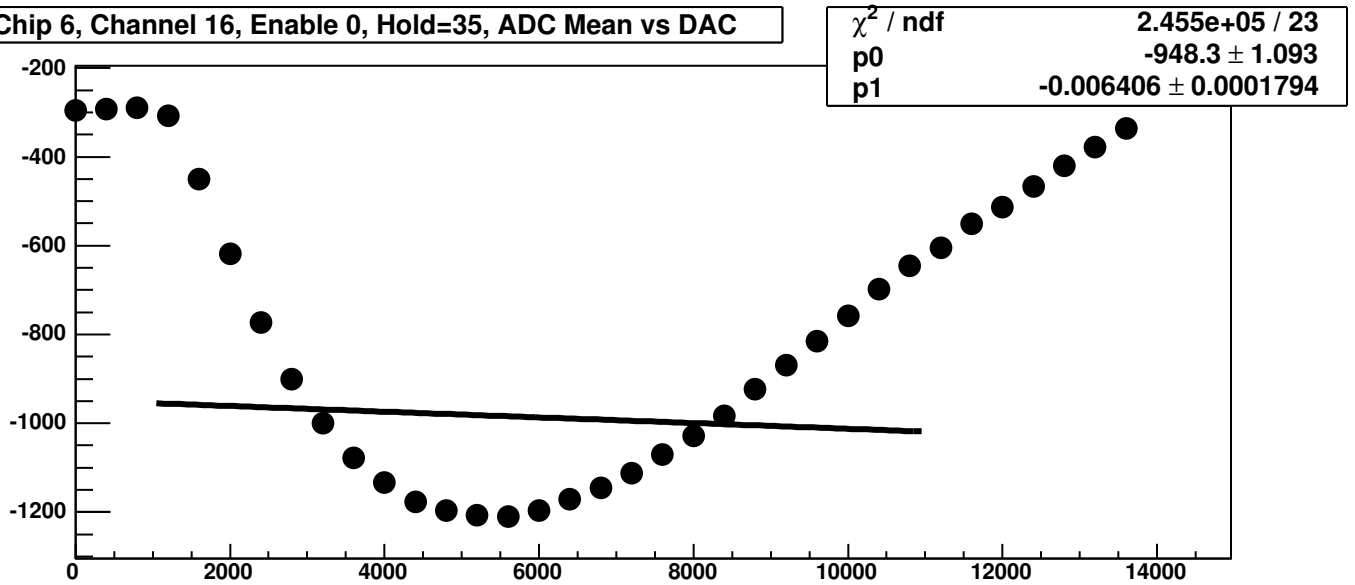

Chip 6, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1535 / 23
p0 -482.5 ± 1.067
p1 0.1651 ± 0.0001927
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

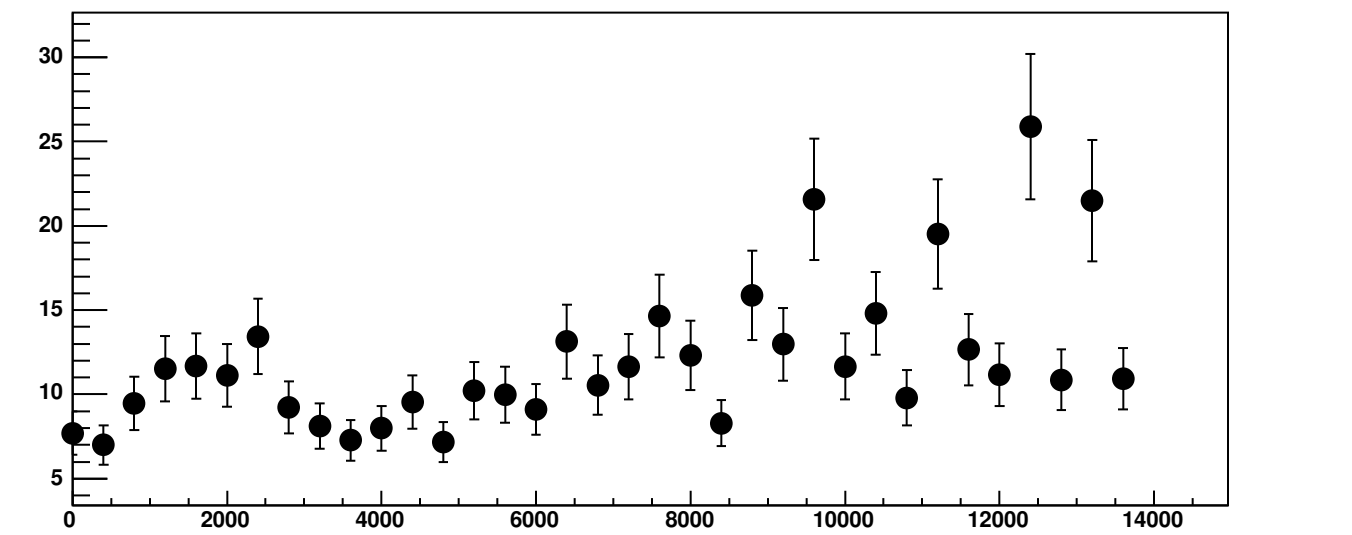

Chip 6, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

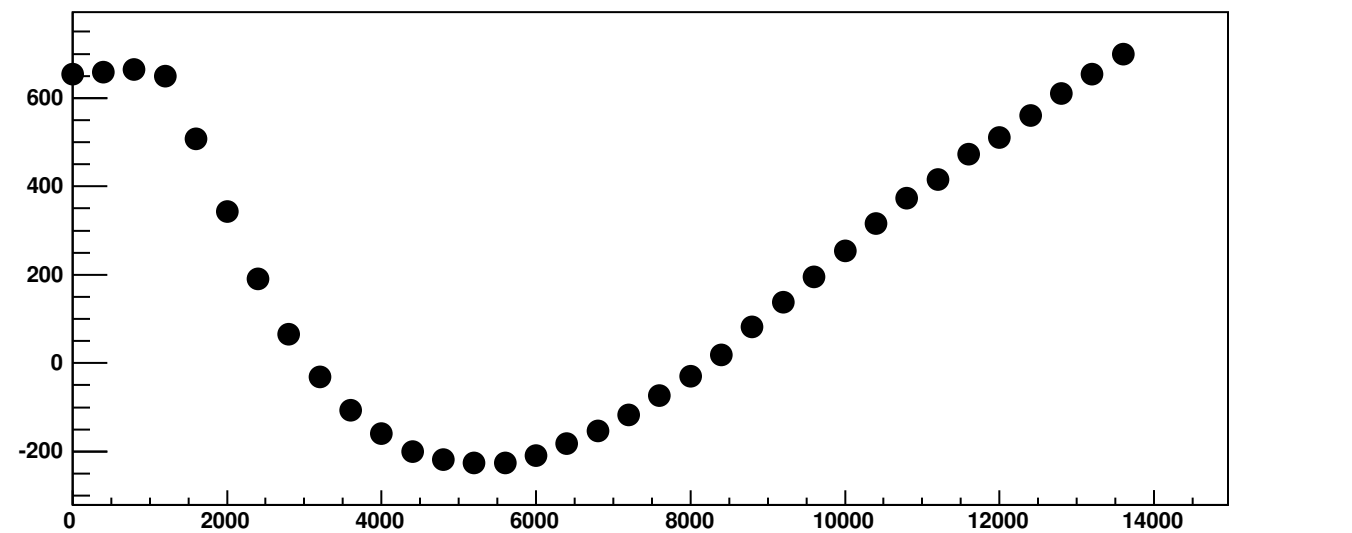
Chip 6, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

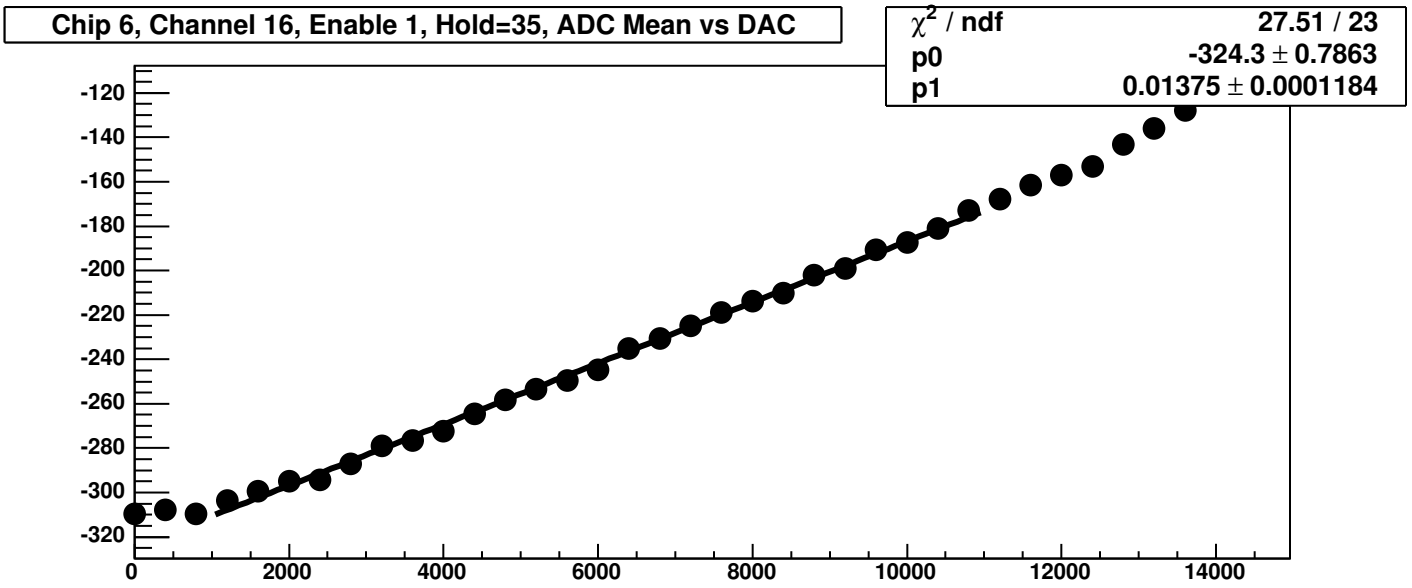


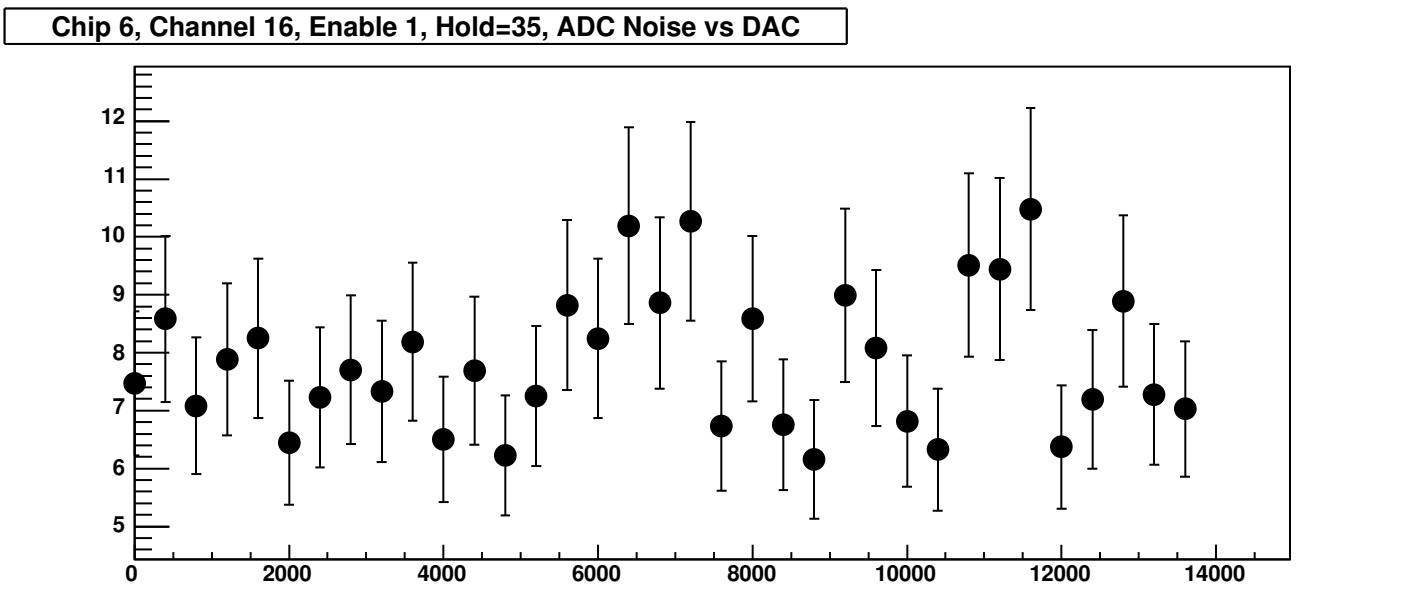
Chip 6, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC

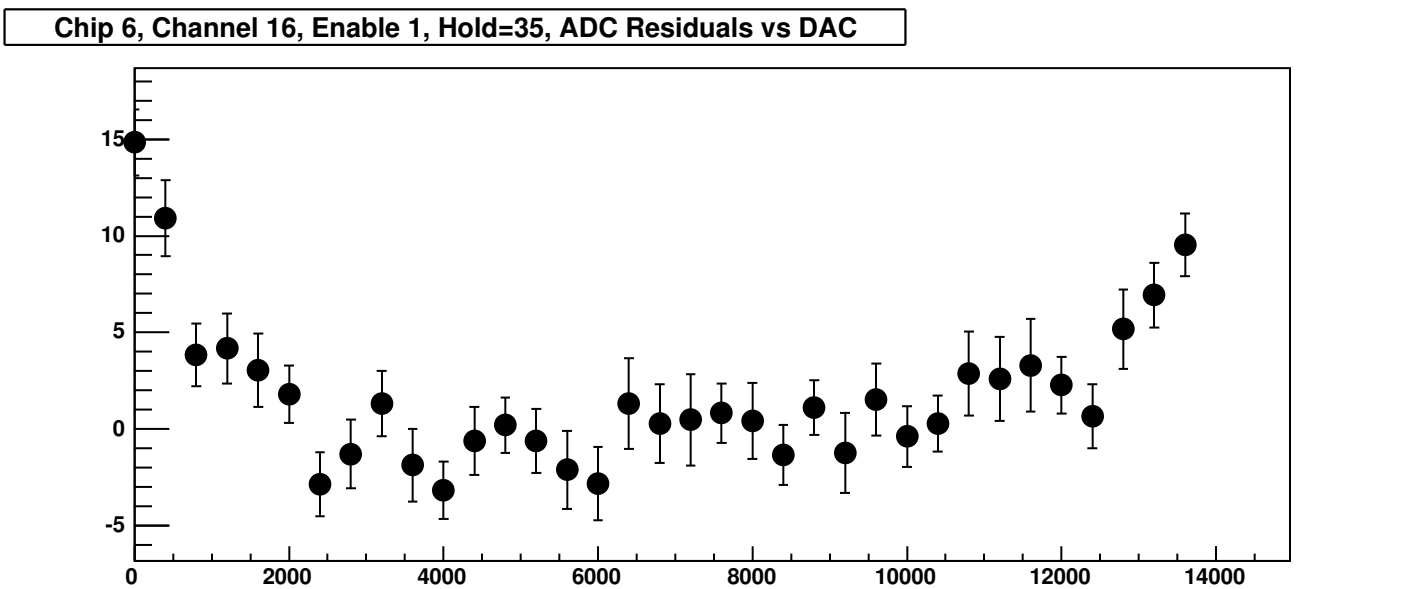


Chip 6, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

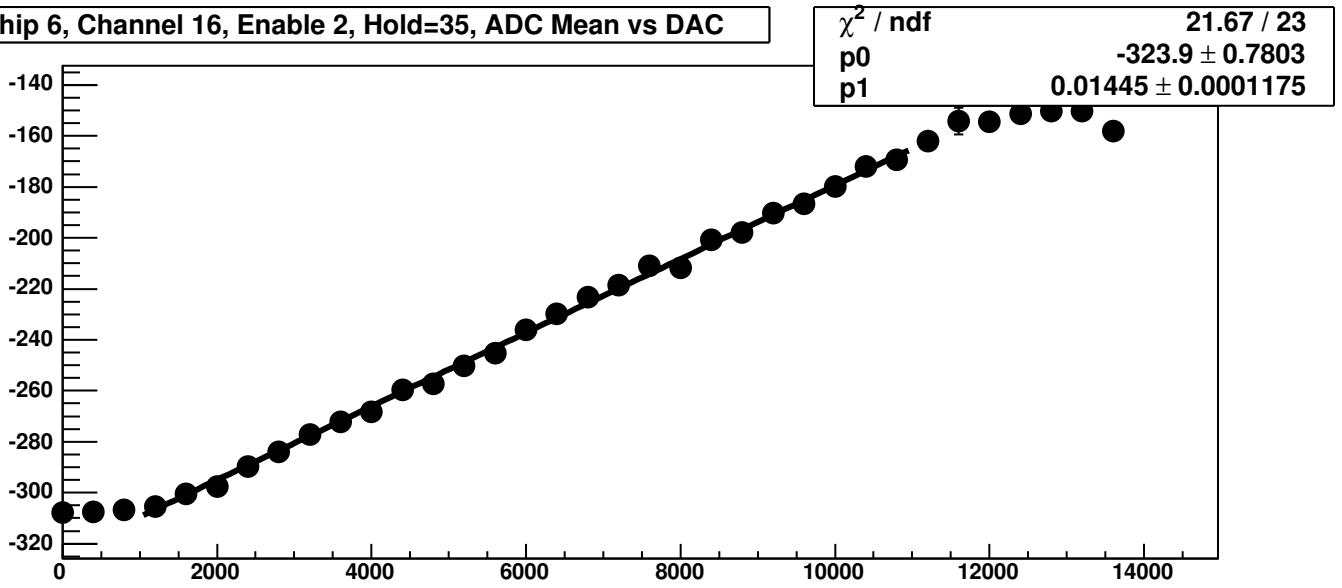



Chip 6, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 27.51 / 23
p0 -324.3 ± 0.7863
p1 0.01375 ± 0.0001184
-120
-140
-160
-180
-200
-220
-240
-260
-280
-300
-320
0
2000
4000
6000
8000
10000
12000
14000

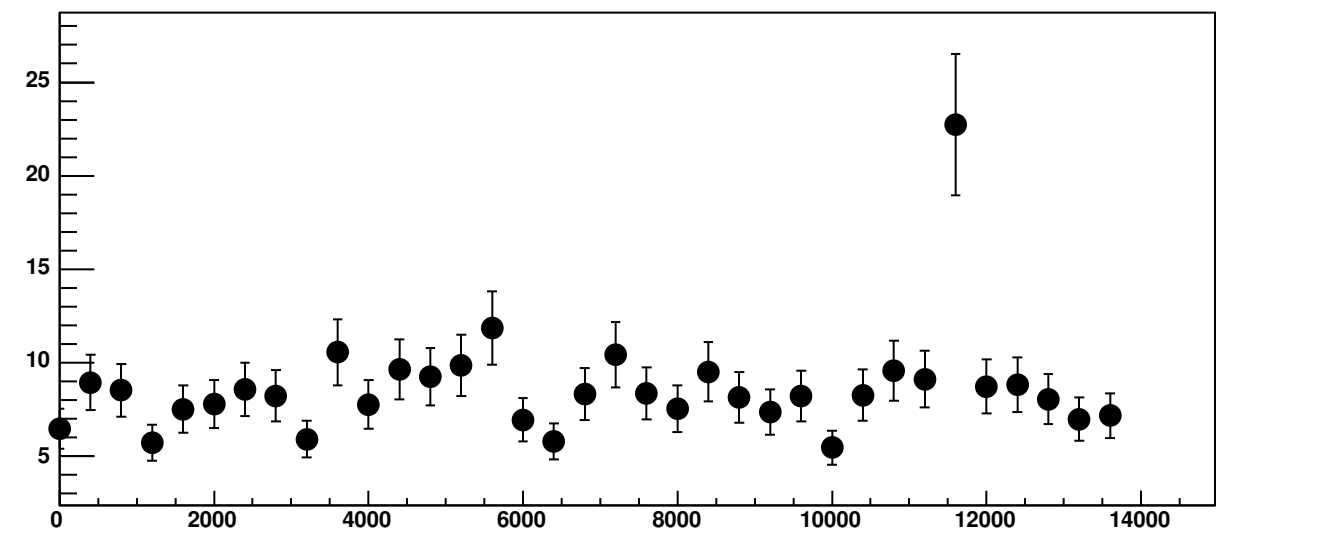

Chip 6, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

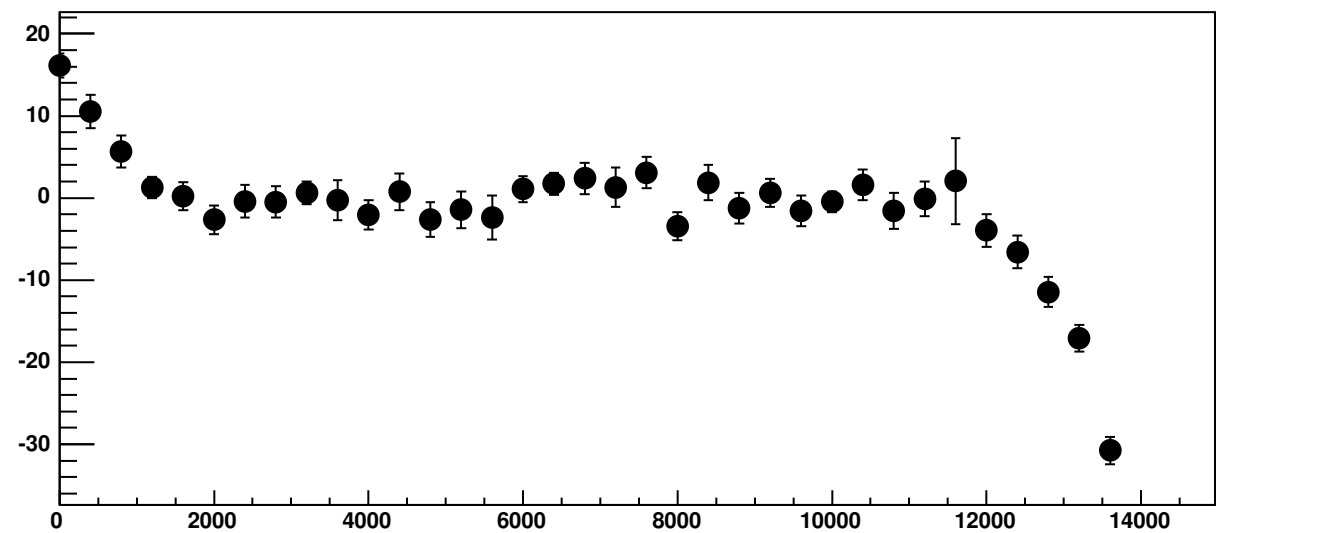
Chip 6, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

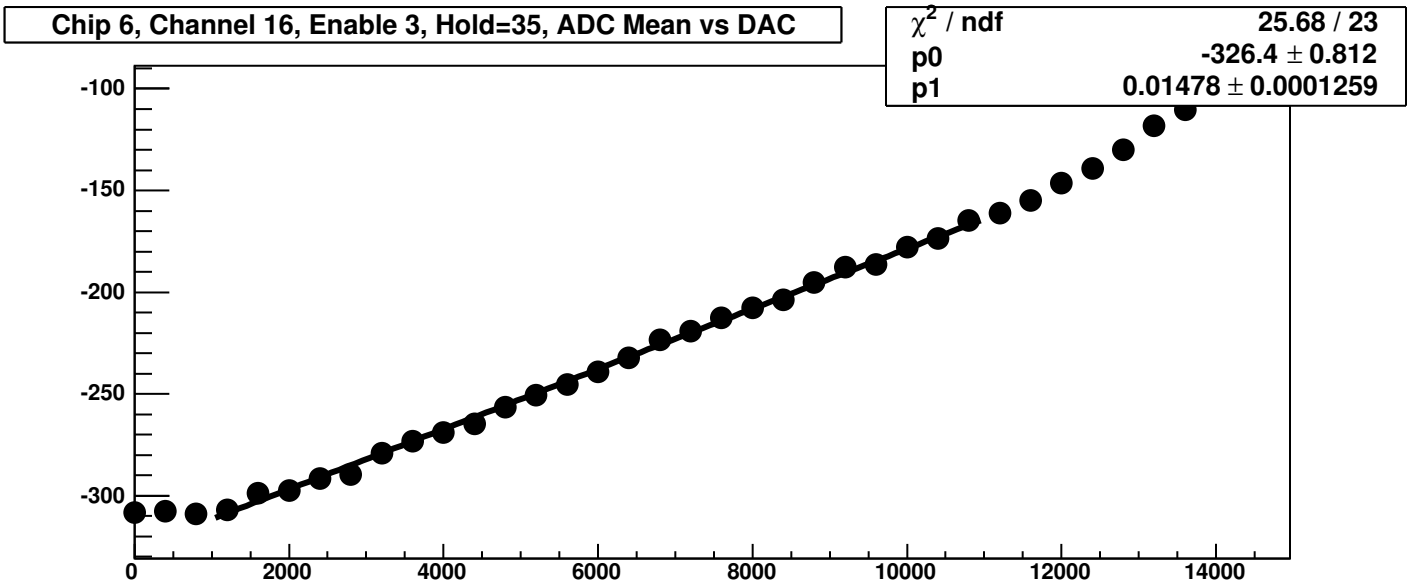


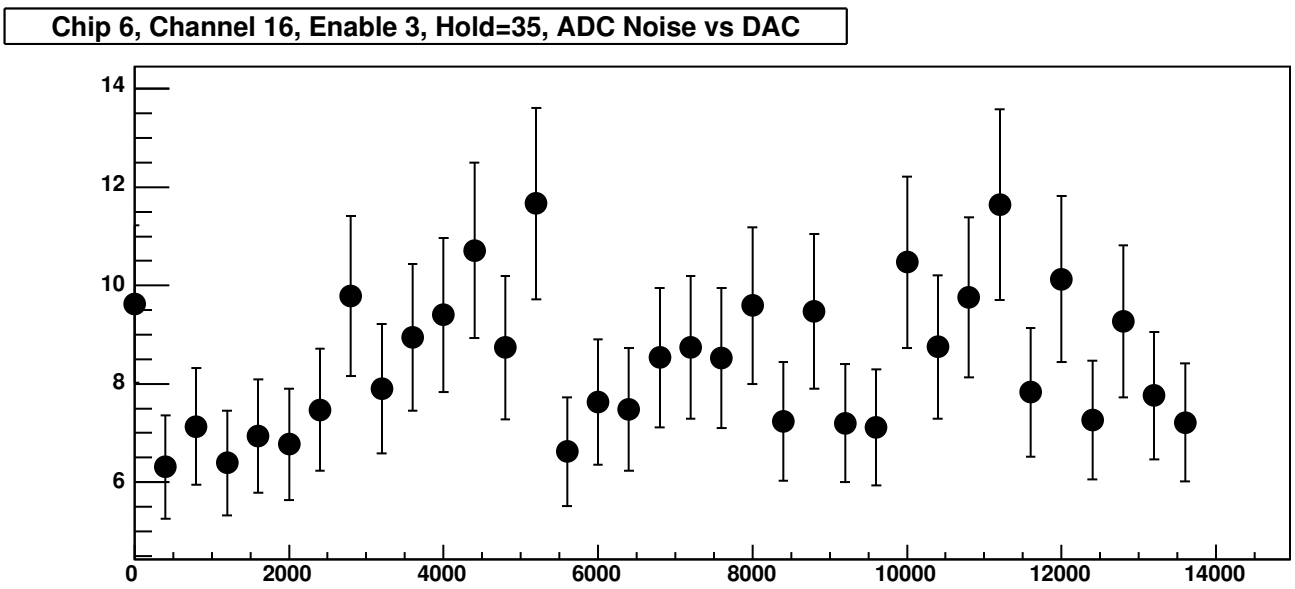
Chip 6, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC

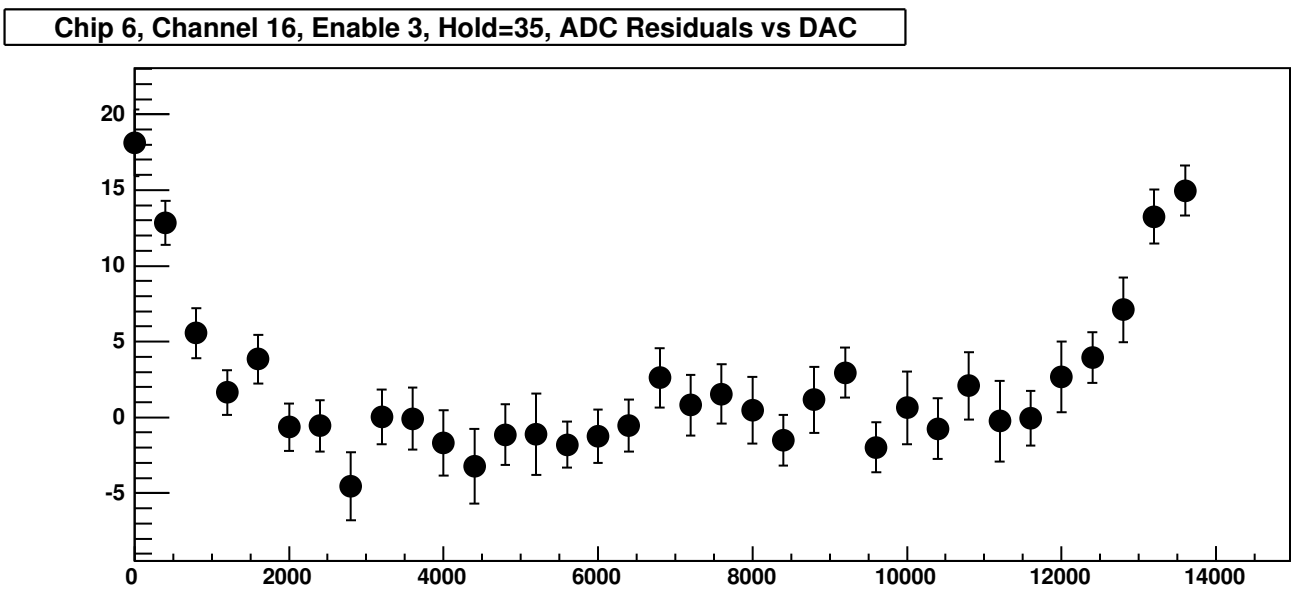


Chip 6, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

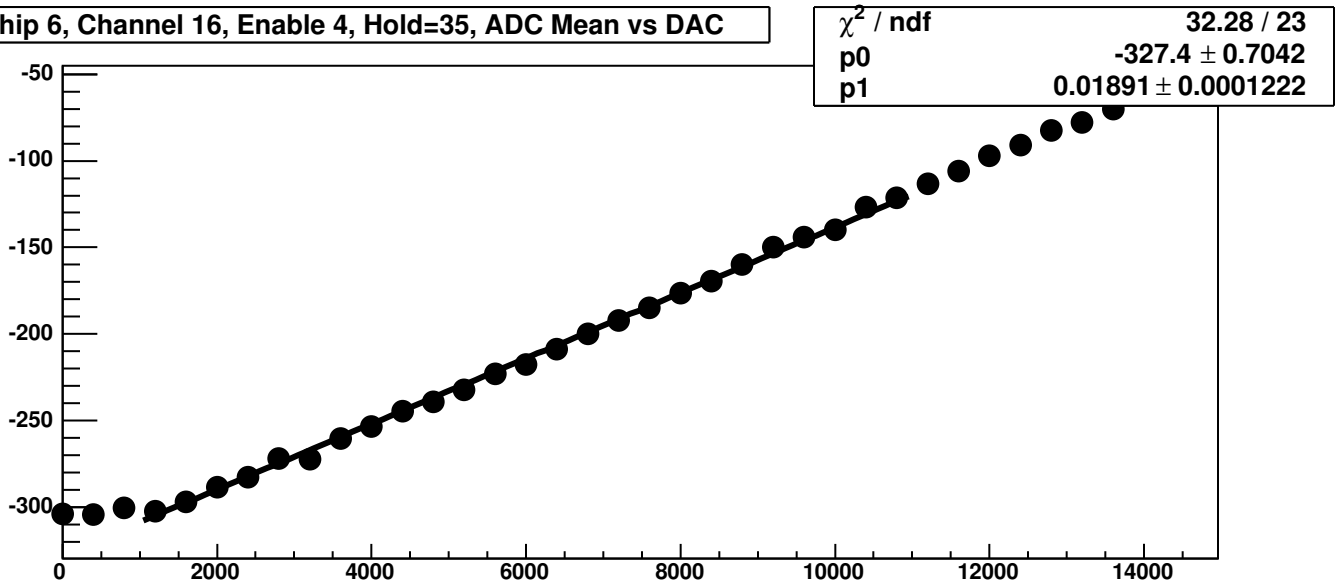



Chip 6, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.68 / 23
p0 -326.4 ± 0.812
p1 0.01478 ± 0.0001259
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

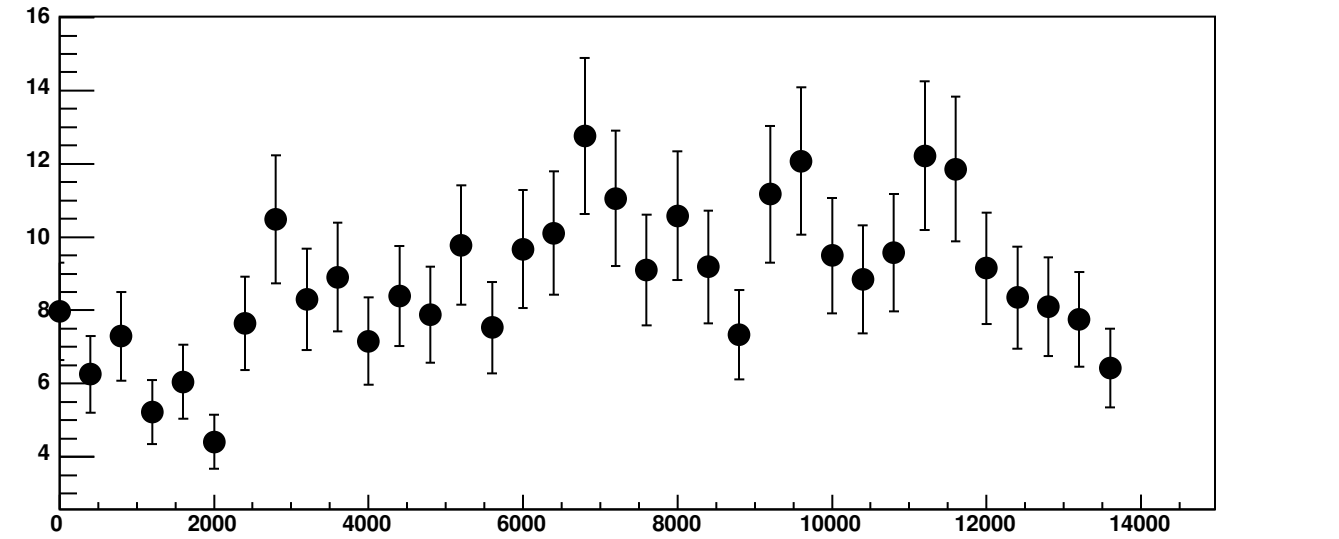

Chip 6, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

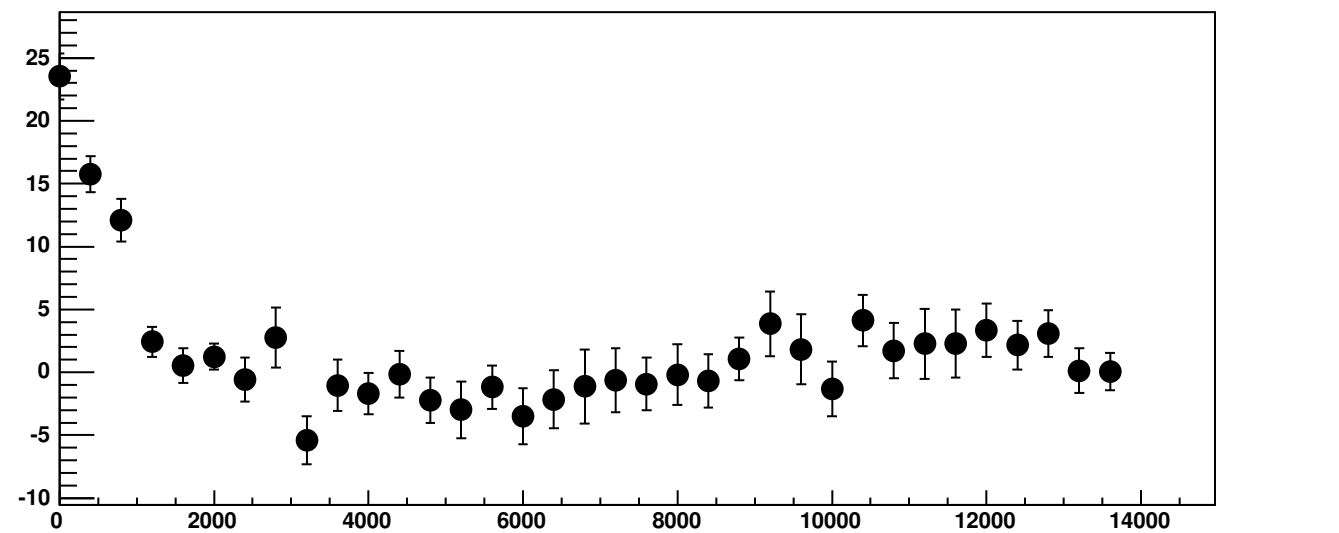
Chip 6, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

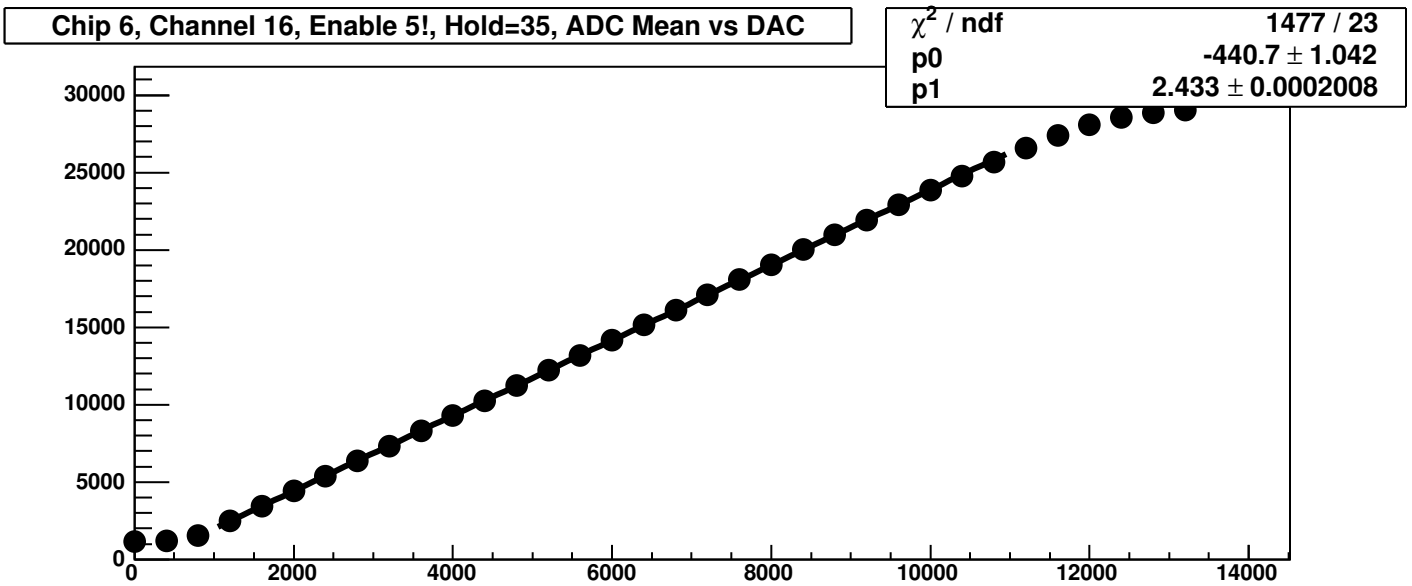


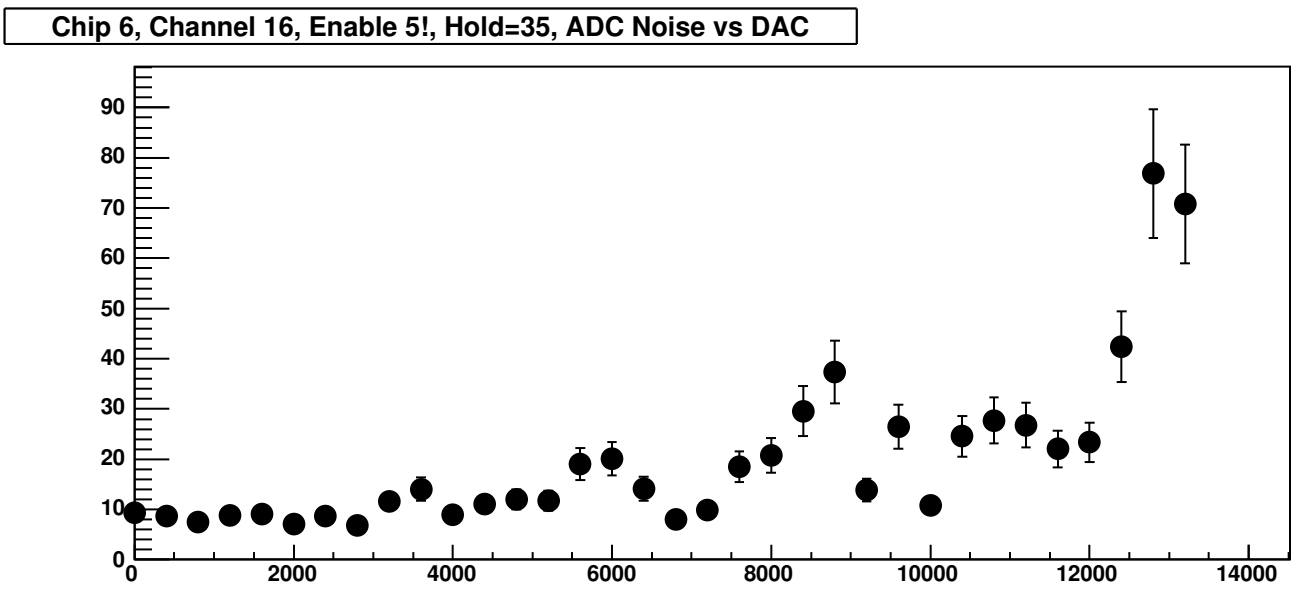
Chip 6, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC

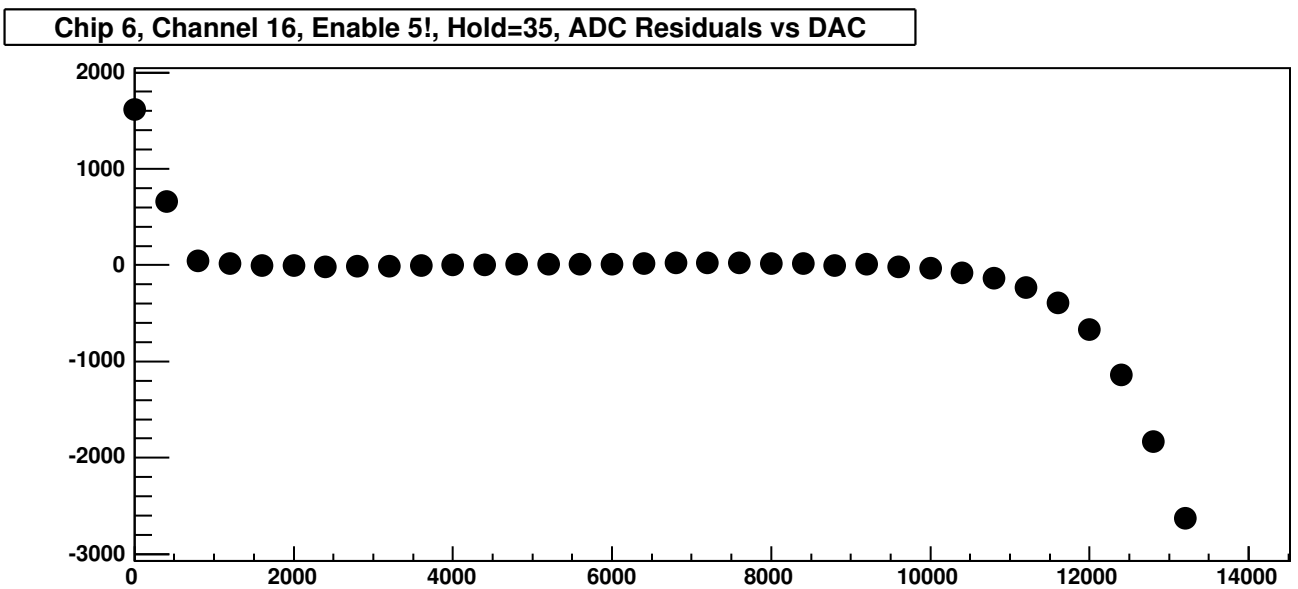


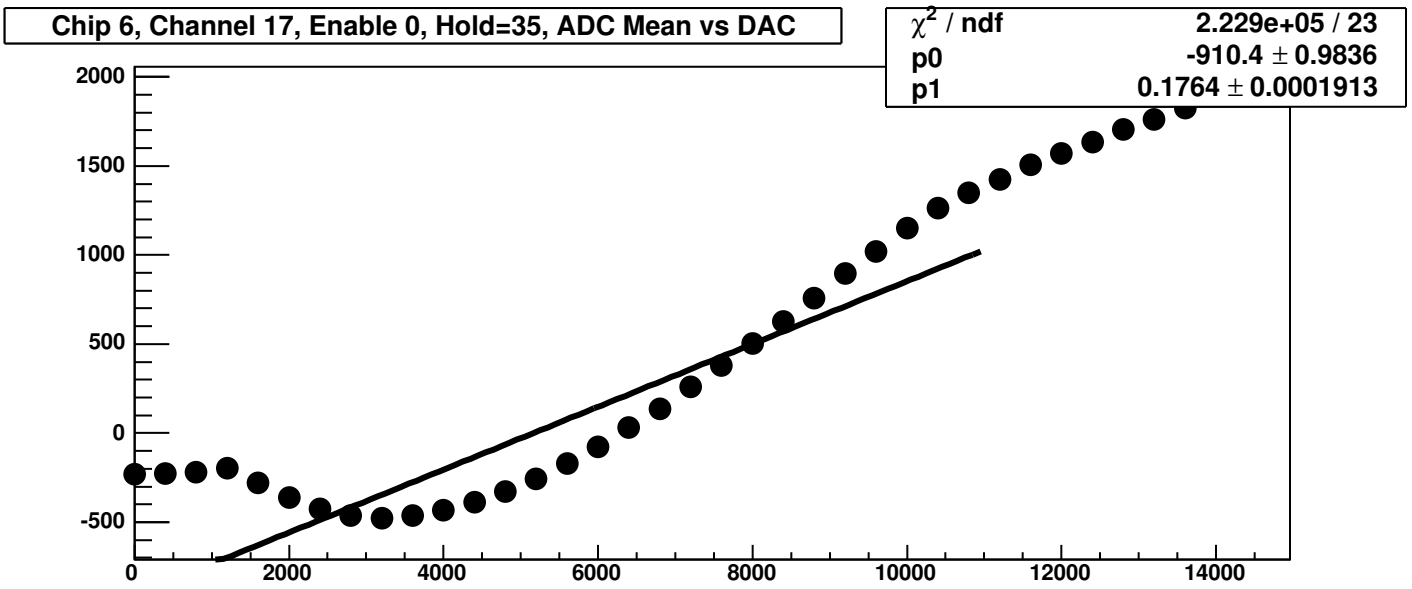
Chip 6, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

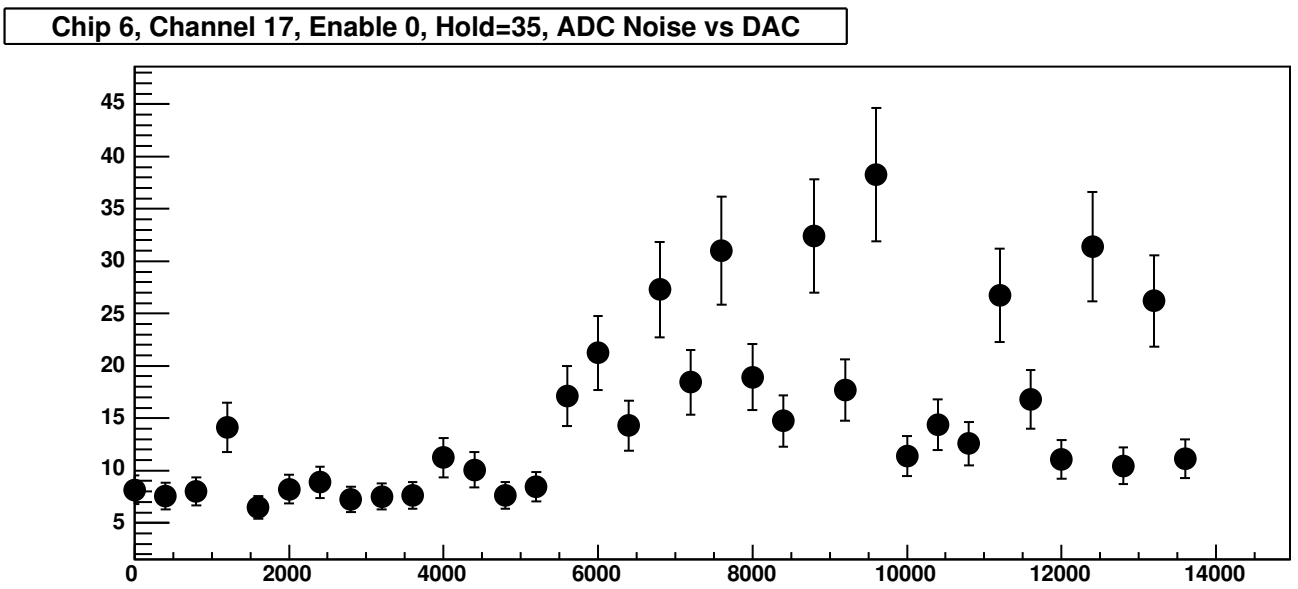


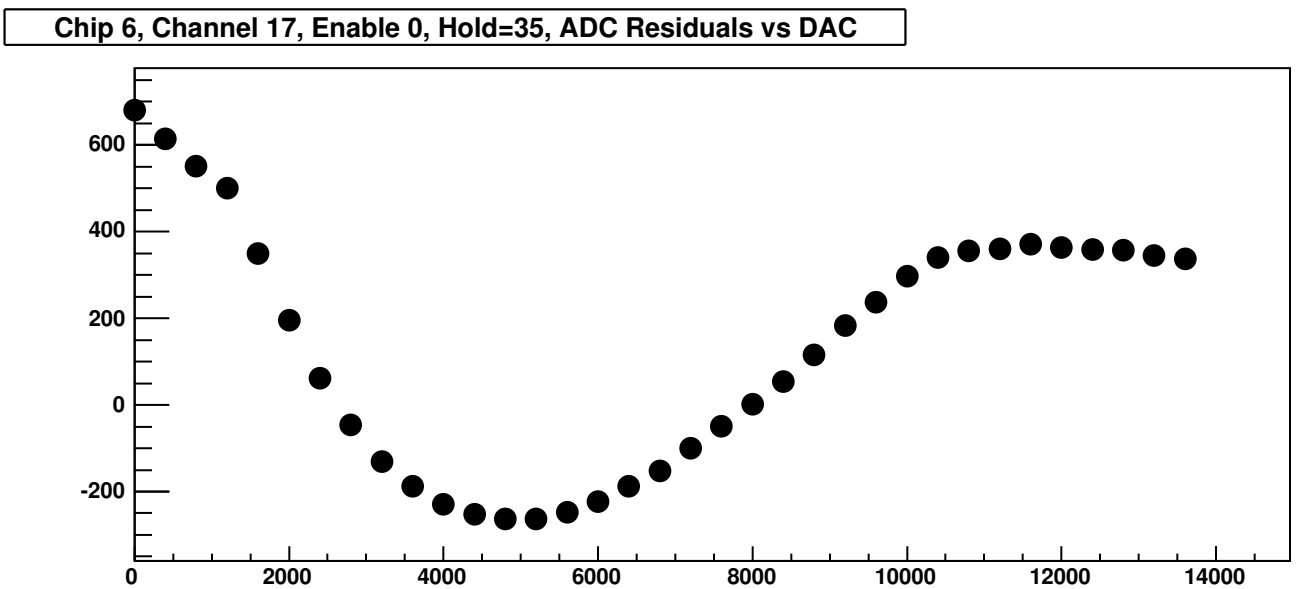

Chip 6, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1477 / 23
p0 -440.7 ± 1.042
p1 2.433 ± 0.0002008
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

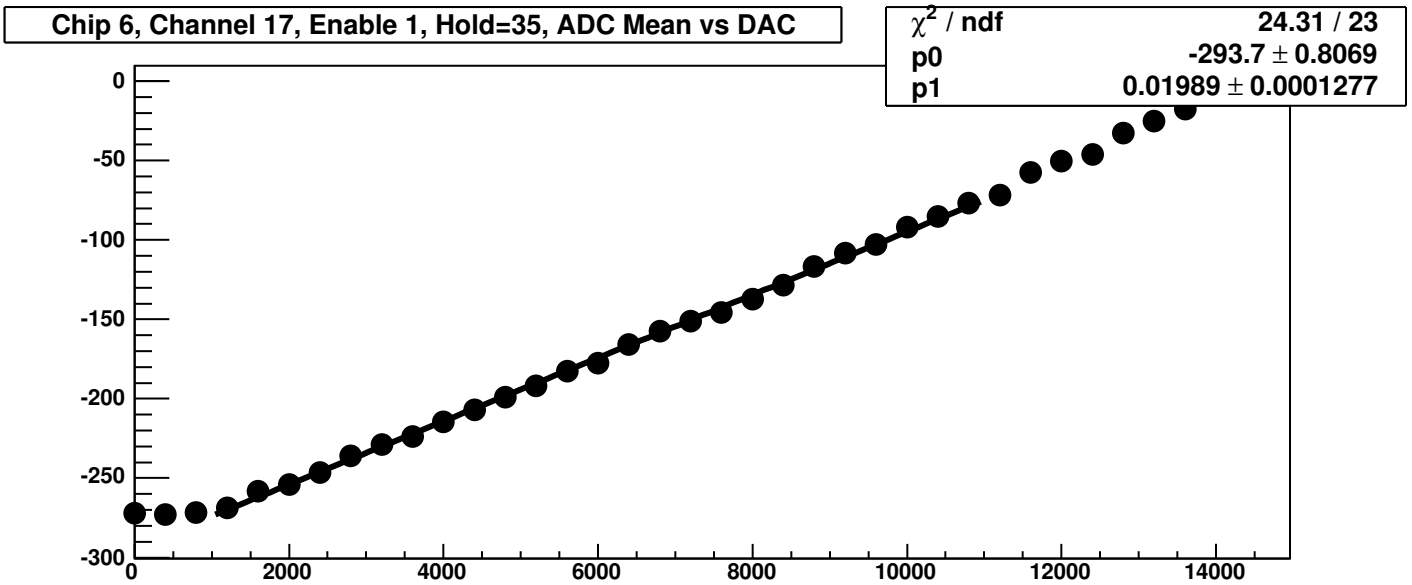

Chip 6, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

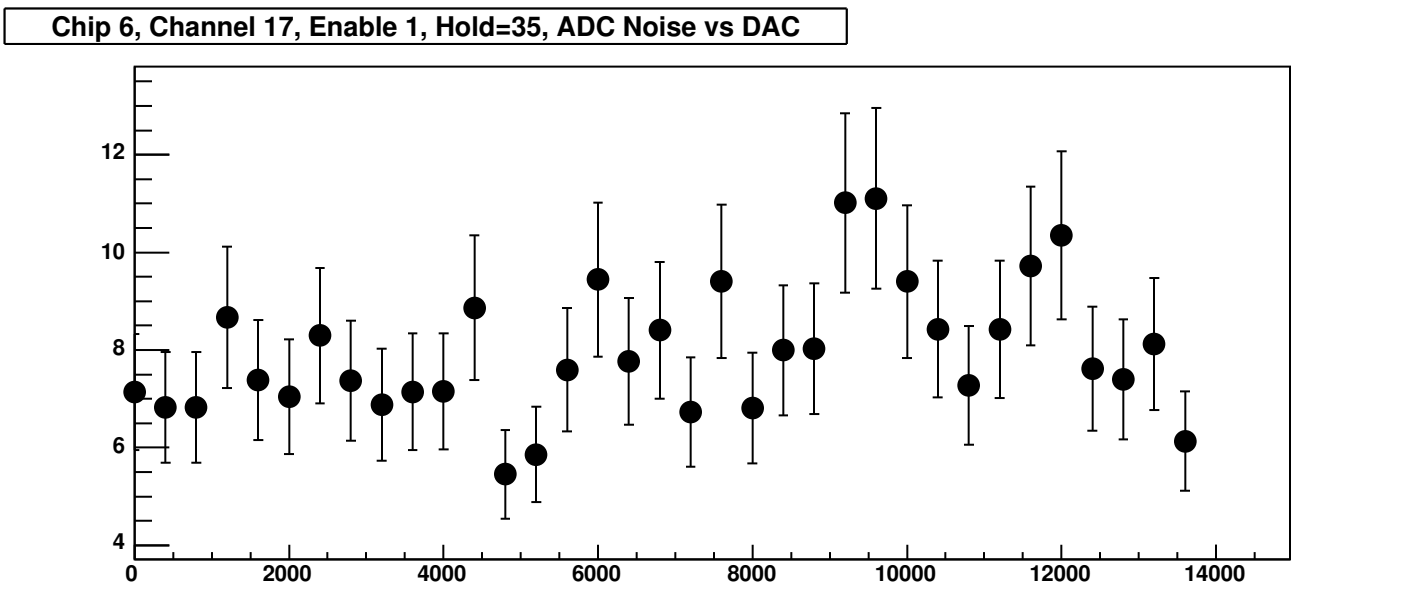

Chip 6, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

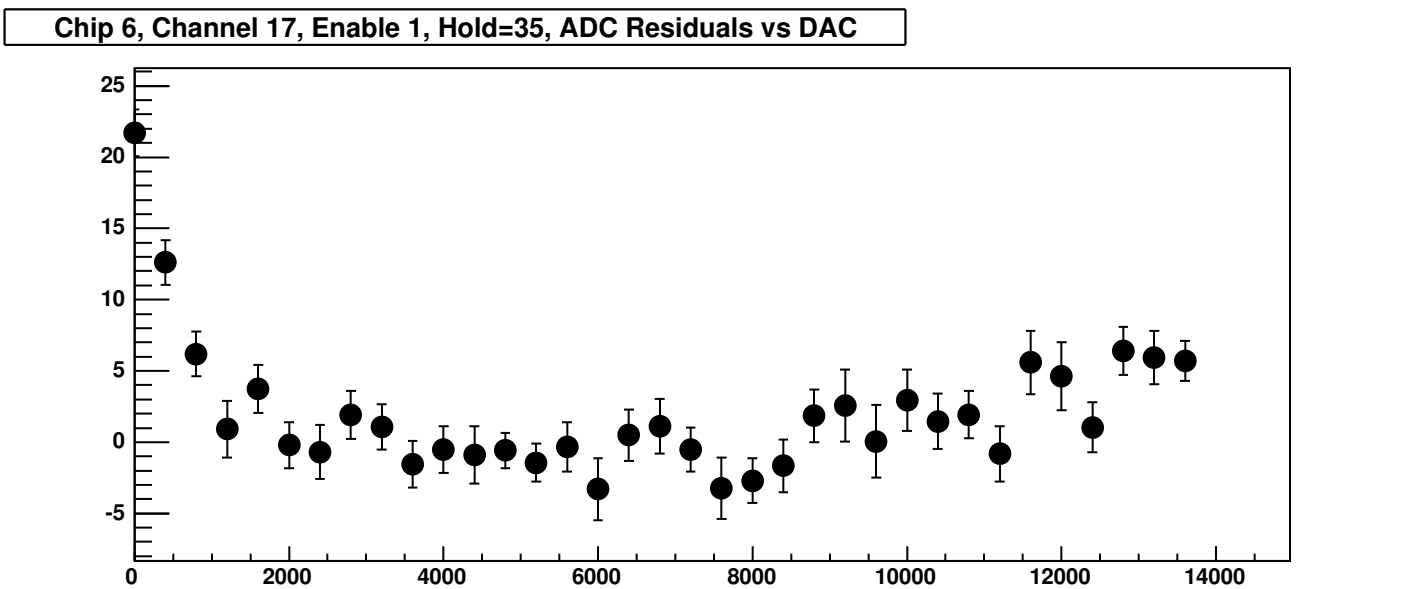

Chip 6, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.229e+05 / 23
p0 -910.4 ± 0.9836
p1 0.1764 ± 0.0001913
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

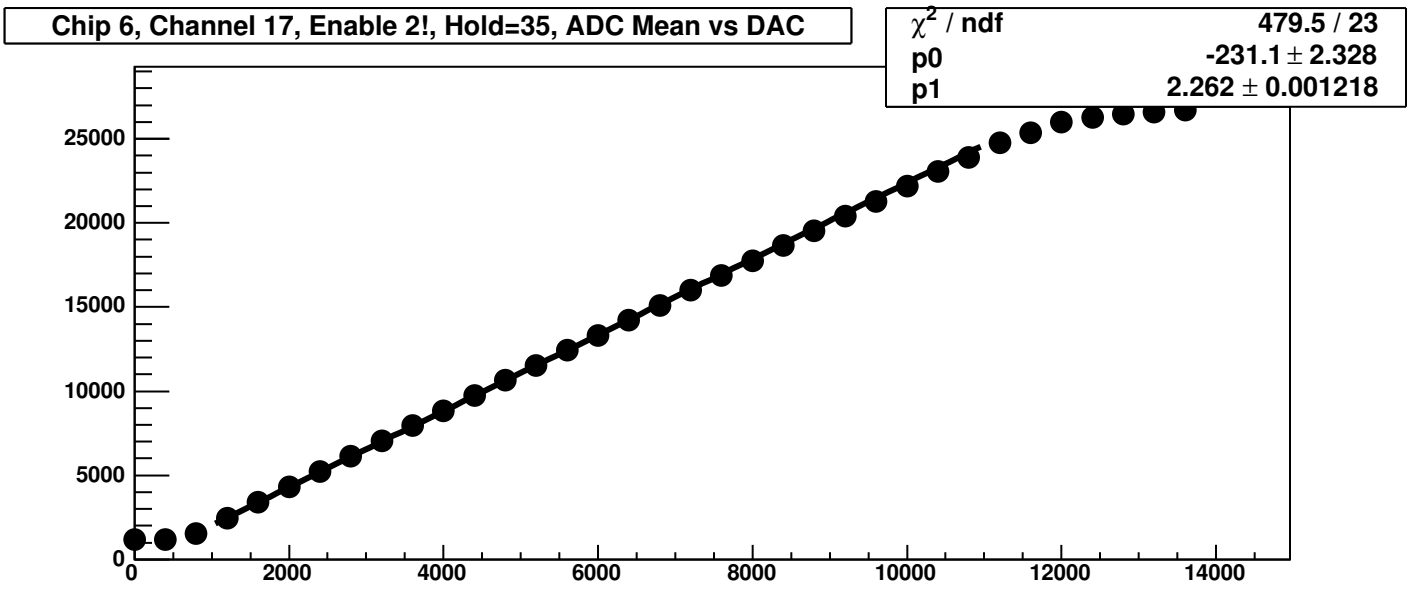

Chip 6, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

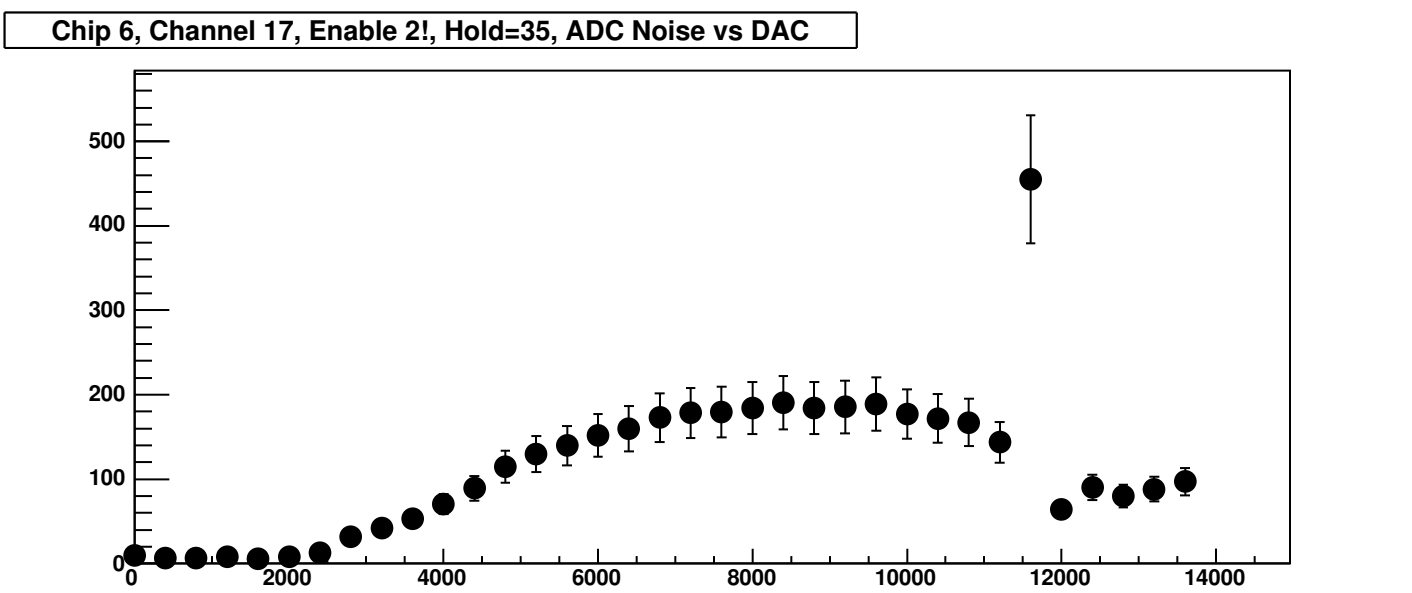

Chip 6, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

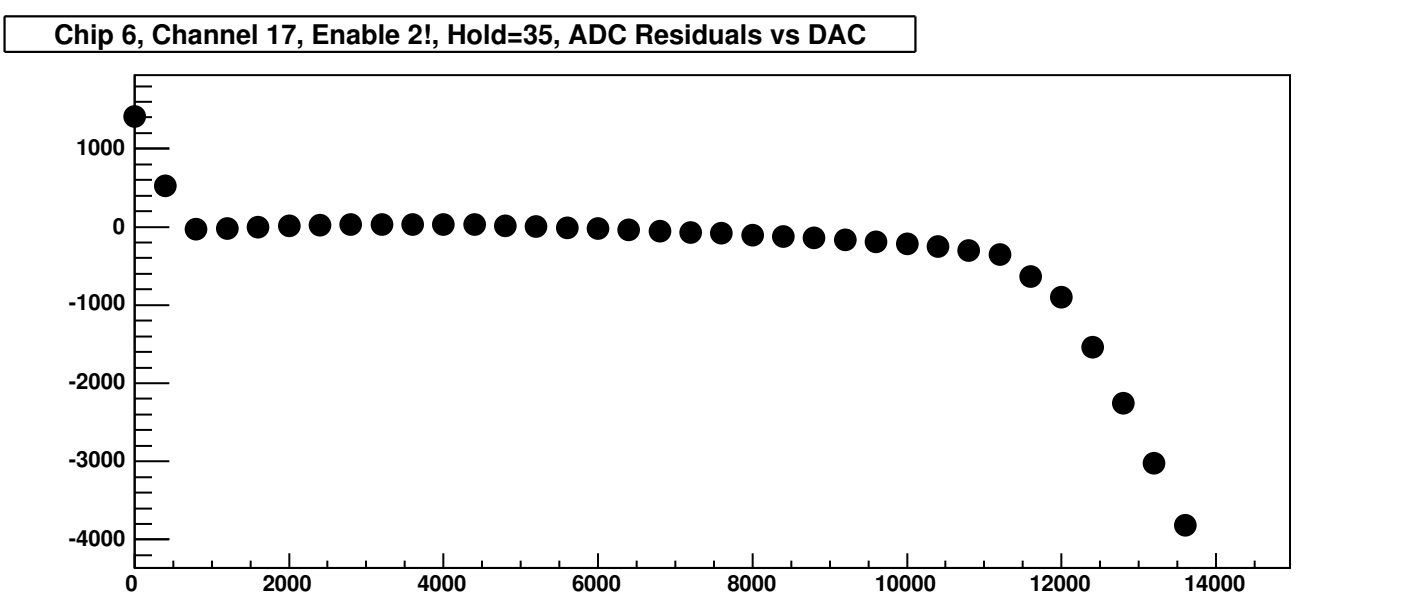

Chip 6, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 24.31 / 23
p0 -293.7 ± 0.8069
p1 0.01989 ± 0.0001277
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

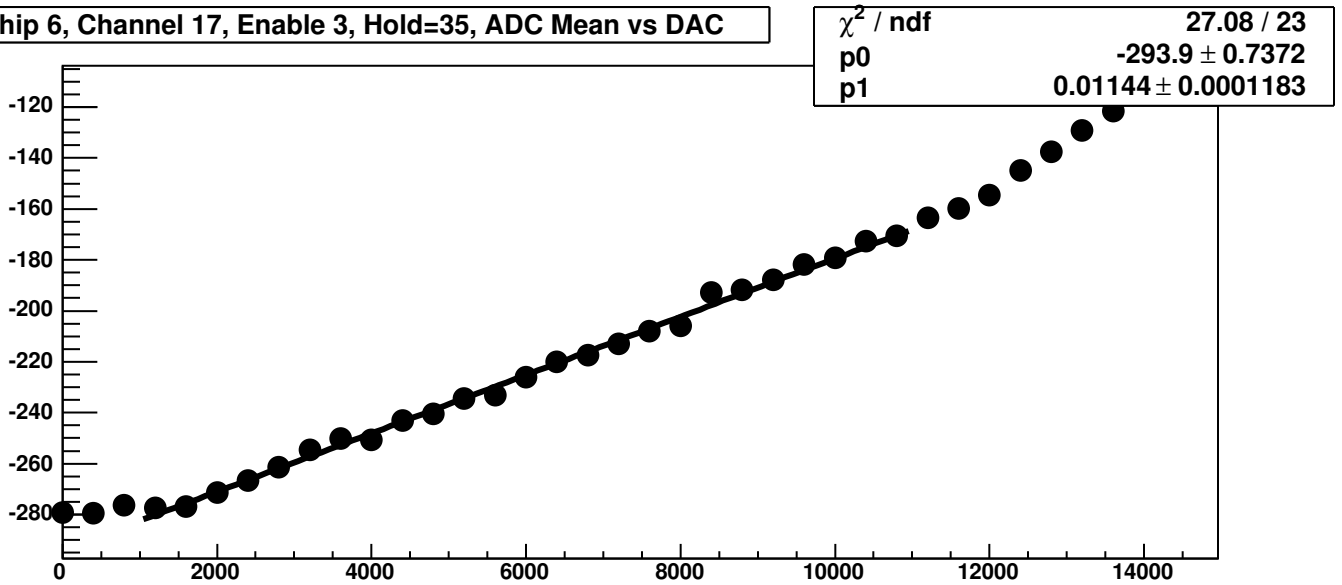

Chip 6, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 479.5 / 23
p0 -231.1 ± 2.328
p1 2.262 ± 0.001218
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

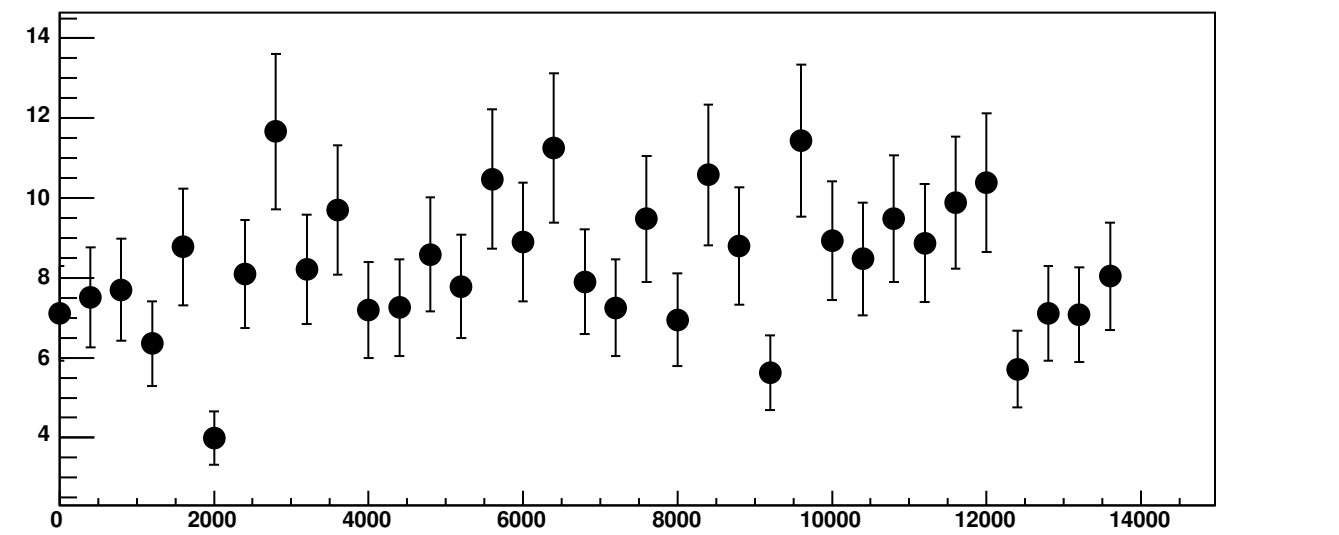

Chip 6, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 6, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

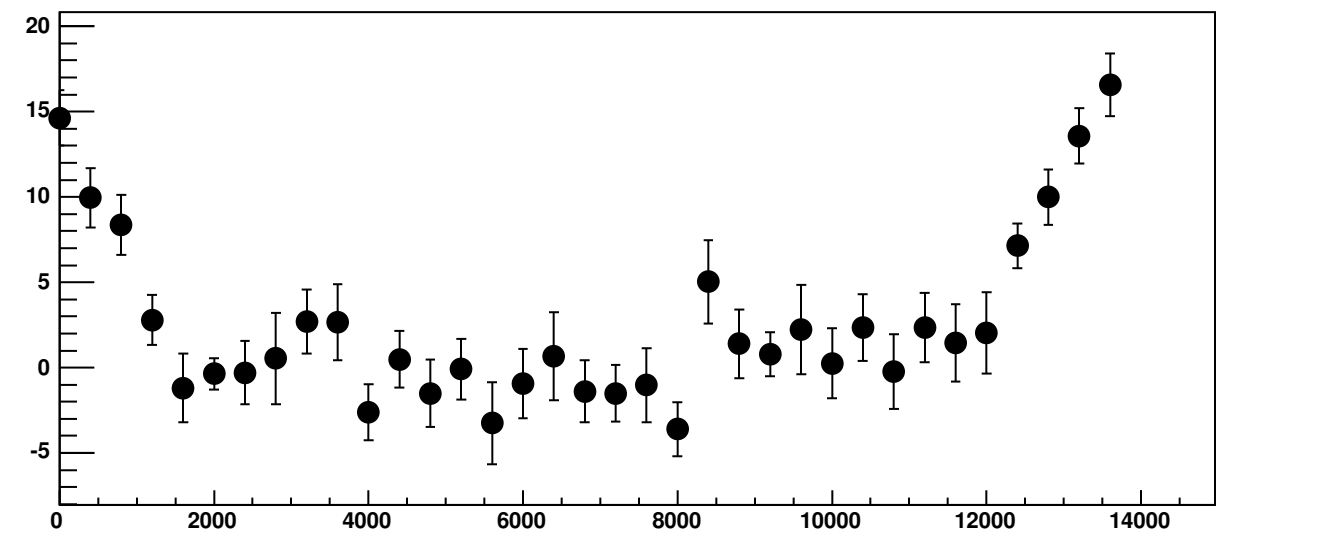
Chip 6, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

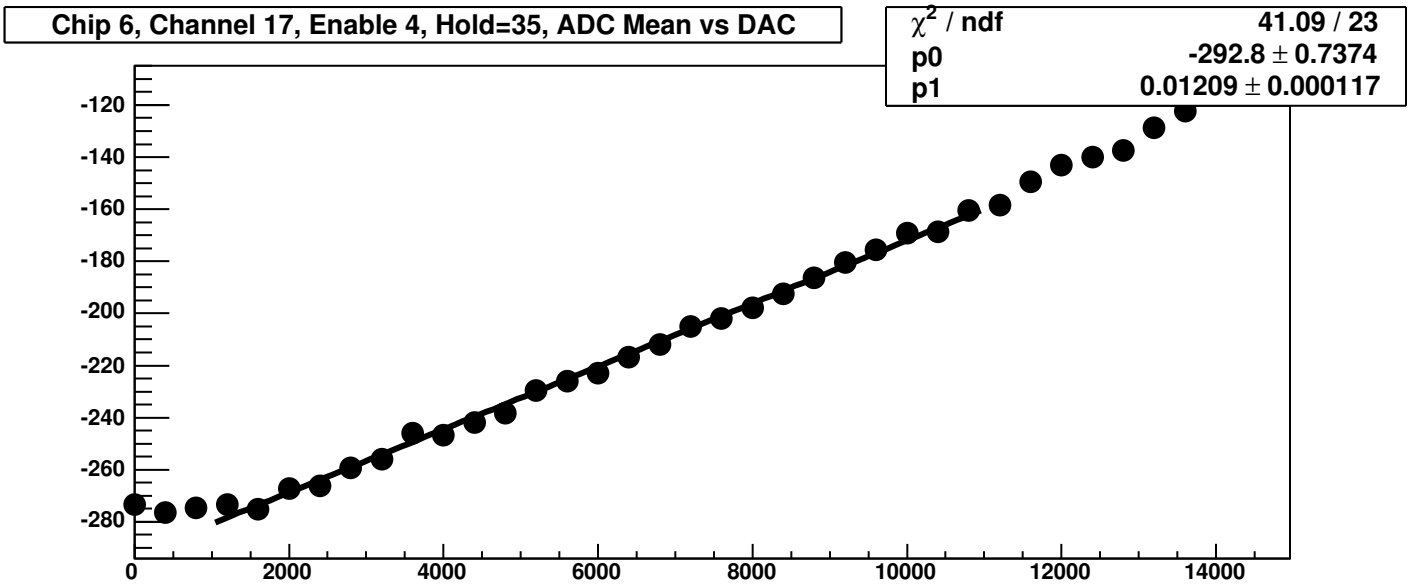


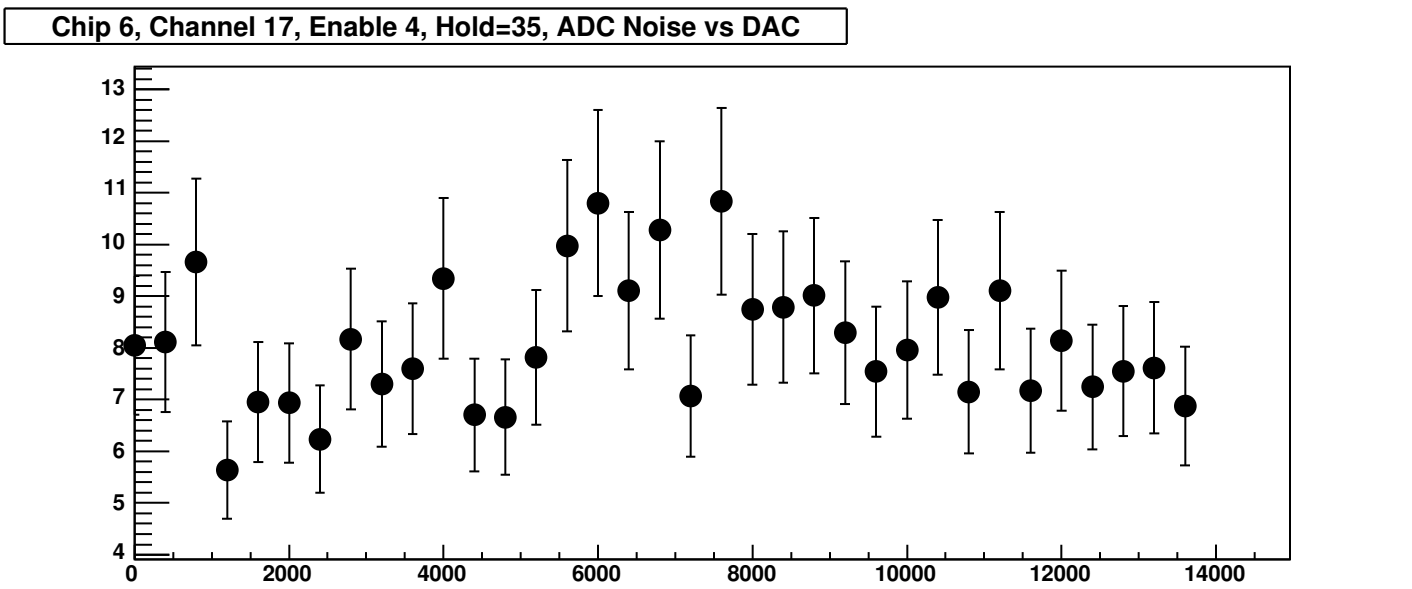
Chip 6, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC

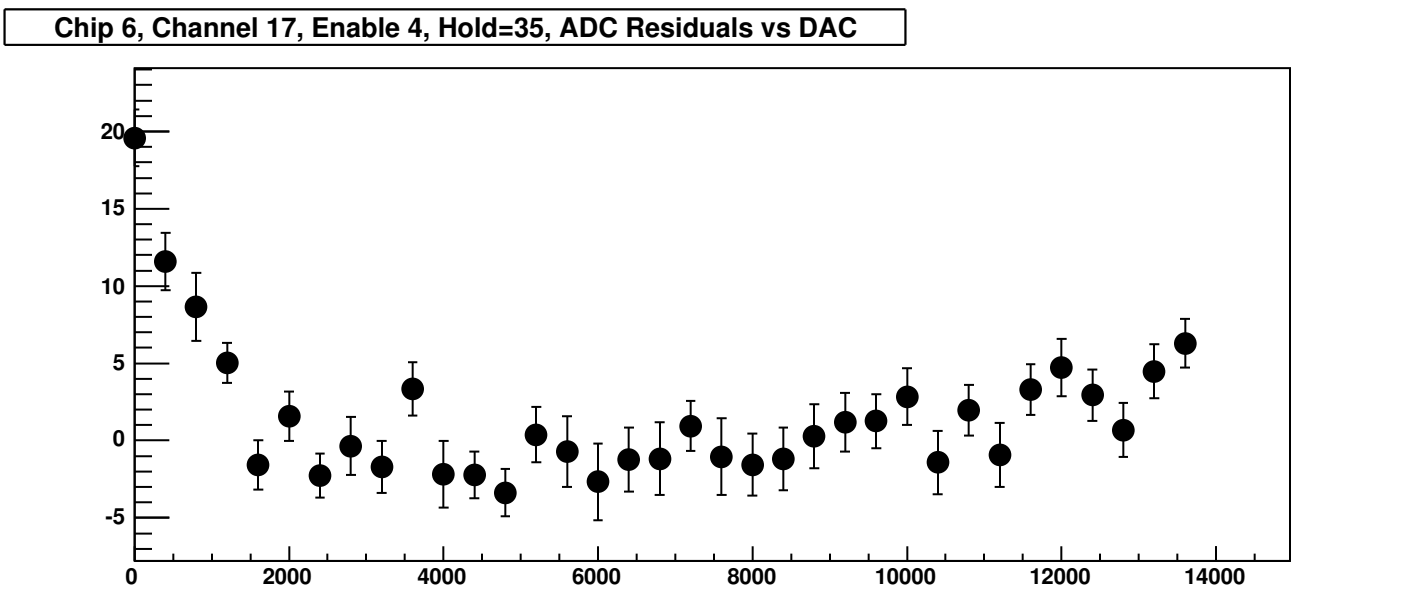


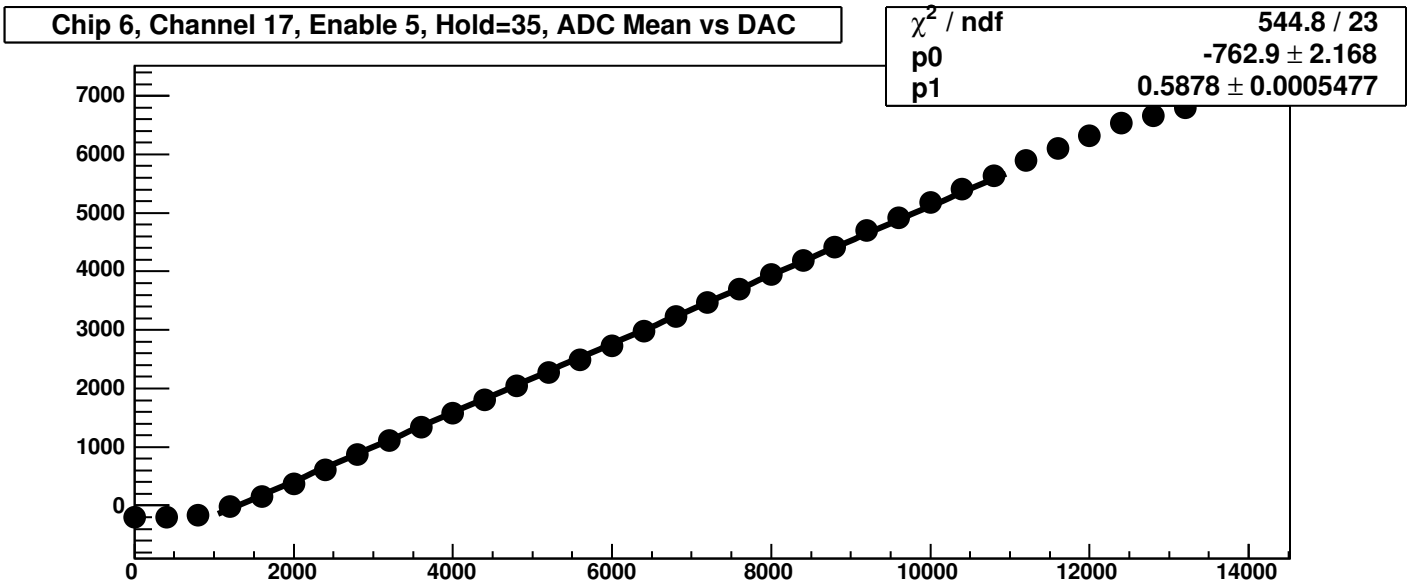
Chip 6, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

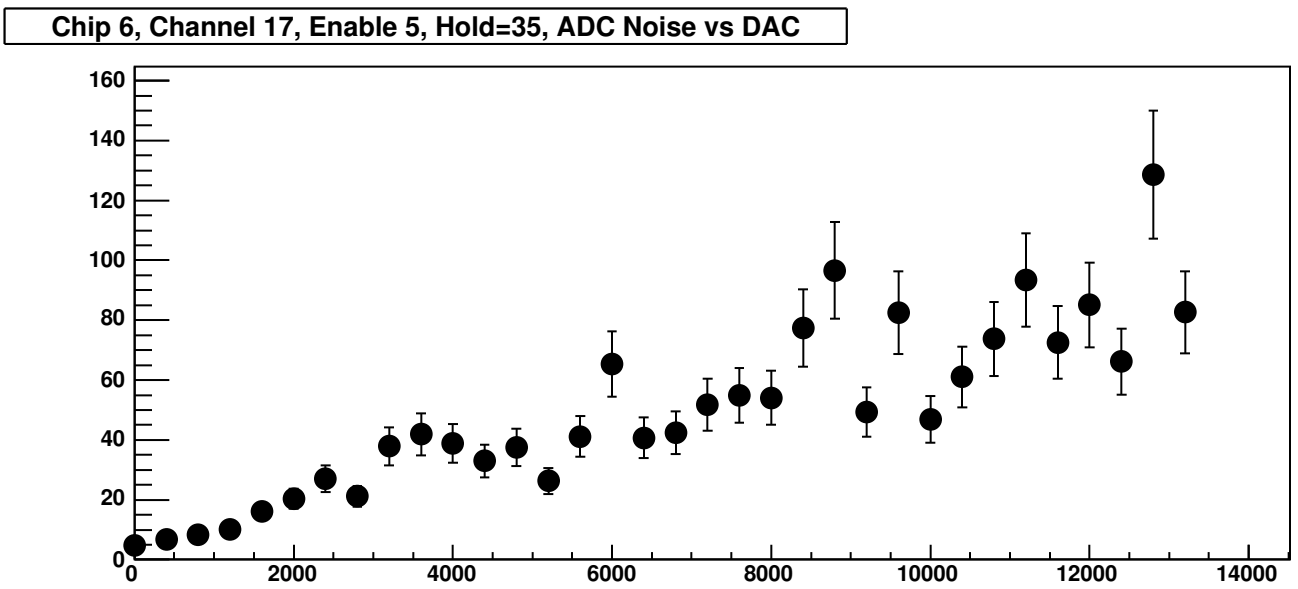


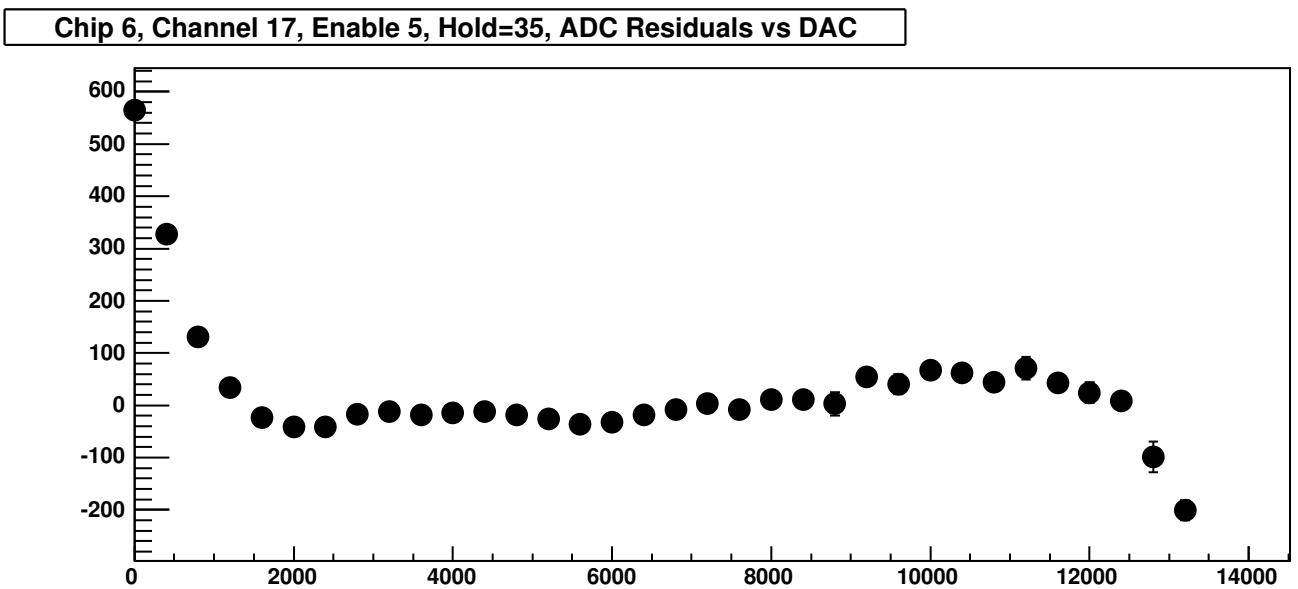

Chip 6, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ^2 / ndf 41.09 / 23
p0 -292.8 ± 0.7374
p1 0.01209 ± 0.000117
-120
-140
-160
-180
-200
-220
-240
-260
-280
0
2000
4000
6000
8000
10000
12000
14000

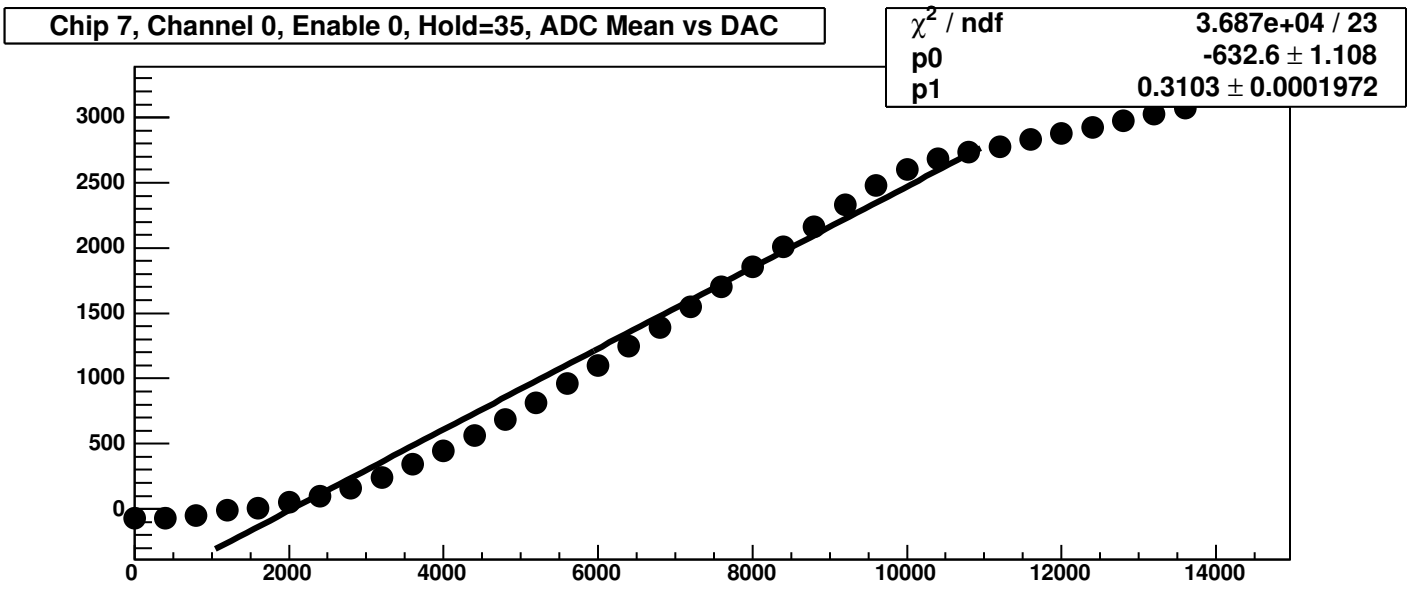

Chip 6, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

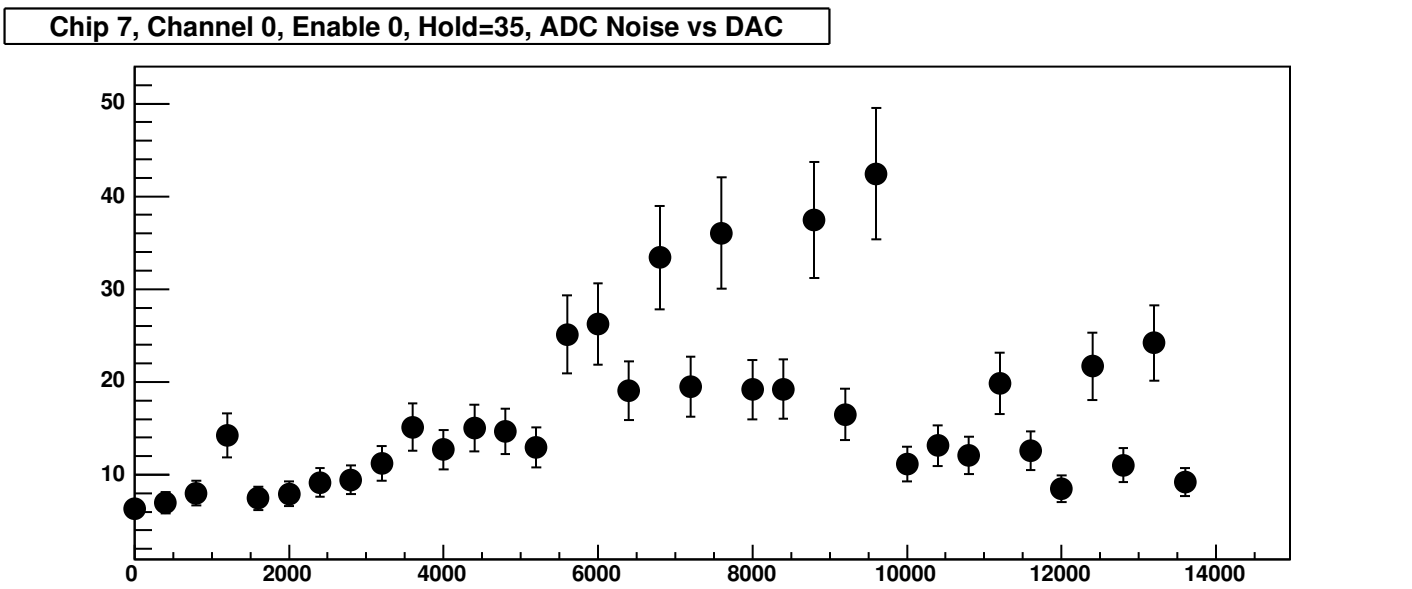

Chip 6, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

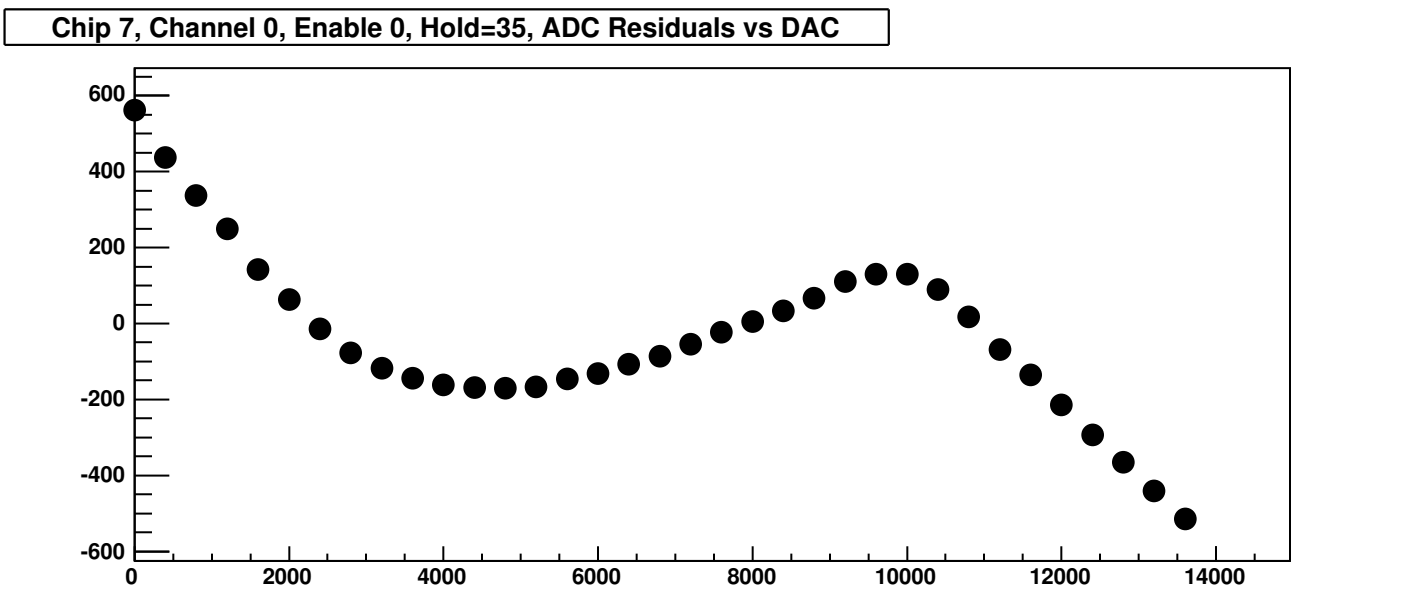

Chip 6, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 544.8 / 23
p0 -762.9 ± 2.168
p1 0.5878 ± 0.0005477
7000
6000
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

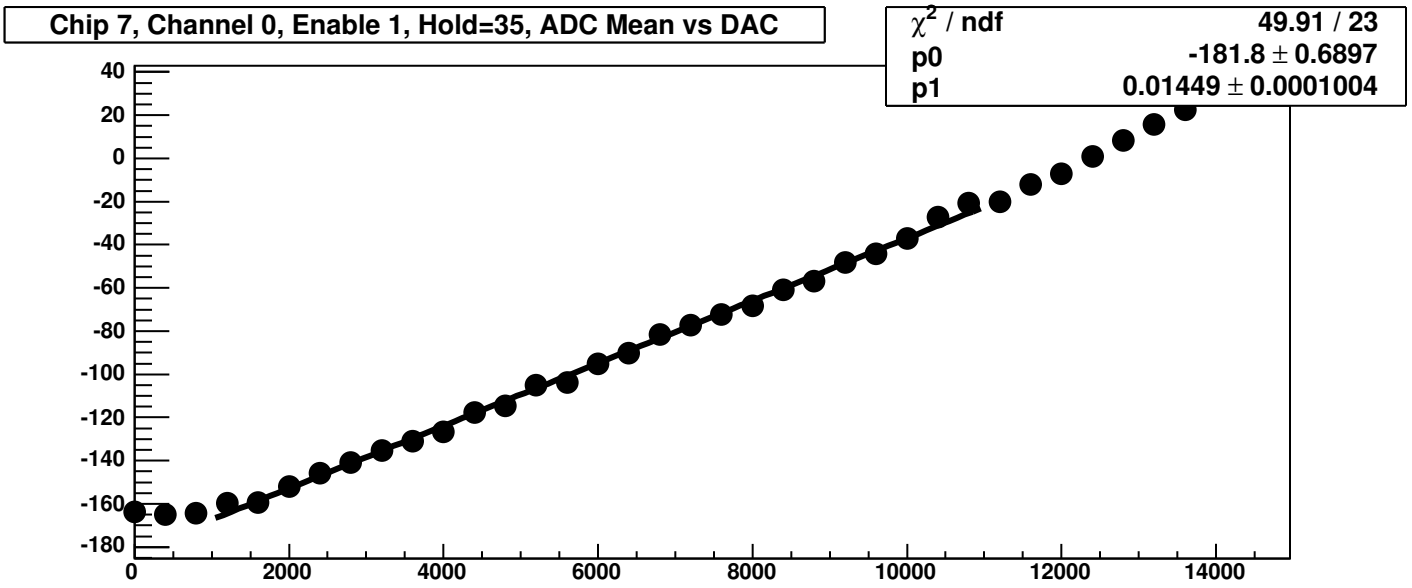

Chip 6, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

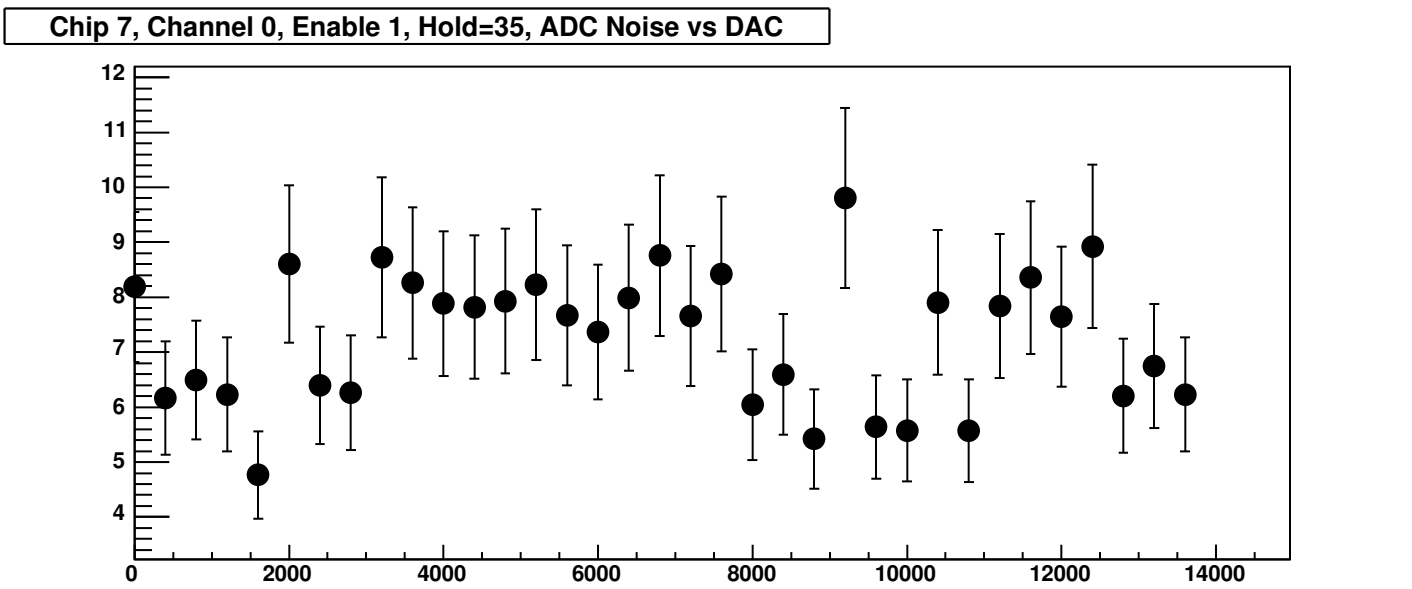

Chip 6, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

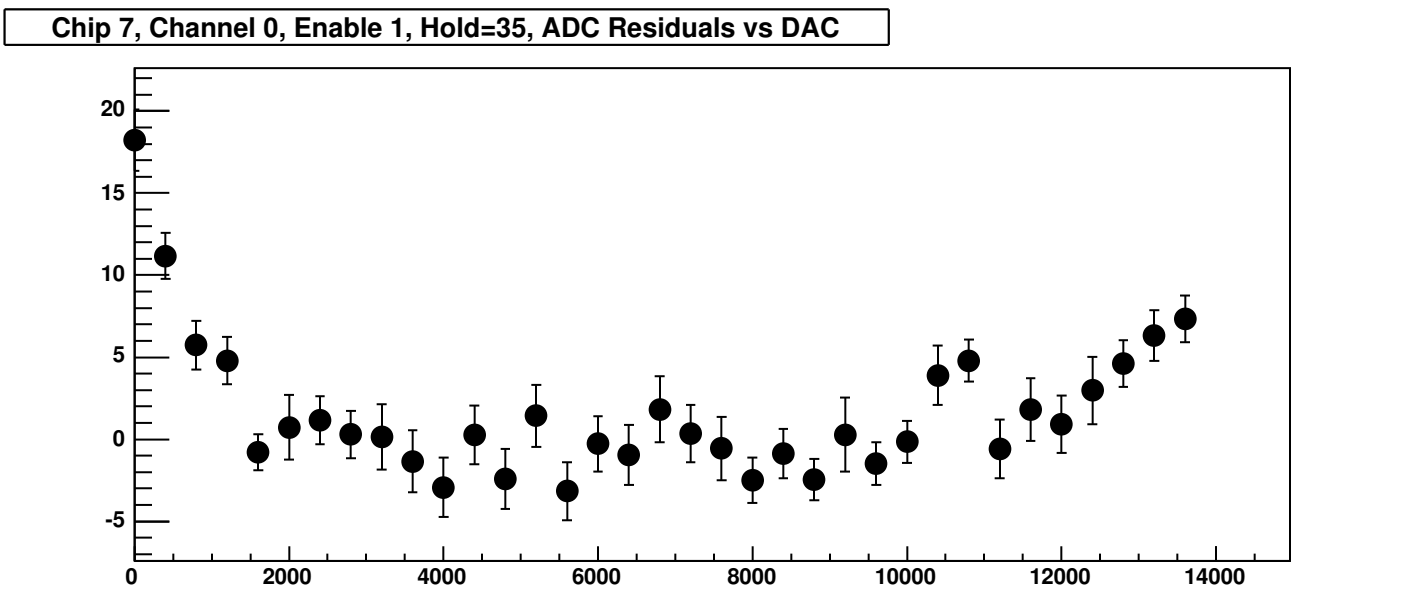

Chip 7, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.687e+04 / 23
p0 -632.6 ± 1.108
p1 0.3103 ± 0.0001972
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

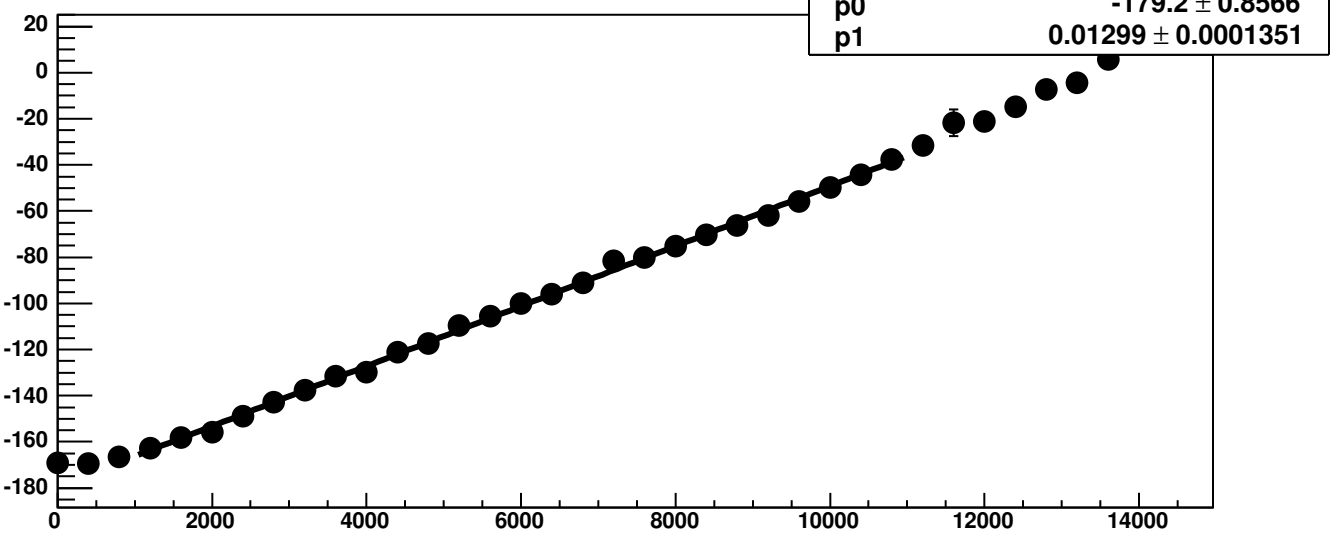

Chip 7, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 49.91 / 23
p0 -181.8 ± 0.6897
p1 0.01449 ± 0.0001004
40
20
0
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
2000
4000
6000
8000
10000
12000
14000

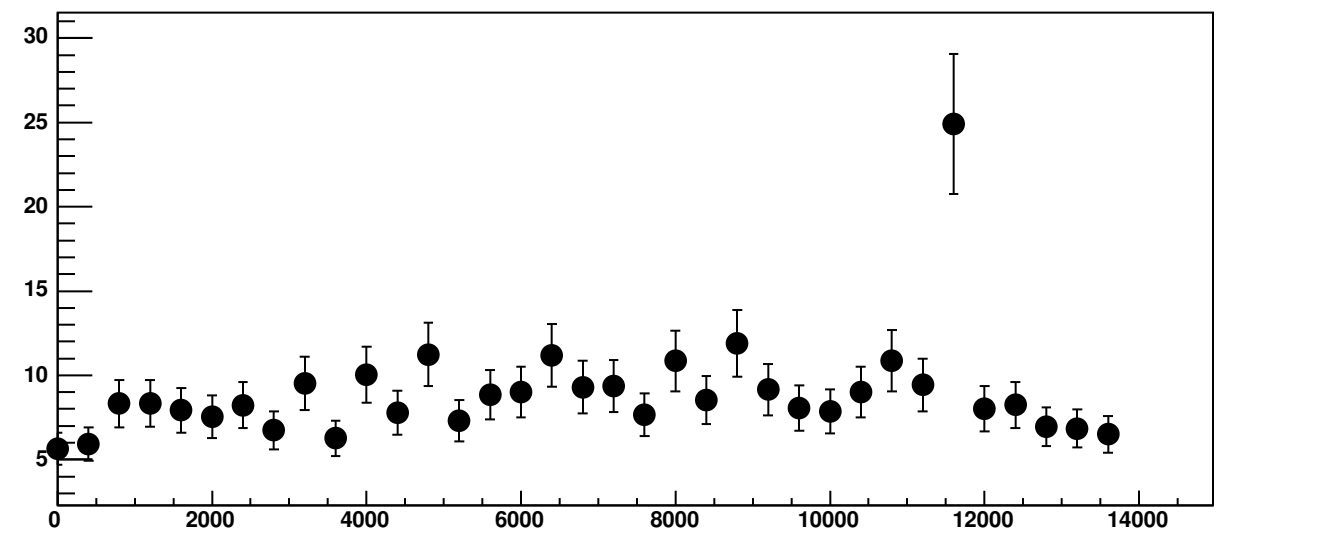

Chip 7, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

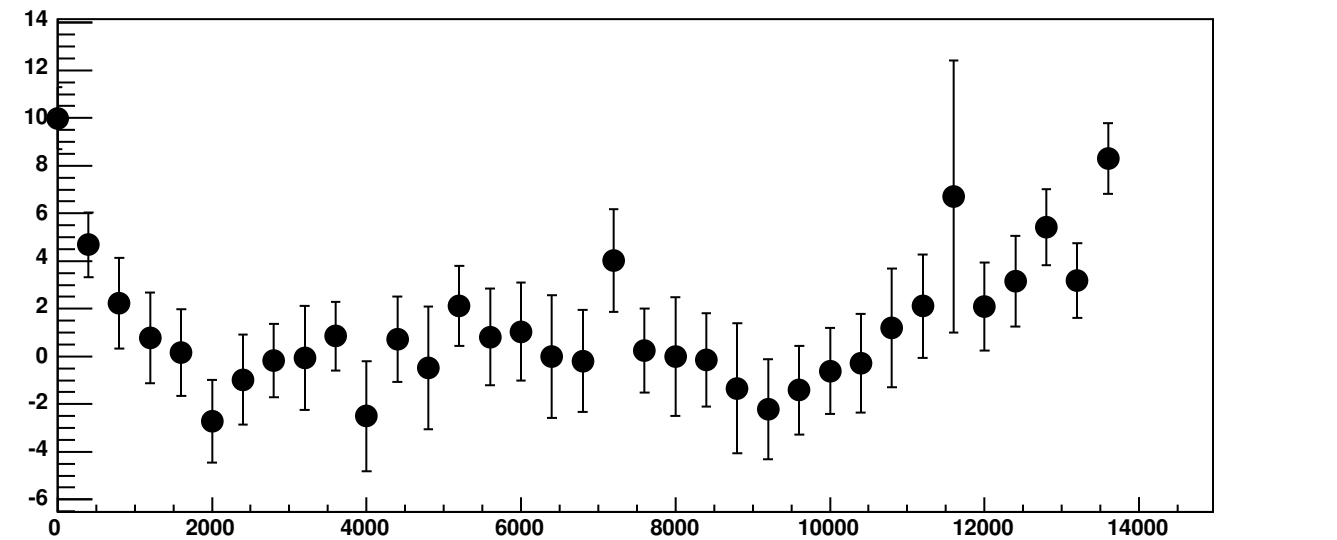
Chip 7, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

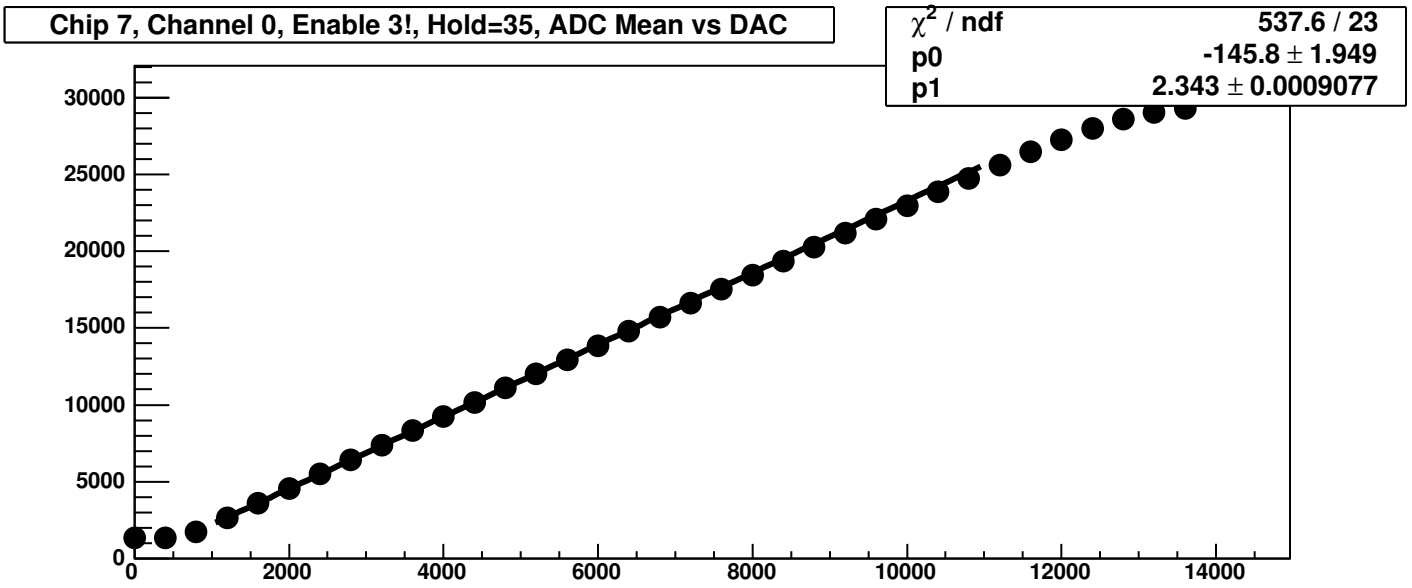


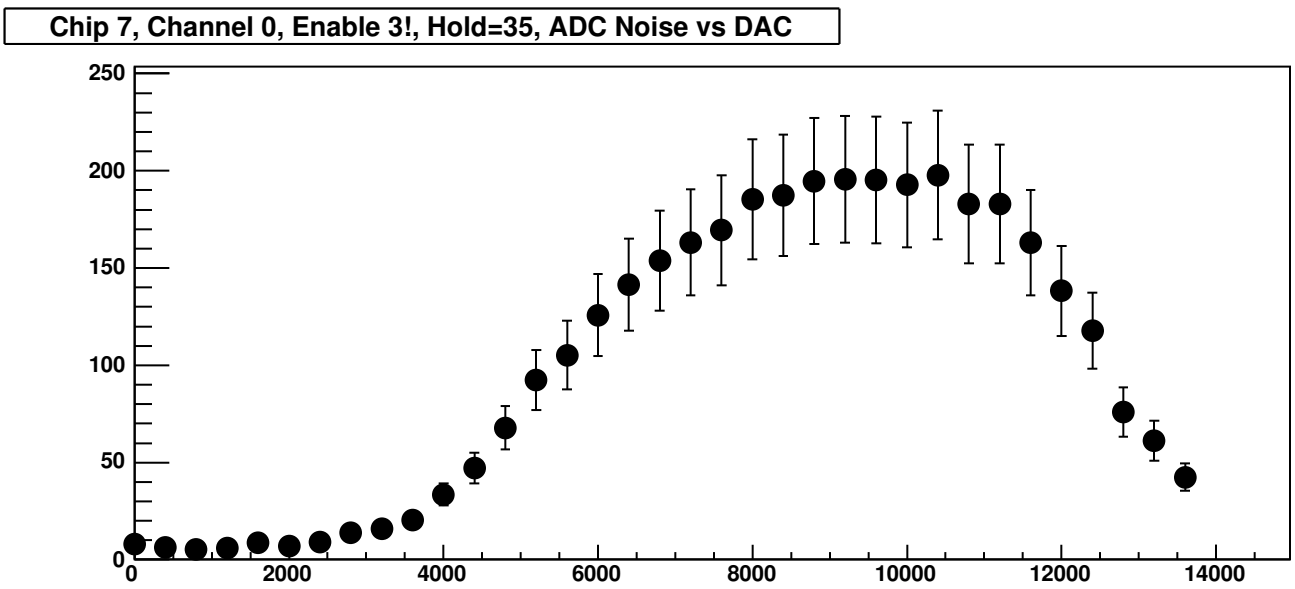
Chip 7, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC

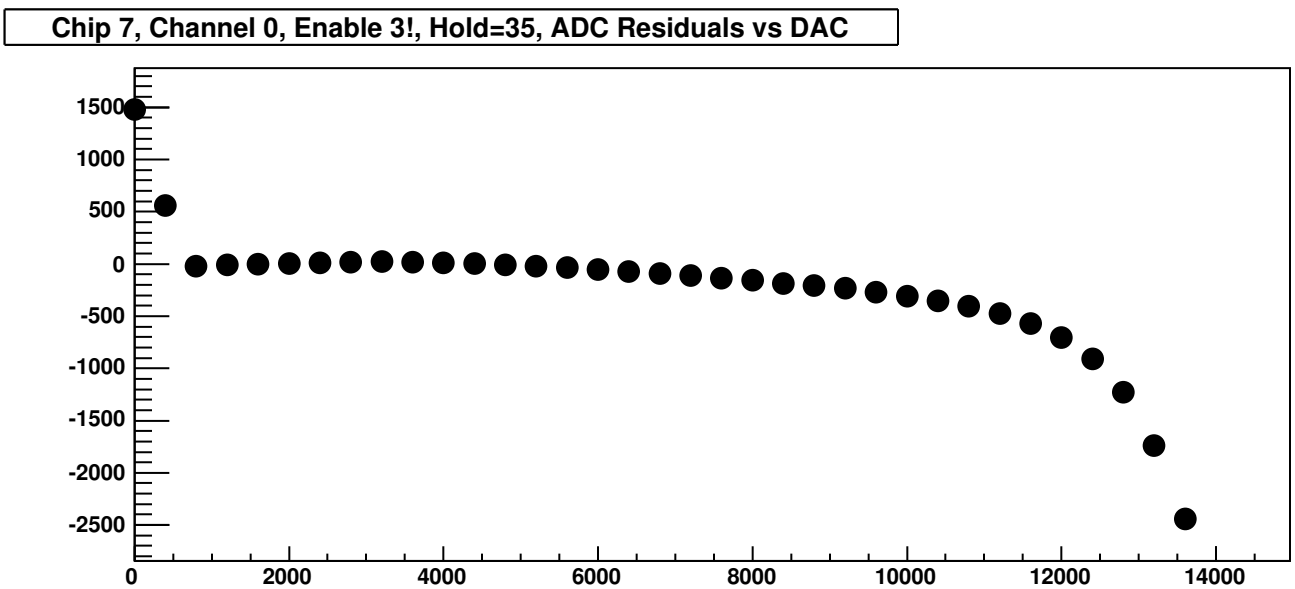


Chip 7, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

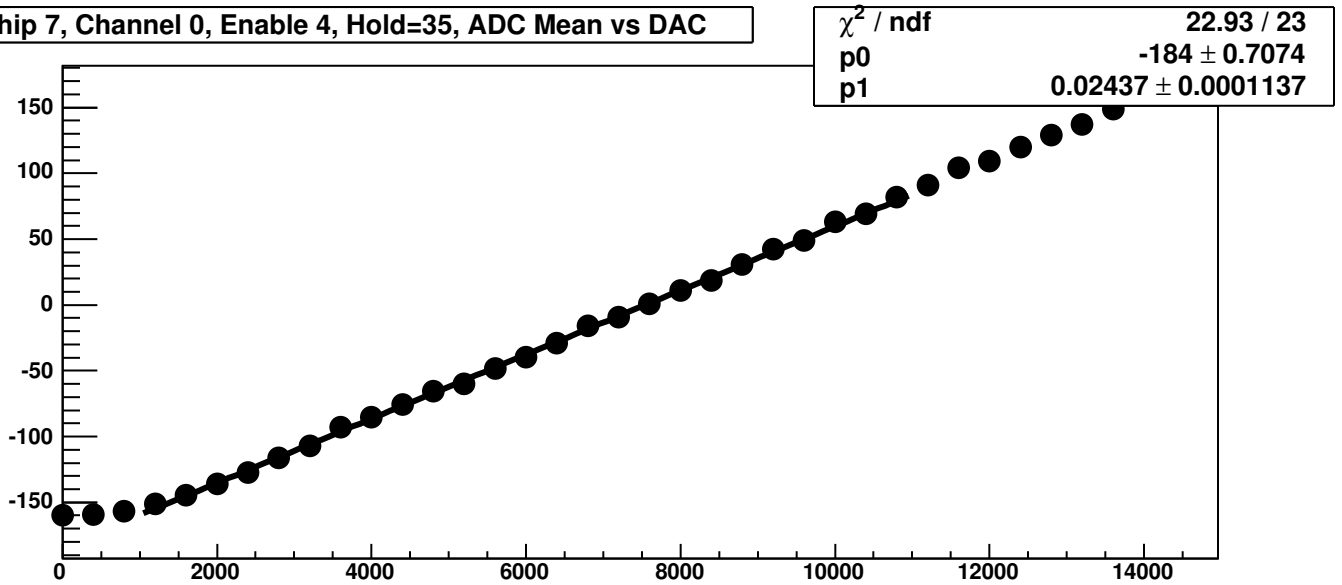



Chip 7, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 537.6 / 23
p0 -145.8 ± 1.949
p1 2.343 ± 0.0009077
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

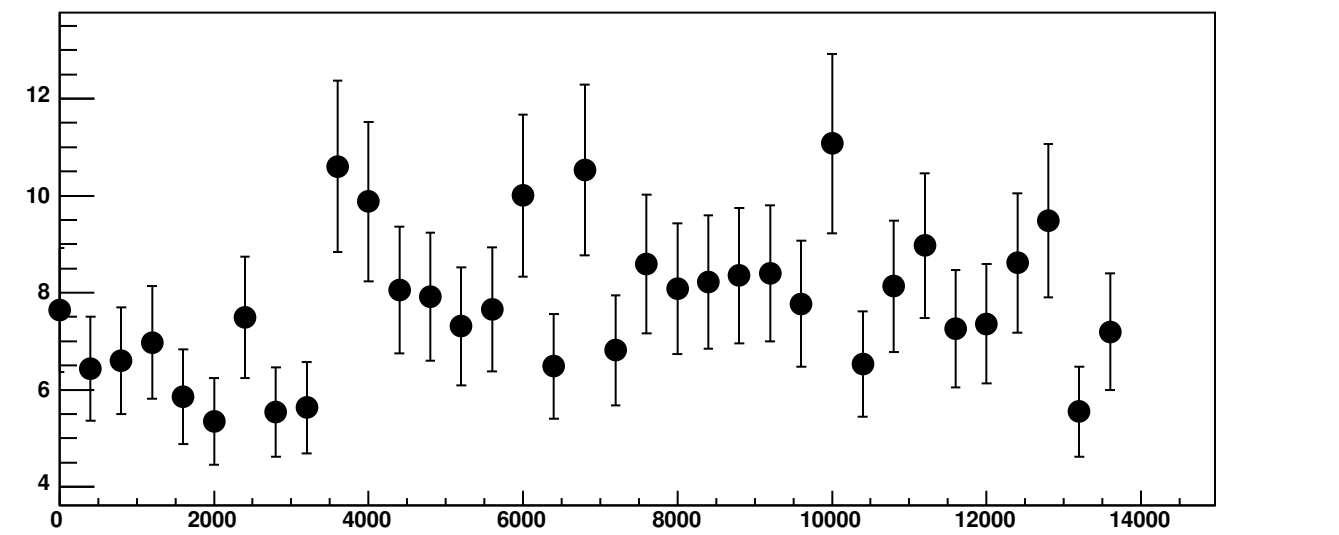

Chip 7, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

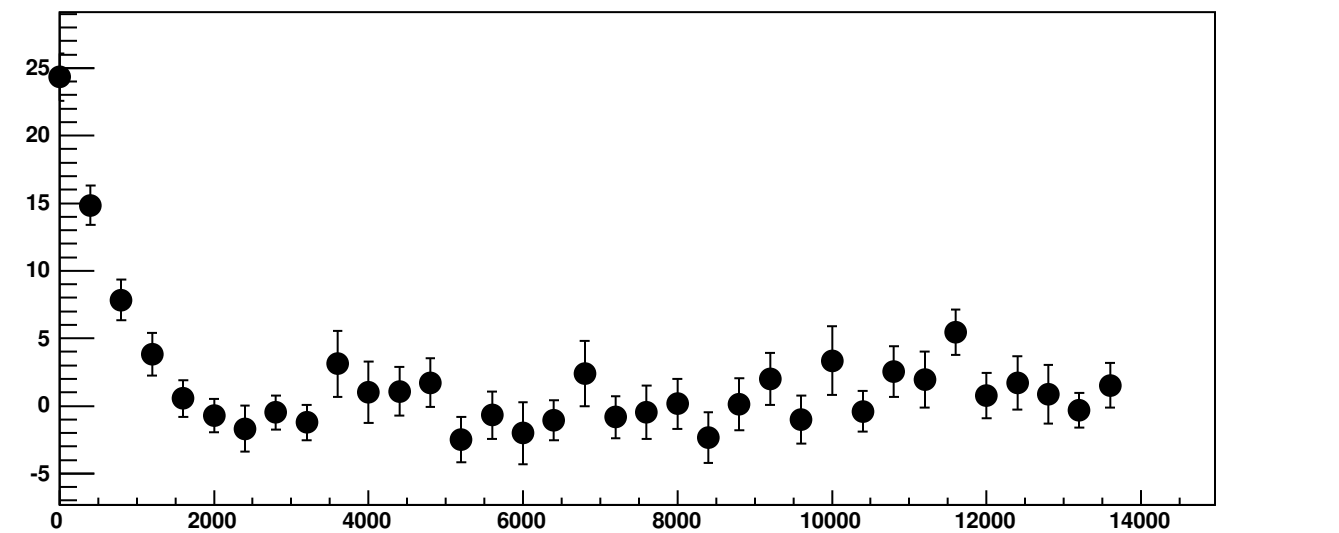
Chip 7, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC



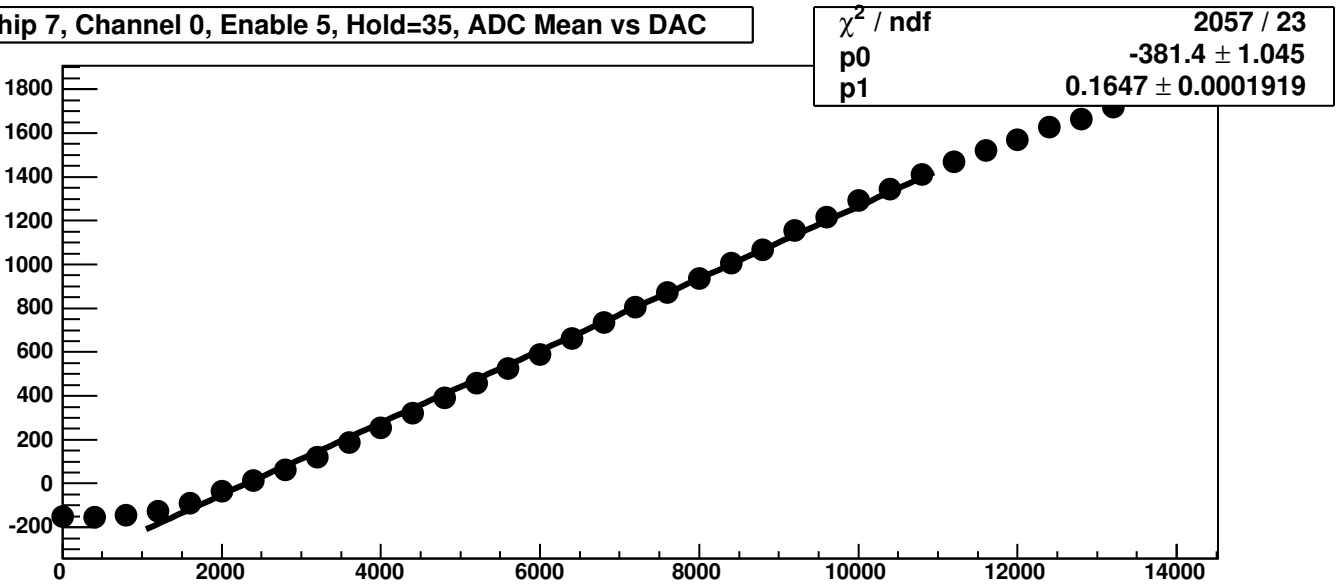
Chip 7, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC



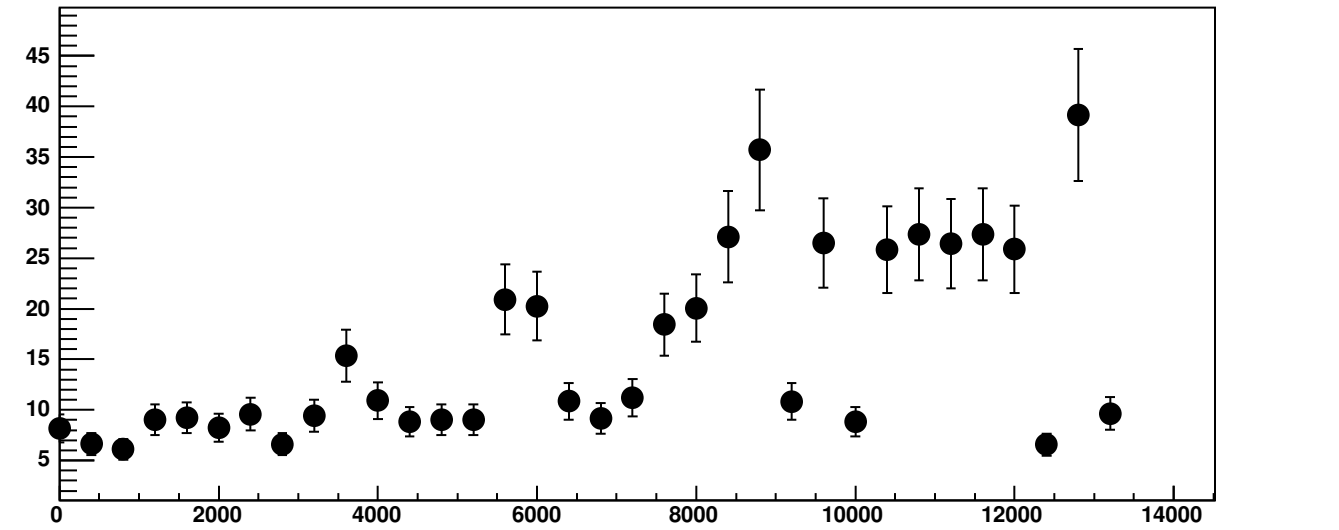
Chip 7, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC



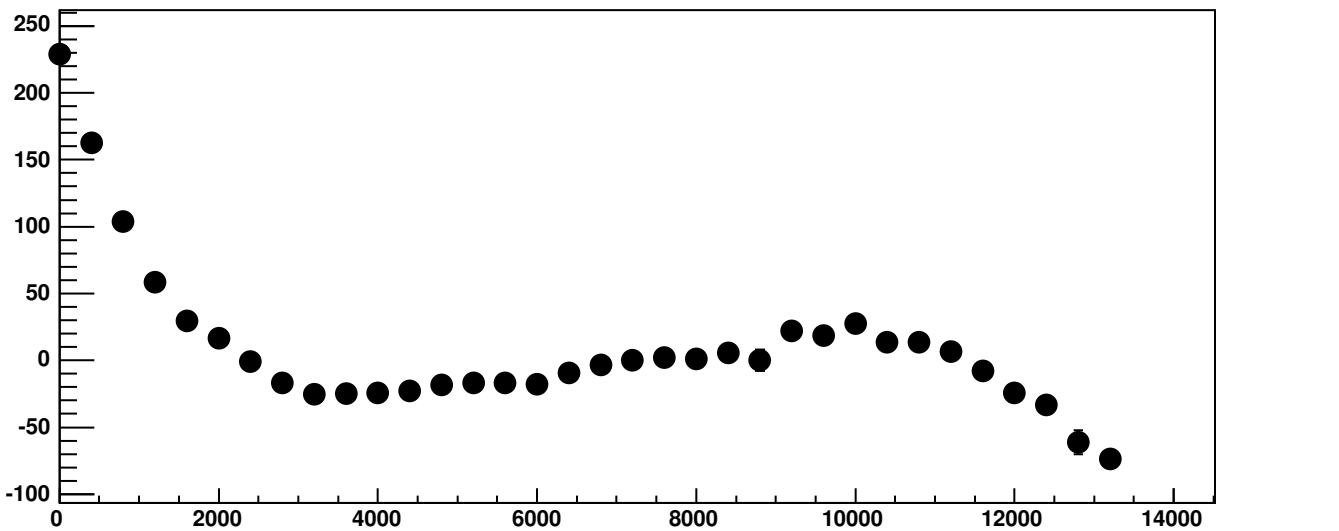
Chip 7, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

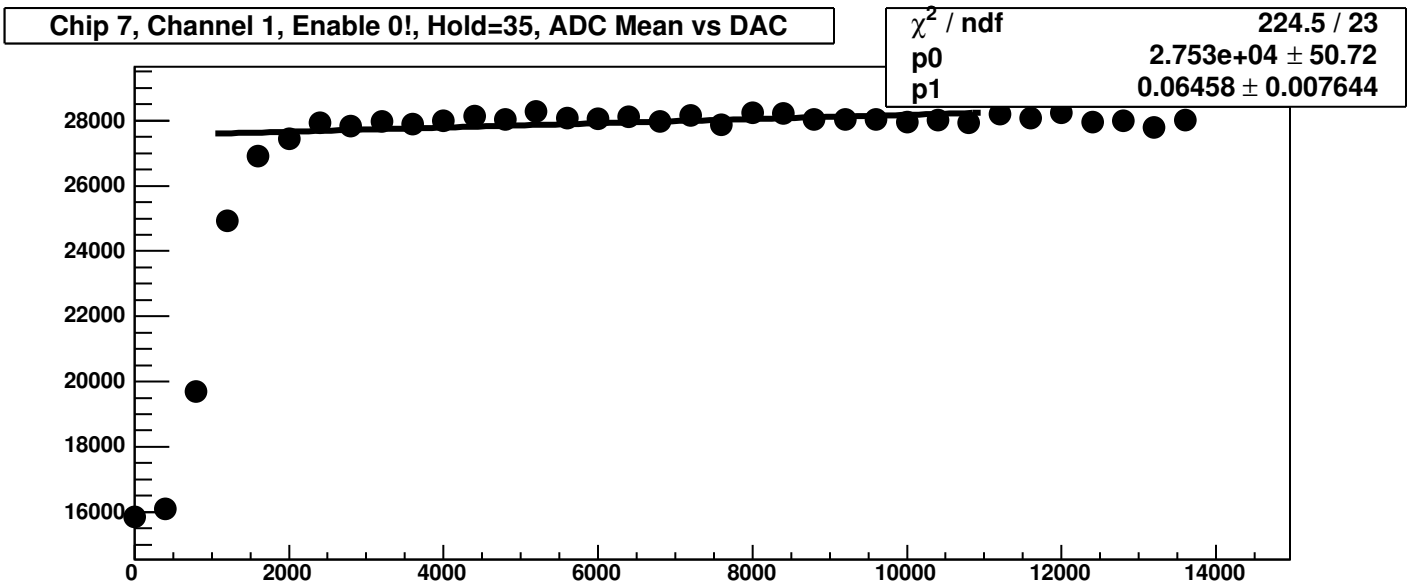


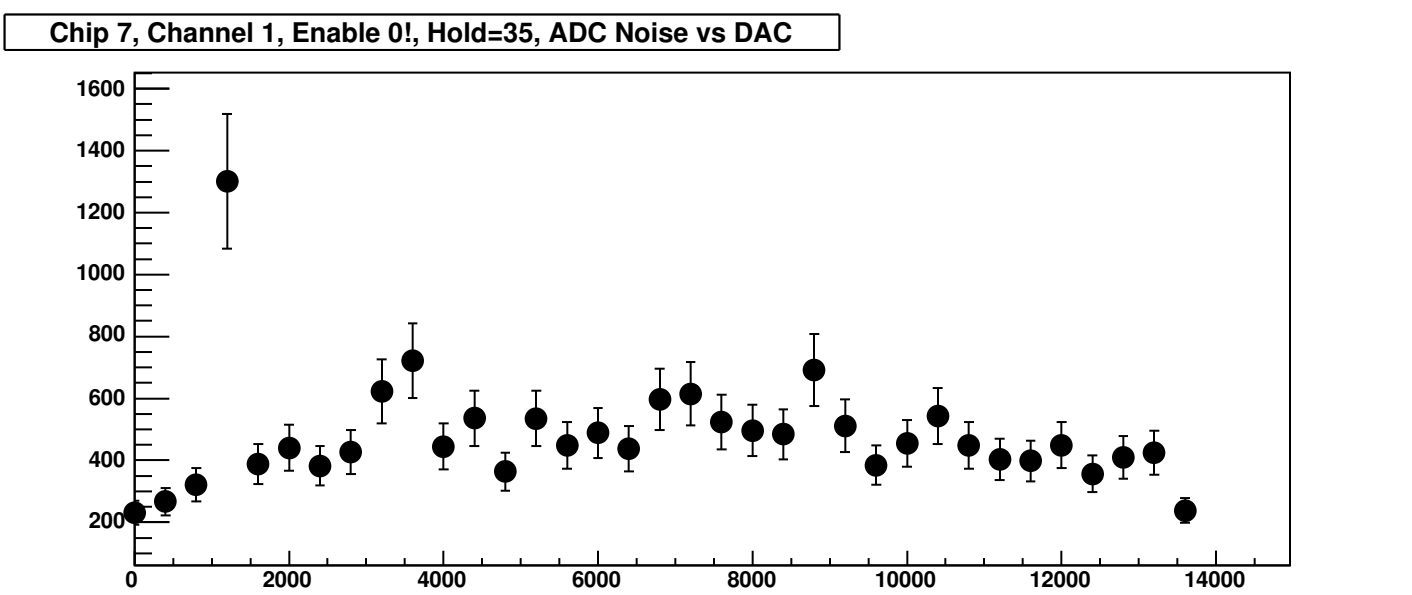
Chip 7, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC

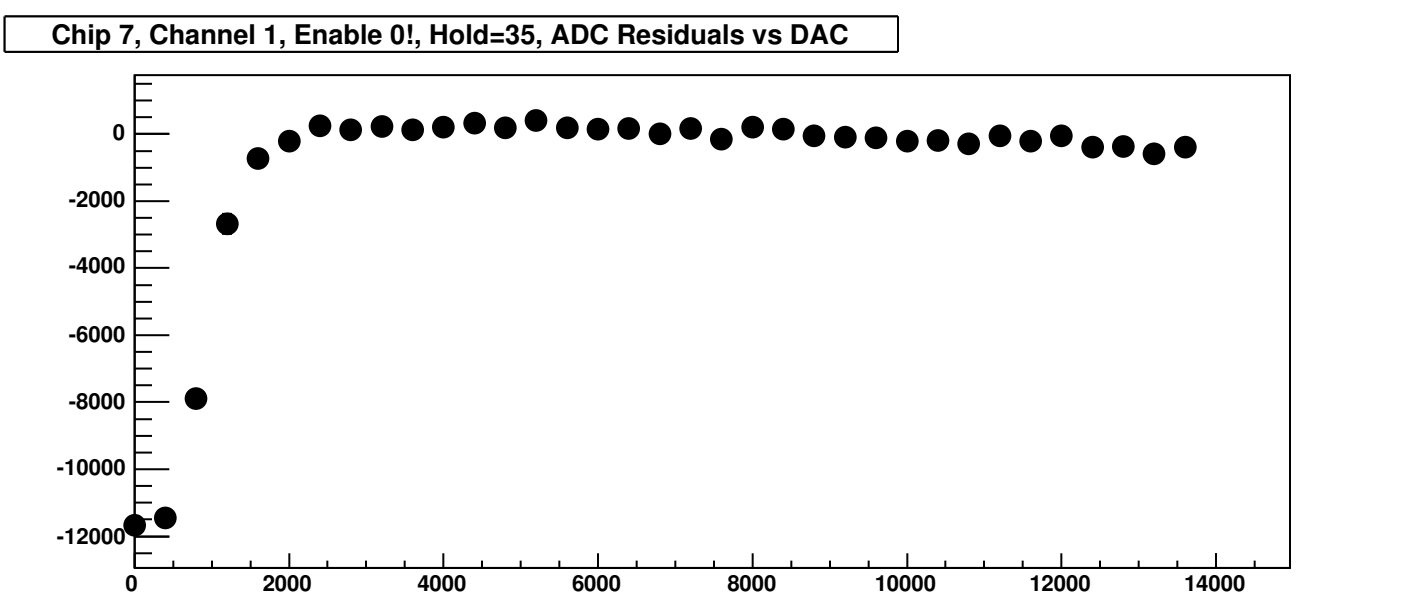


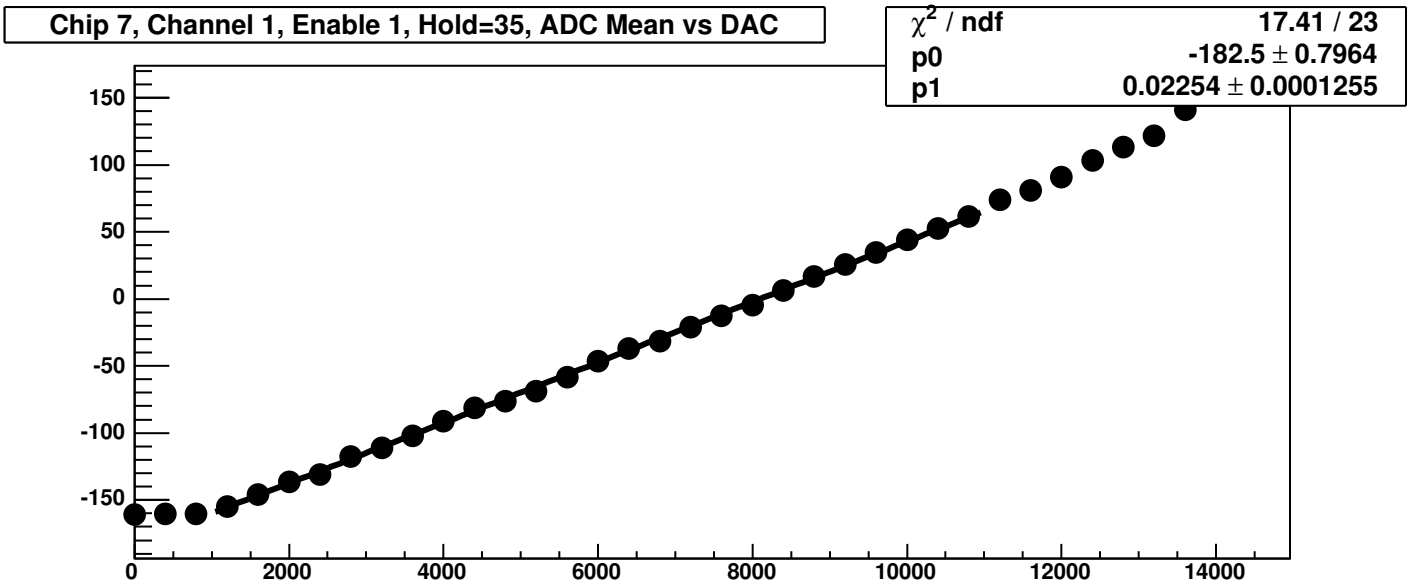
Chip 7, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC

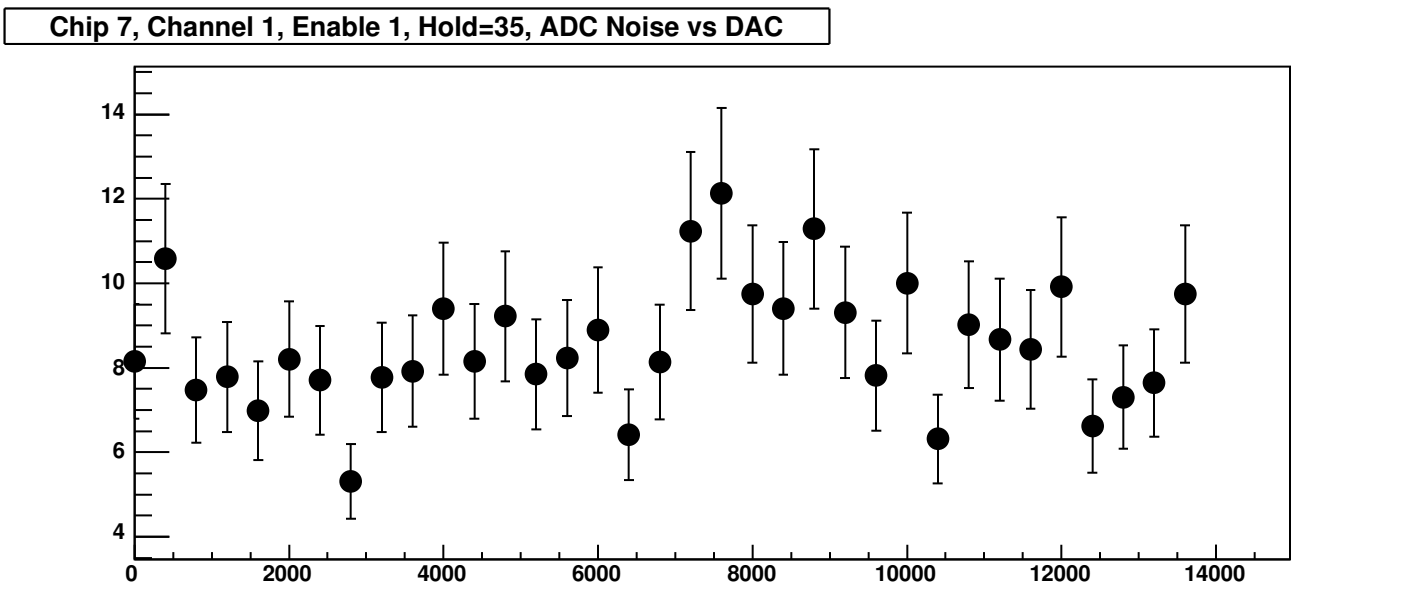


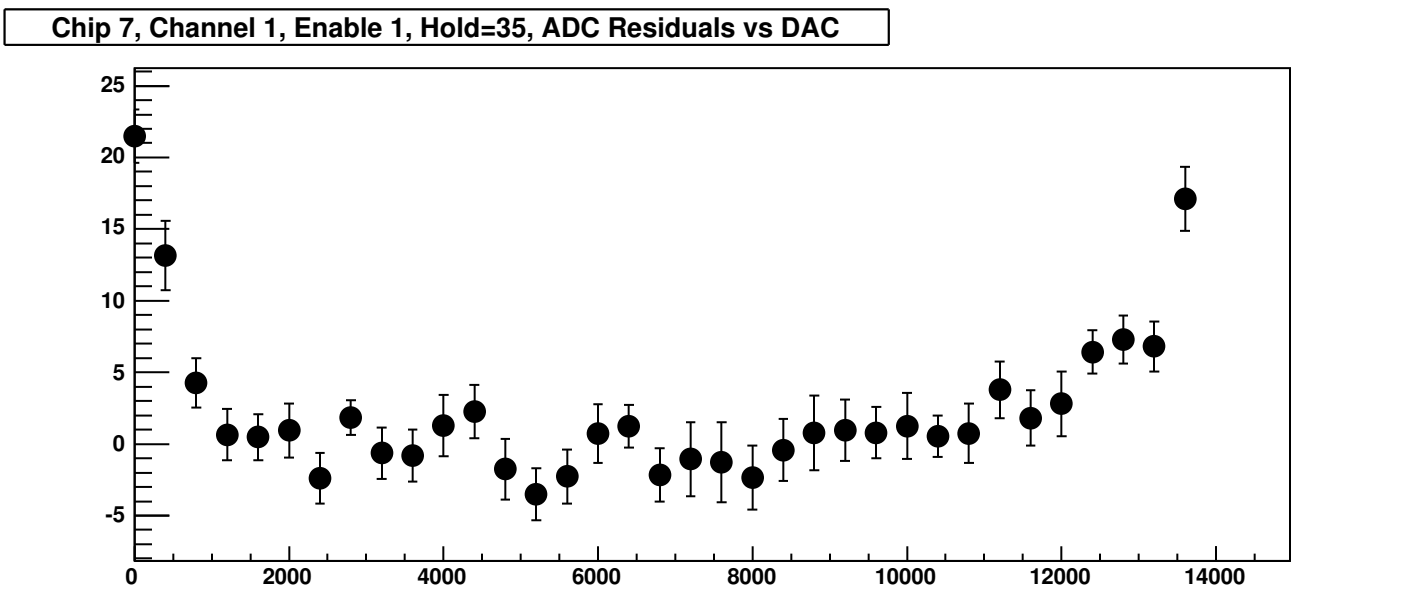

Chip 7, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 224.5 / 23
p0 2.753e+04 ± 50.72
p1 0.06458 ± 0.007644
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

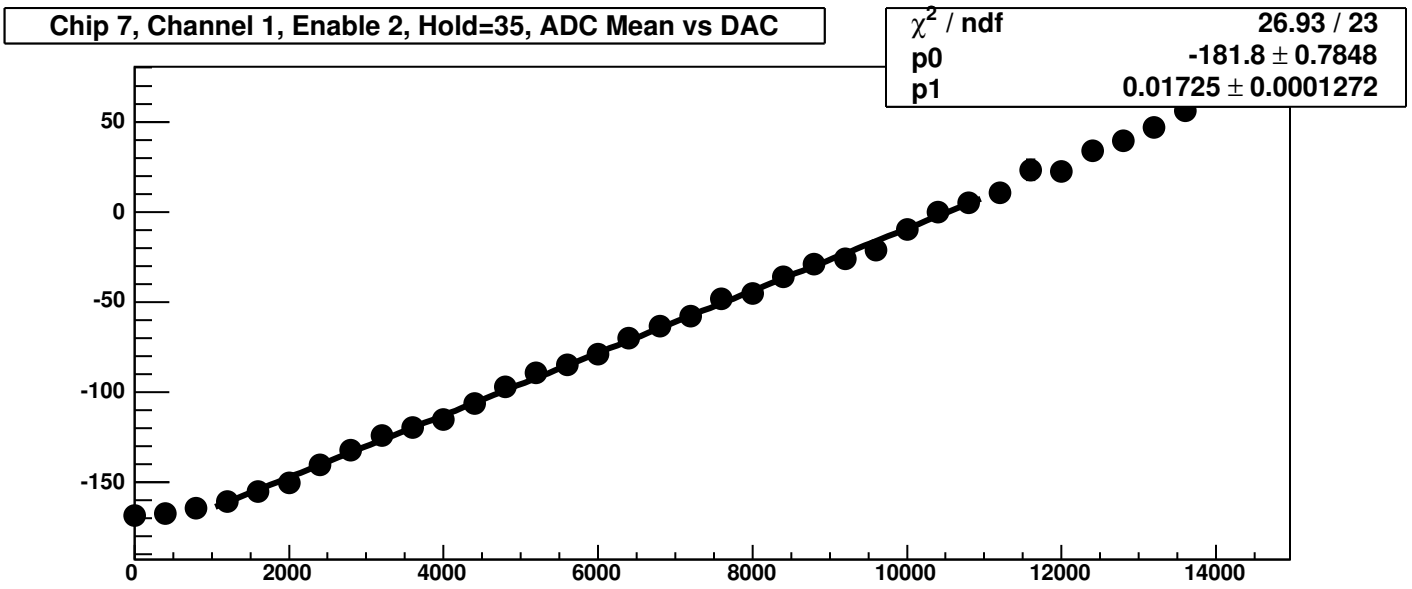

Chip 7, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

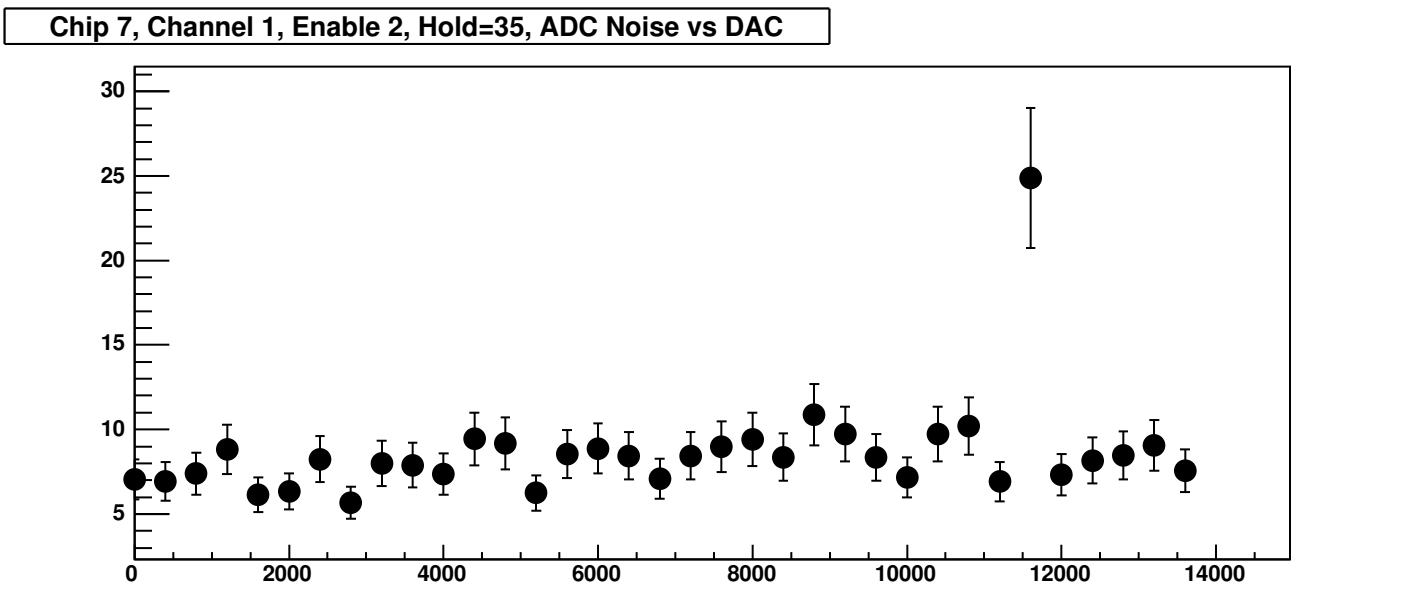

Chip 7, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
2000
4000
6000
8000
10000
12000
14000

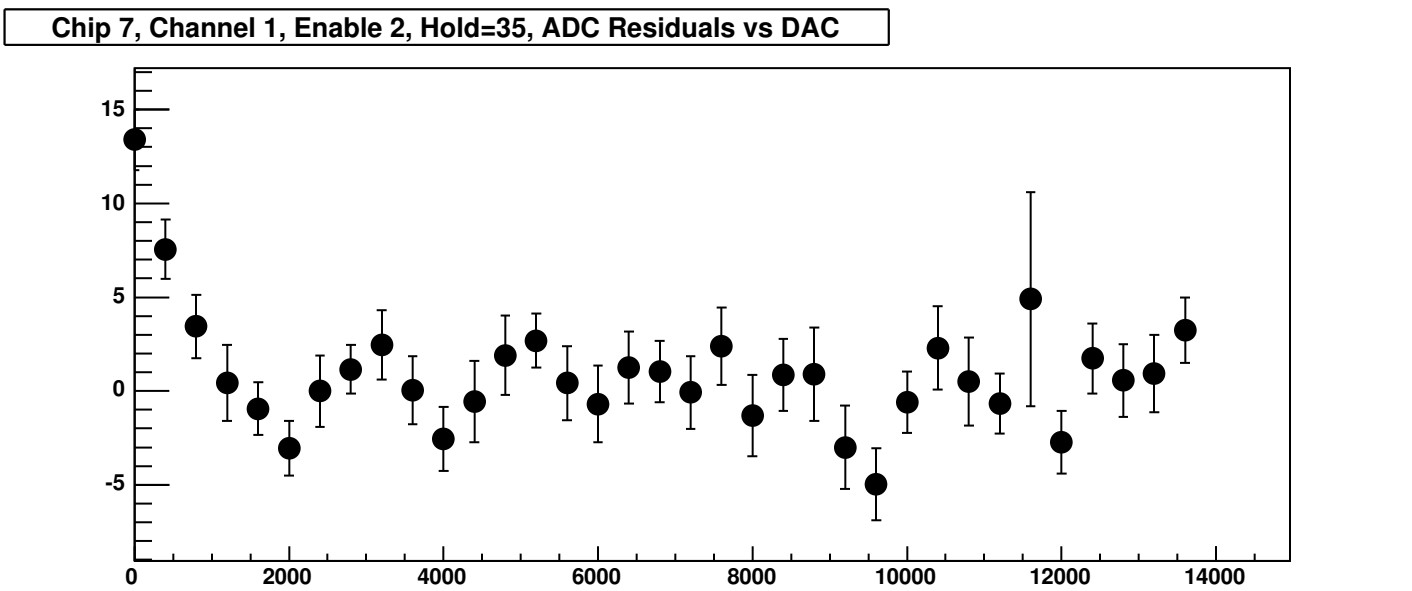

Chip 7, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 17.41 / 23
p0 -182.5 ± 0.7964
p1 0.02254 ± 0.0001255
150
100
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

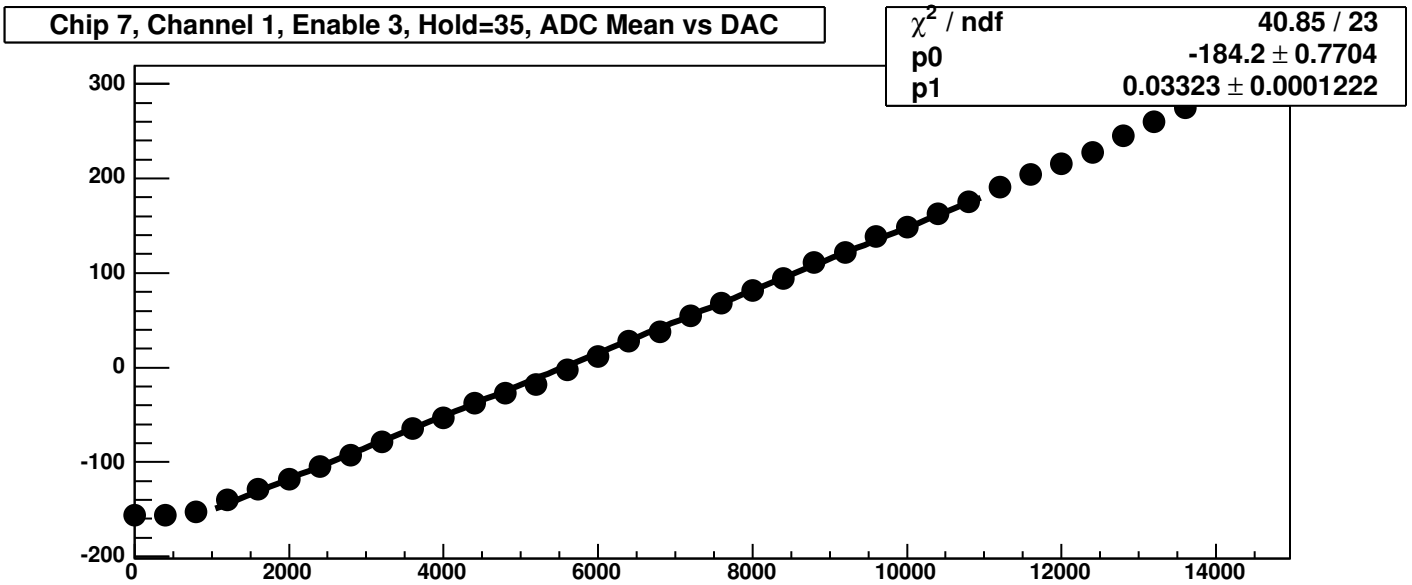

Chip 7, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

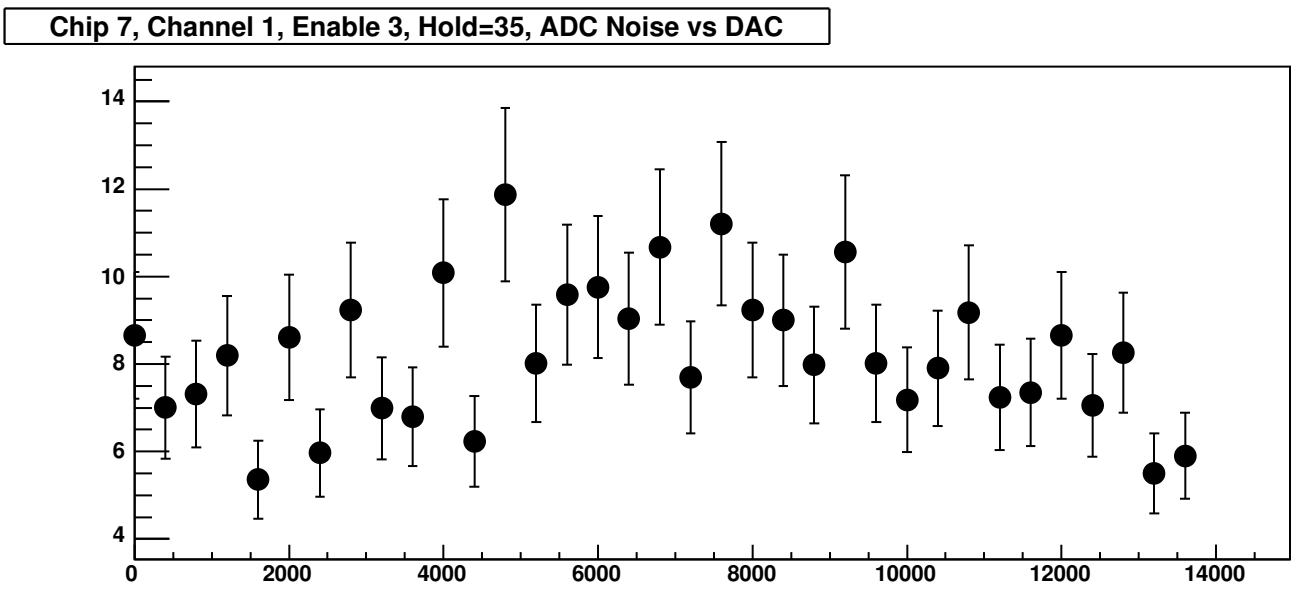

Chip 7, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

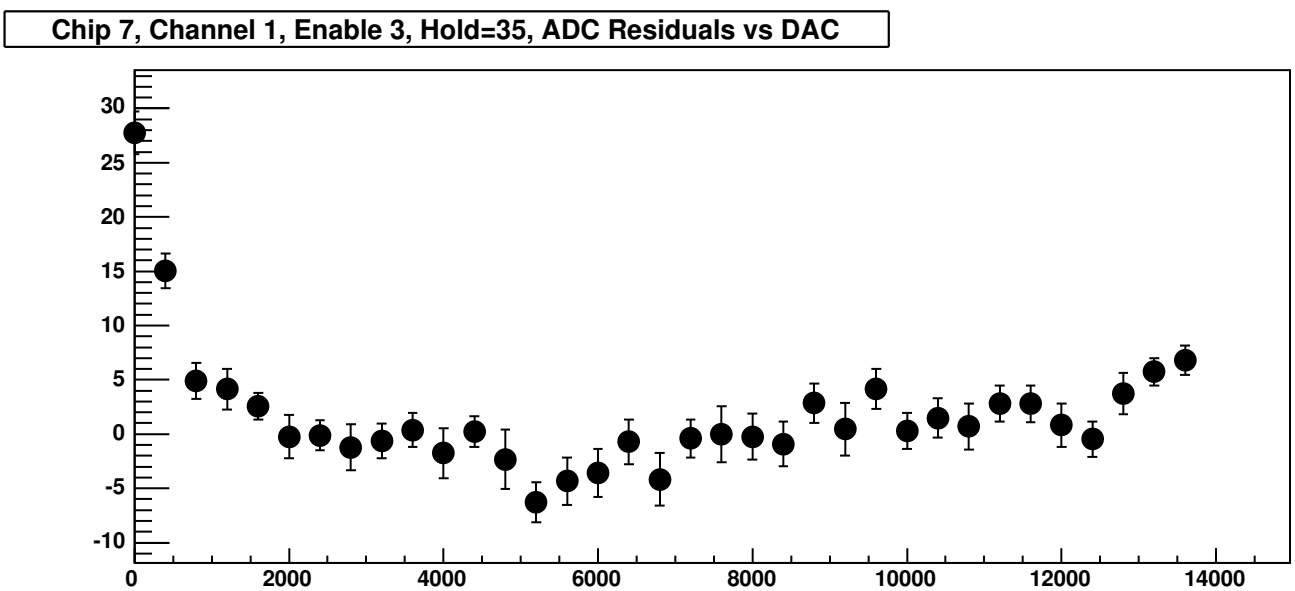

Chip 7, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 26.93 / 23
p0 -181.8 ± 0.7848
p1 0.01725 ± 0.0001272
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

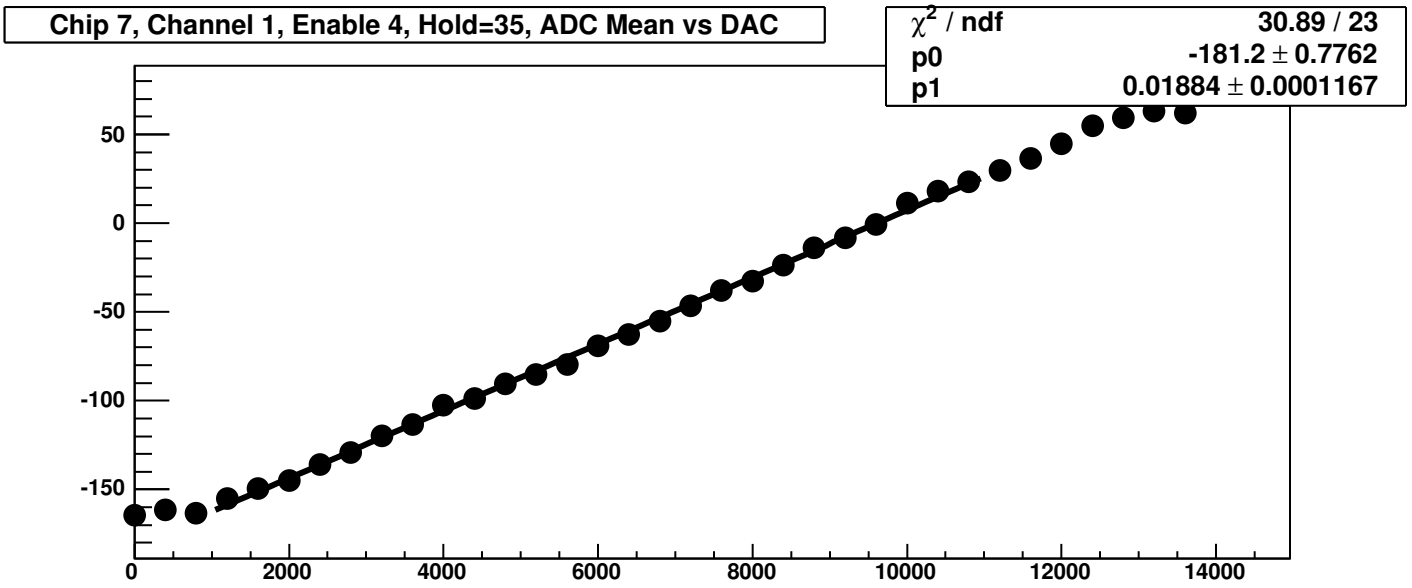

Chip 7, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

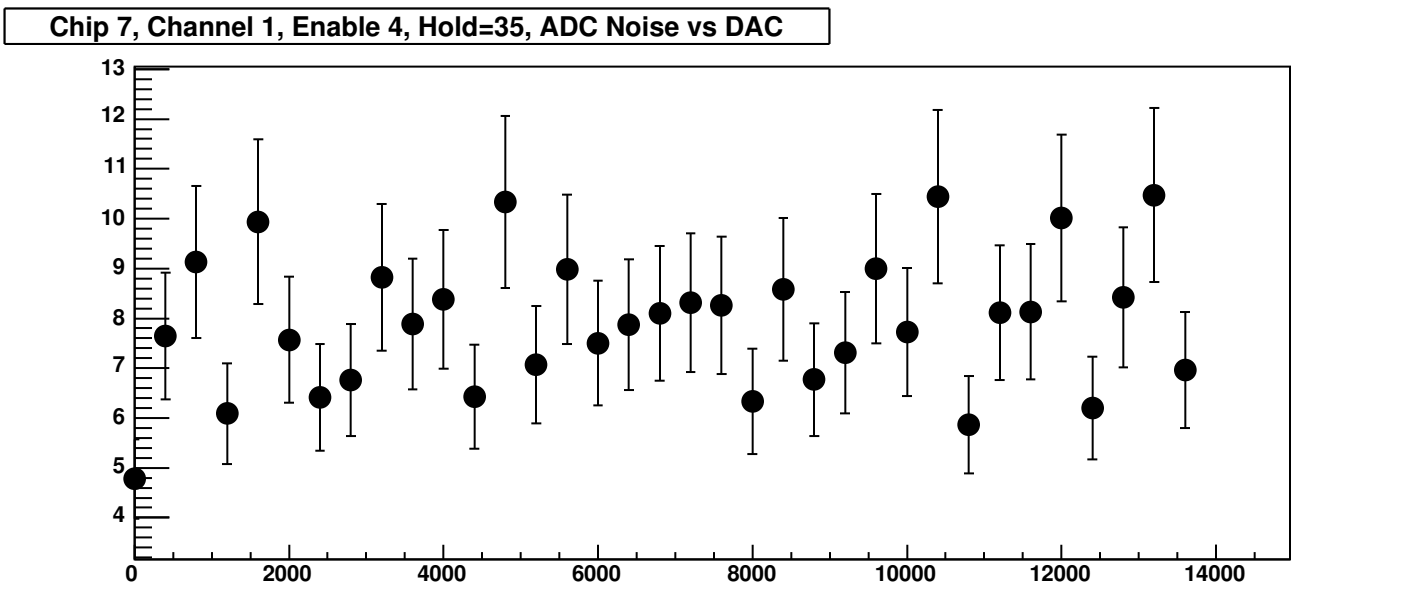

Chip 7, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

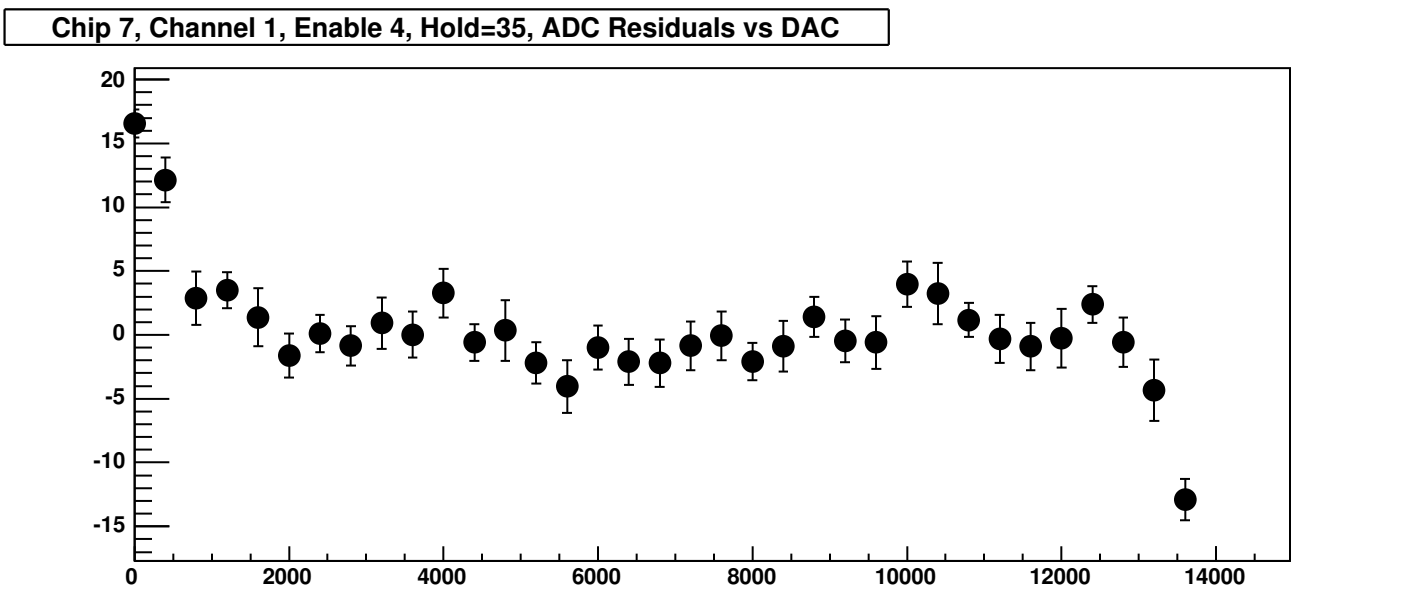

Chip 7, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 40.85 / 23
p0 -184.2 ± 0.7704
p1 0.03323 ± 0.0001222
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

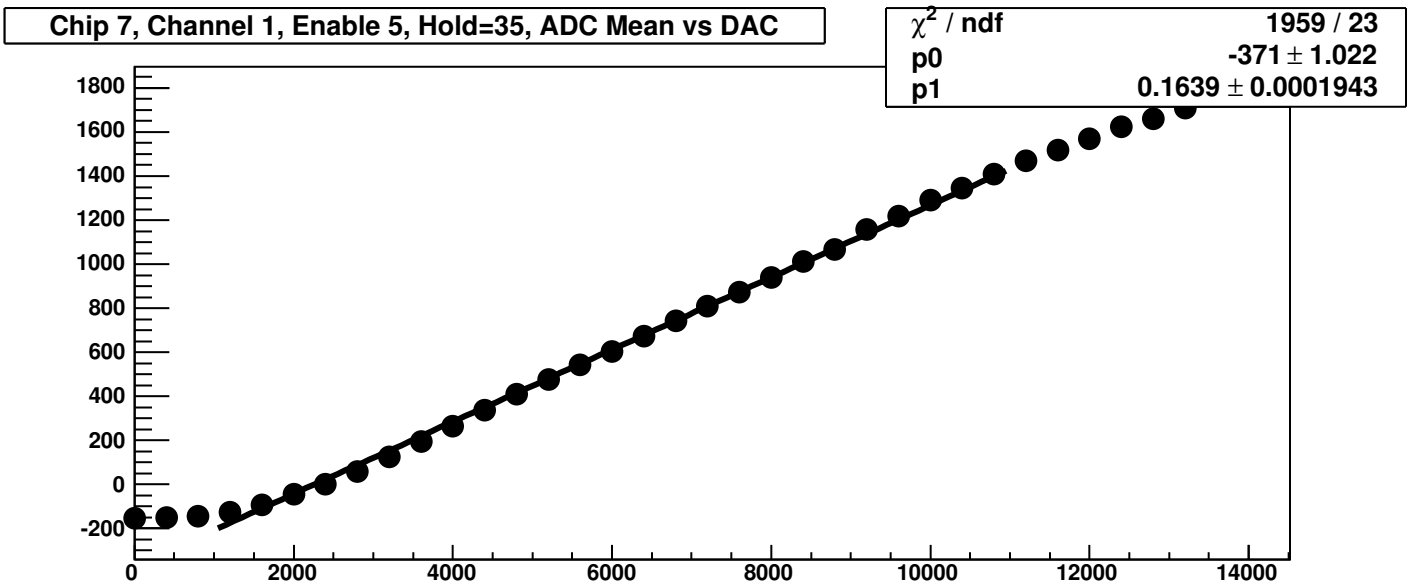

Chip 7, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

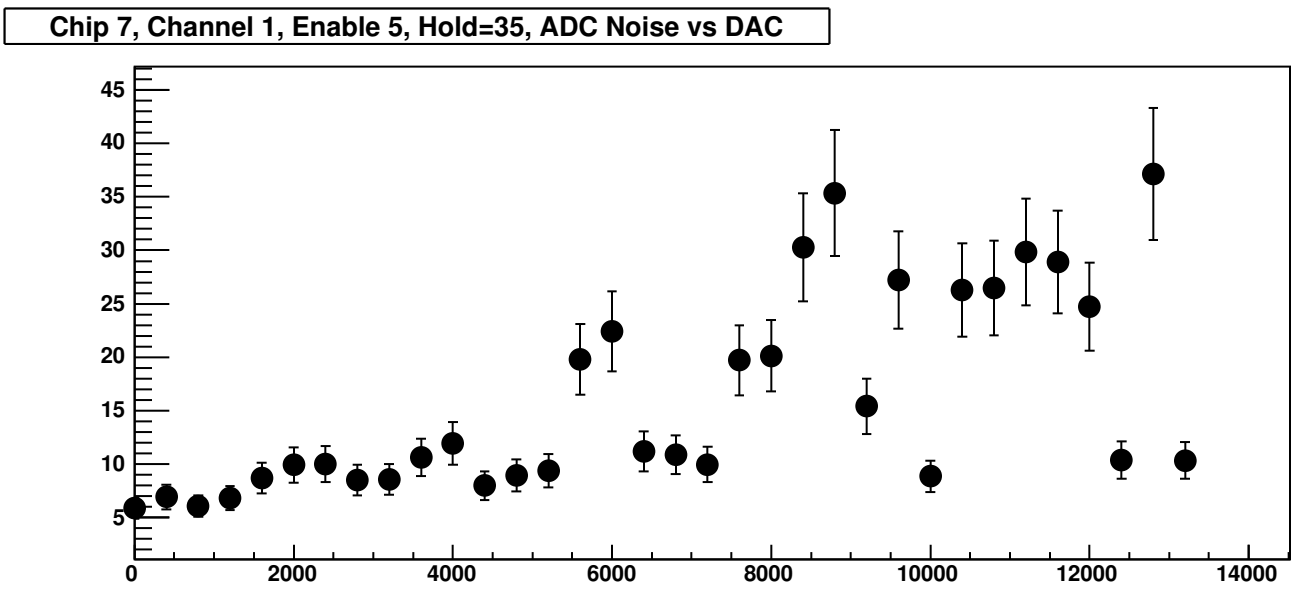

Chip 7, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

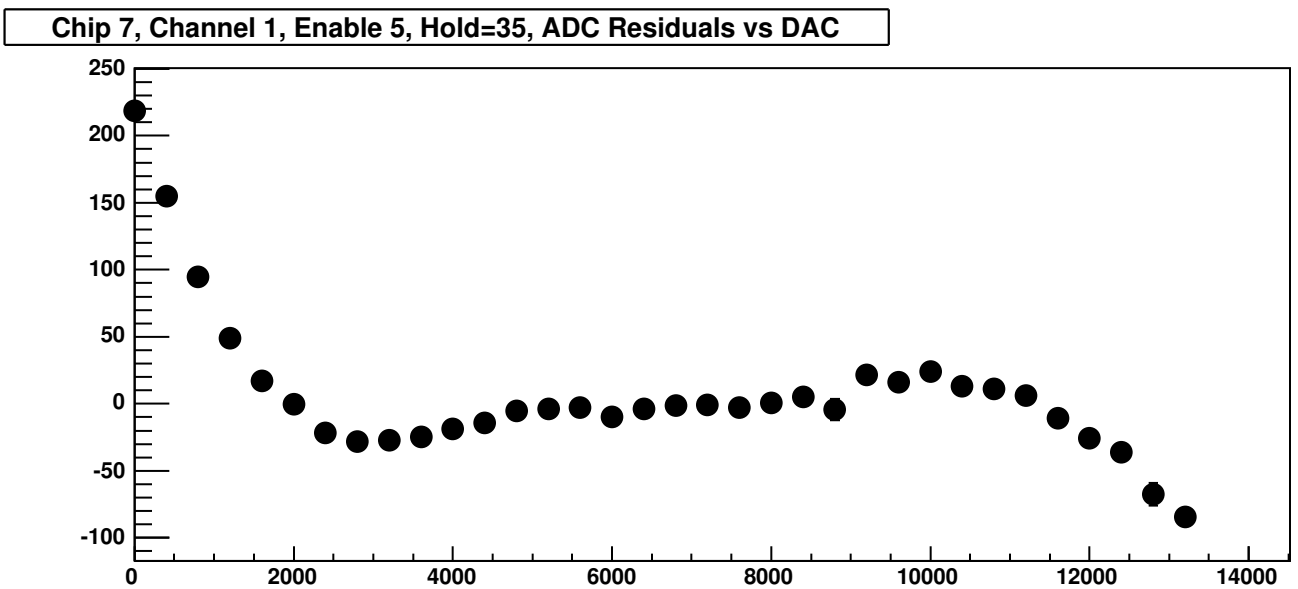

Chip 7, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.89 / 23
p0 -181.2 ± 0.7762
p1 0.01884 ± 0.0001167
50
0
-50
-100
-150
0
2000
4000
6000
8000
10000
12000
14000

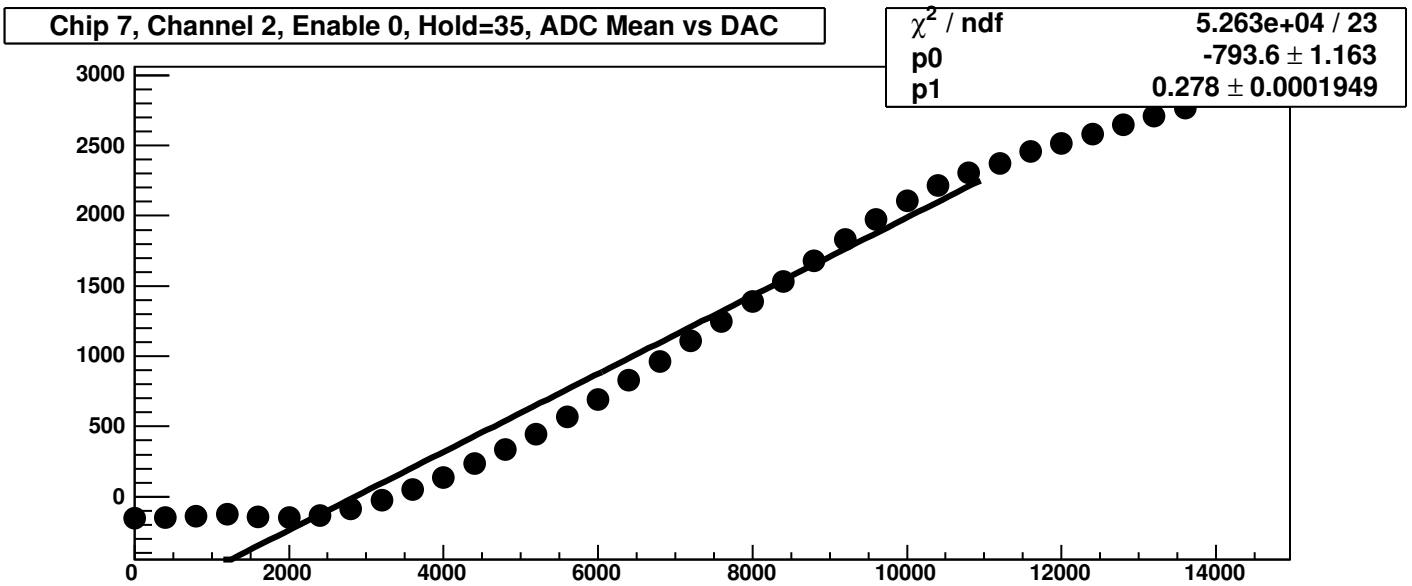

Chip 7, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

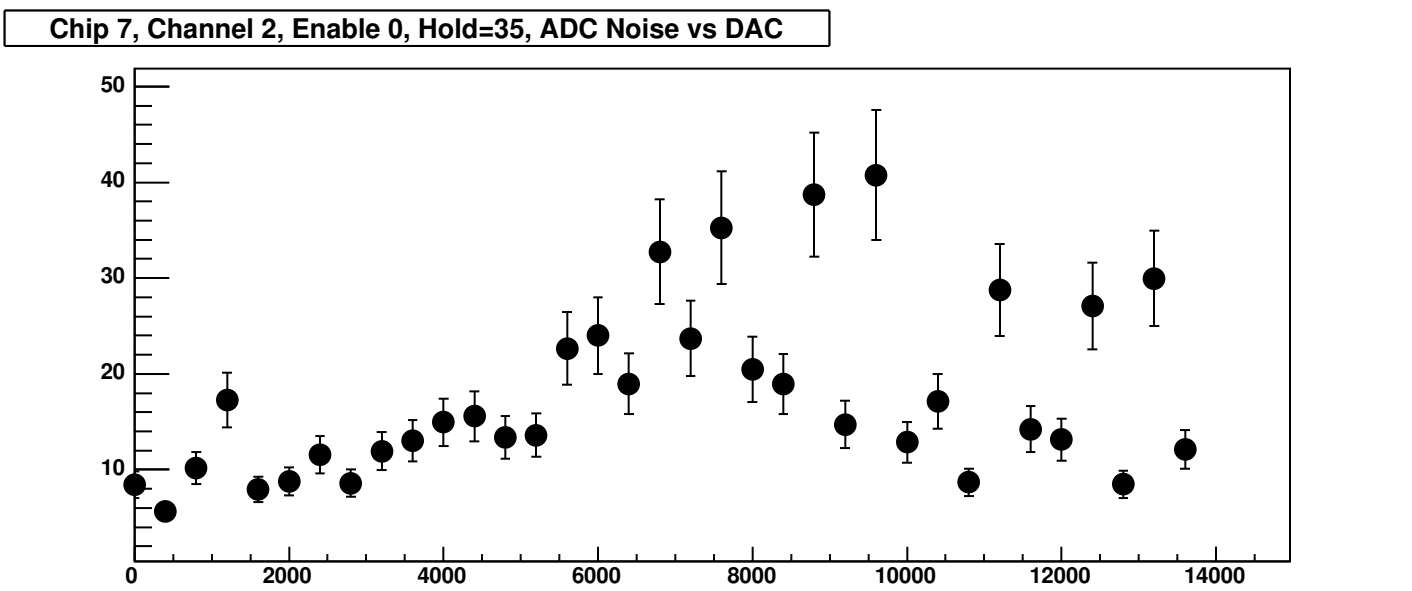

Chip 7, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

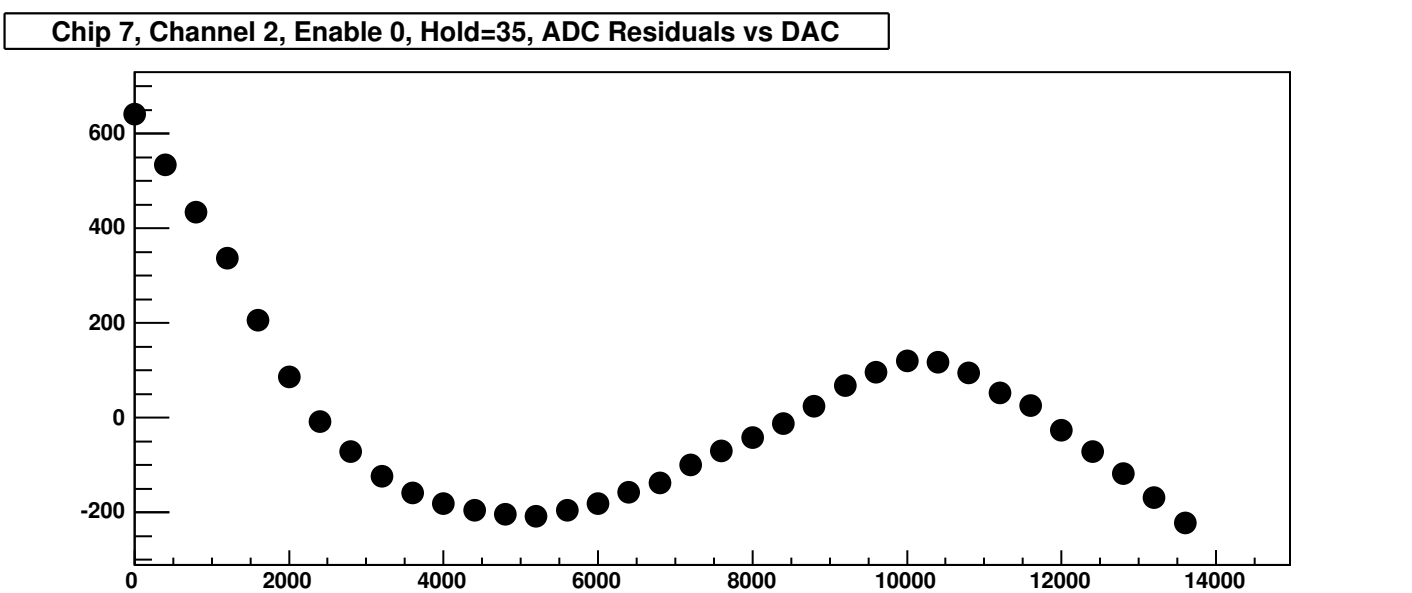

Chip 7, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1959 / 23
p0 -371 ± 1.022
p1 0.1639 ± 0.0001943
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

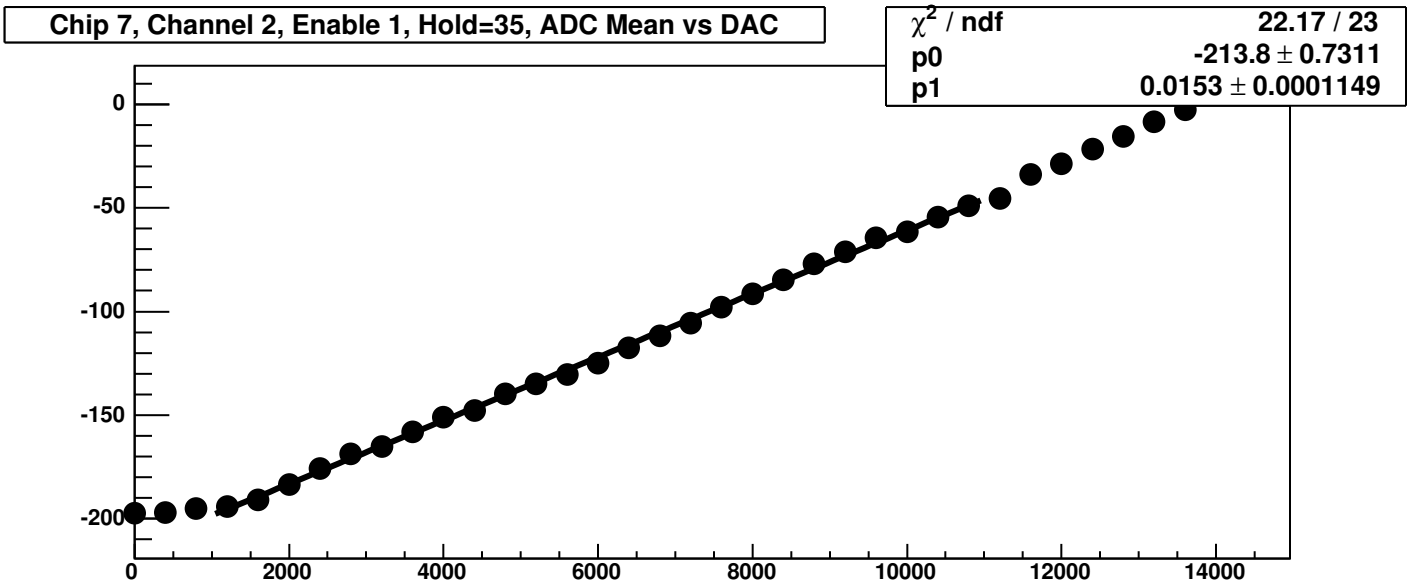

Chip 7, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

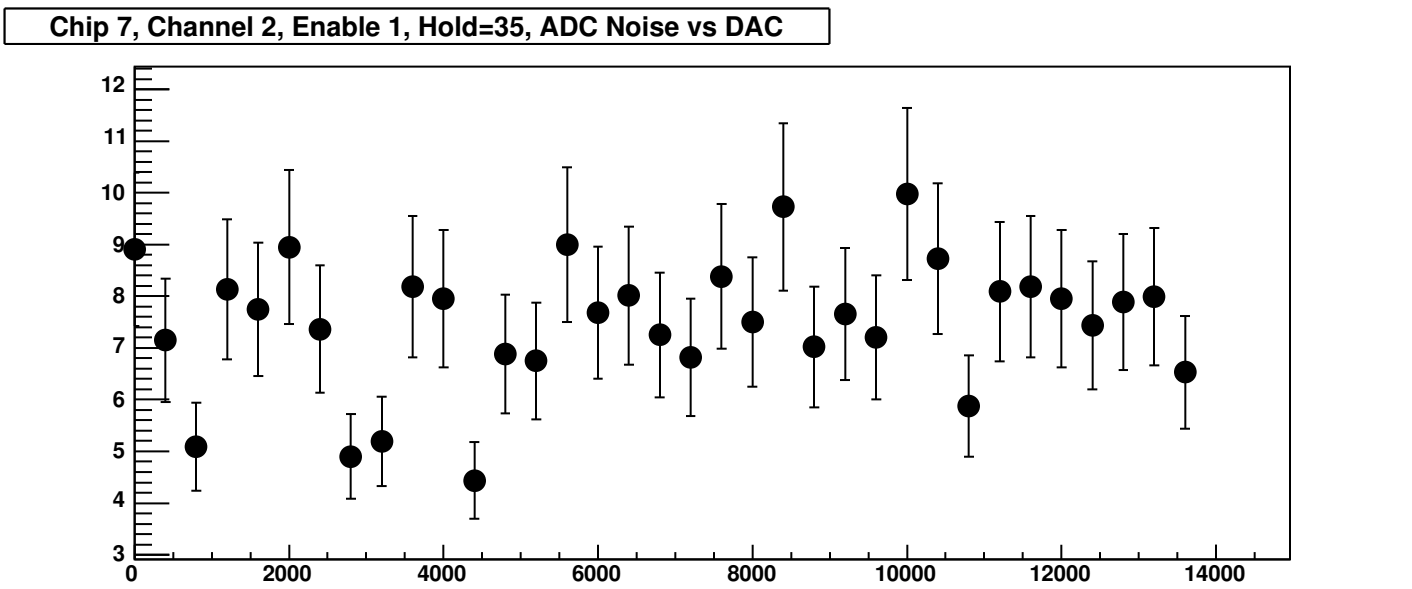

Chip 7, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

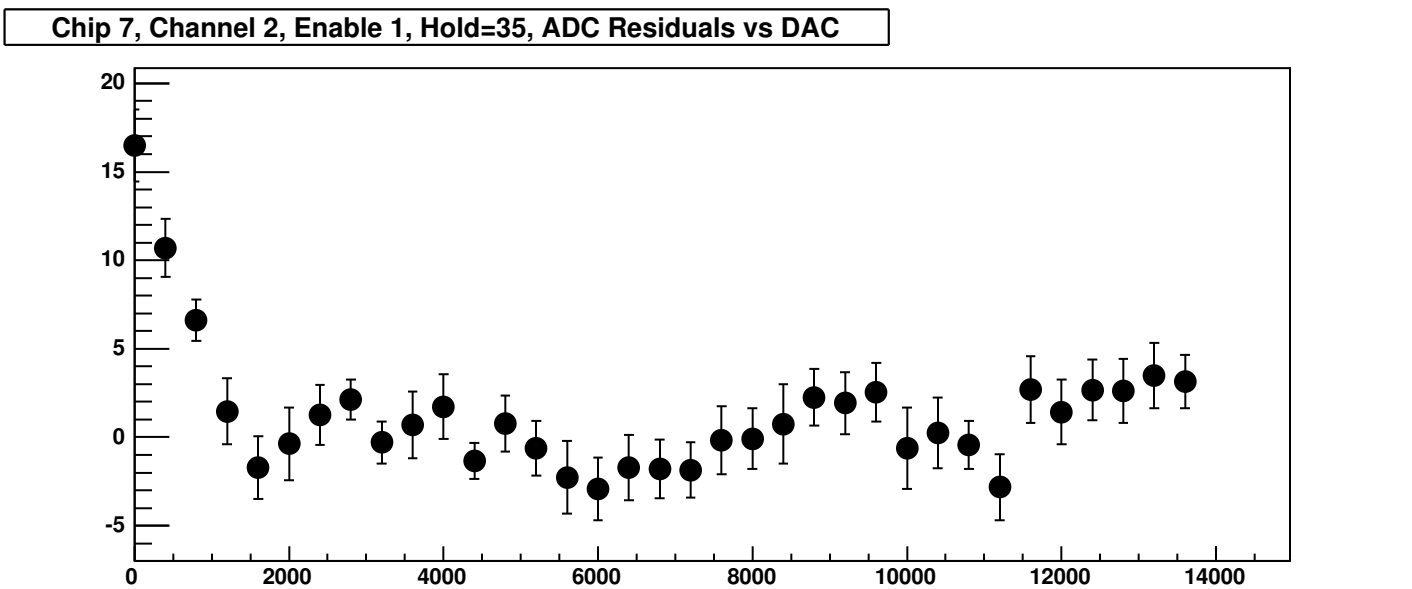

Chip 7, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.263e+04 / 23
p0 -793.6 ± 1.163
p1 0.278 ± 0.0001949
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

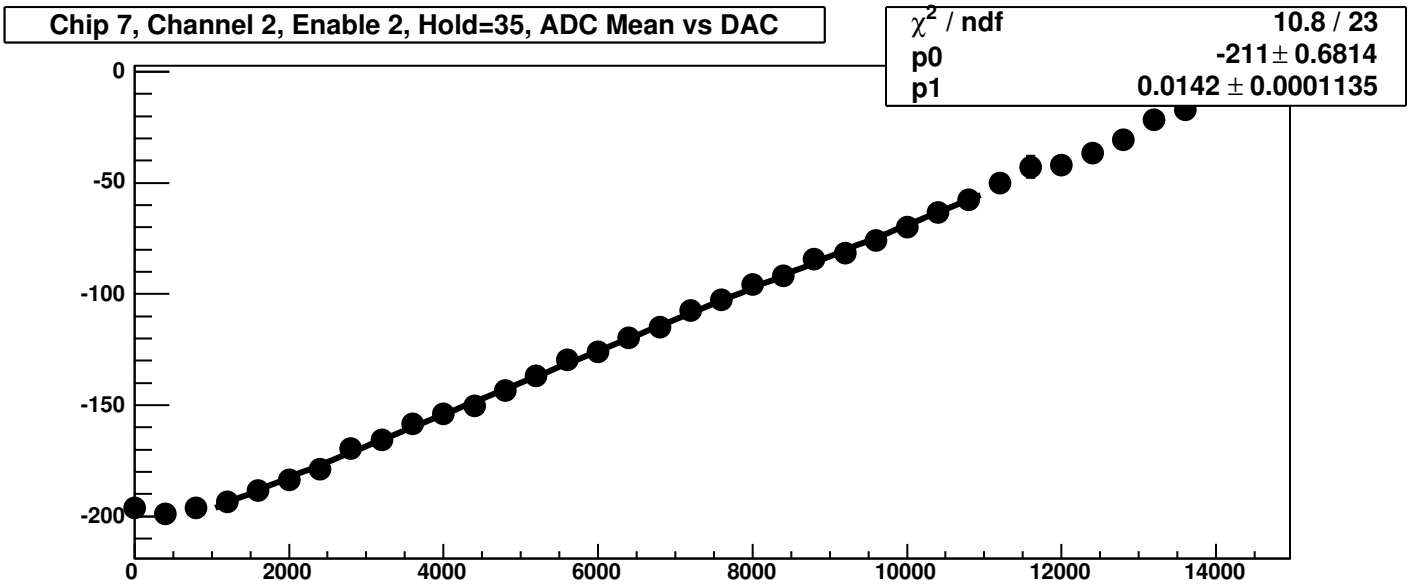

Chip 7, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

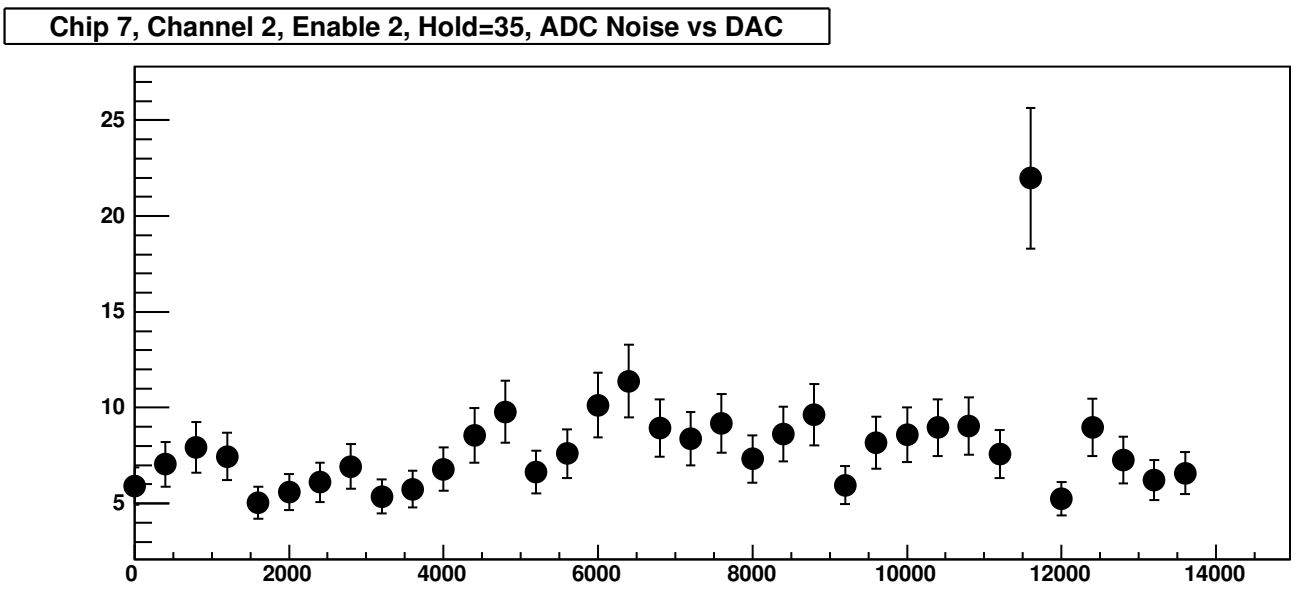

Chip 7, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

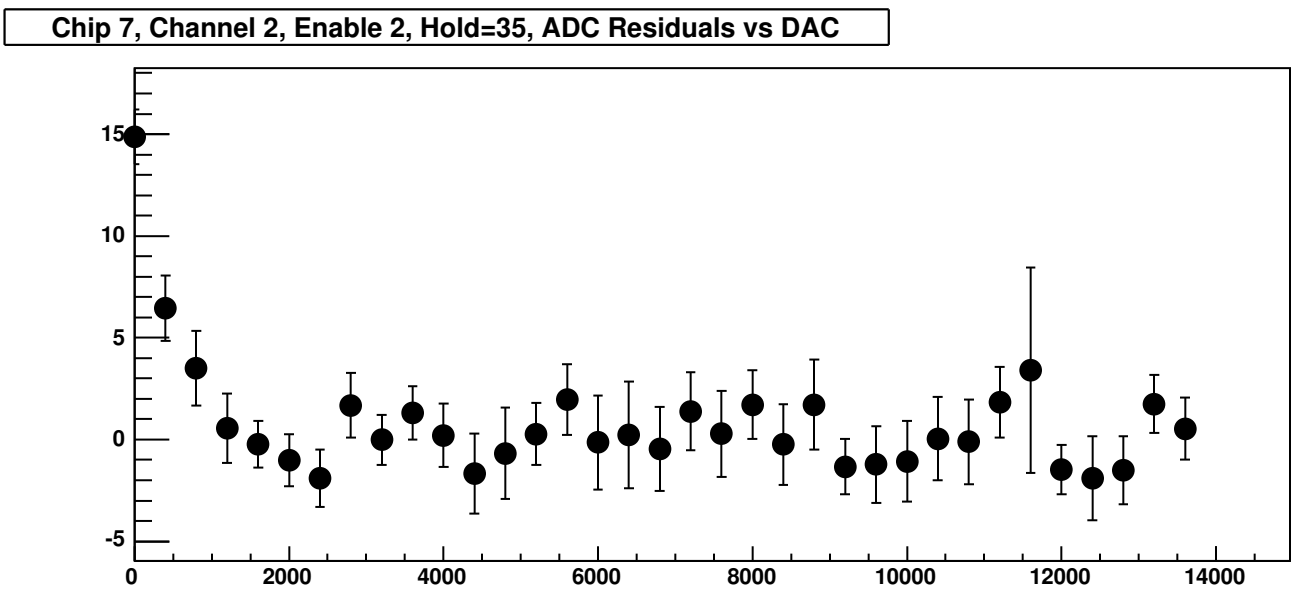

Chip 7, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 22.17 / 23
p0 -213.8 ± 0.7311
p1 0.0153 ± 0.0001149
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

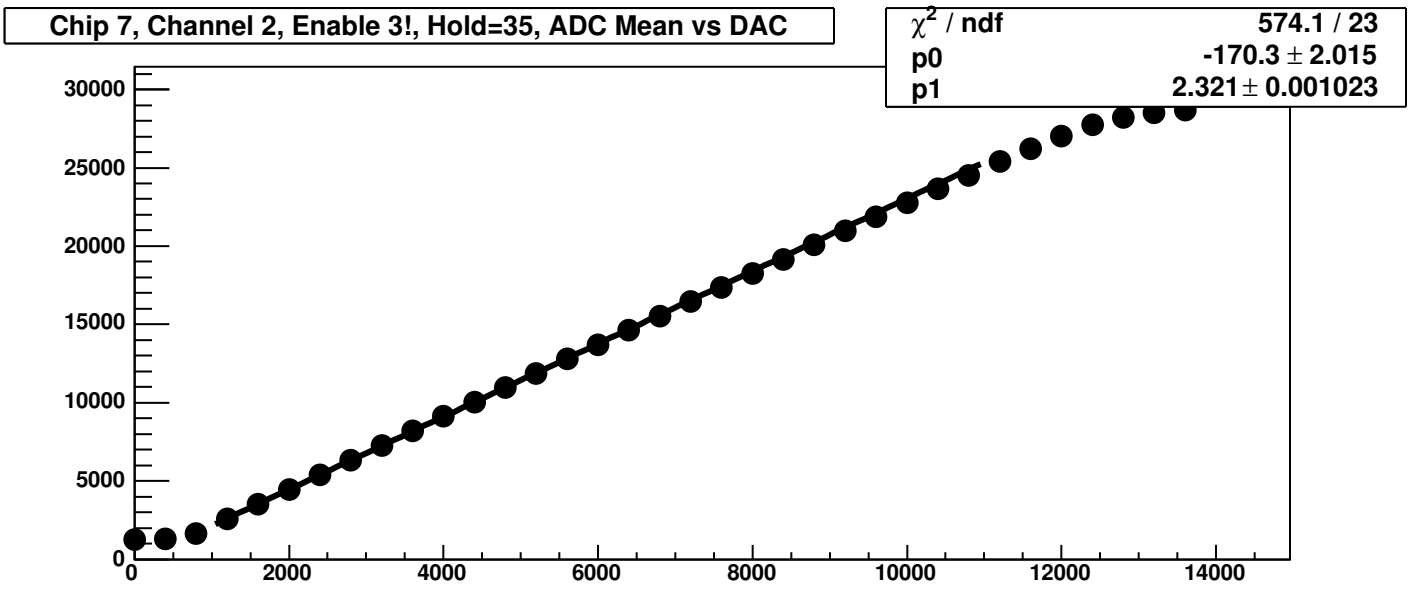

Chip 7, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

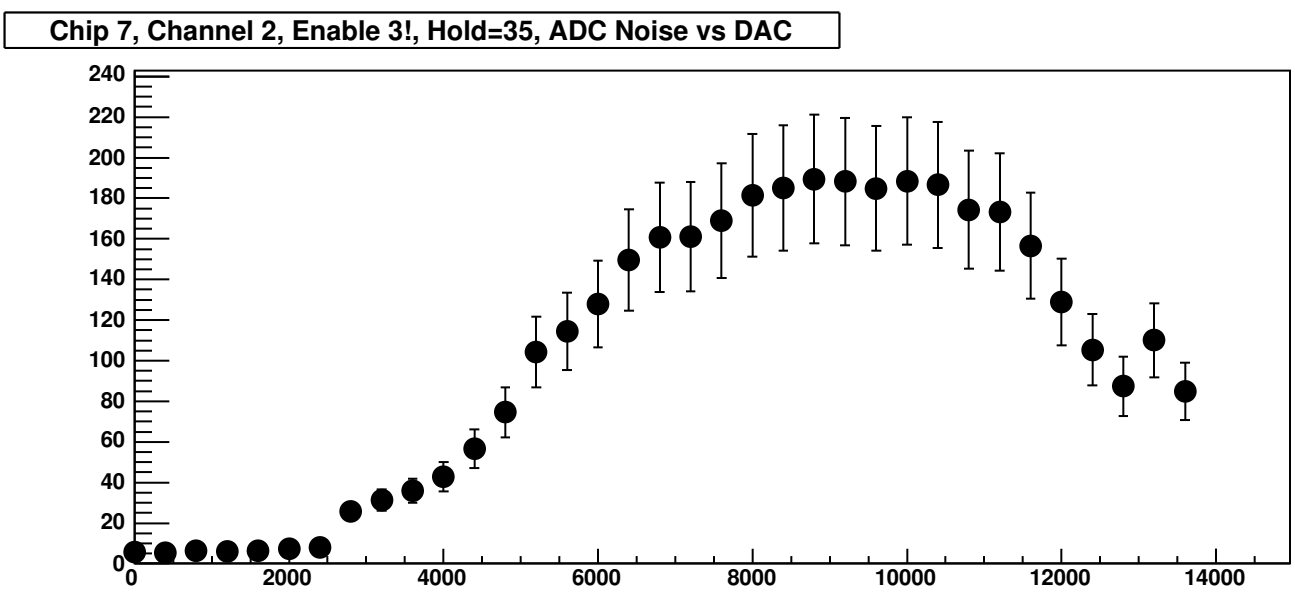

Chip 7, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

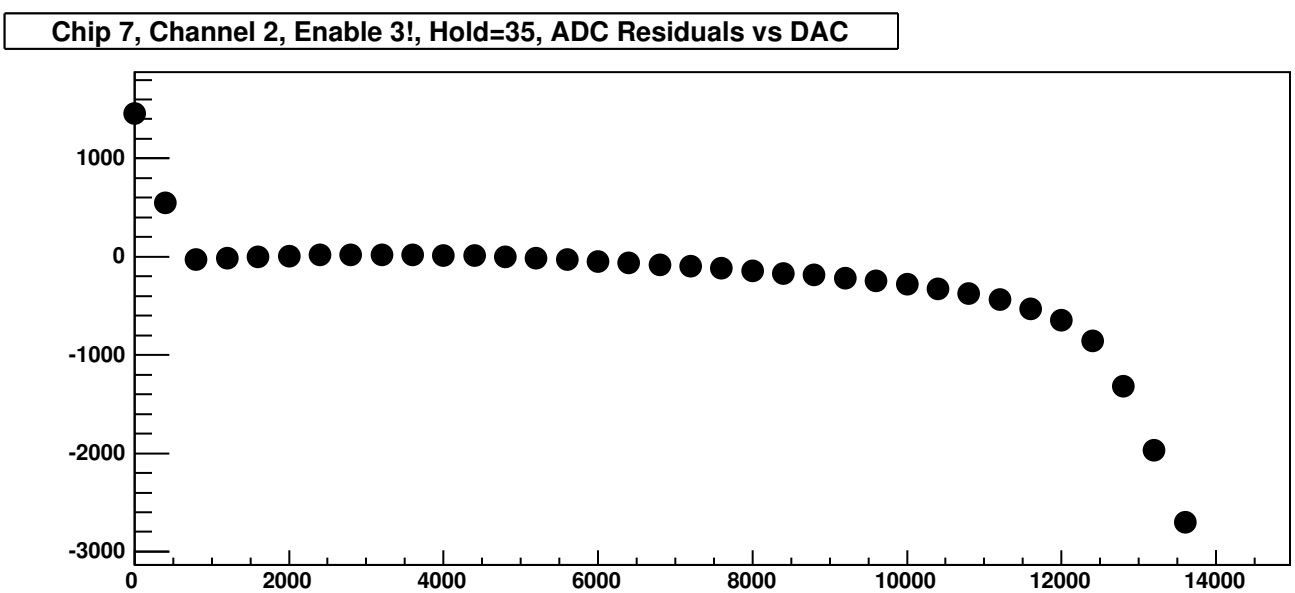

Chip 7, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 10.8 / 23
p0 -211 ± 0.6814
p1 0.0142 ± 0.0001135
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

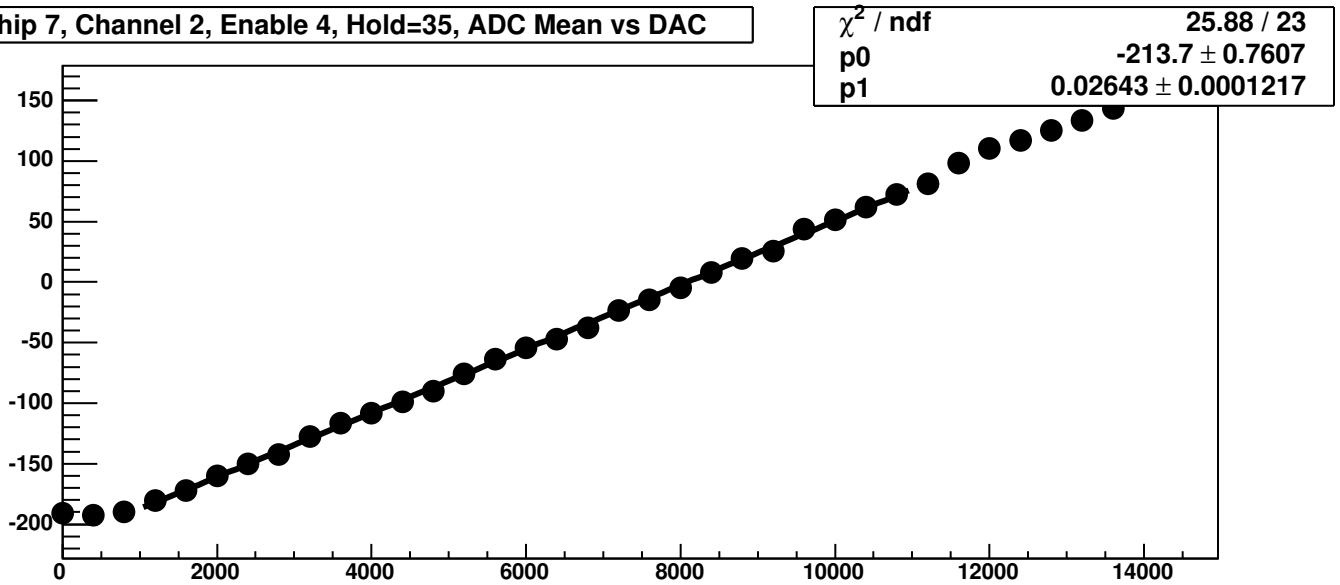

Chip 7, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 574.1 / 23
p0 -170.3 ± 2.015
p1 2.321 ± 0.001023
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

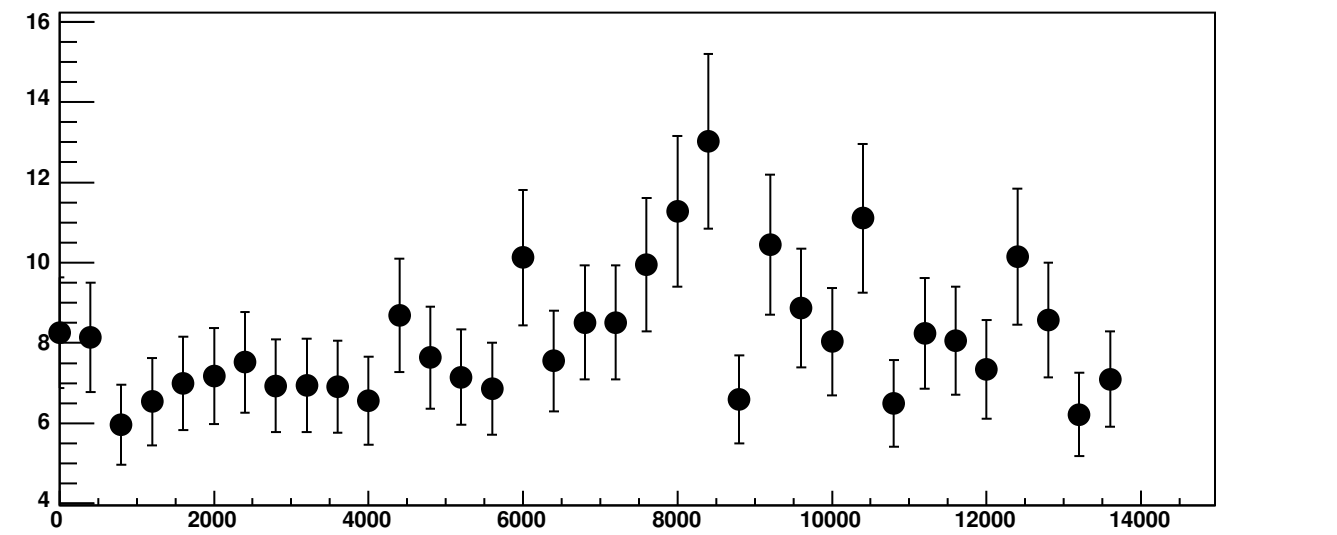

Chip 7, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
240
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

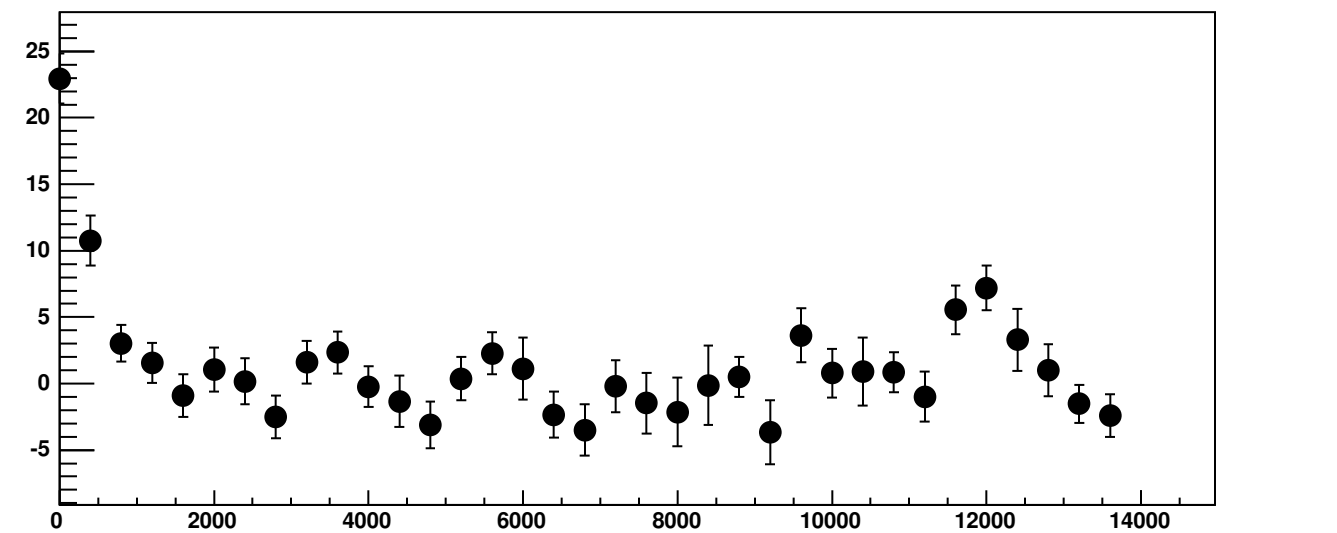
Chip 7, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

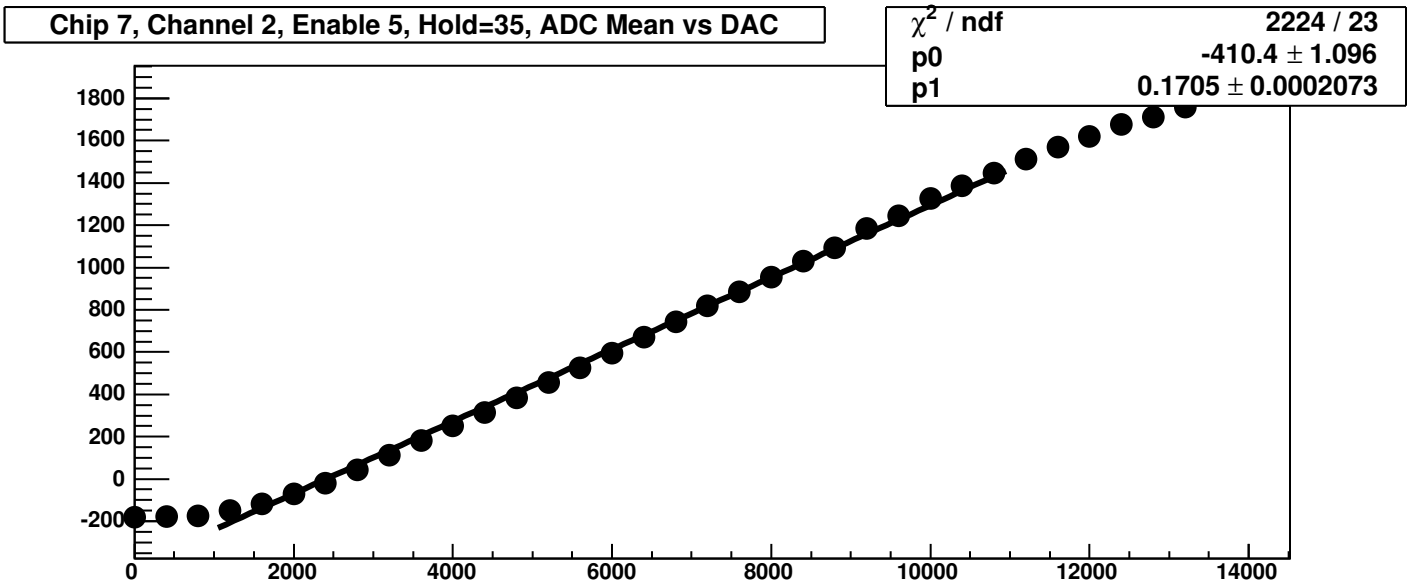


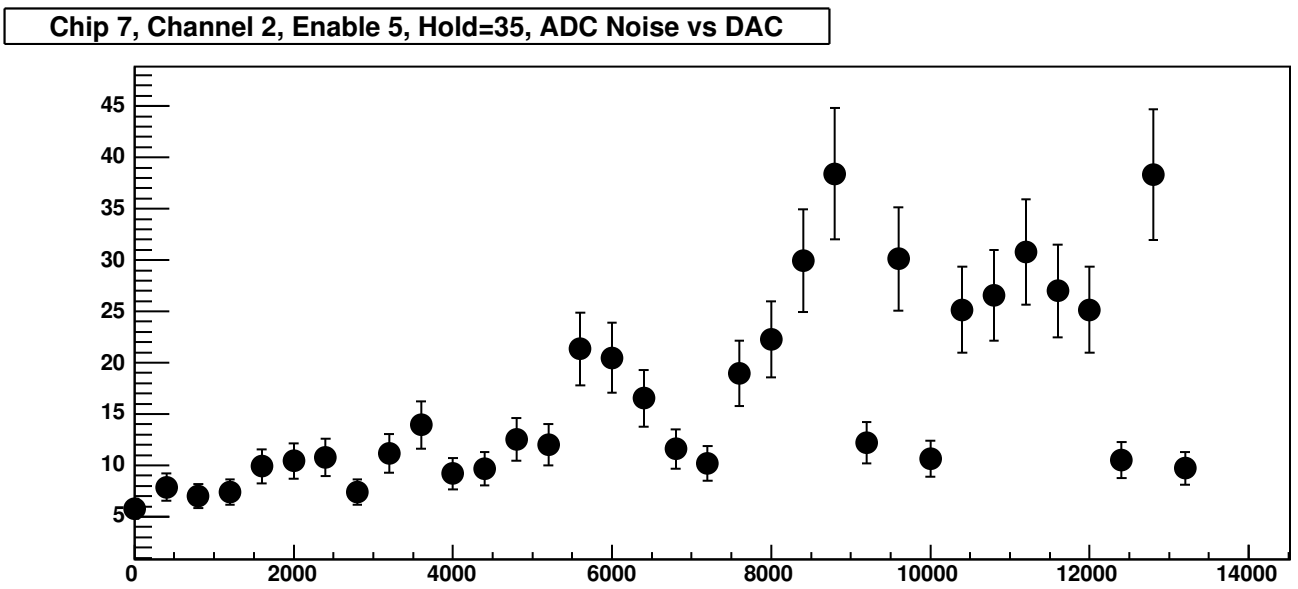
Chip 7, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

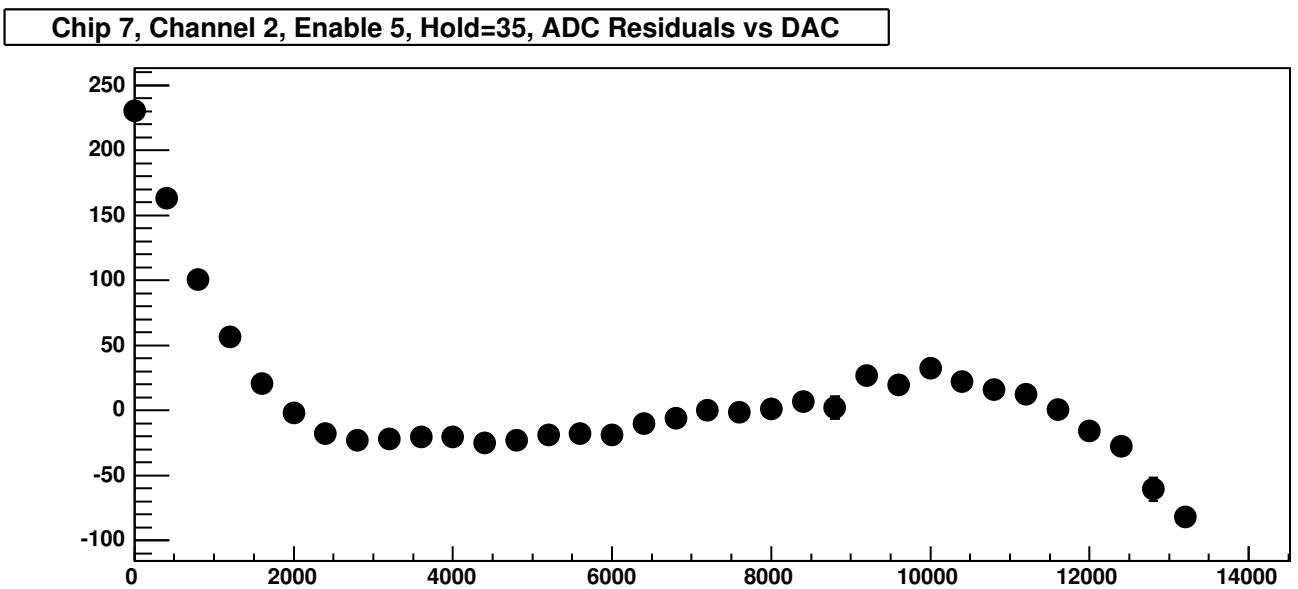


Chip 7, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

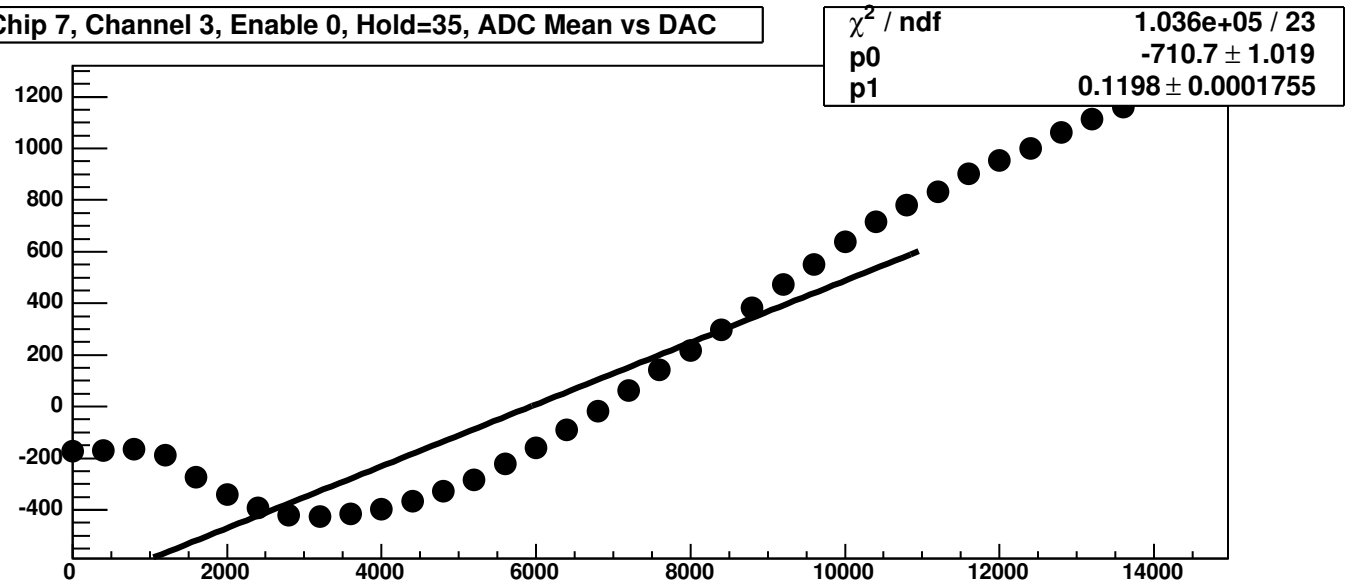



Chip 7, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2224 / 23
p0 -410.4 ± 1.096
p1 0.1705 ± 0.0002073
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

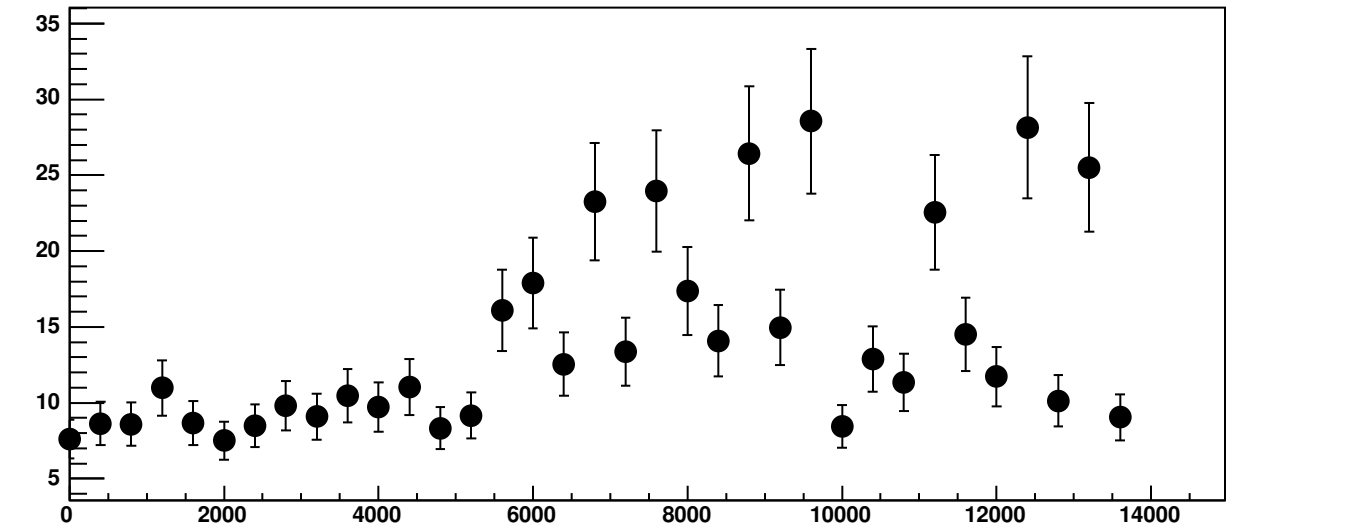

Chip 7, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

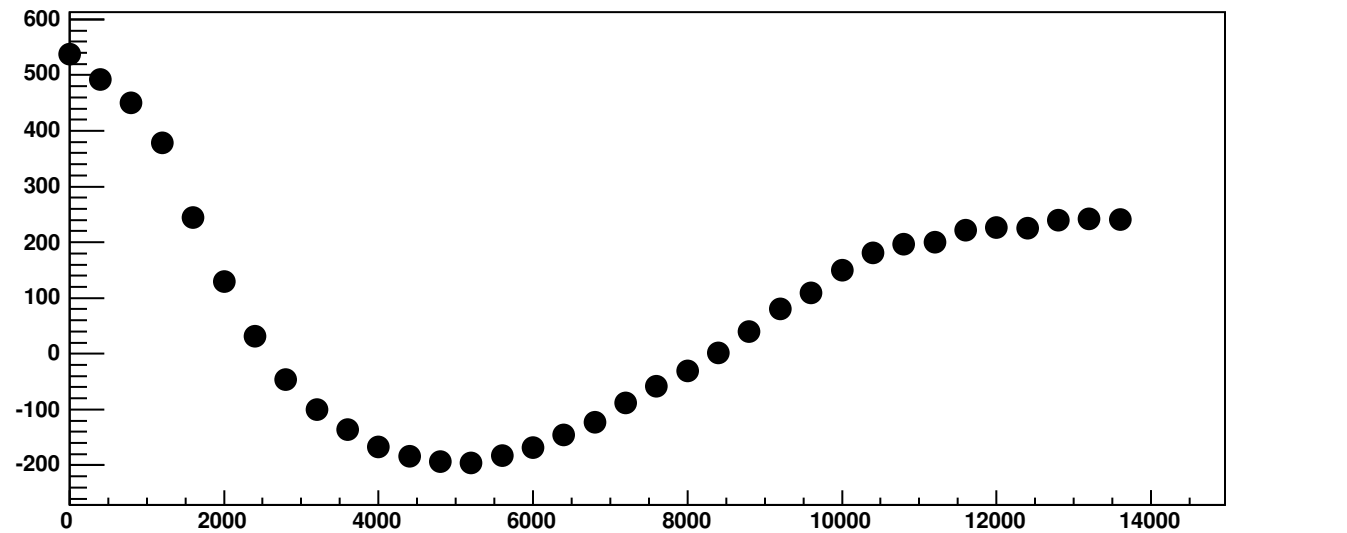
Chip 7, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

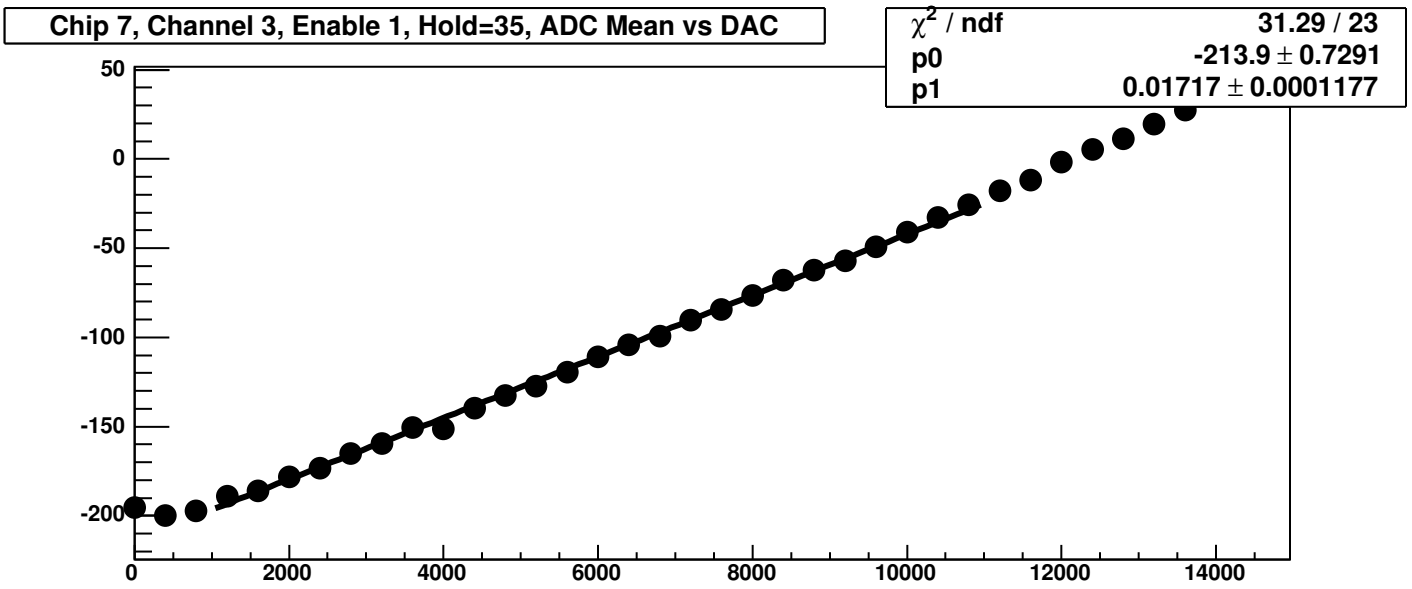


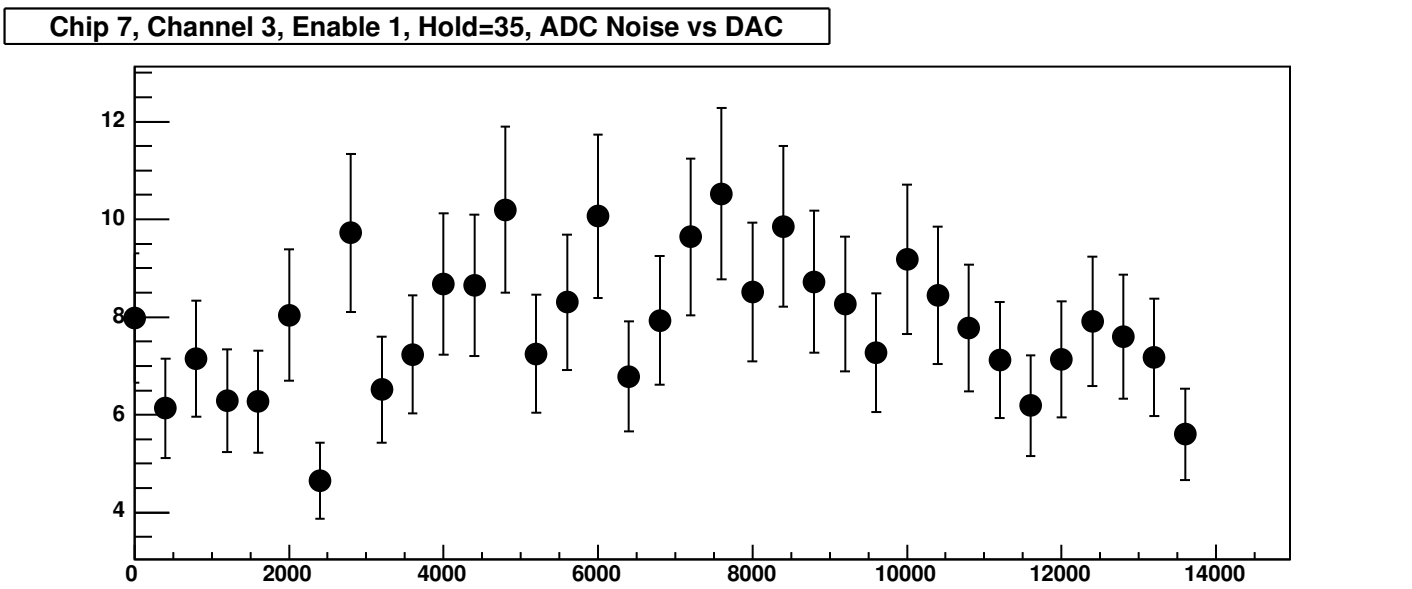
Chip 7, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

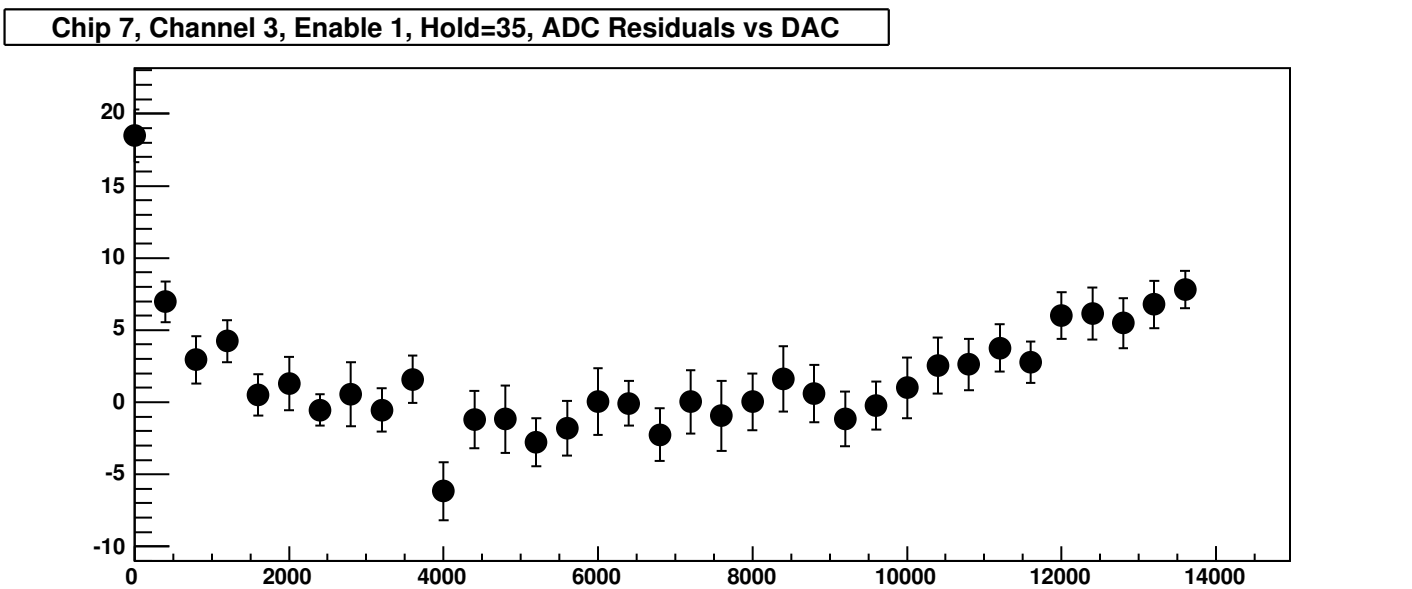


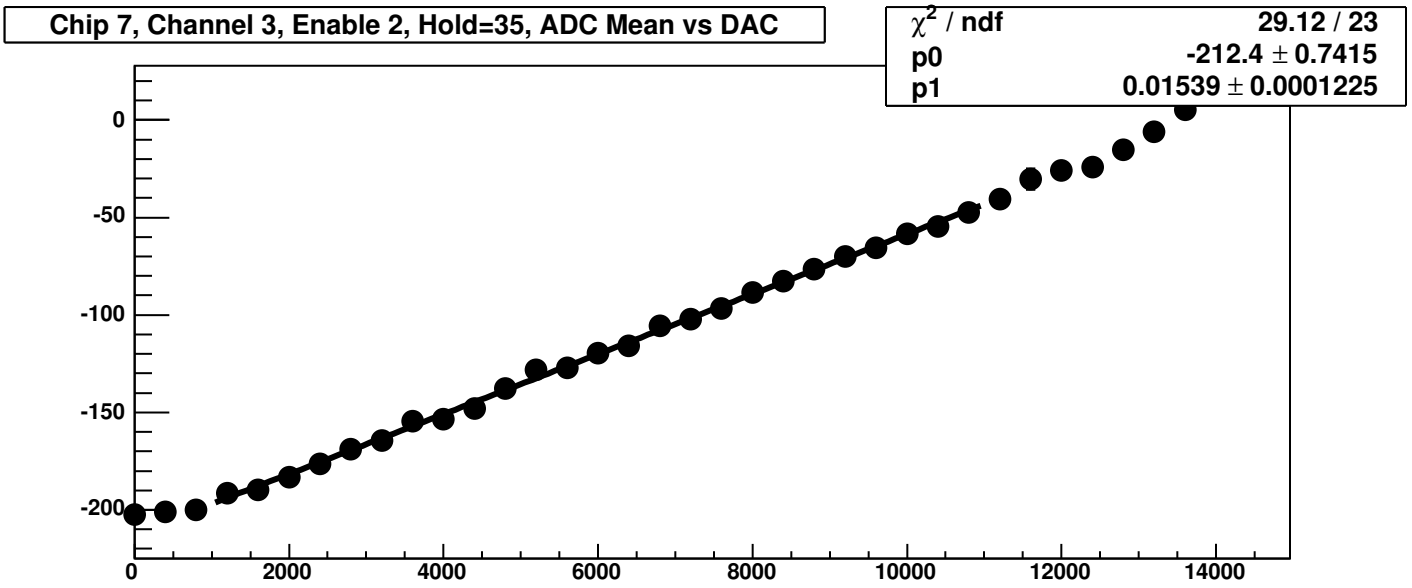
Chip 7, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

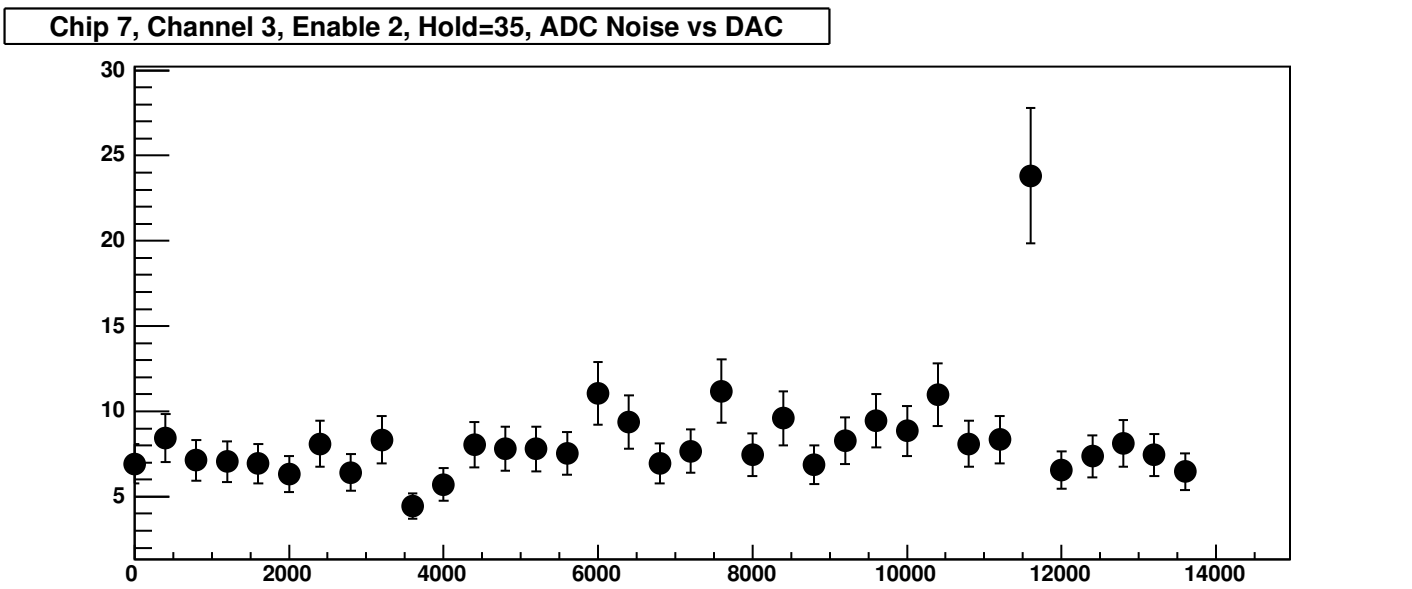


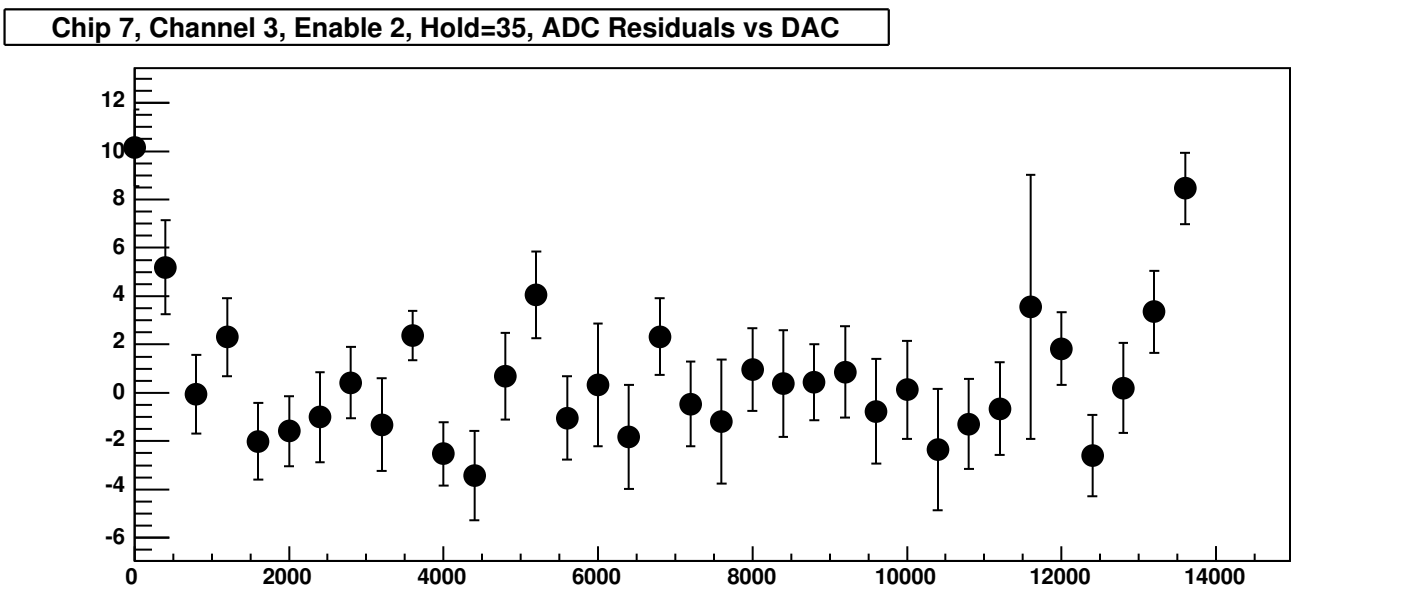

Chip 7, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.29 / 23
p0 -213.9 ± 0.7291
p1 0.01717 ± 0.0001177
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

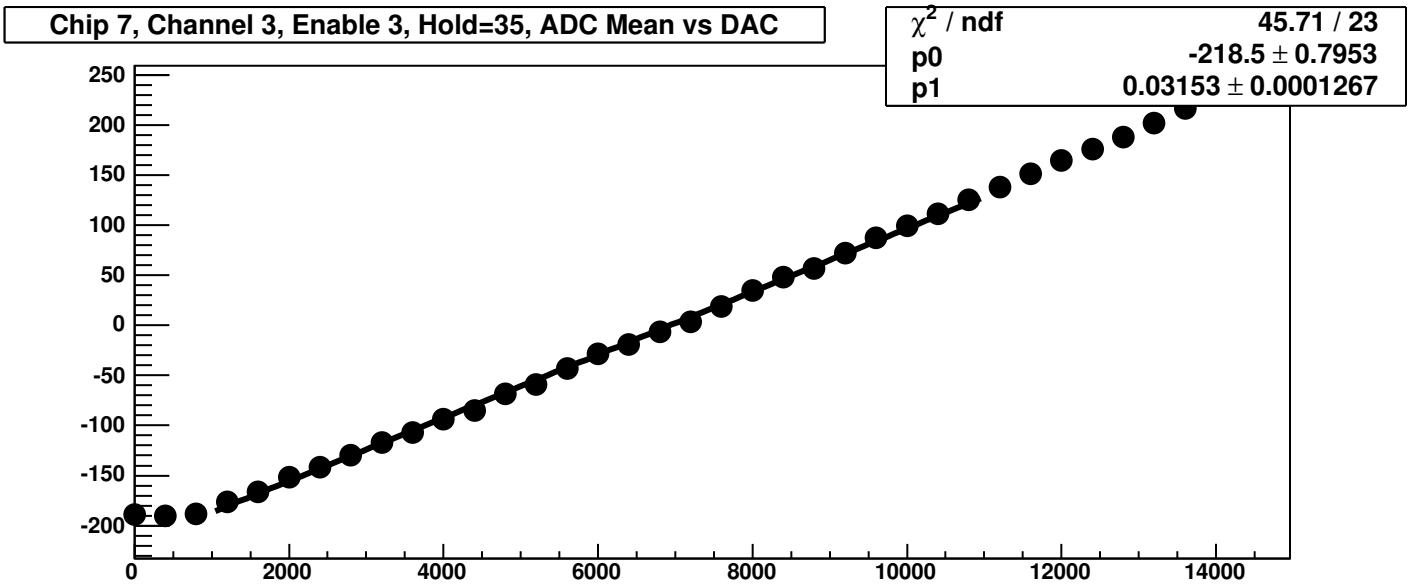

Chip 7, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

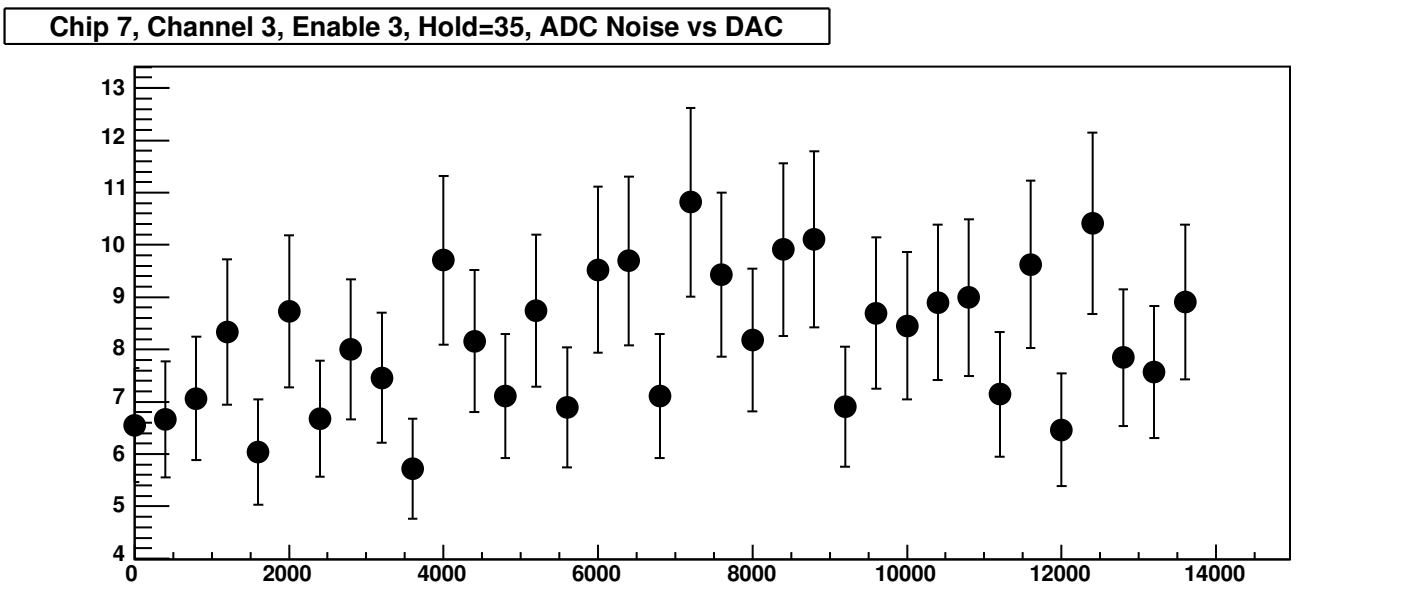

Chip 7, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

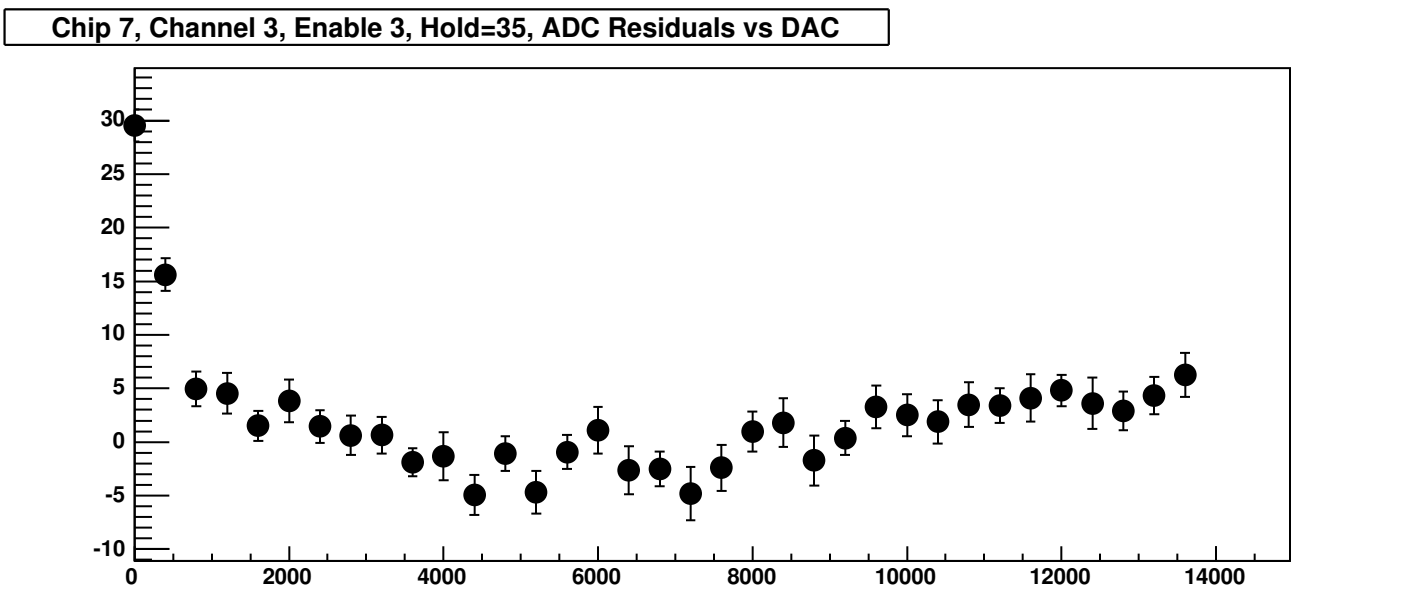

Chip 7, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 29.12 / 23
p0 -212.4 ± 0.7415
p1 0.01539 ± 0.0001225
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

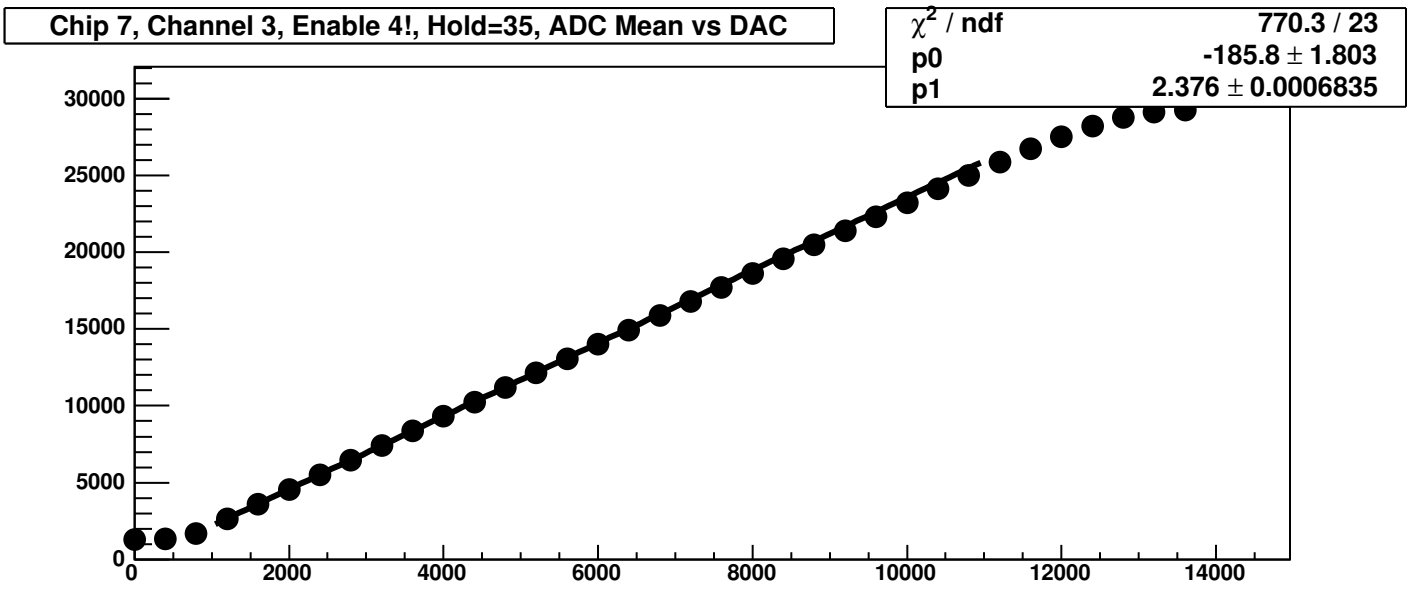

Chip 7, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

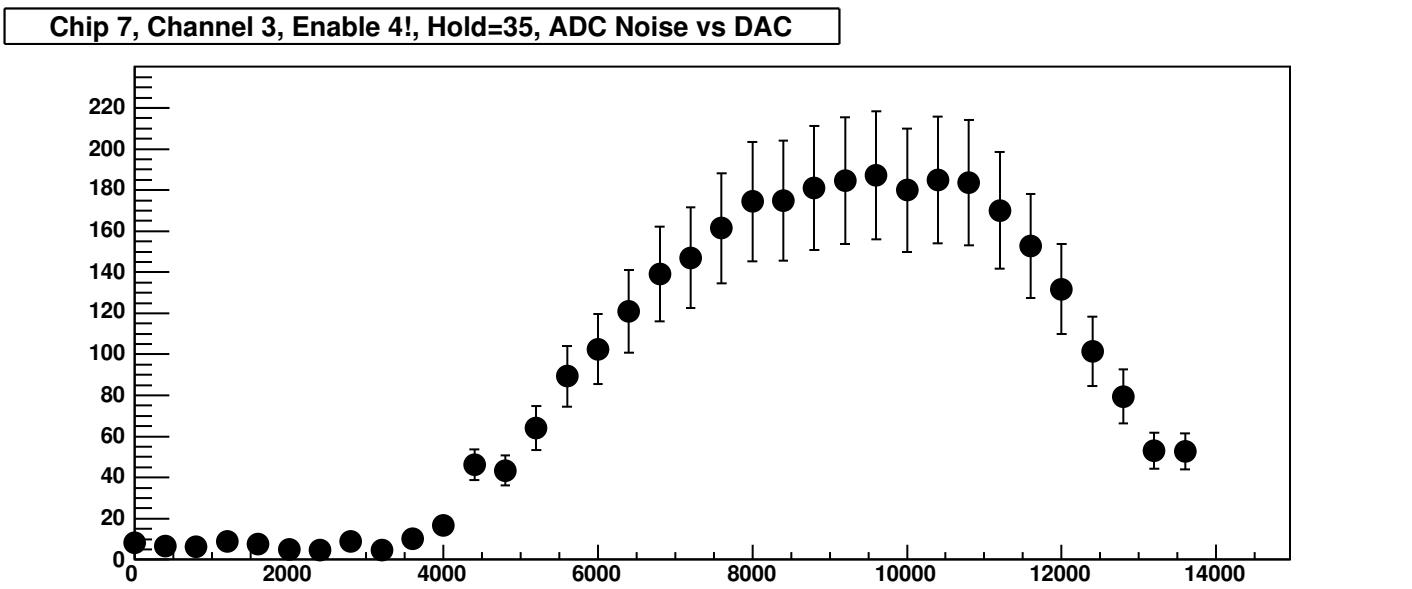

Chip 7, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000

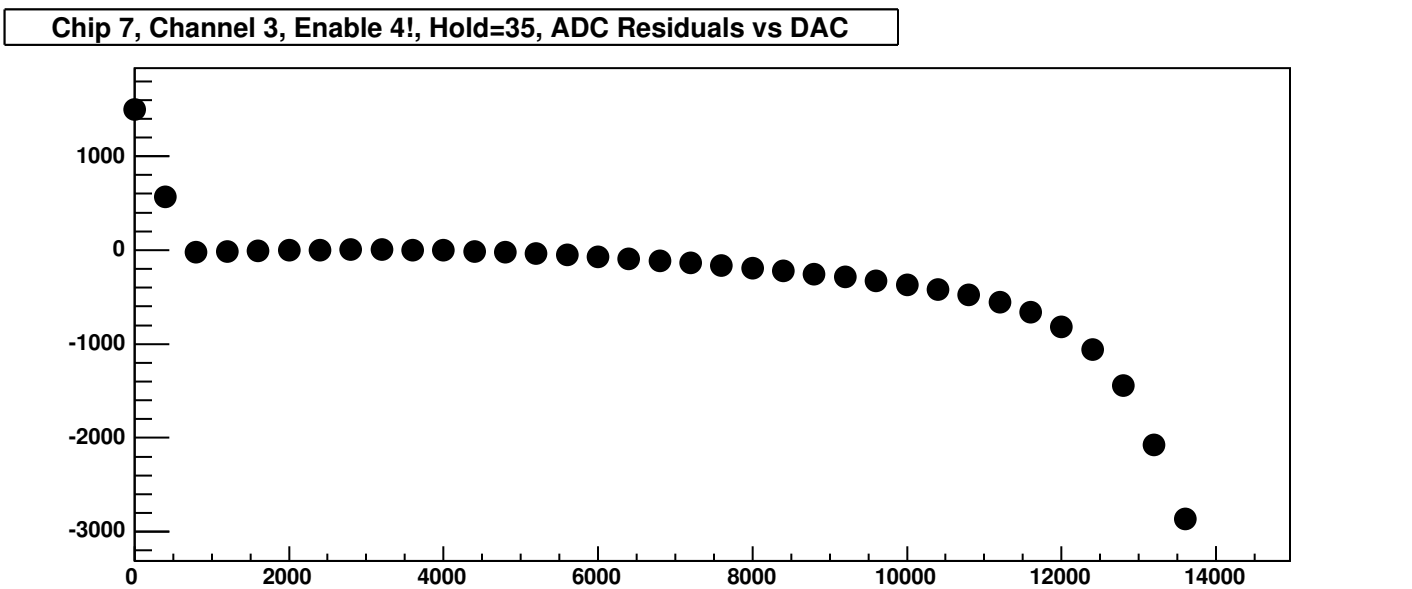

Chip 7, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 45.71 / 23
p0 -218.5 ± 0.7953
p1 0.03153 ± 0.0001267
250
200
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

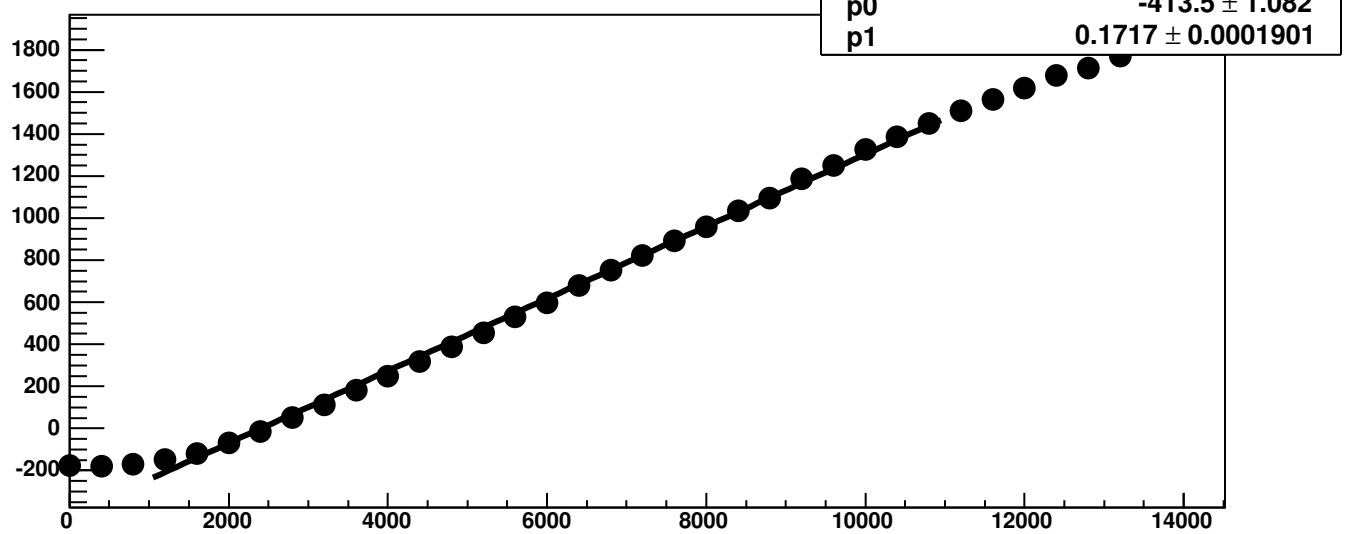

Chip 7, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 770.3 / 23
p0 -185.8 ± 1.803
p1 2.376 ± 0.0006835
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

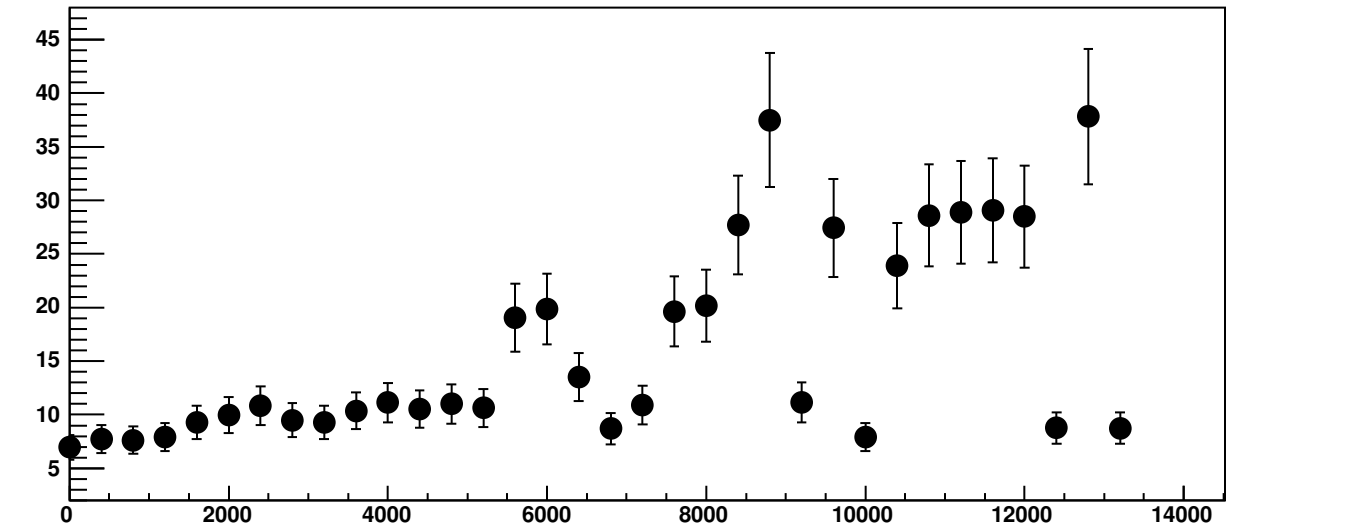

Chip 7, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

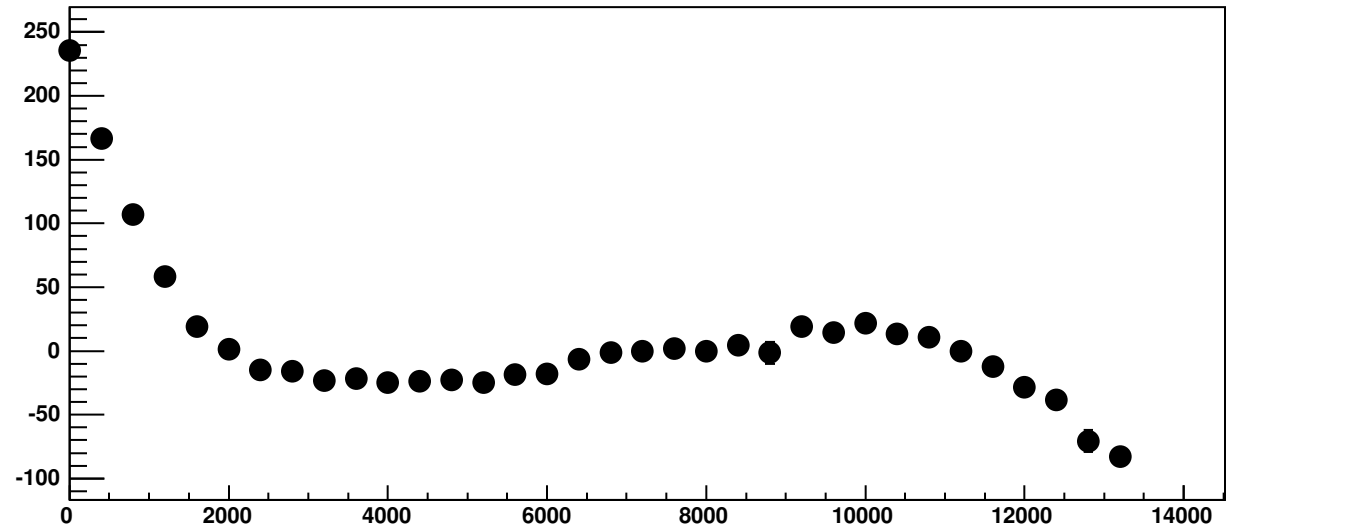
Chip 7, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC



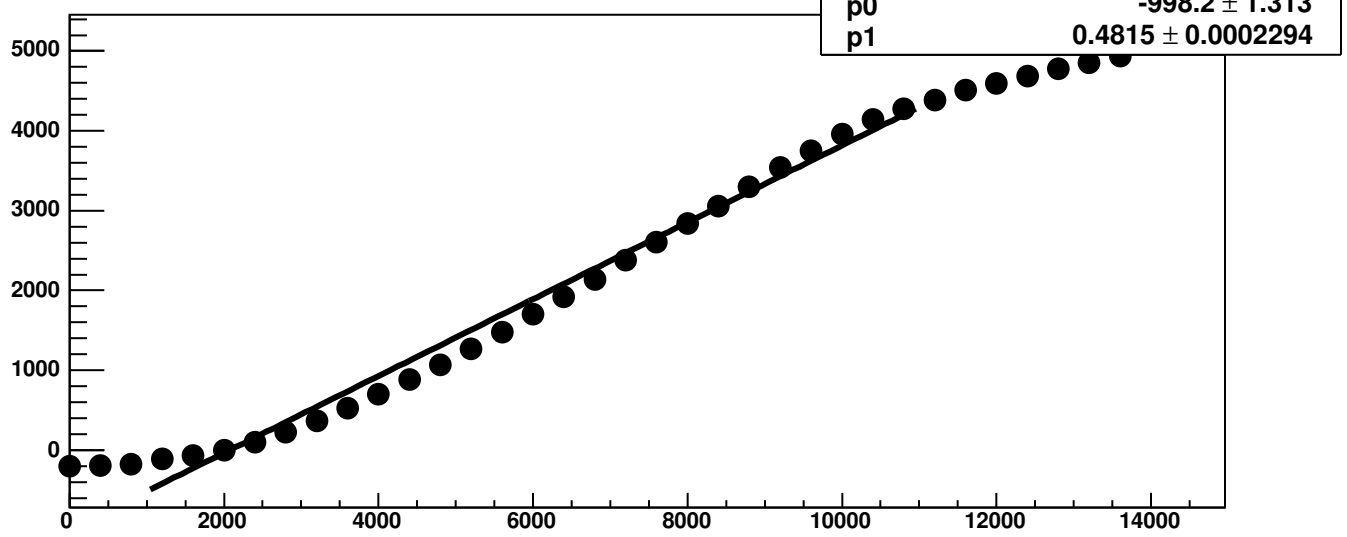
Chip 7, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC



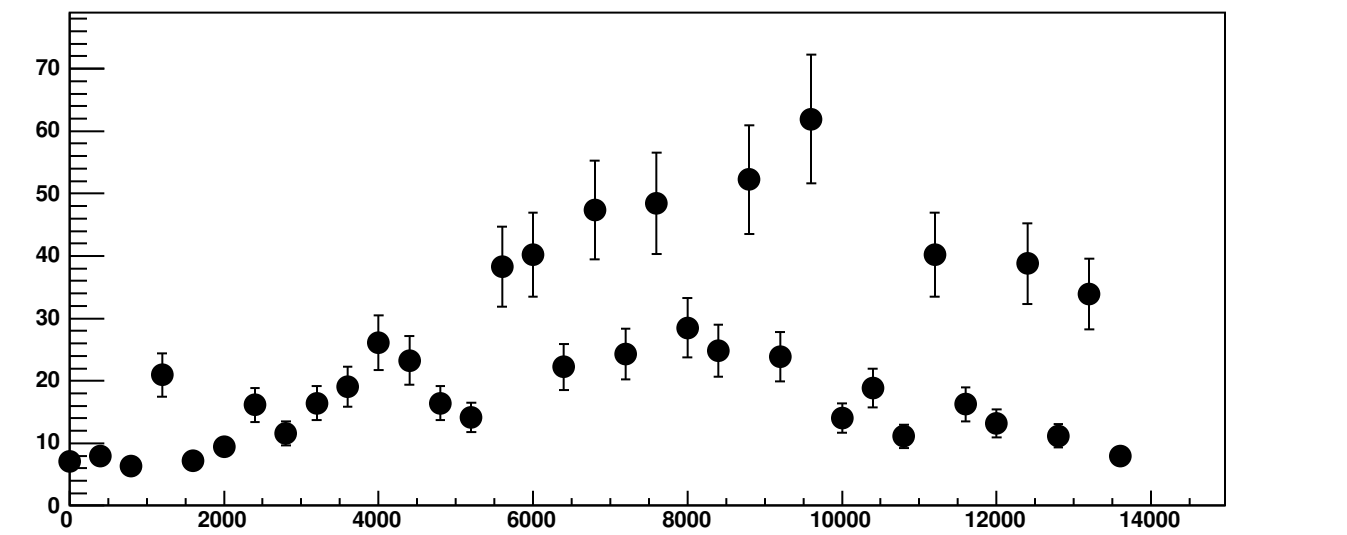
Chip 7, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC



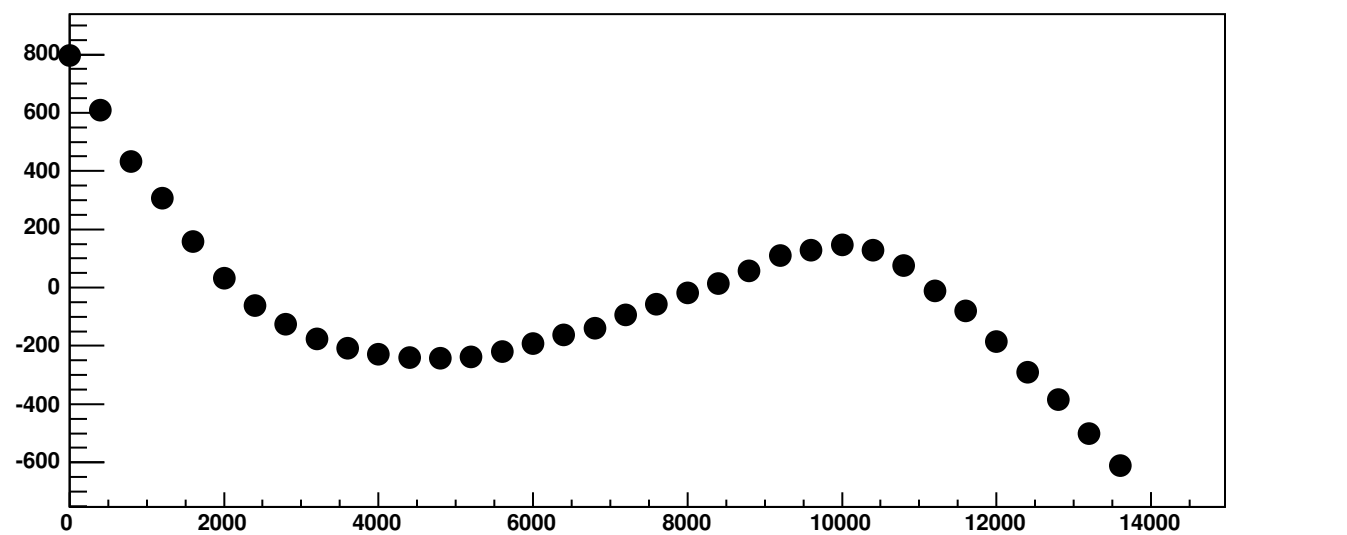
Chip 7, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

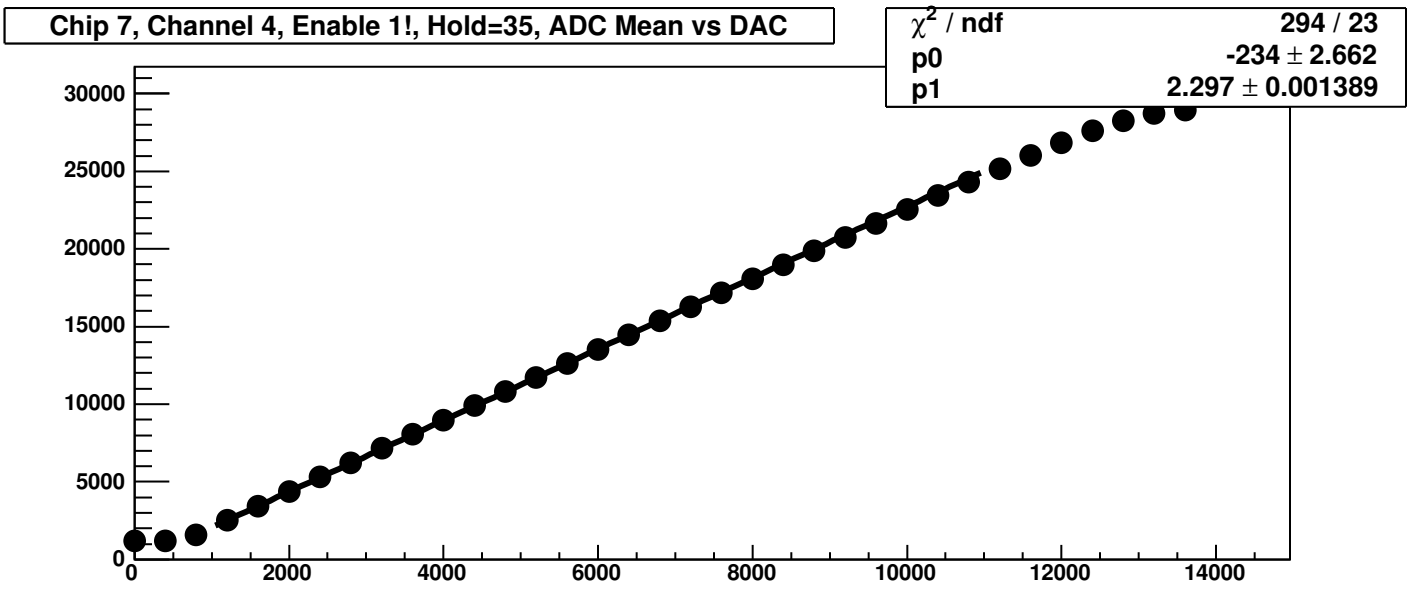


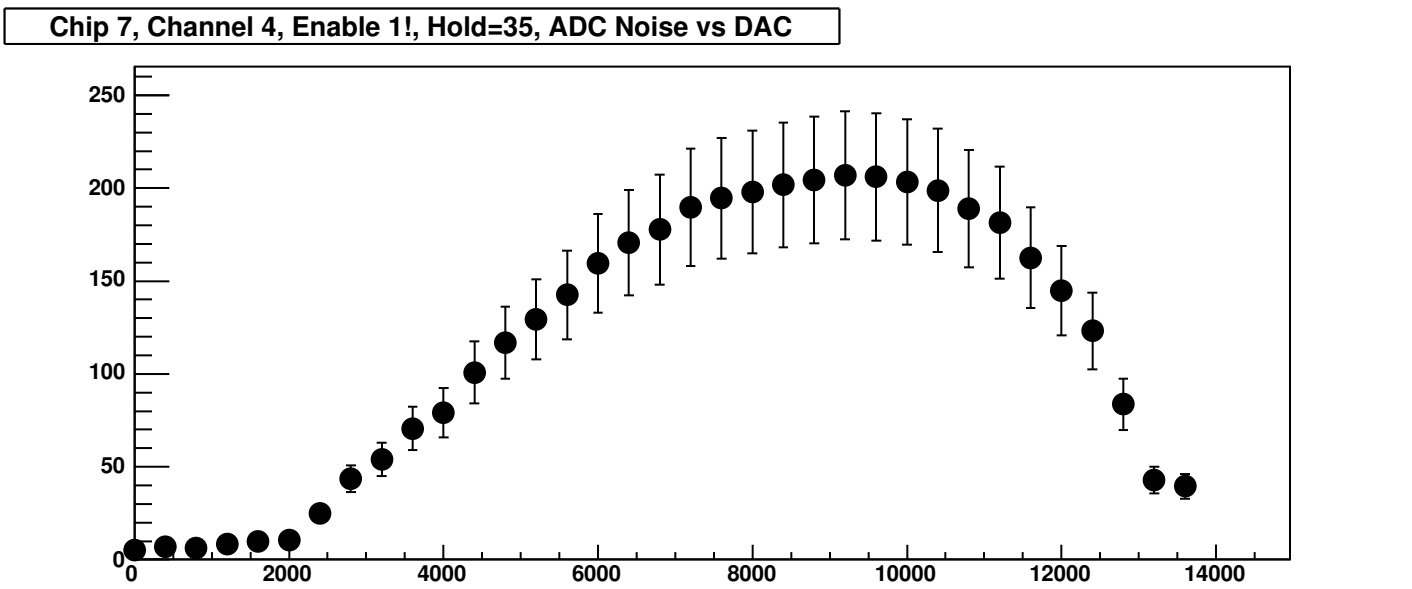
Chip 7, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC

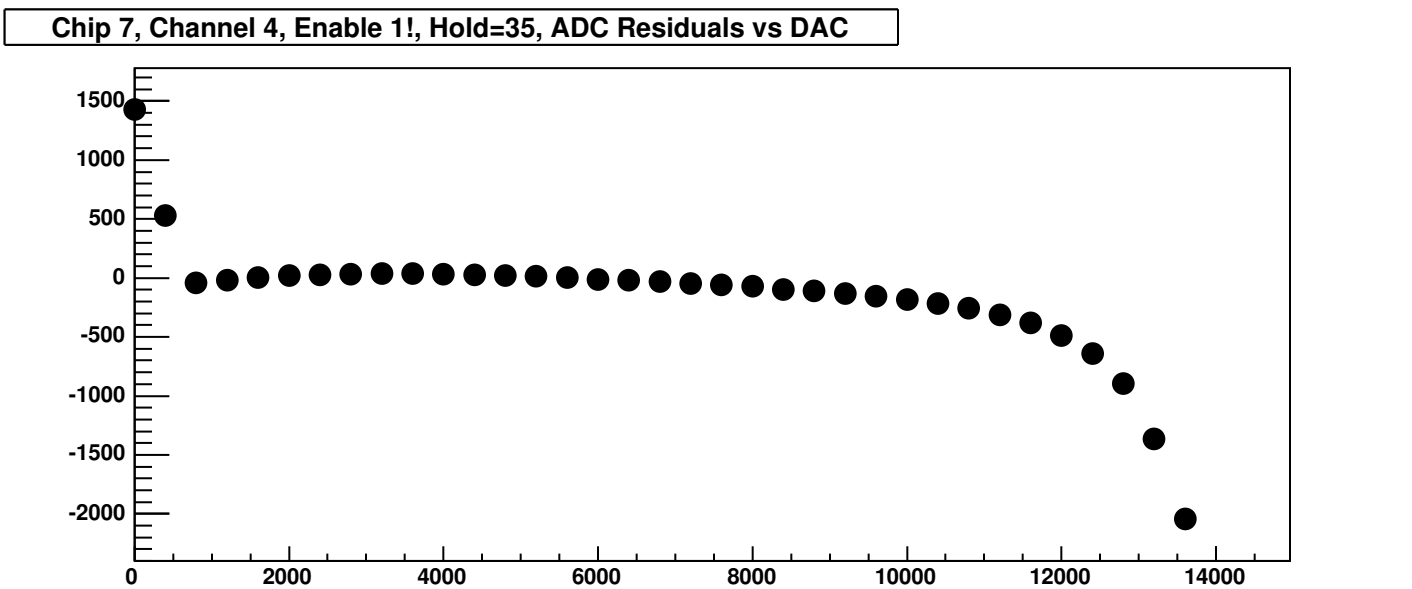


Chip 7, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

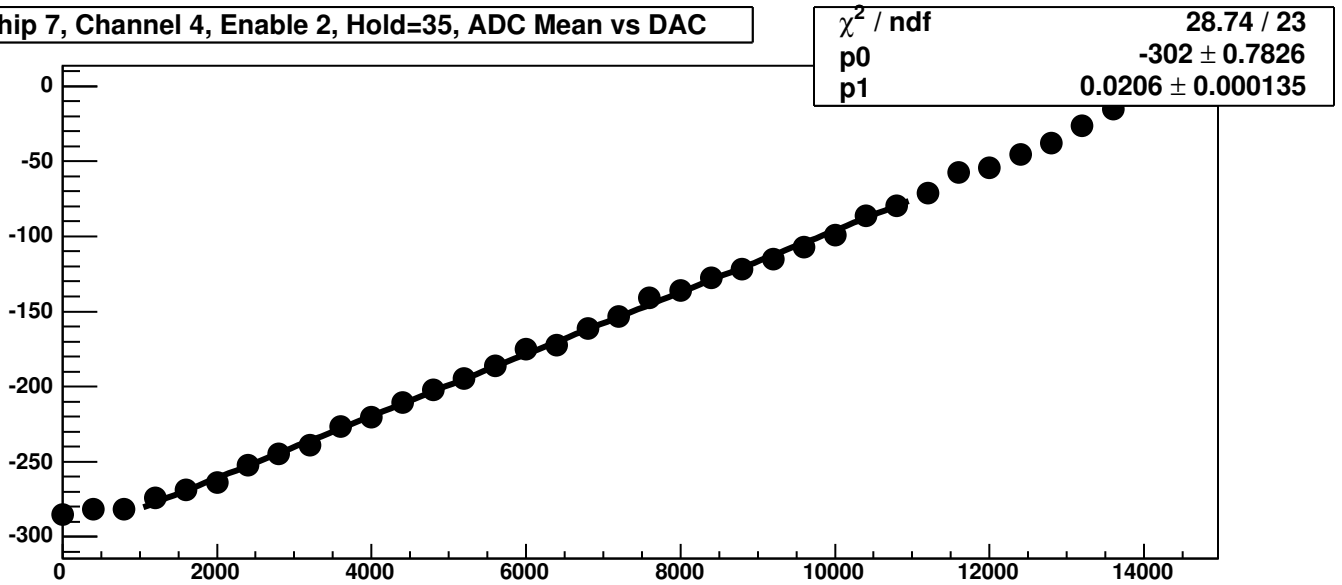



Chip 7, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 294 / 23
p0 -234 ± 2.662
p1 2.297 ± 0.001389
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

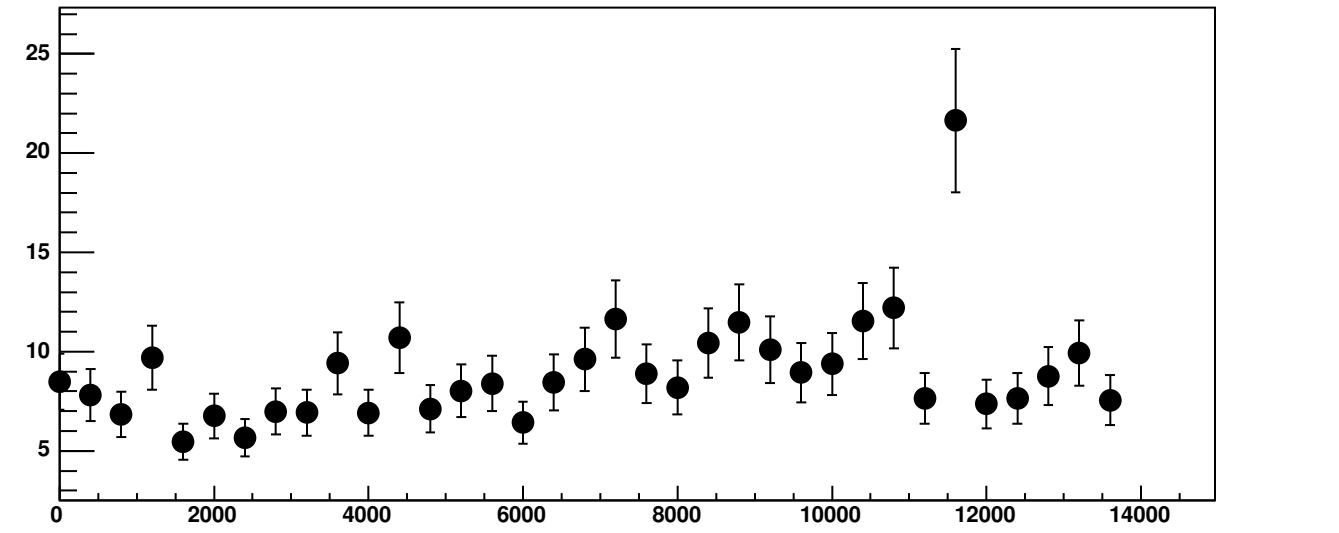

Chip 7, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

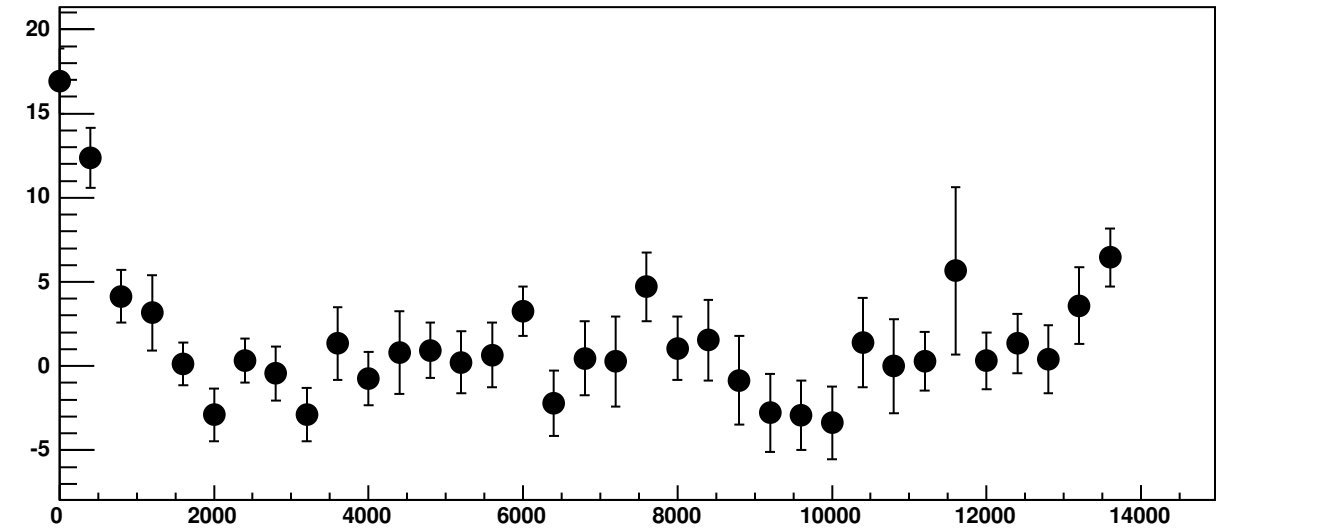
Chip 7, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC

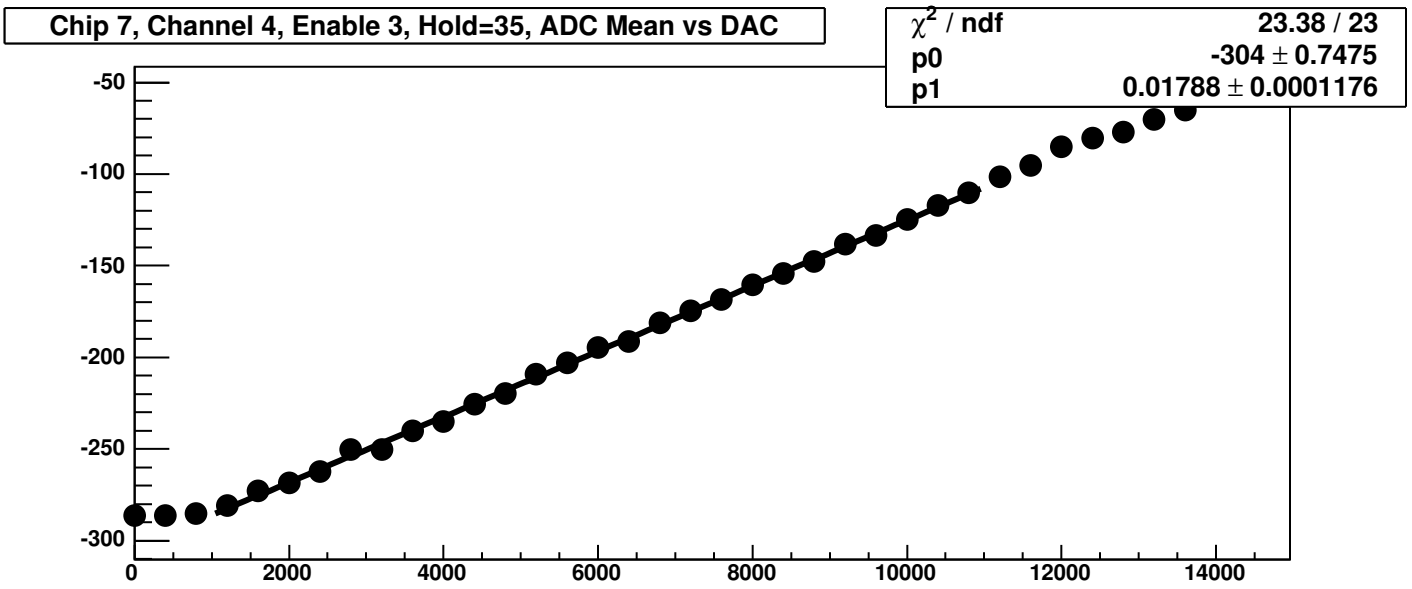


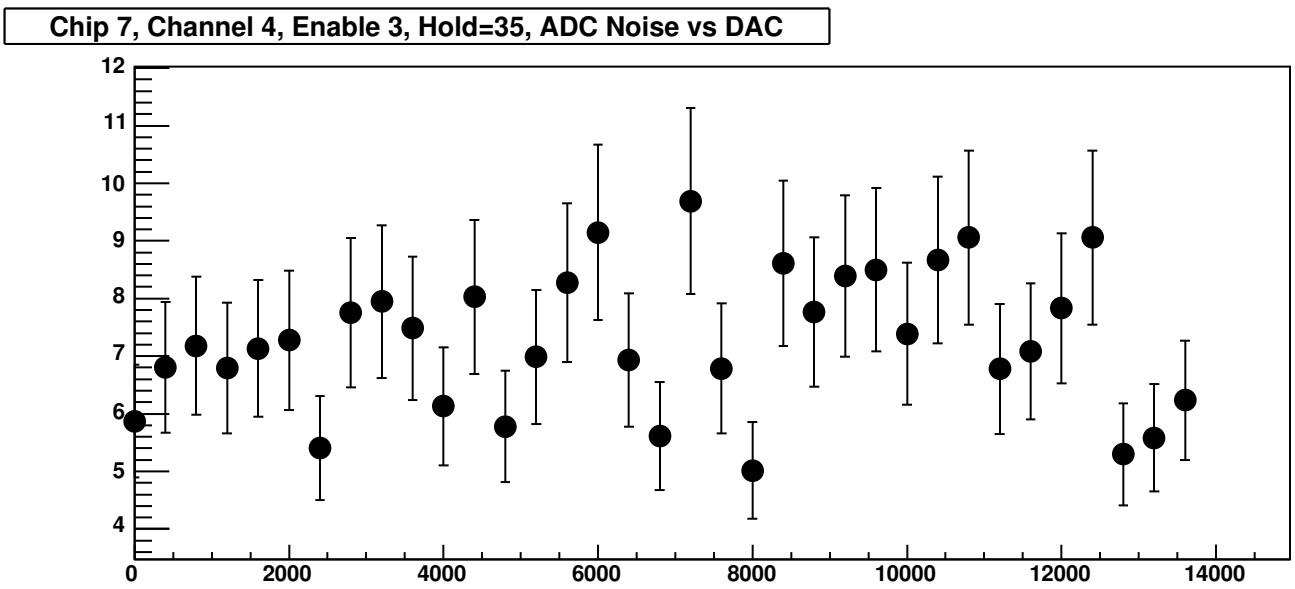
Chip 7, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC

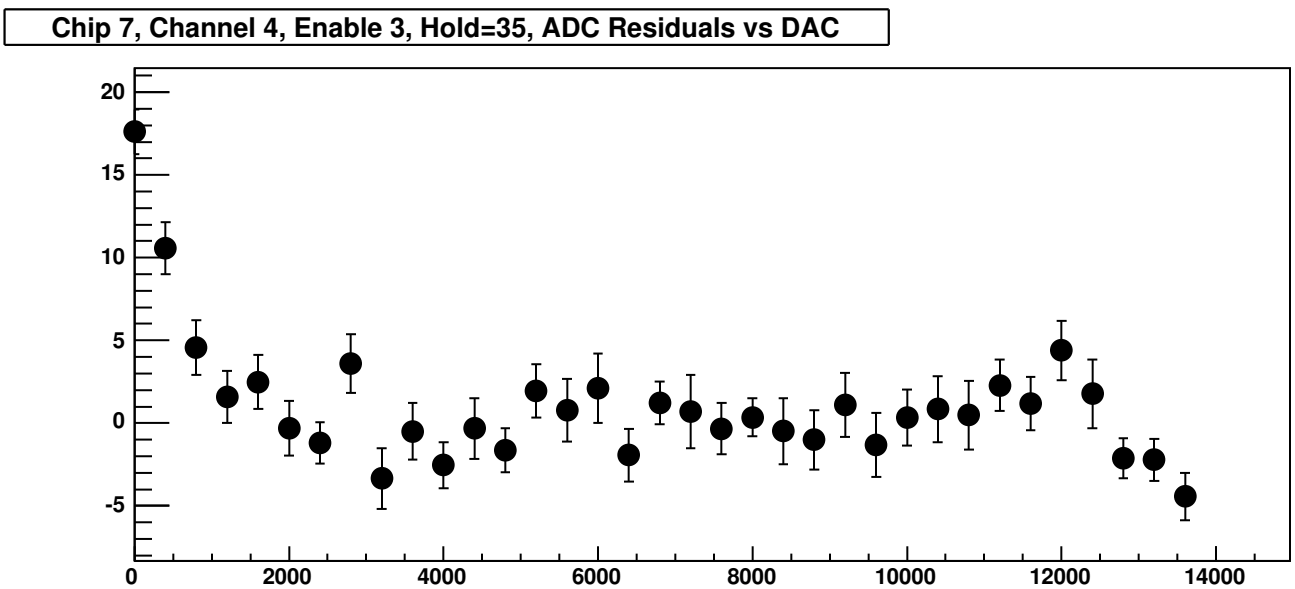


Chip 7, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC

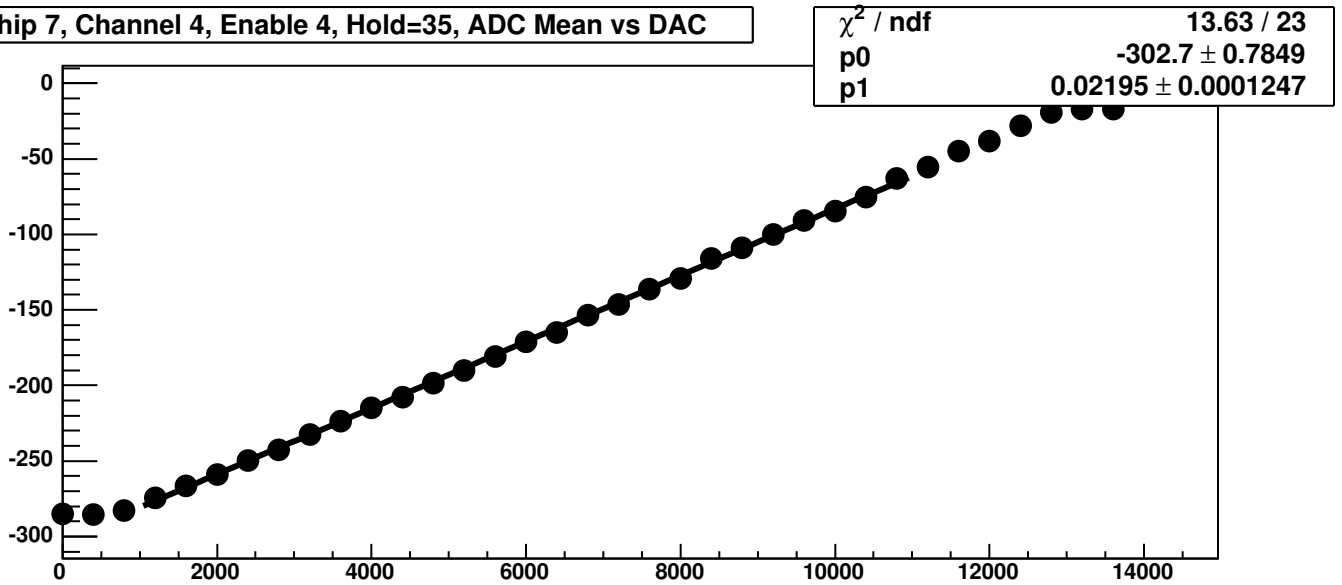



Chip 7, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.38 / 23
p0 -304 ± 0.7475
p1 0.01788 ± 0.0001176
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

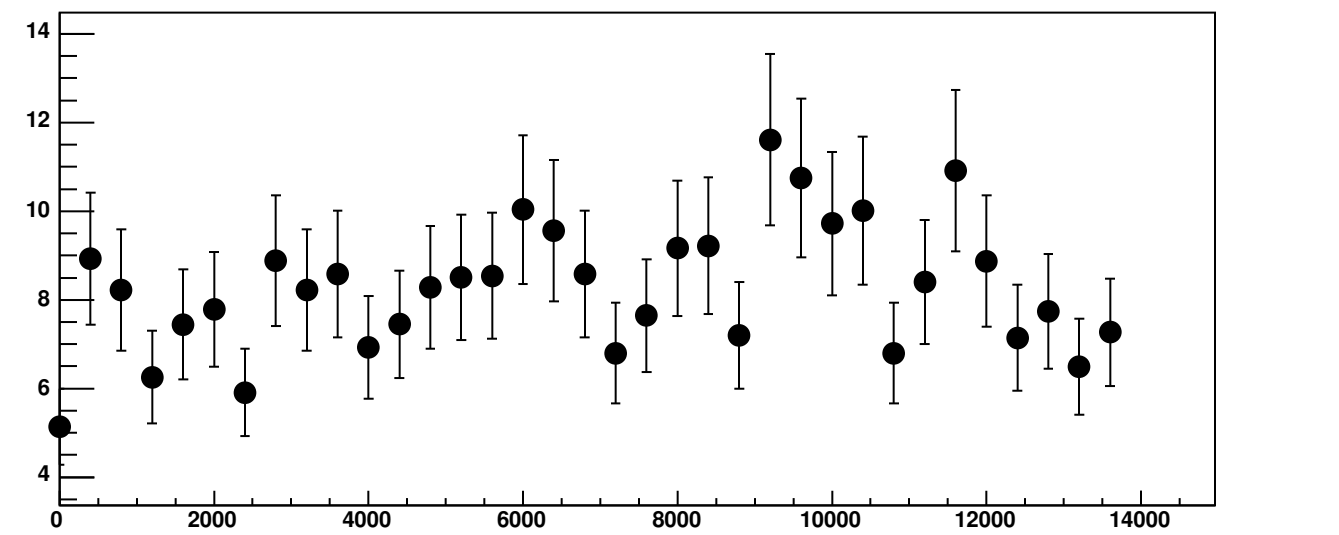

Chip 7, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

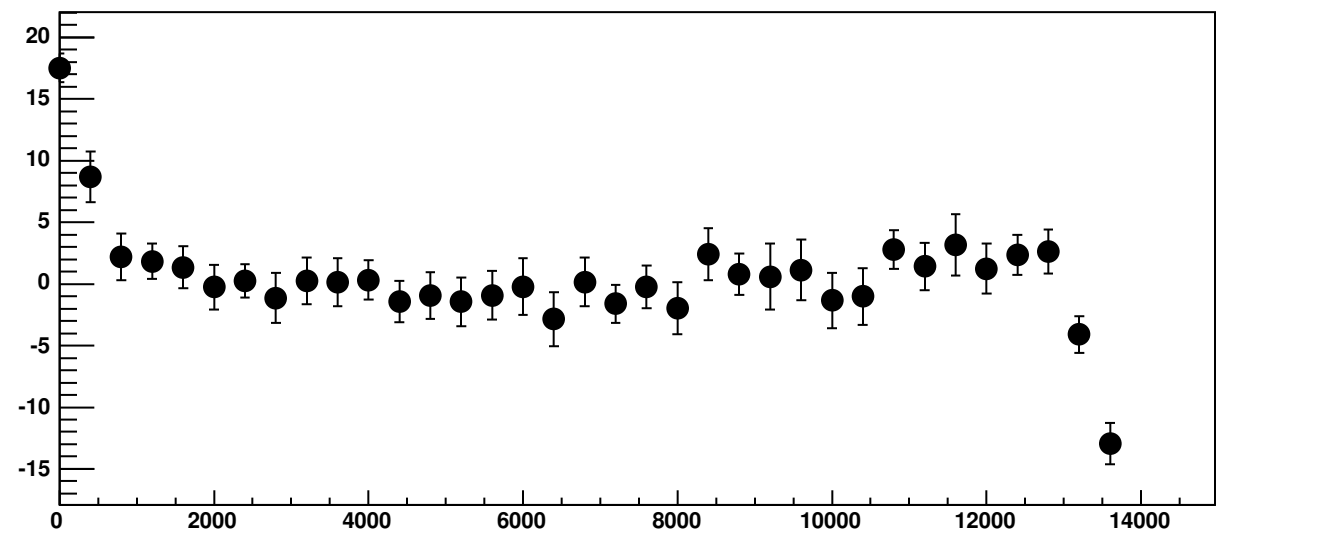
Chip 7, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC

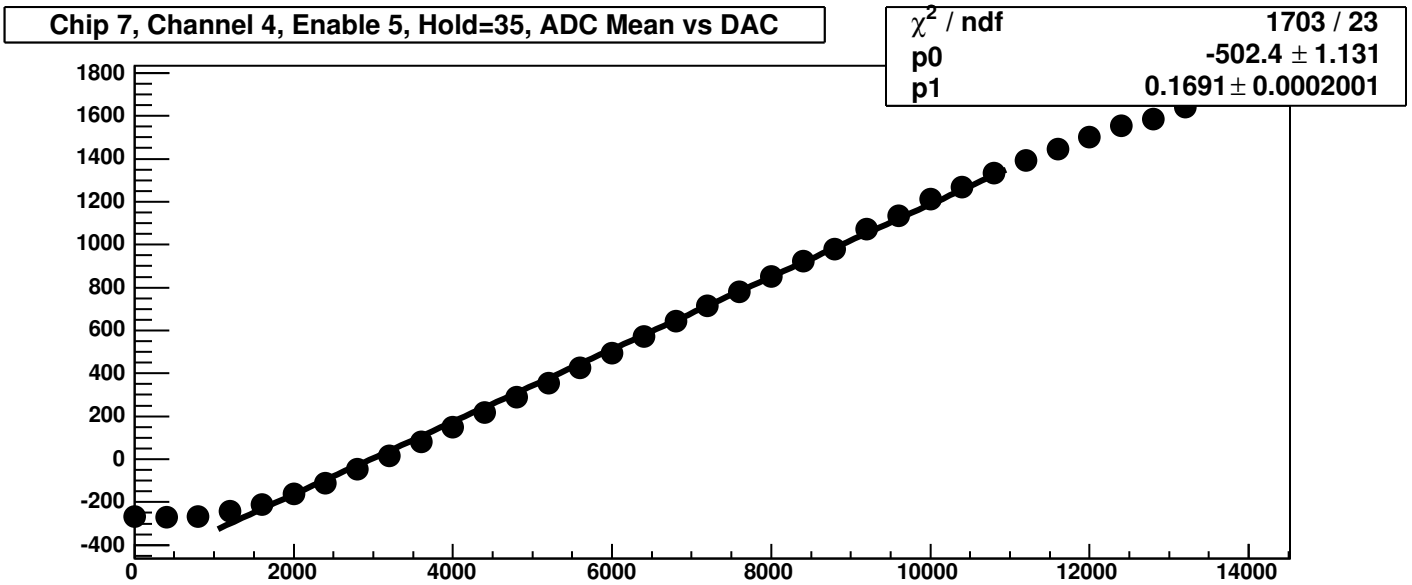


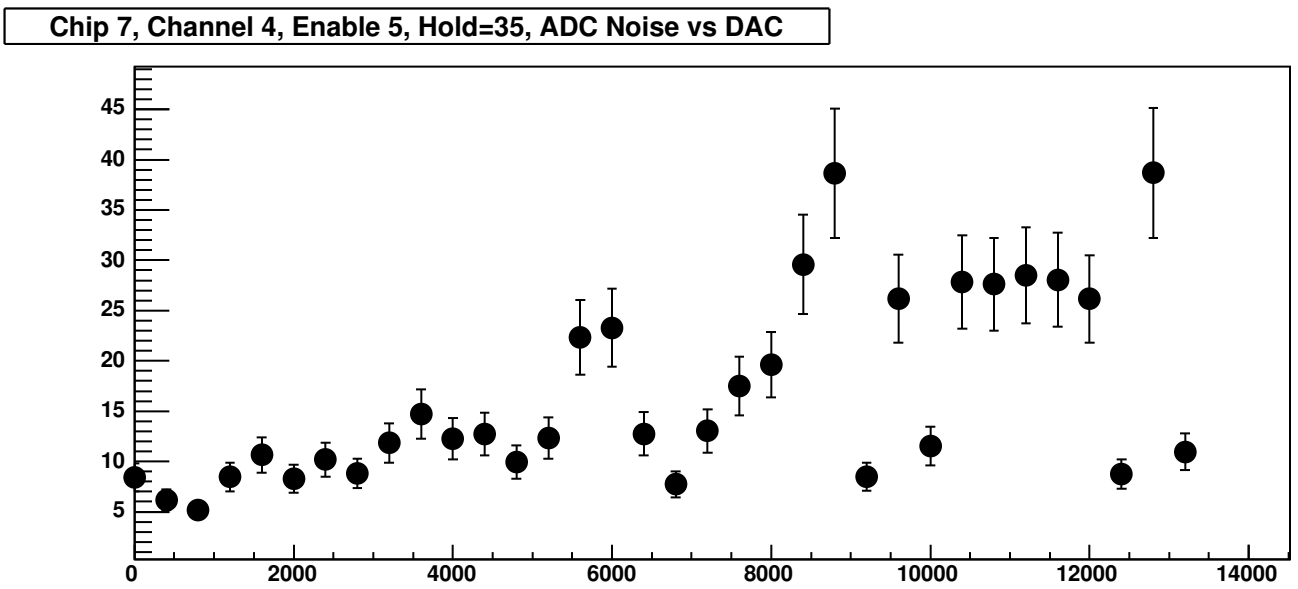
Chip 7, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC

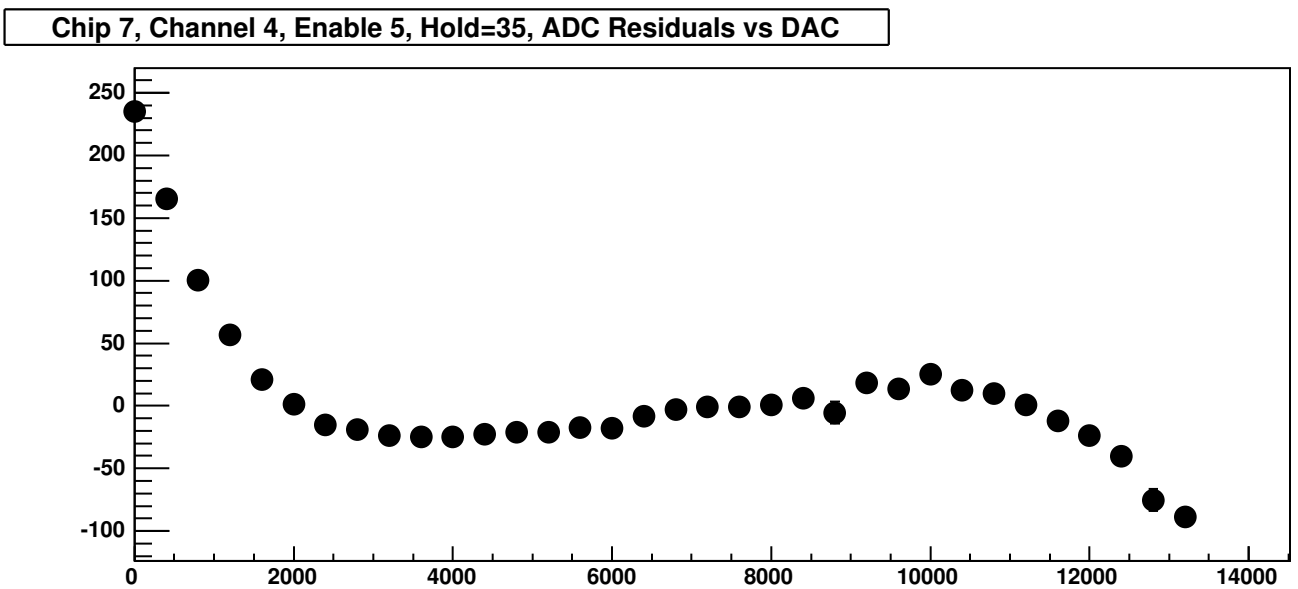


Chip 7, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC

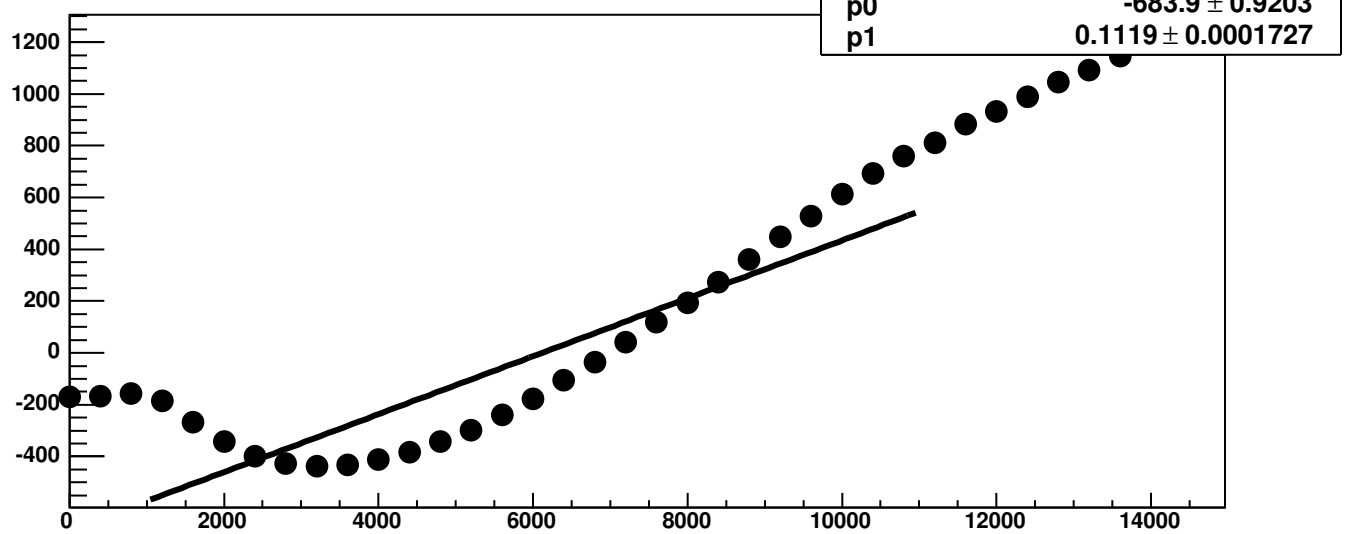



Chip 7, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1703 / 23
p0 -502.4 ± 1.131
p1 0.1691 ± 0.0002001
1800
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

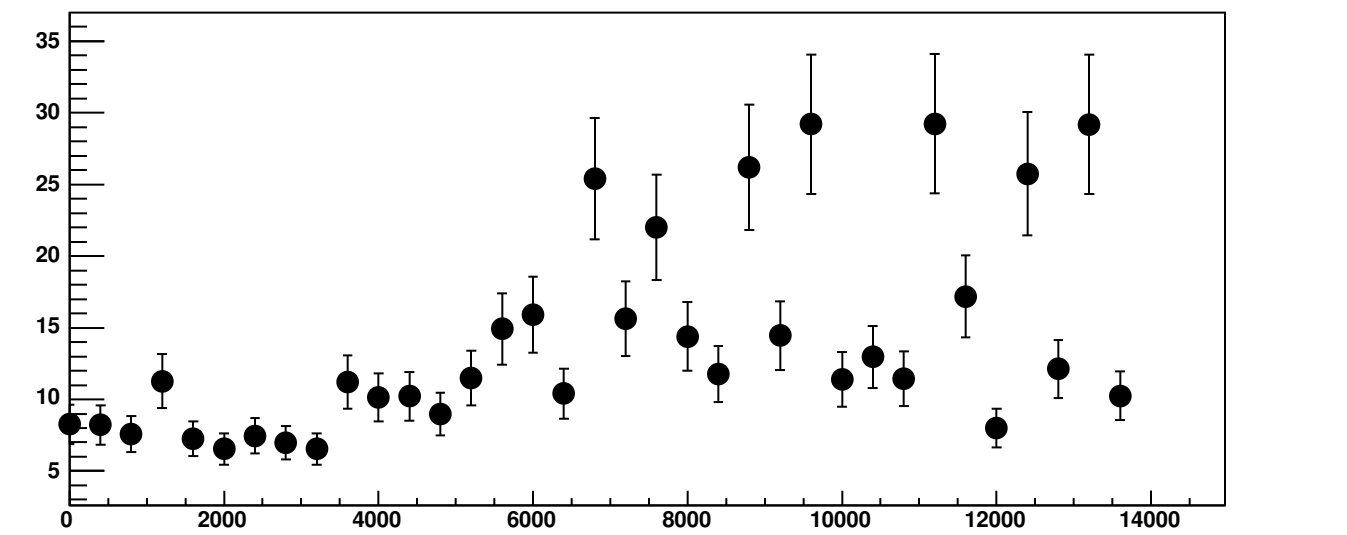

Chip 7, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

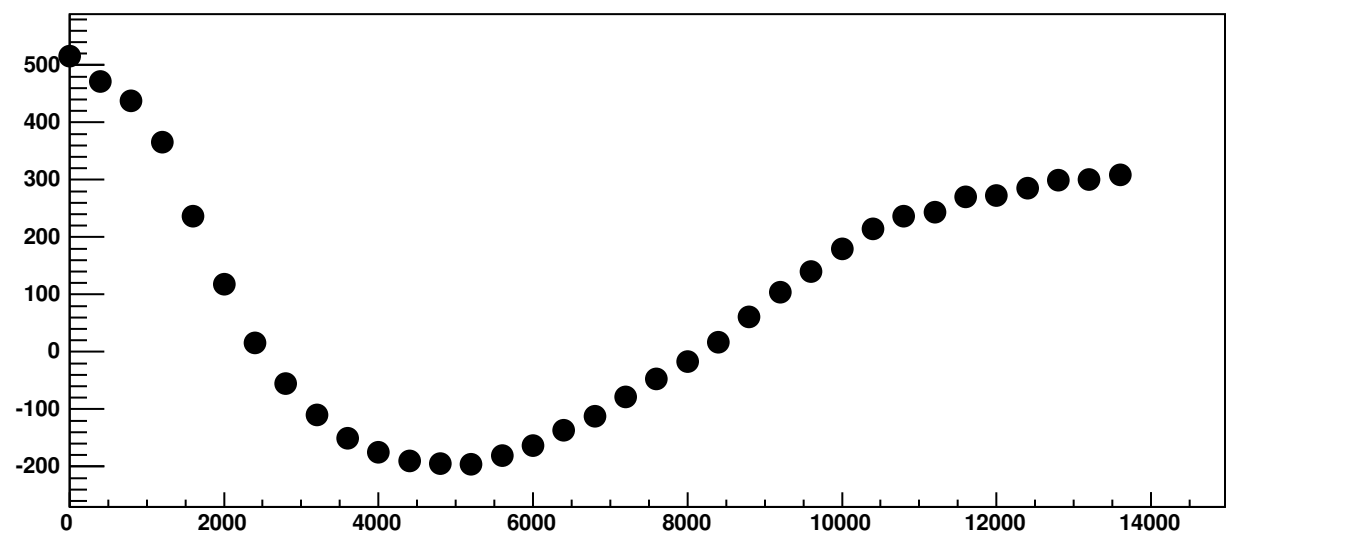
Chip 7, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

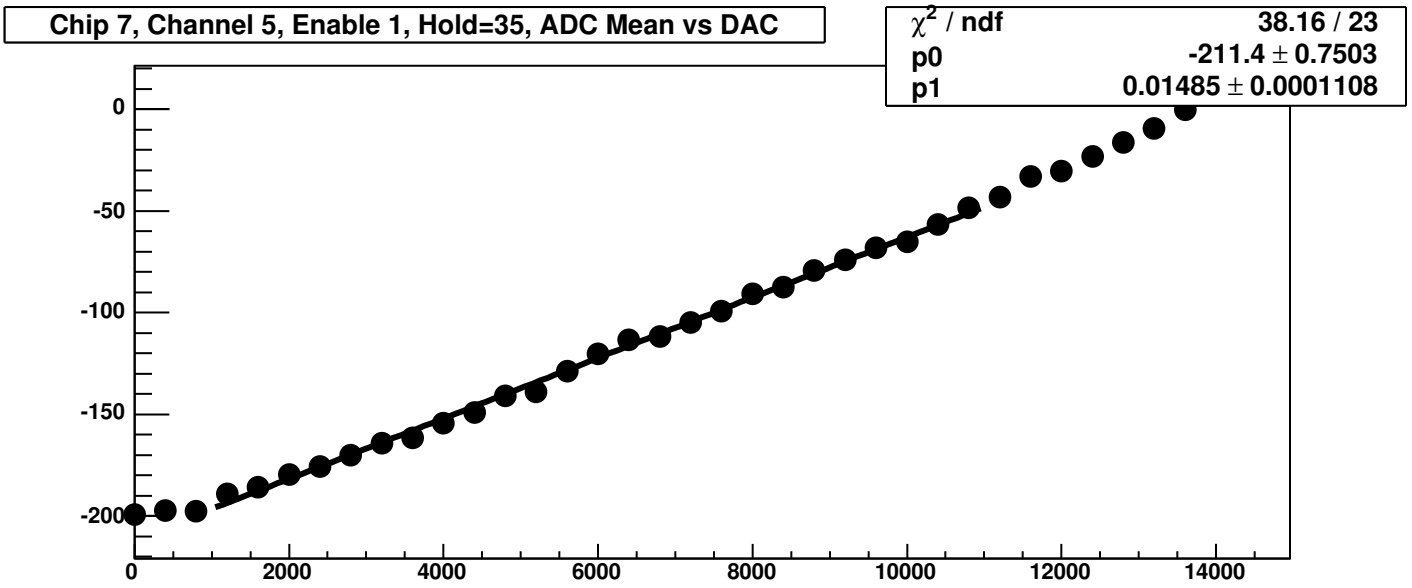


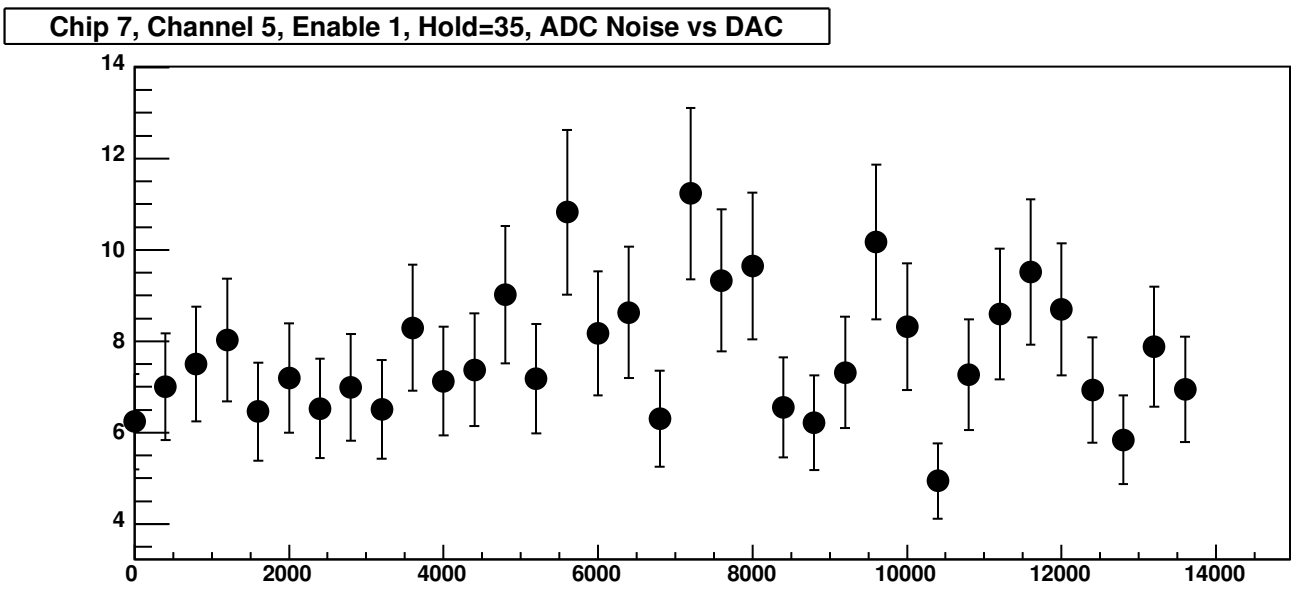
Chip 7, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC

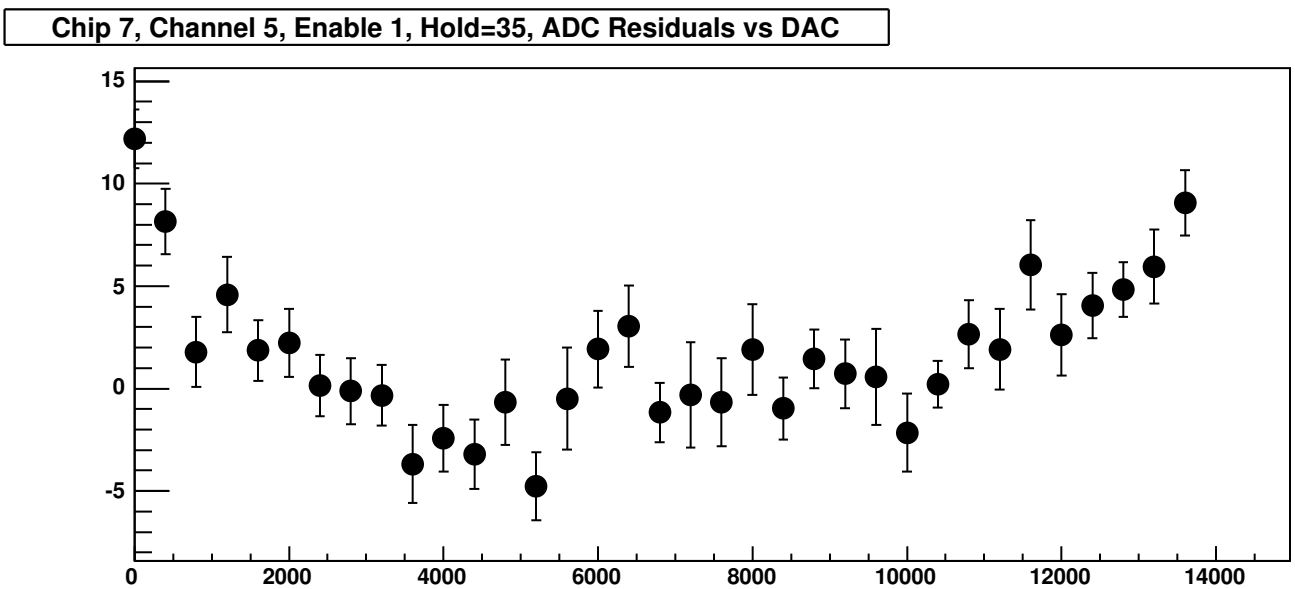


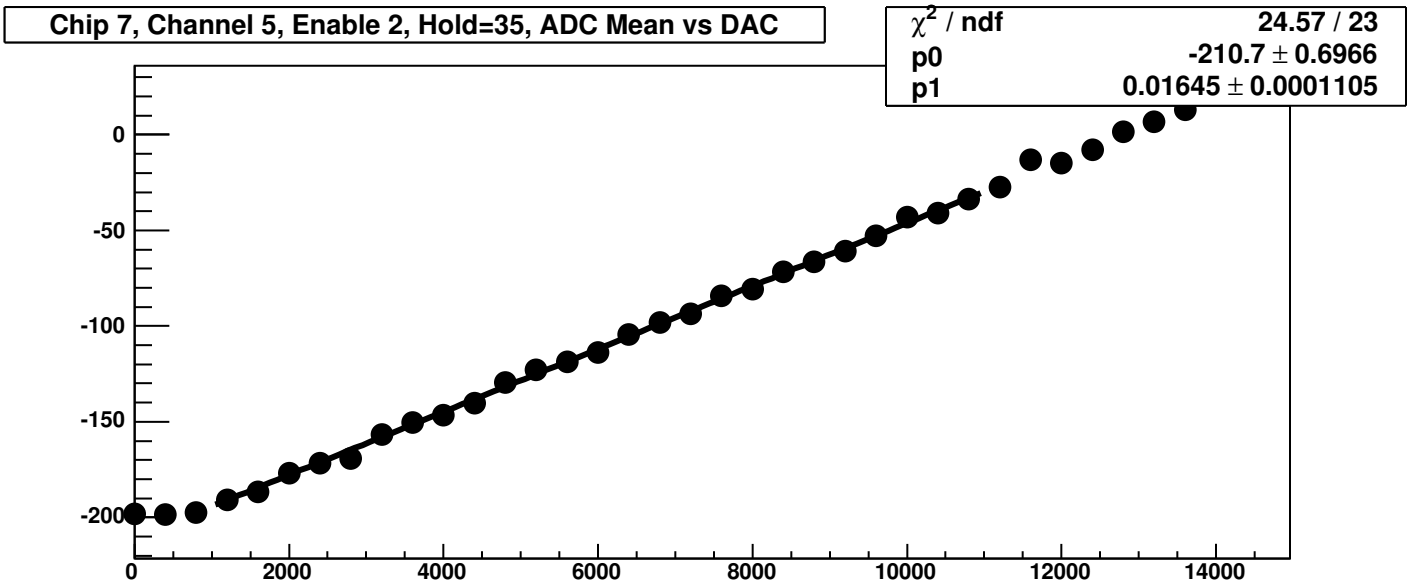
Chip 7, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

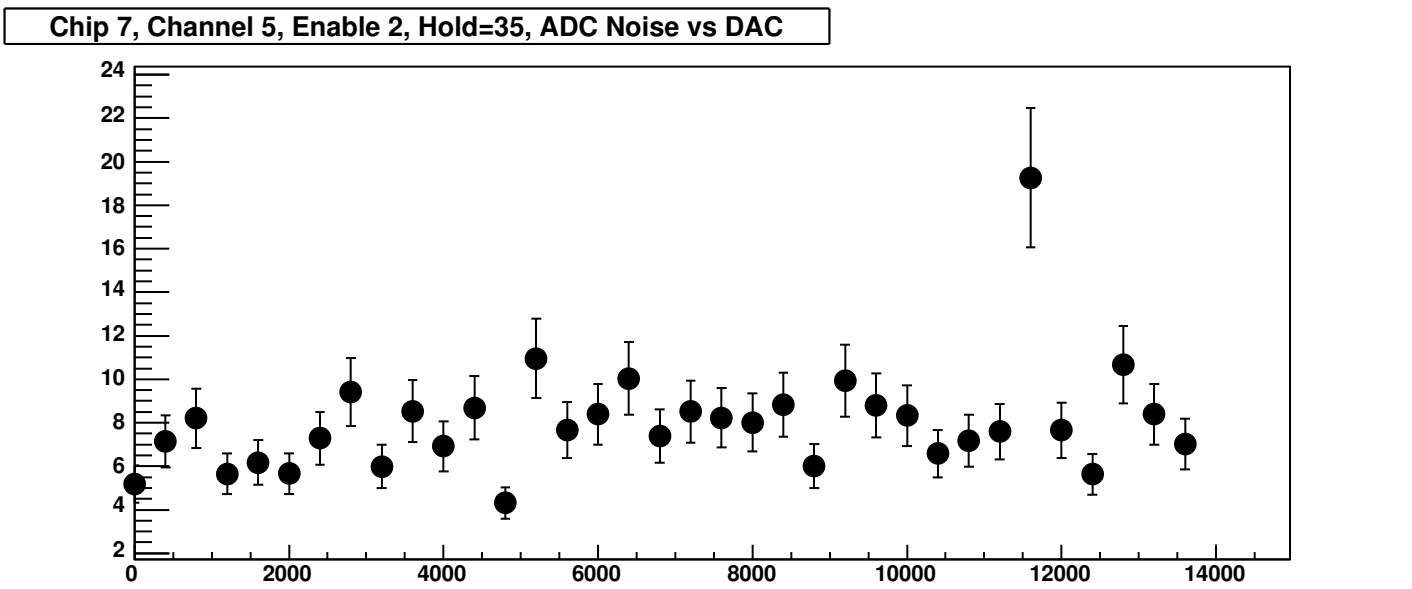


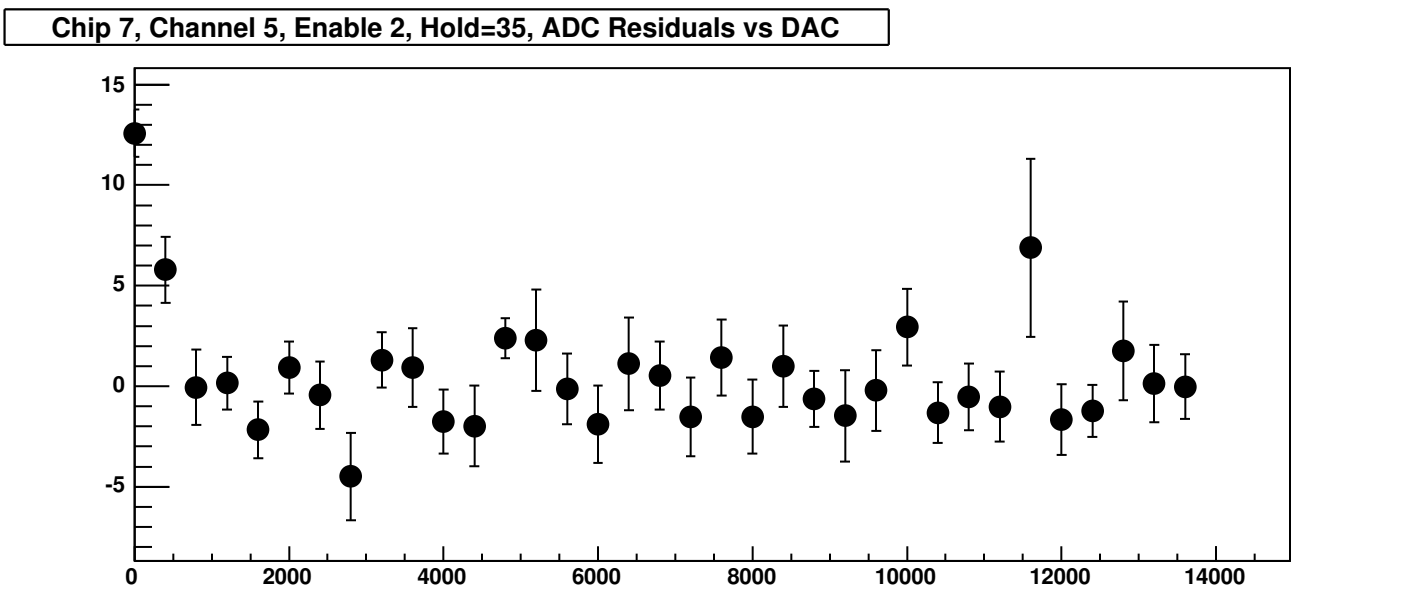

Chip 7, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 38.16 / 23
p0 -211.4 ± 0.7503
p1 0.01485 ± 0.0001108
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

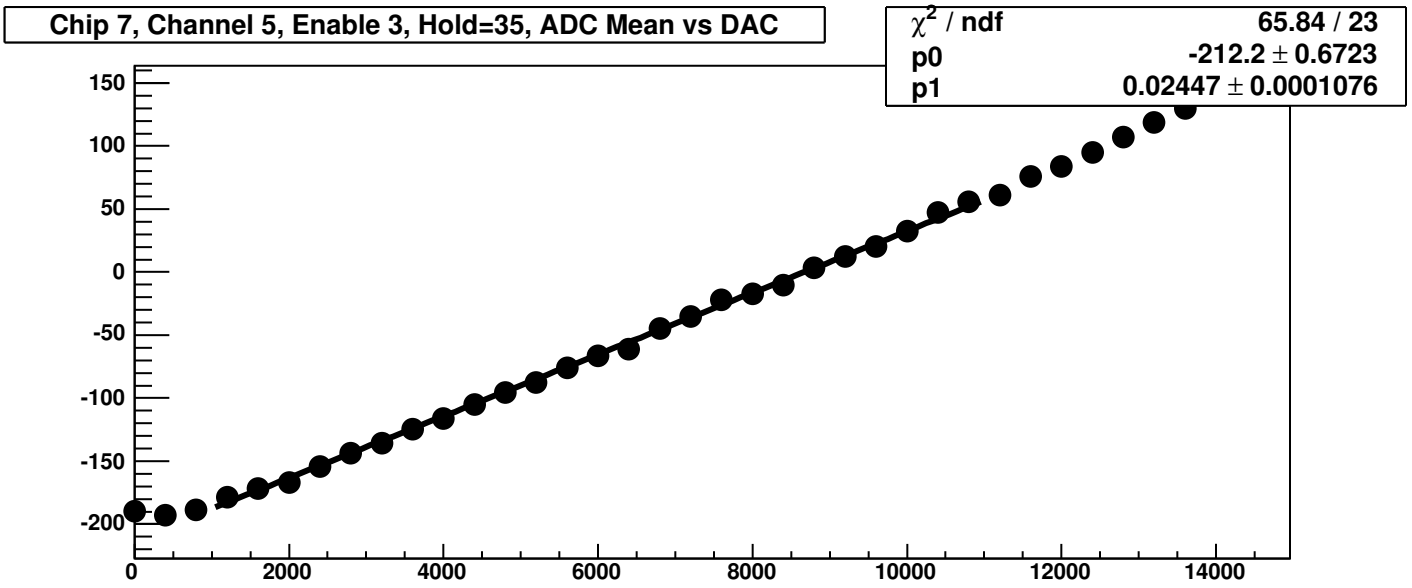

Chip 7, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

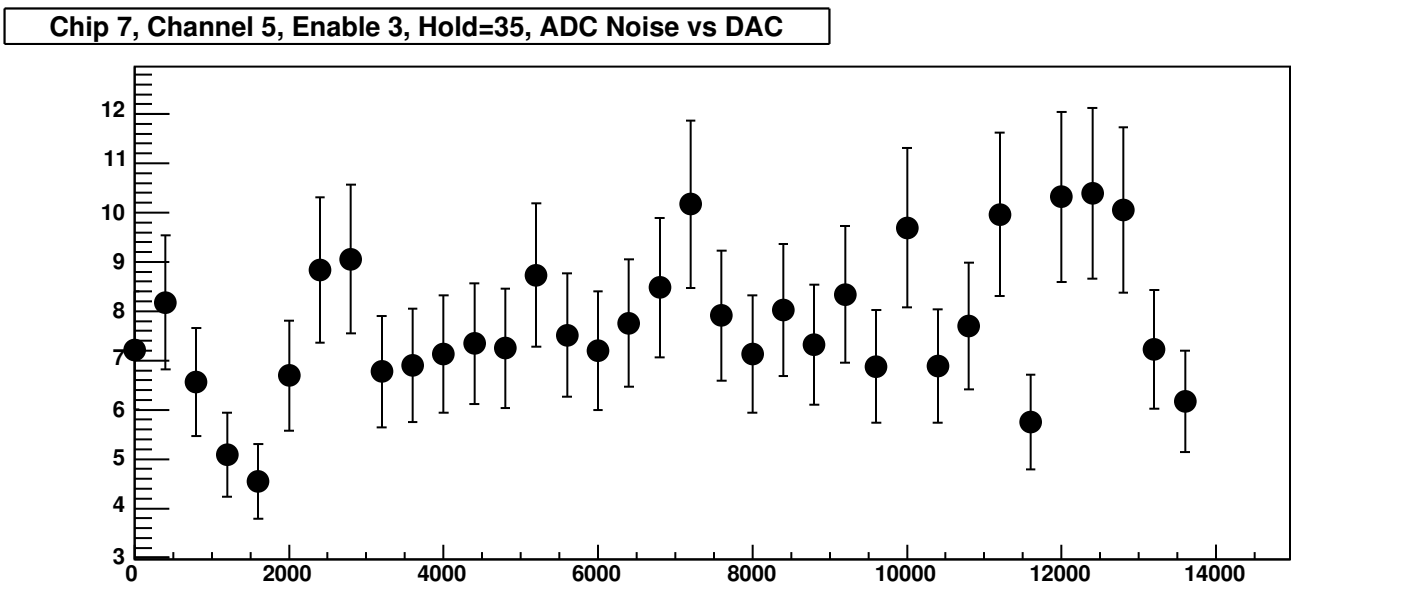

Chip 7, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

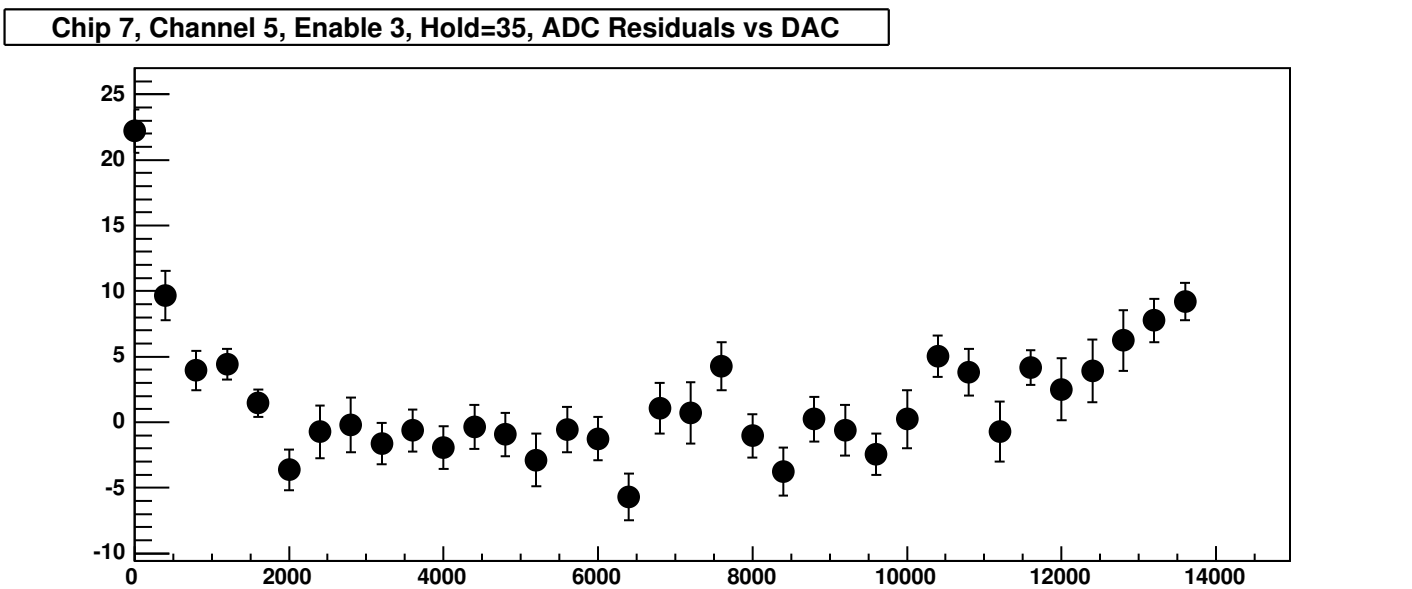

Chip 7, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 24.57 / 23
p0 -210.7 ± 0.6966
p1 0.01645 ± 0.0001105
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

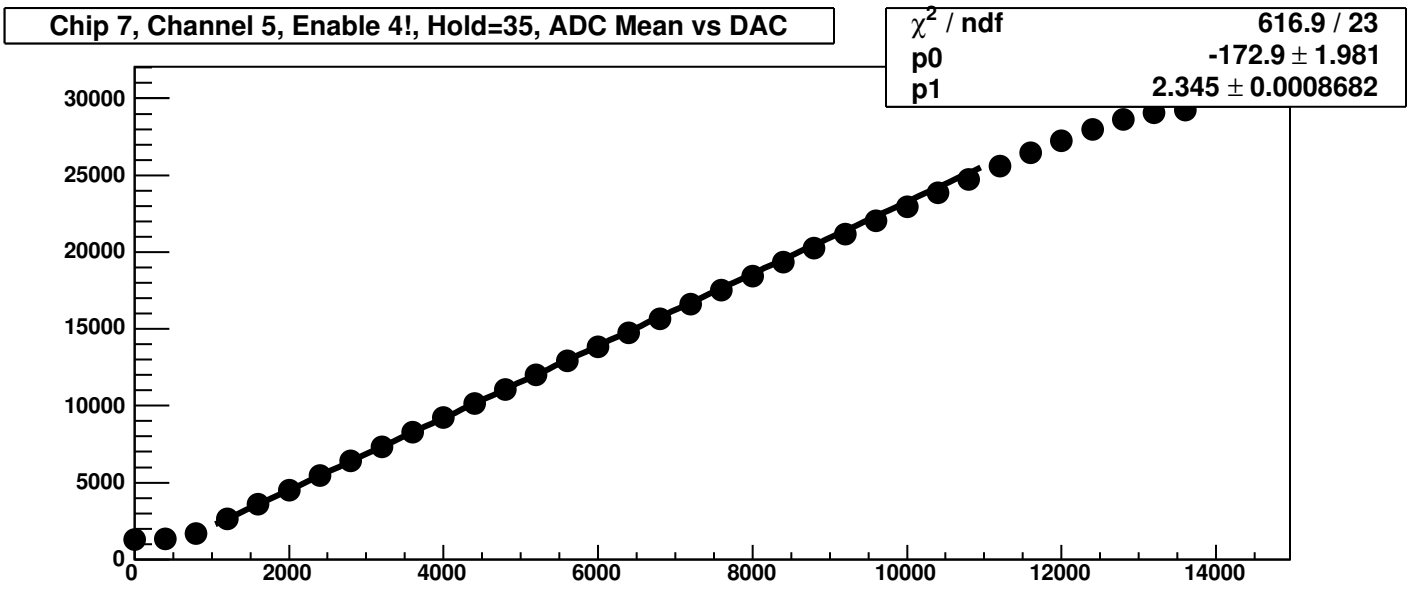

Chip 7, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
24
22
20
18
16
14
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

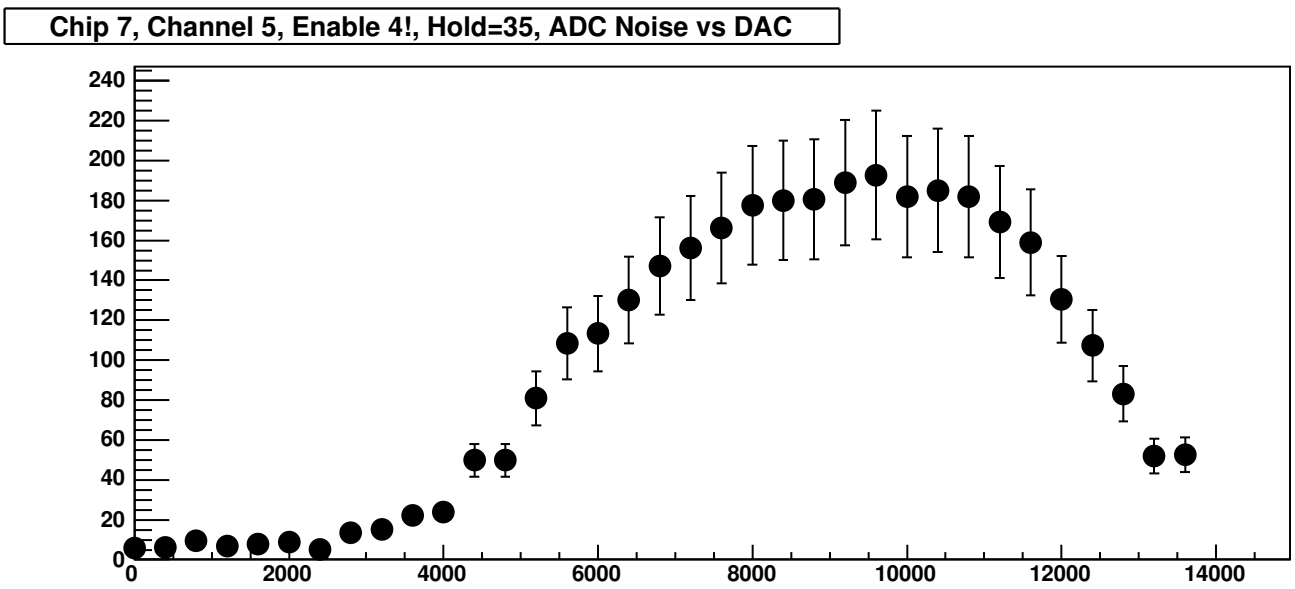

Chip 7, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

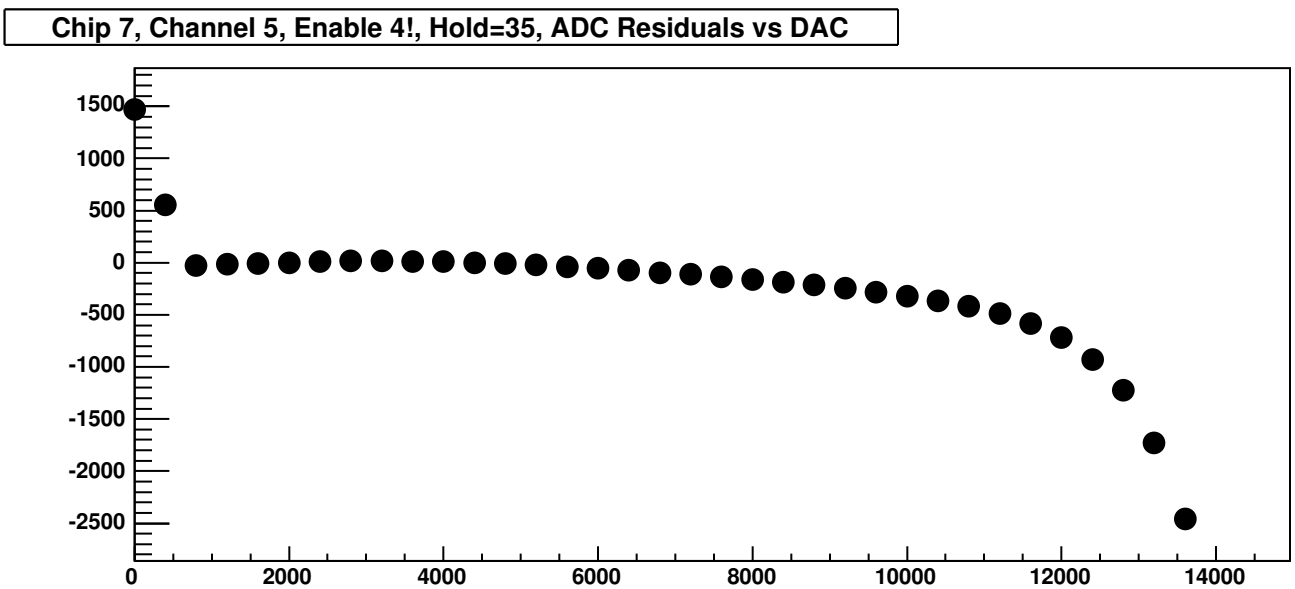

Chip 7, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 65.84 / 23
p0 -212.2 ± 0.6723
p1 0.02447 ± 0.0001076
150
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

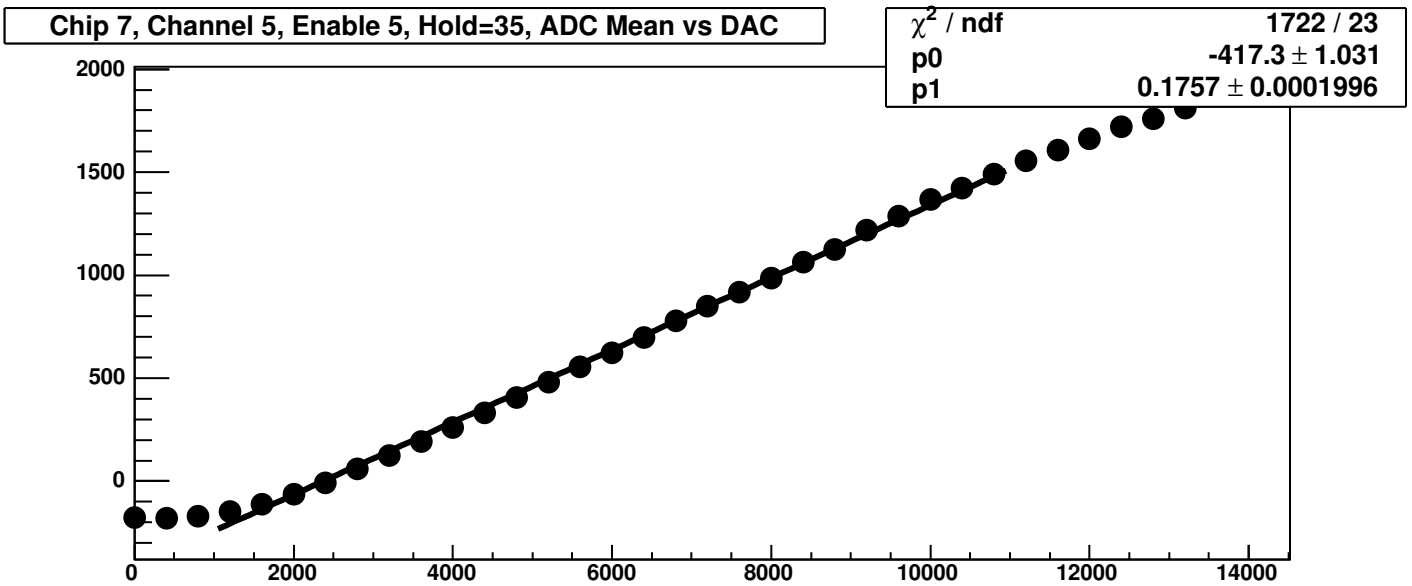

Chip 7, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

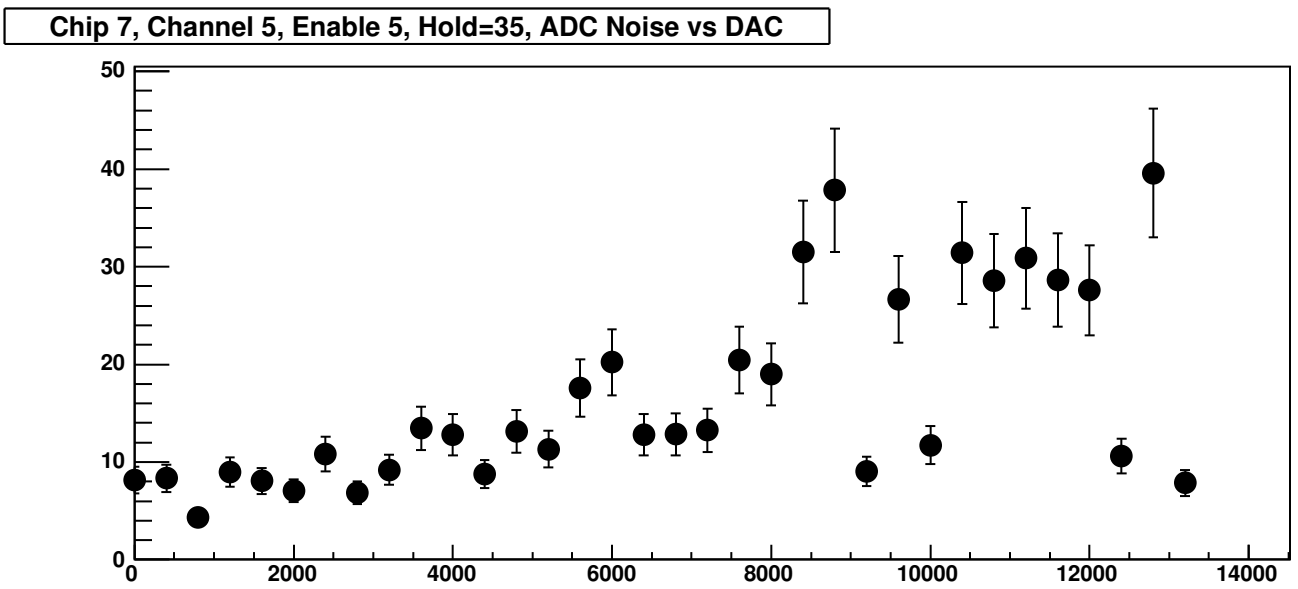

Chip 7, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

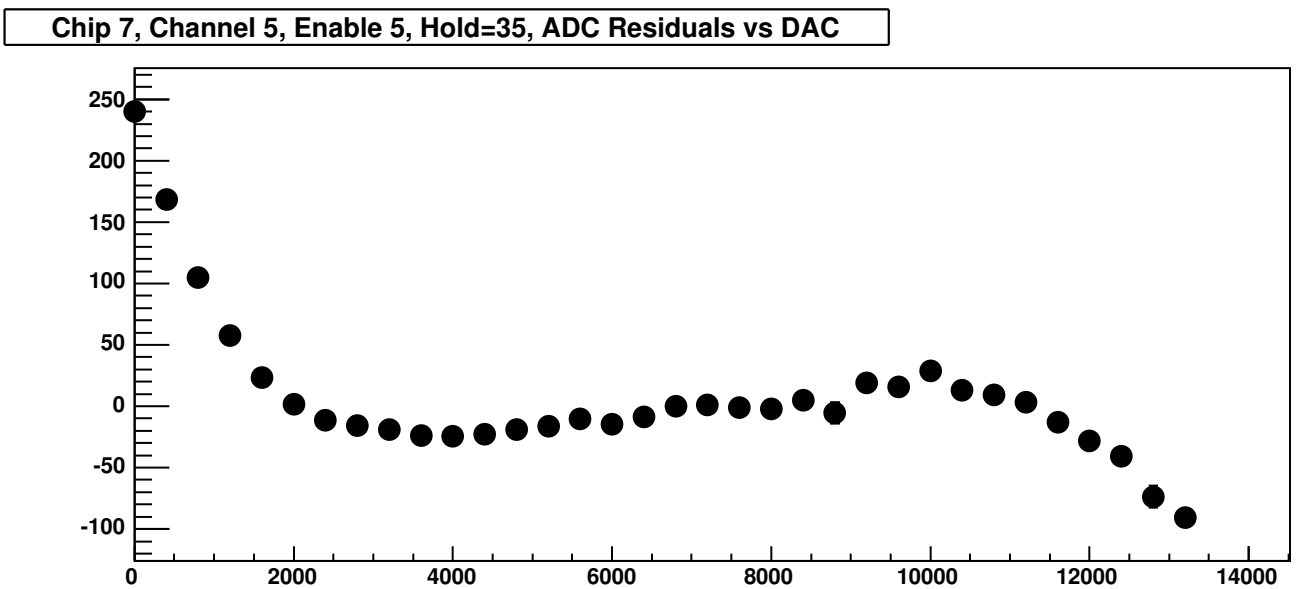

Chip 7, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 616.9 / 23
p0 -172.9 ± 1.981
p1 2.345 ± 0.0008682
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
240
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

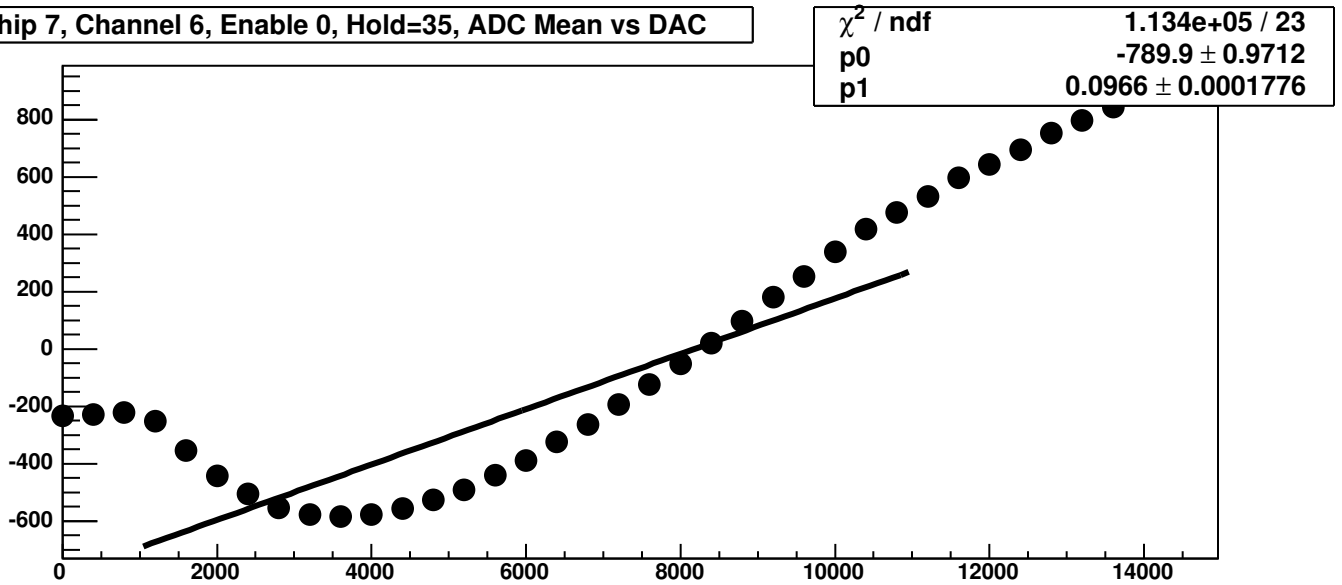

Chip 7, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1722 / 23
p0 -417.3 ± 1.031
p1 0.1757 ± 0.0001996
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

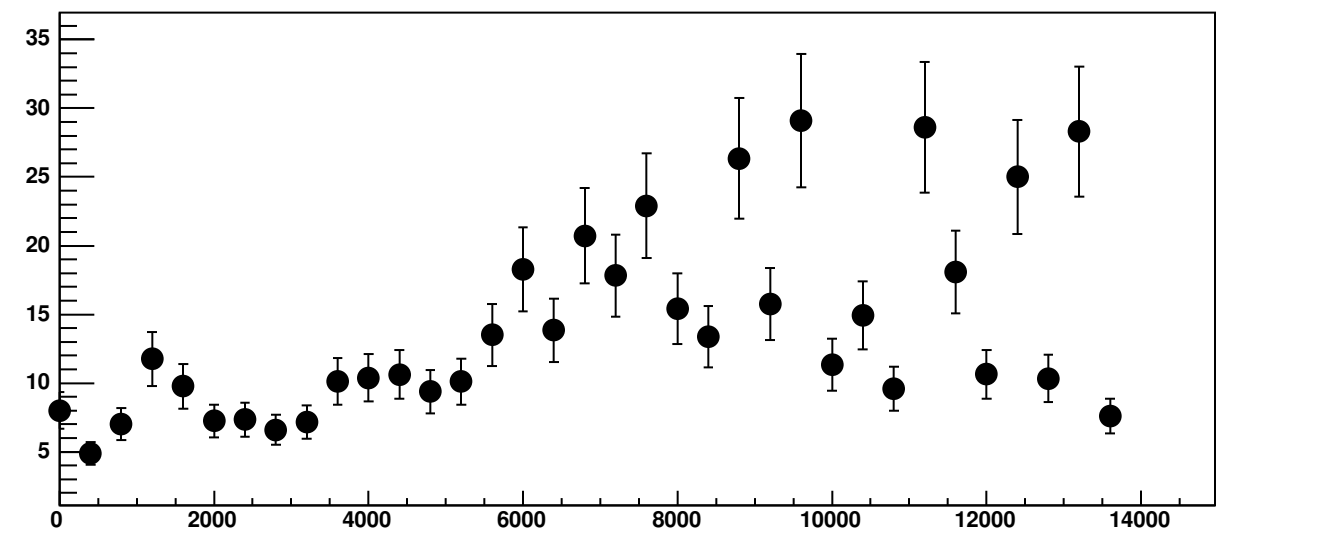

Chip 7, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

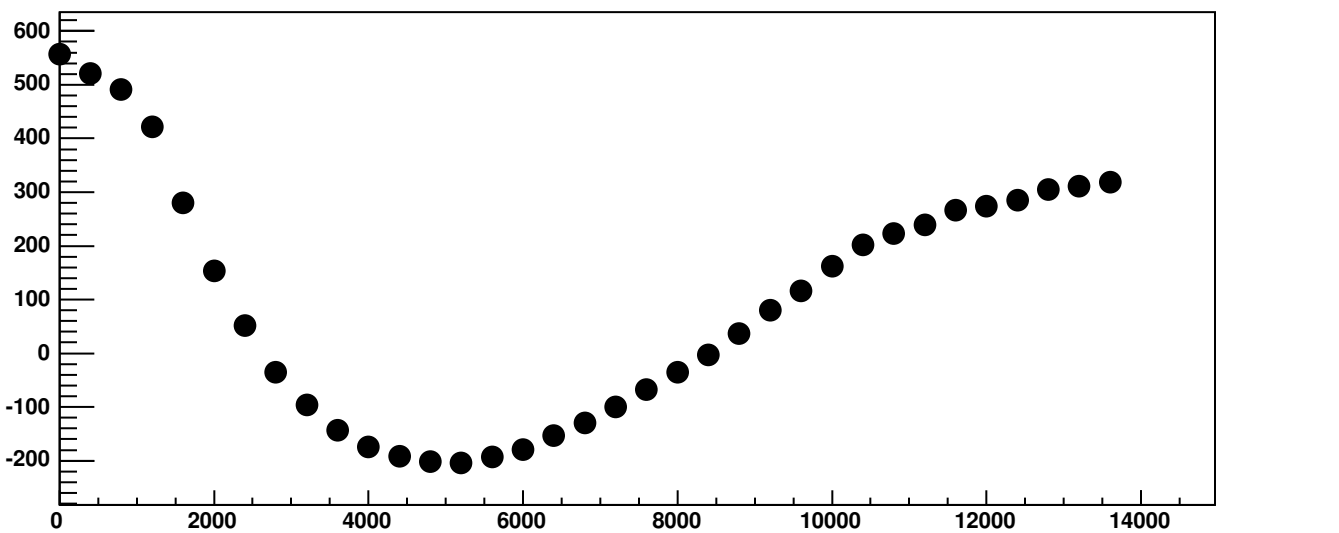
Chip 7, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

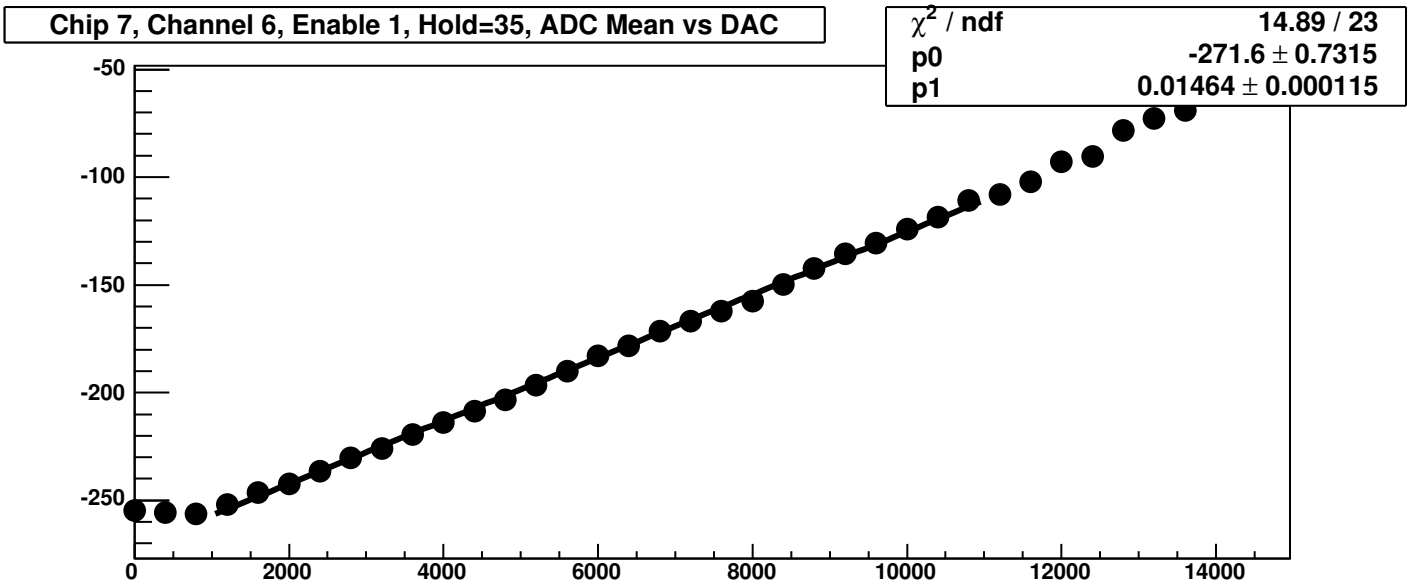


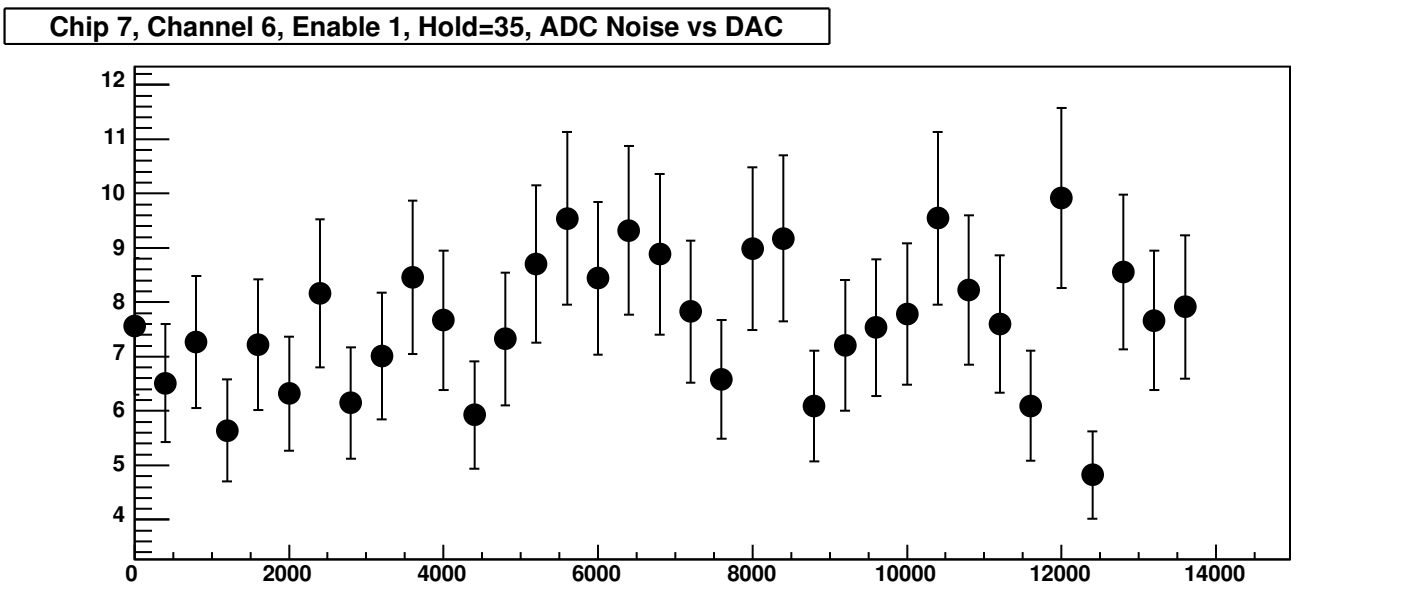
Chip 7, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC

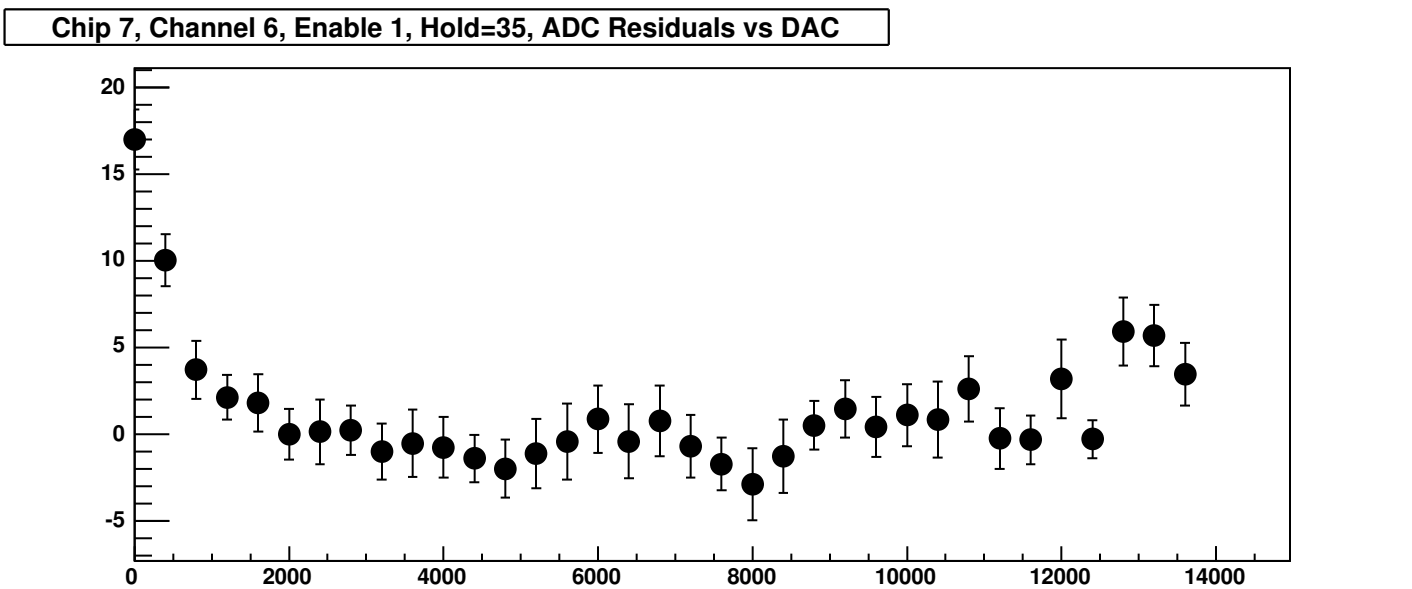


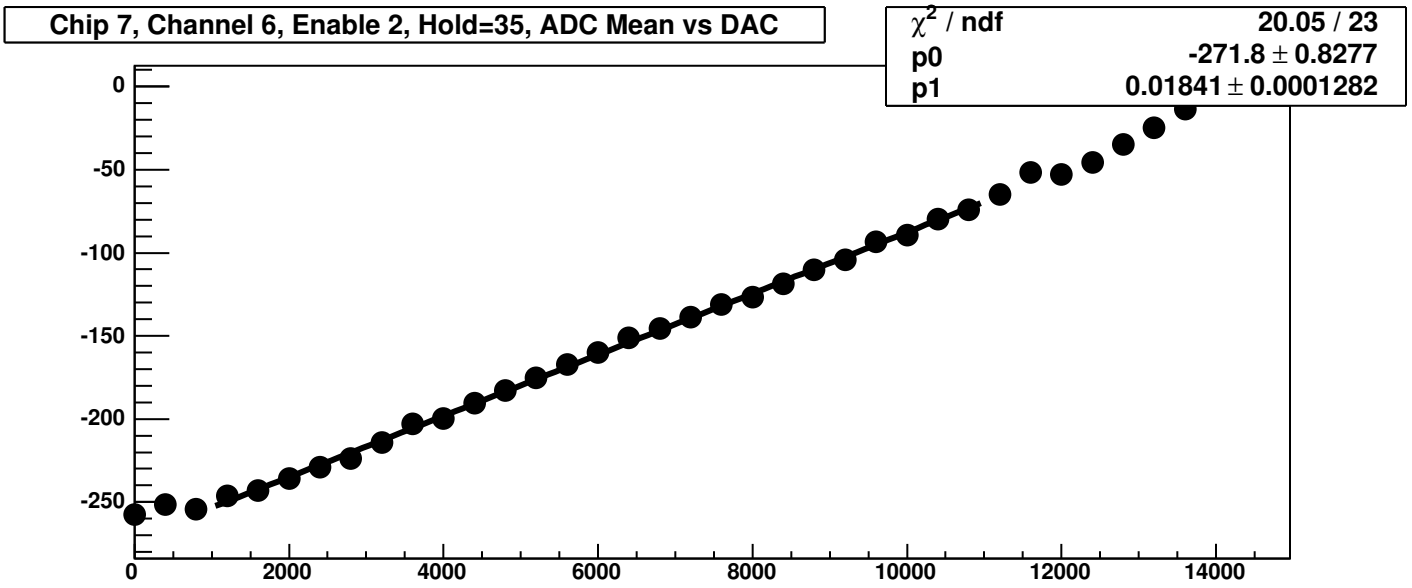
Chip 7, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

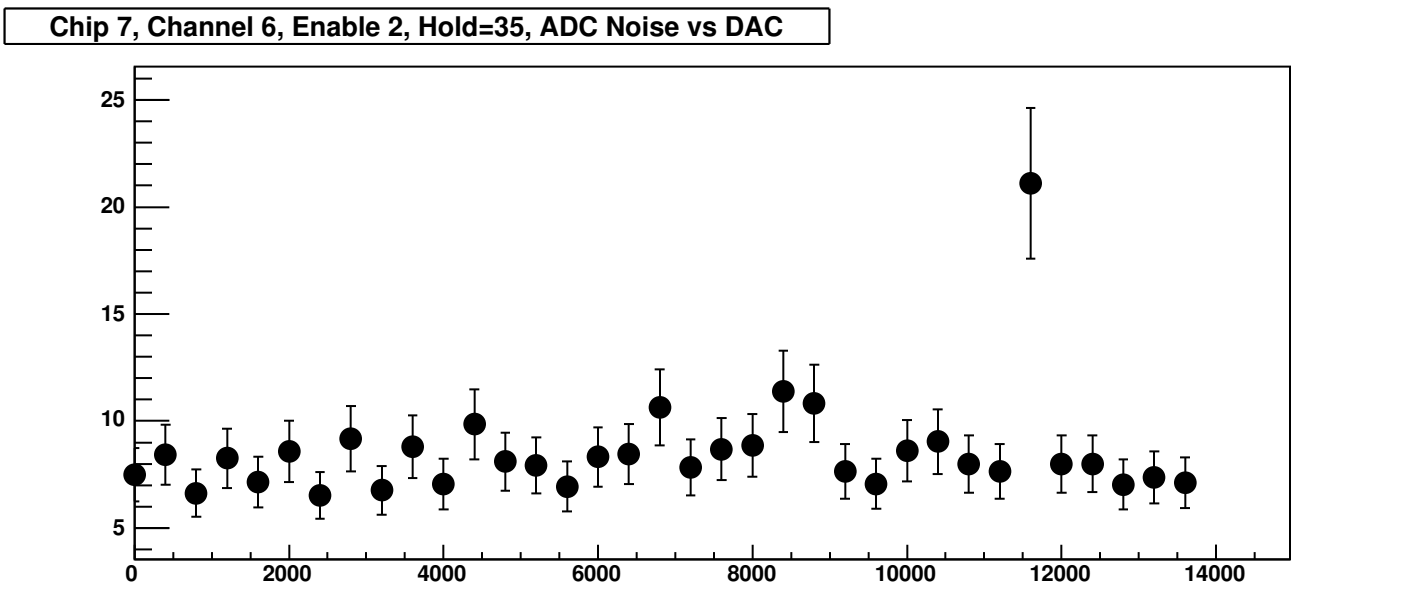


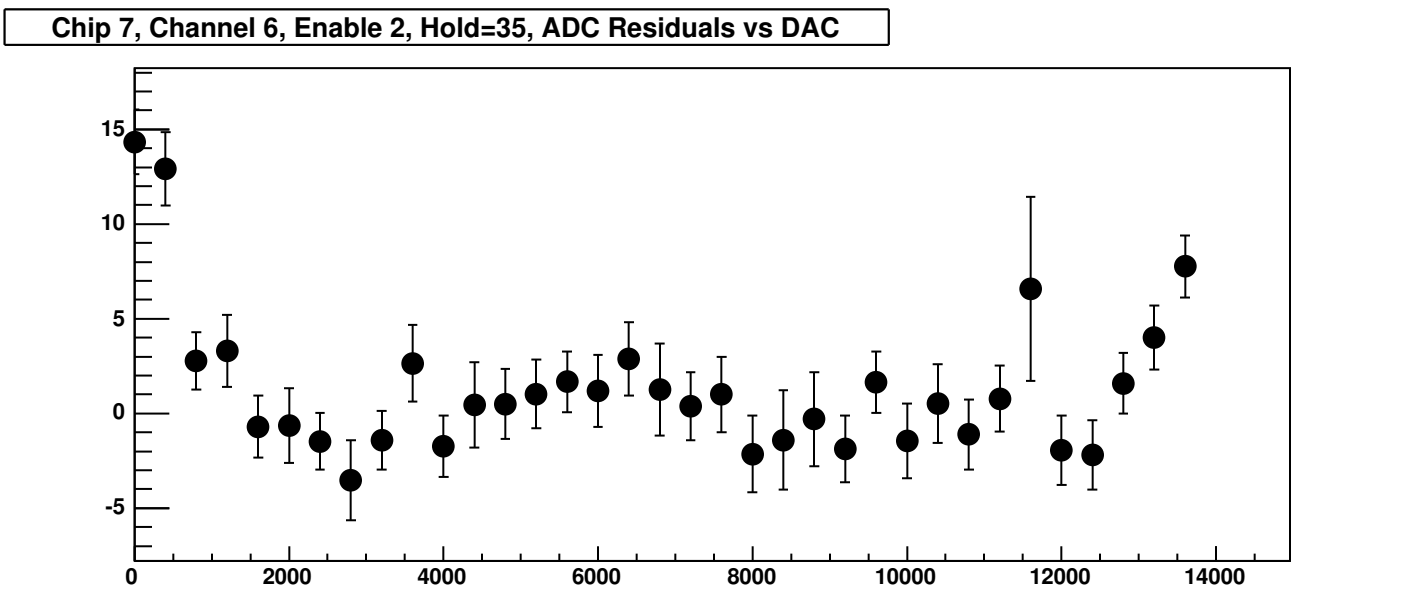

Chip 7, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 14.89 / 23
p0 -271.6 ± 0.7315
p1 0.01464 ± 0.000115
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

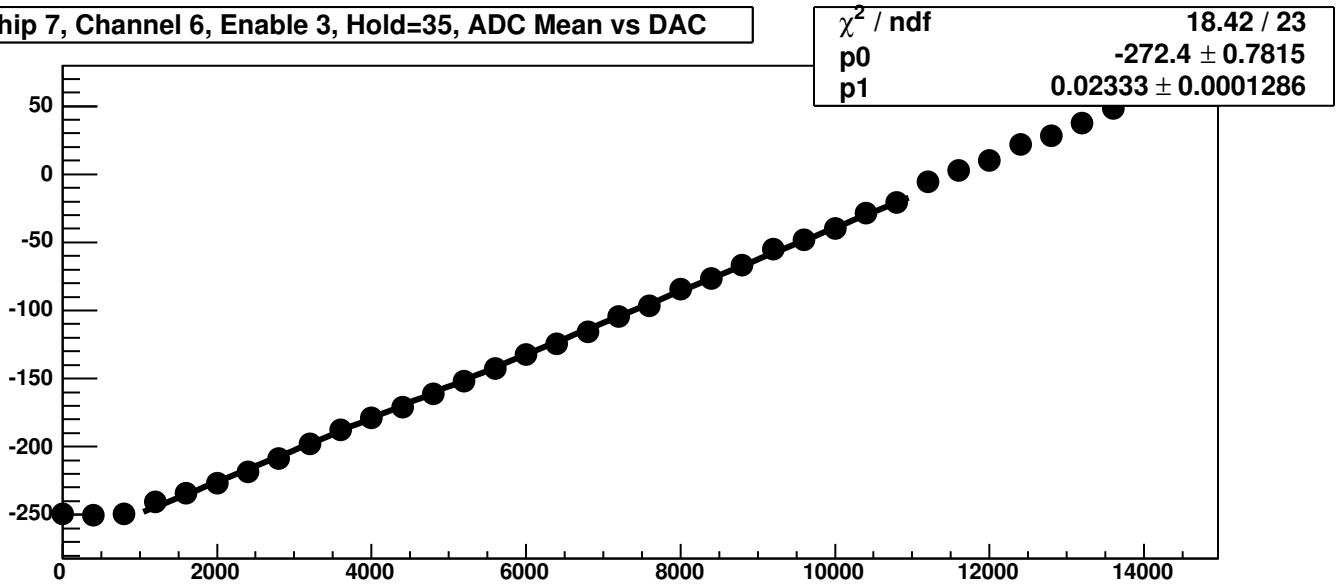

Chip 7, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 20.05 / 23
p0 -271.8 ± 0.8277
p1 0.01841 ± 0.0001282
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

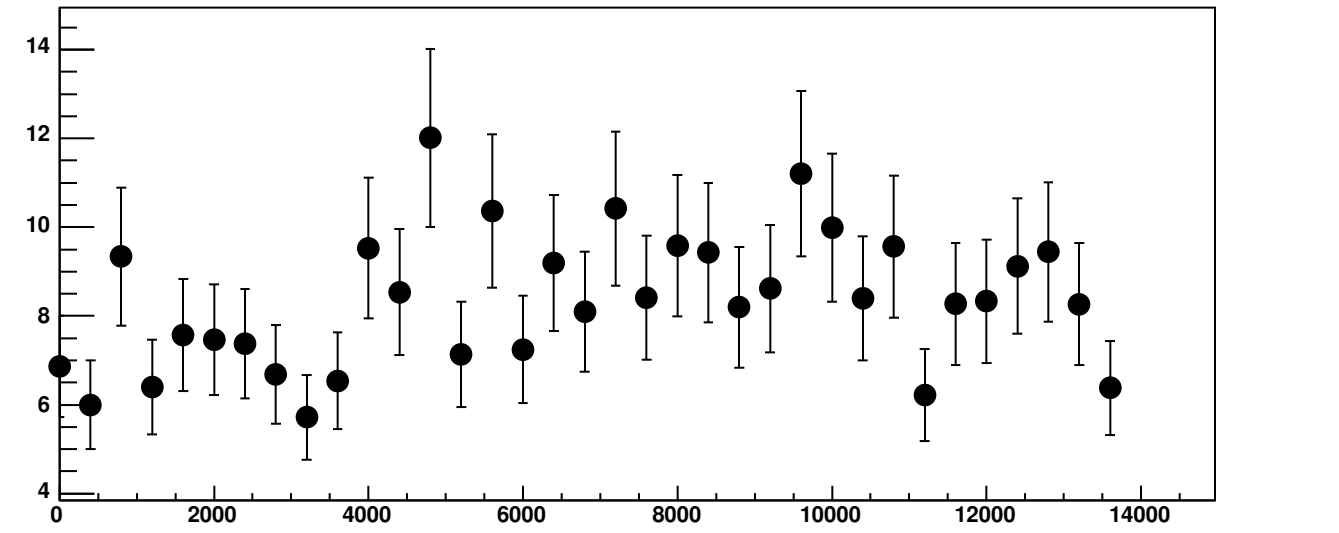

Chip 7, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

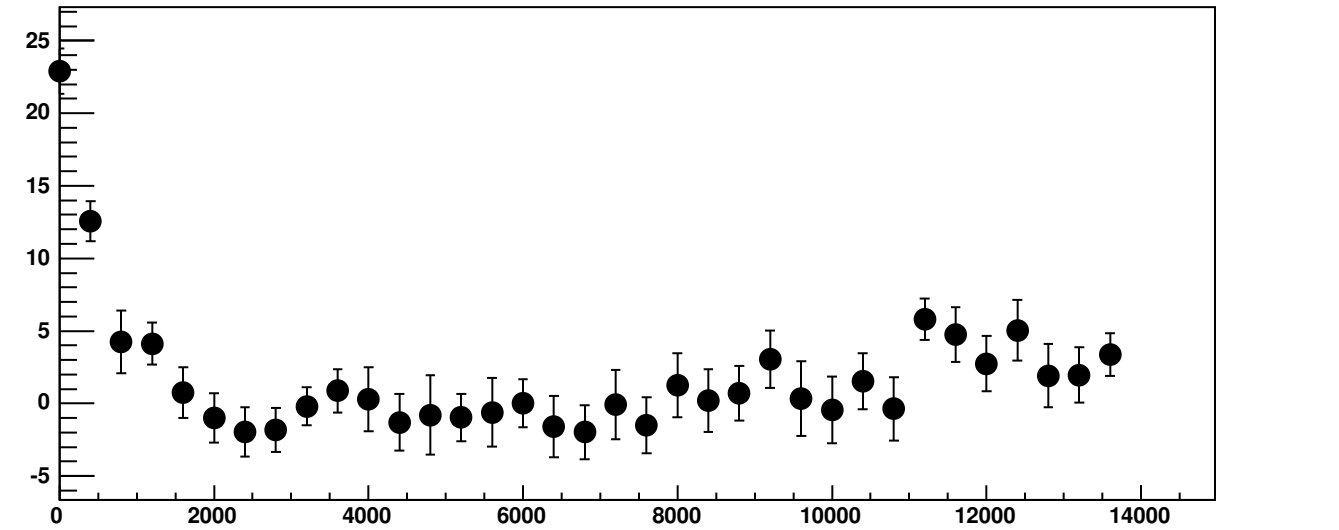
Chip 7, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

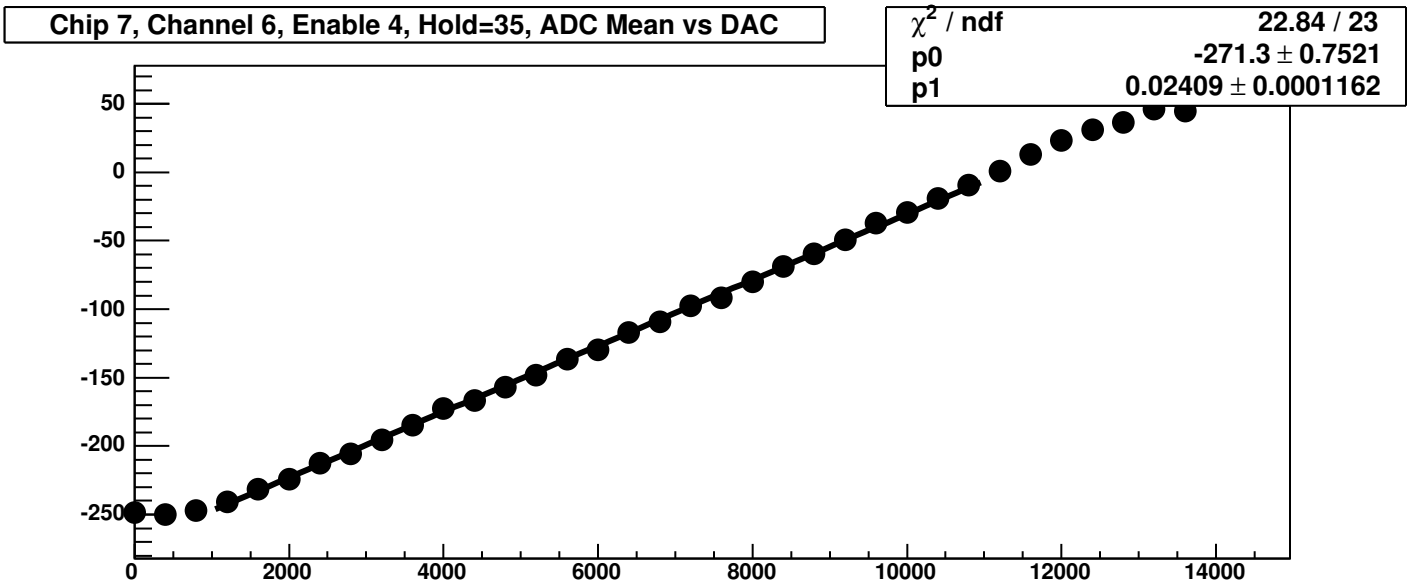


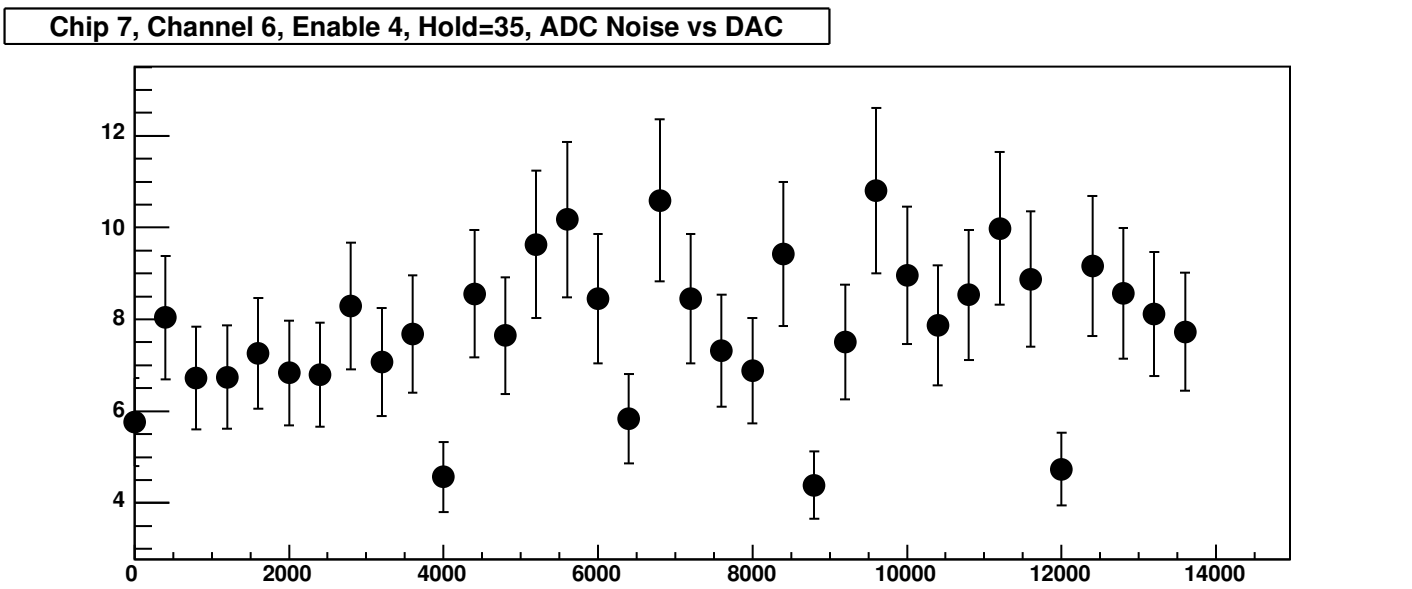
Chip 7, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC

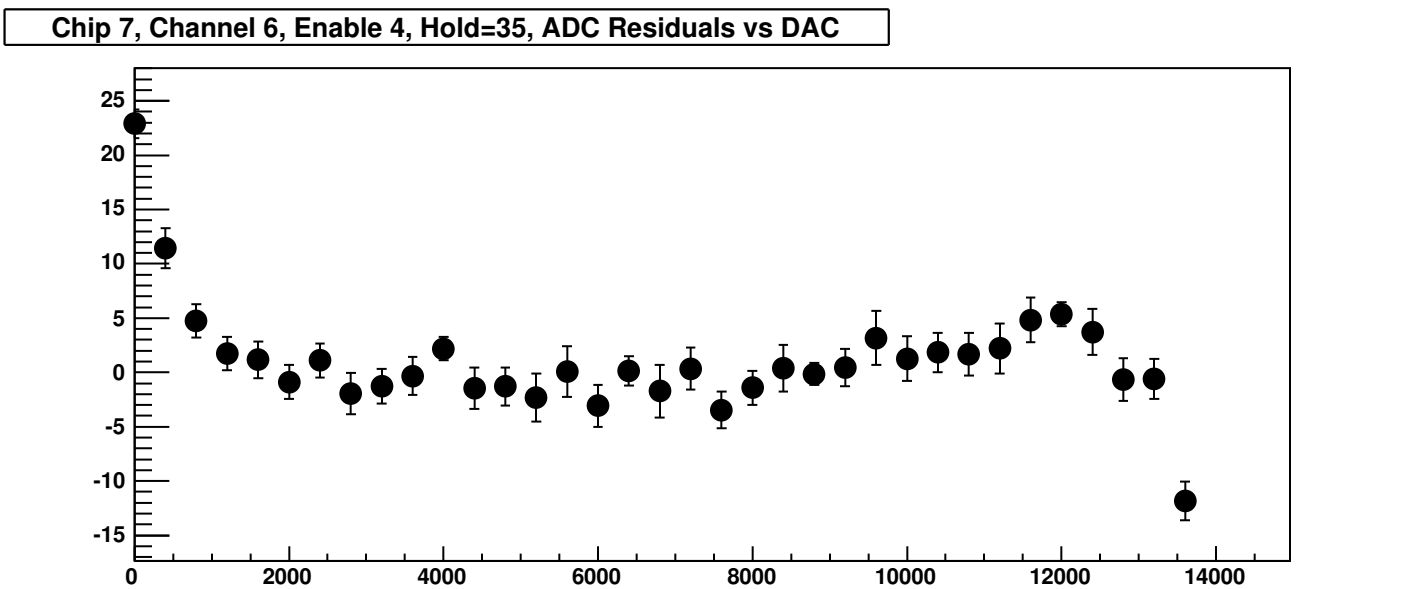


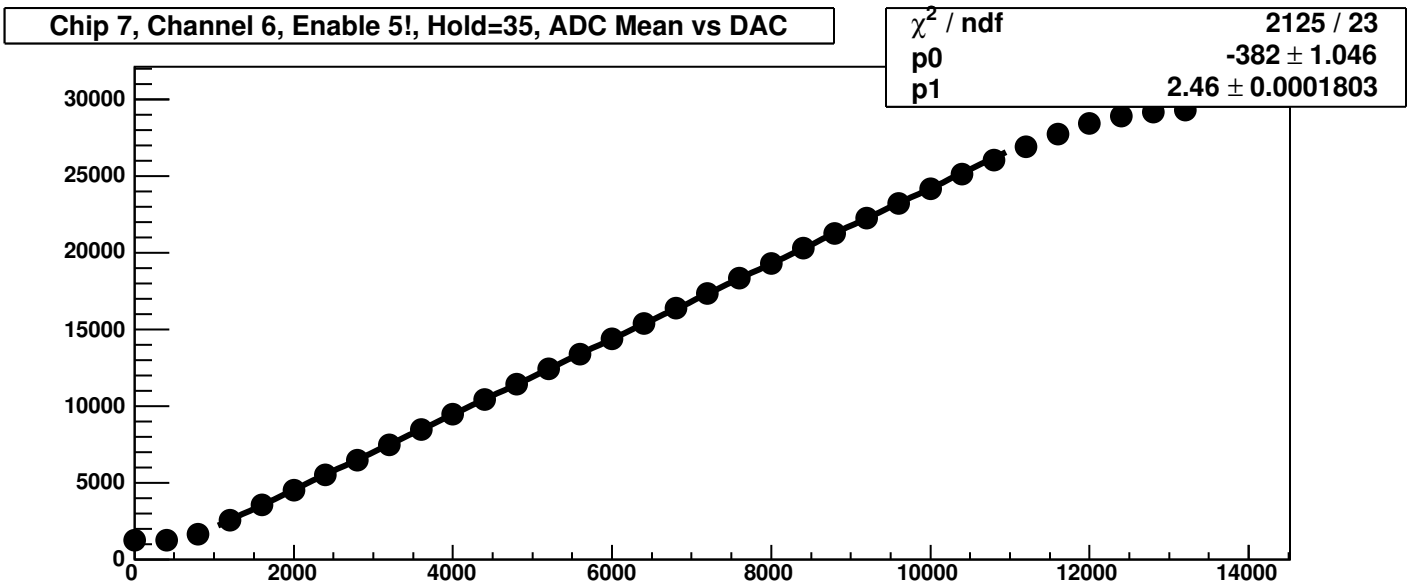
Chip 7, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

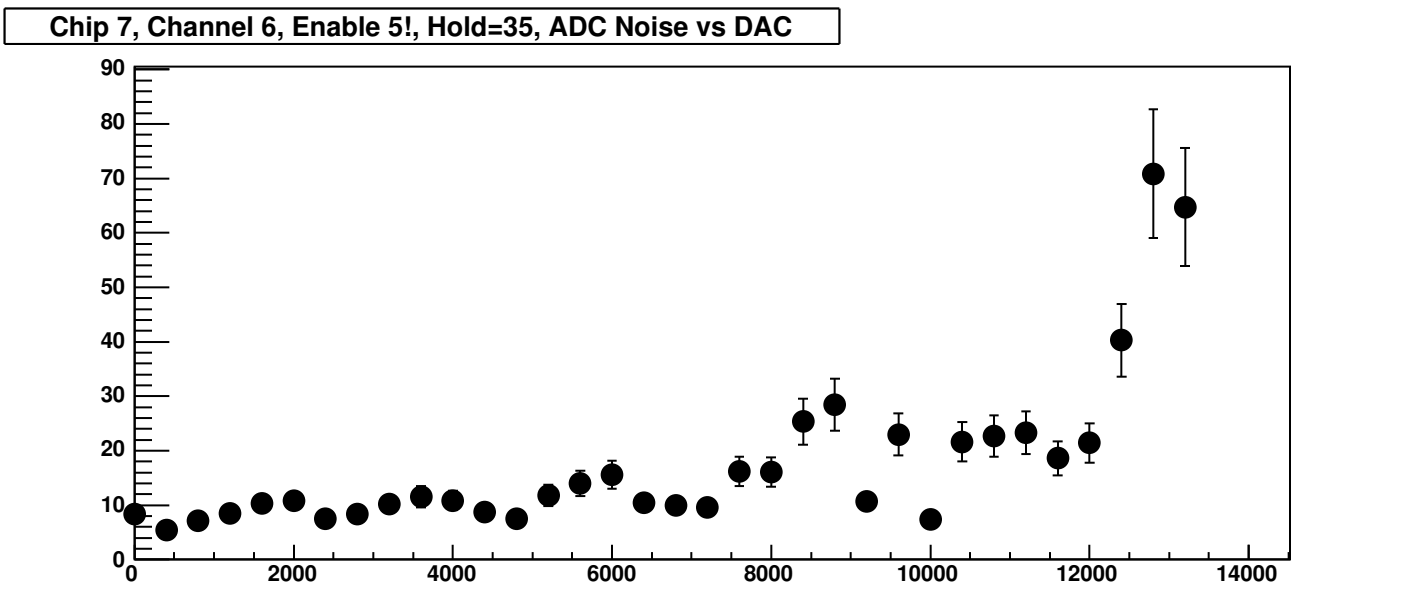


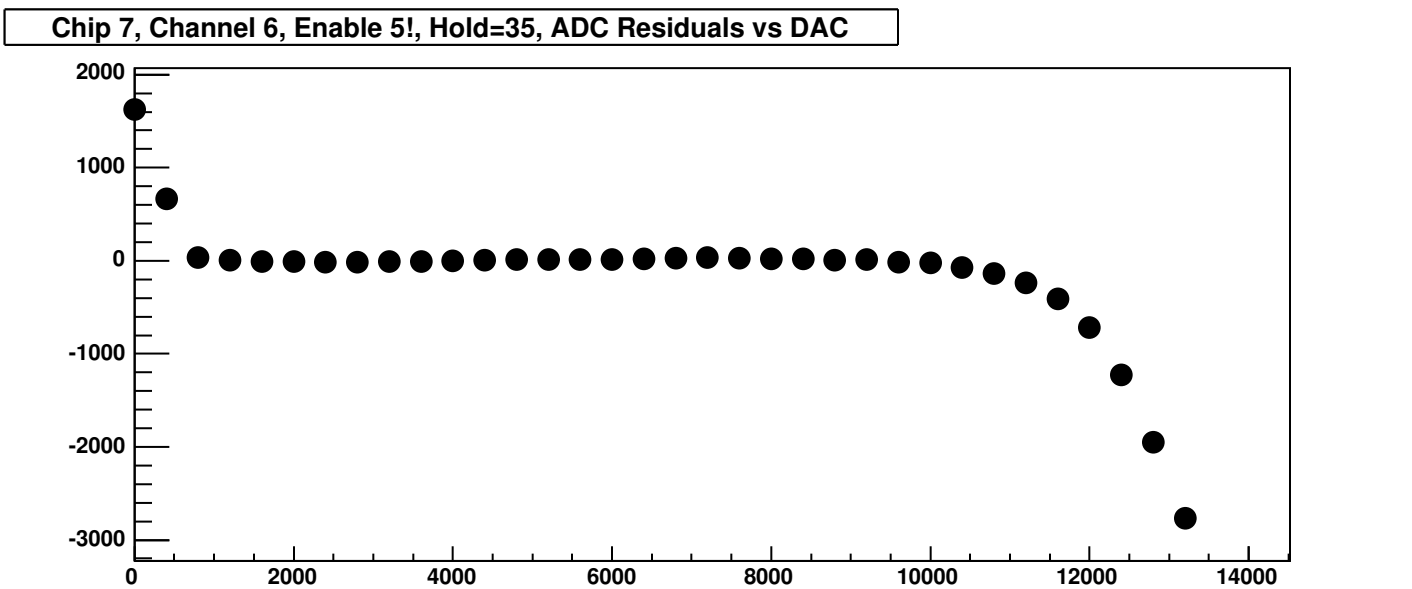

Chip 7, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 22.84 / 23
p0 -271.3 ± 0.7521
p1 0.02409 ± 0.0001162
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

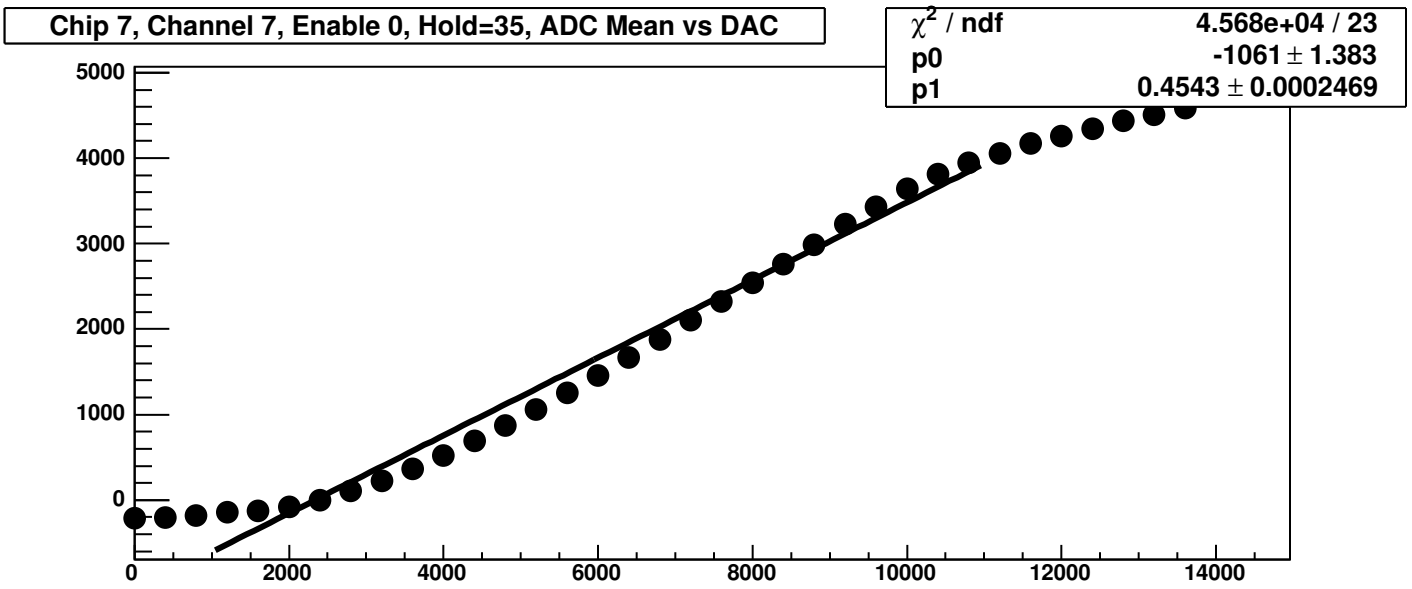

Chip 7, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

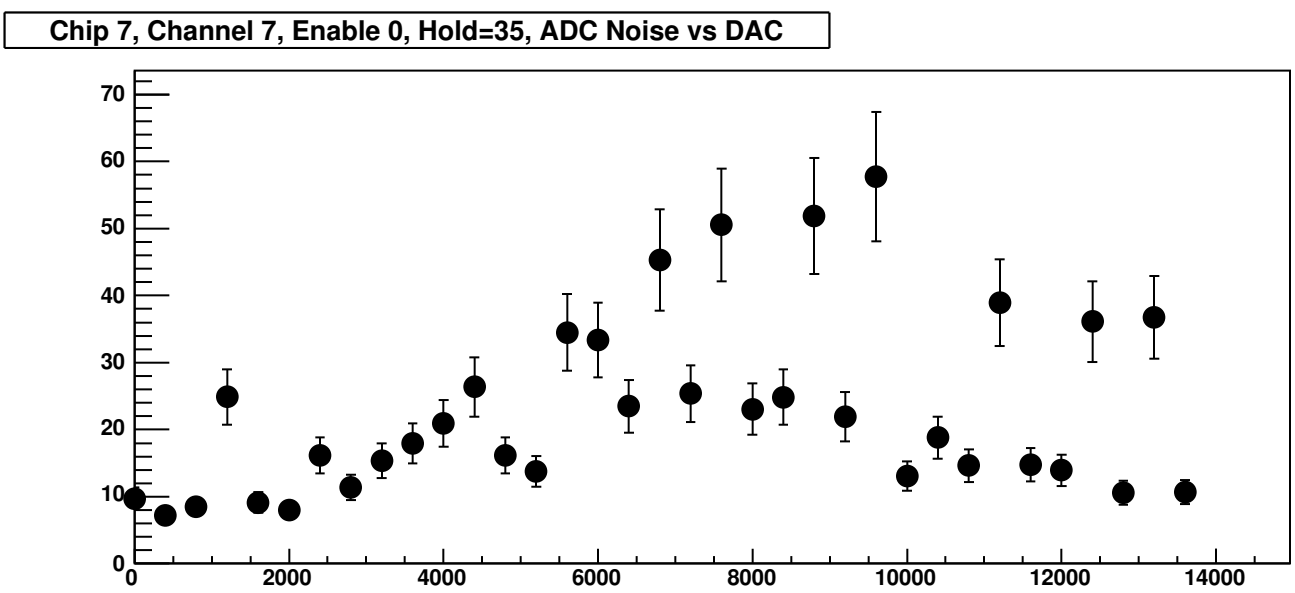

Chip 7, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

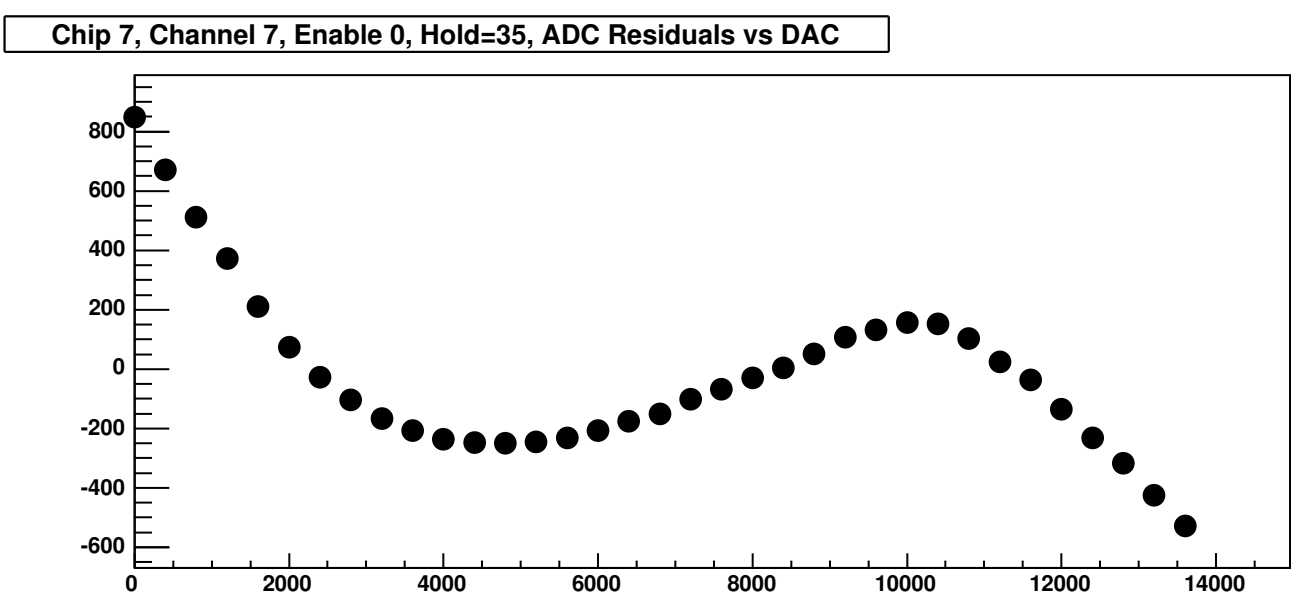

Chip 7, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2125 / 23
p0 -382 ± 1.046
p1 2.46 ± 0.0001803
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

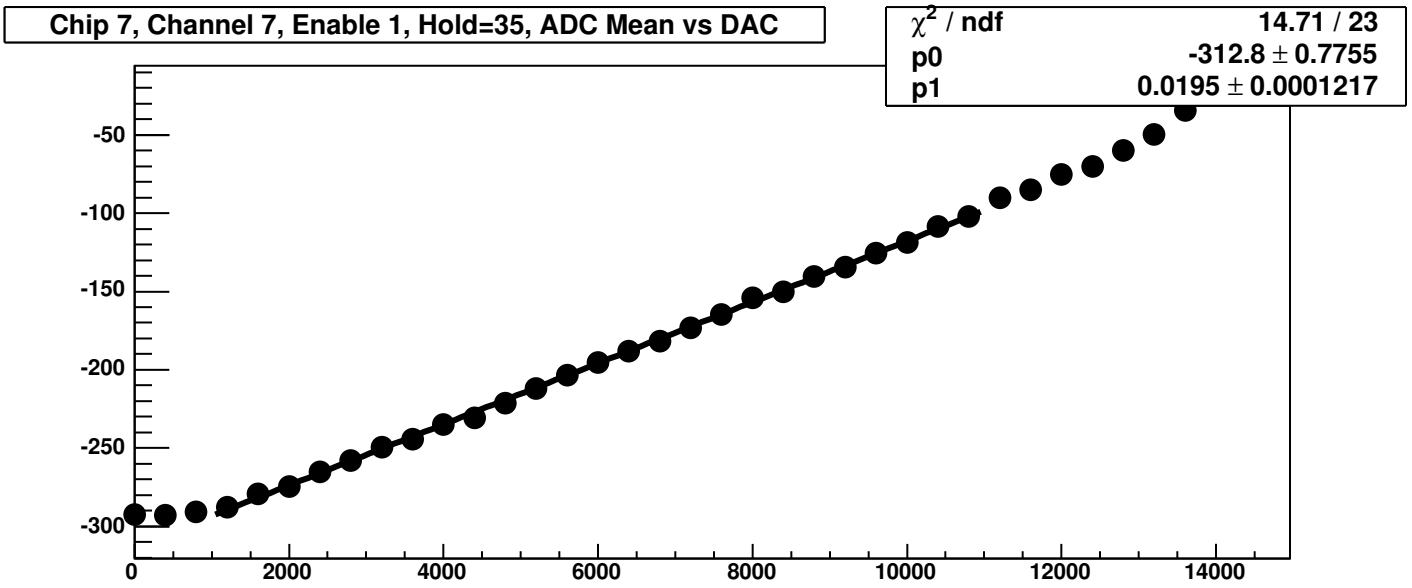

Chip 7, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

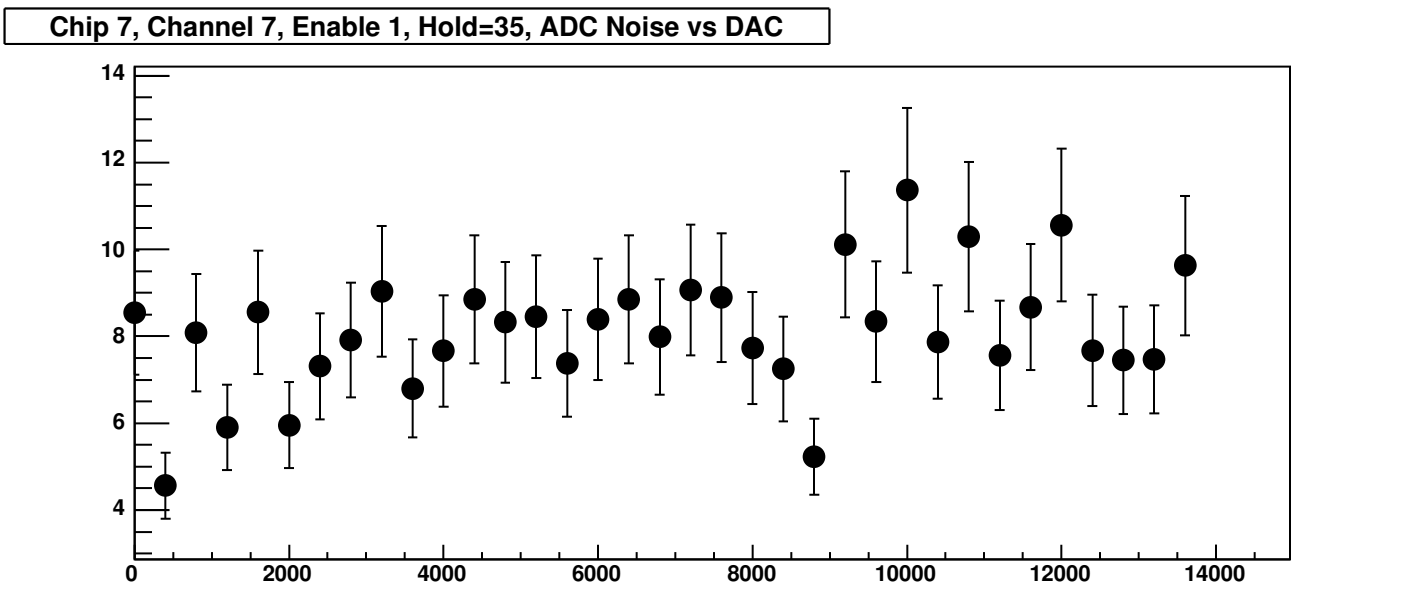

Chip 7, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

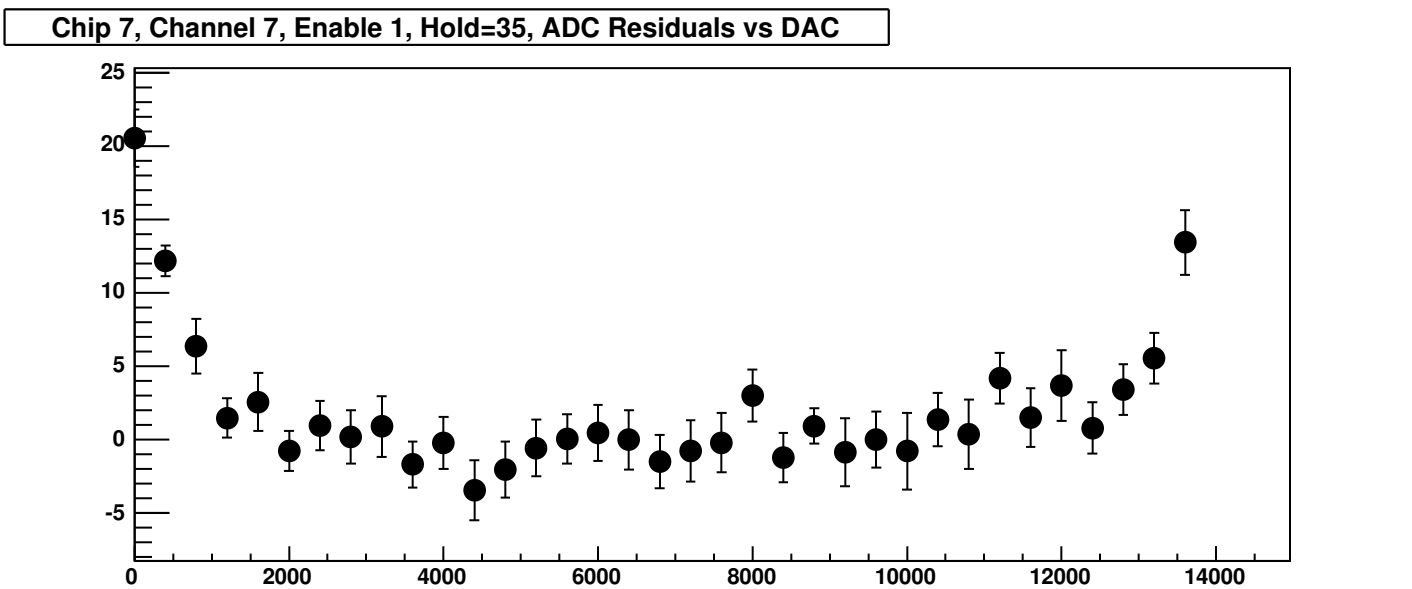

Chip 7, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.568e+04 / 23
p0 -1061 ± 1.383
p1 0.4543 ± 0.0002469
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

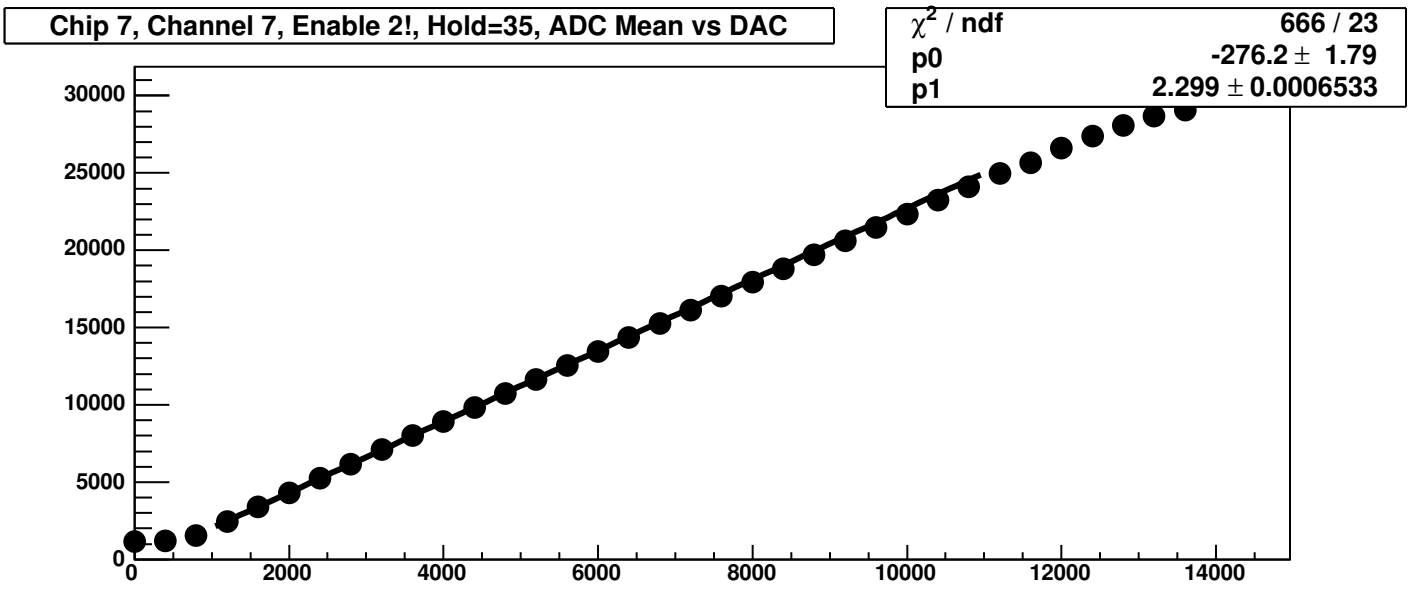

Chip 7, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

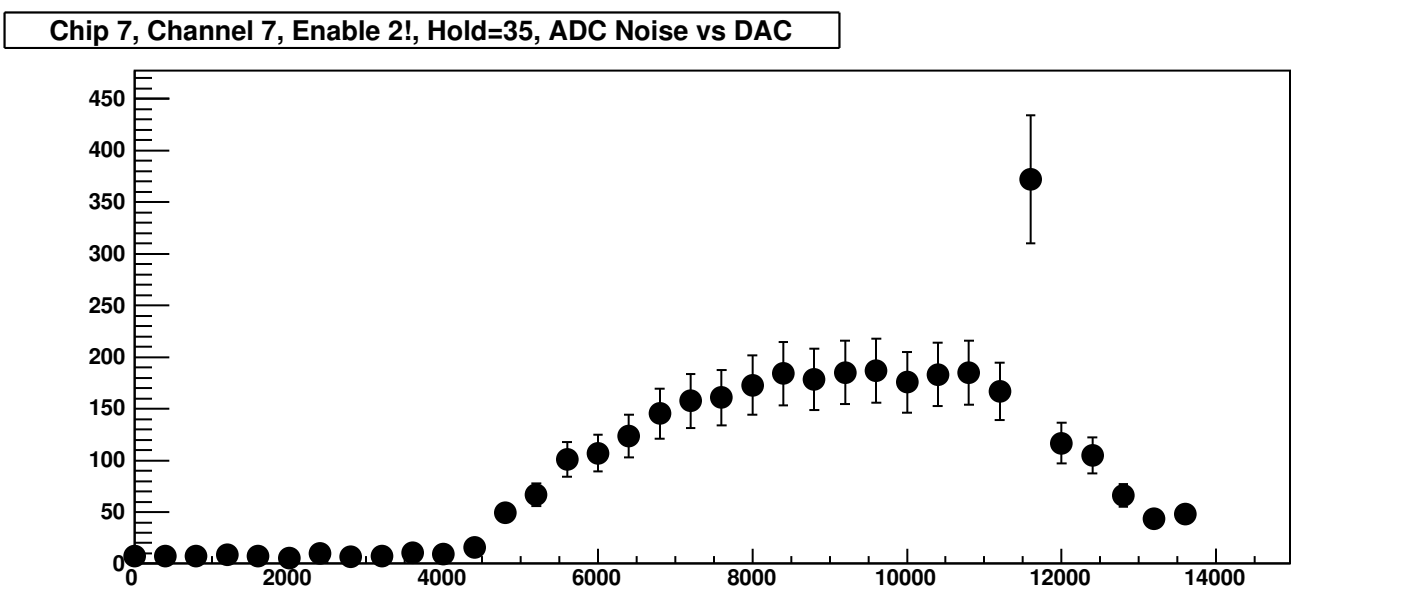

Chip 7, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

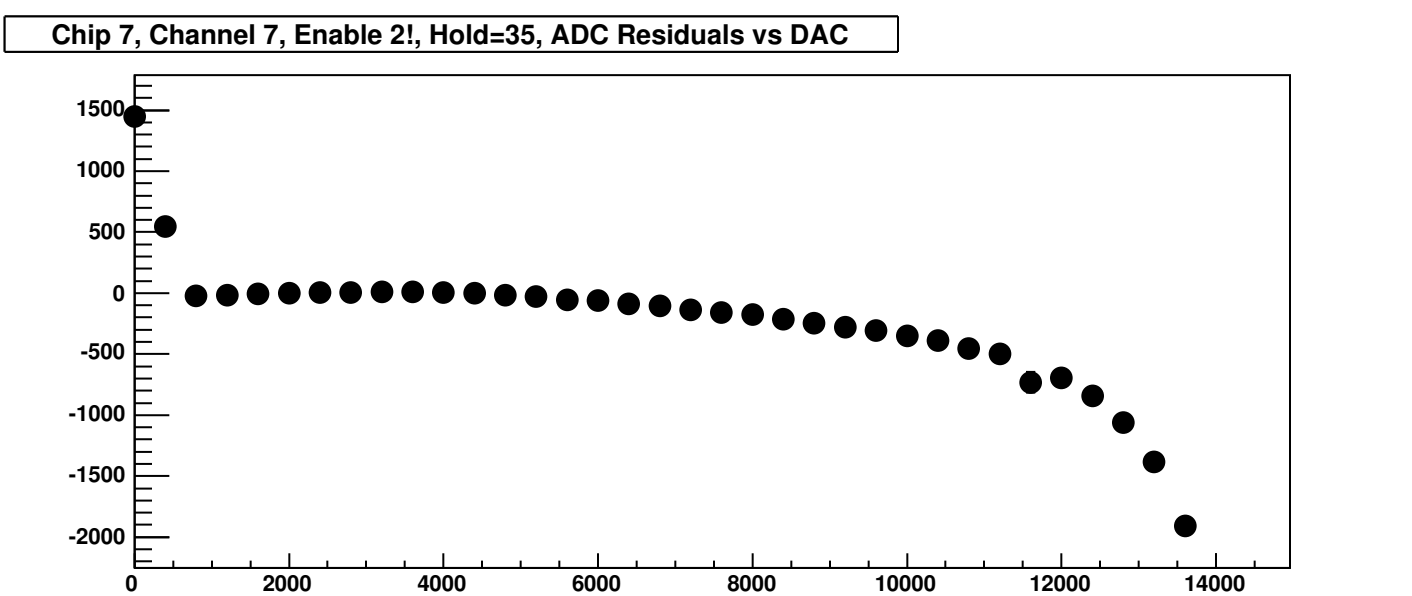

Chip 7, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 14.71 / 23
p0 -312.8 ± 0.7755
p1 0.0195 ± 0.0001217
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

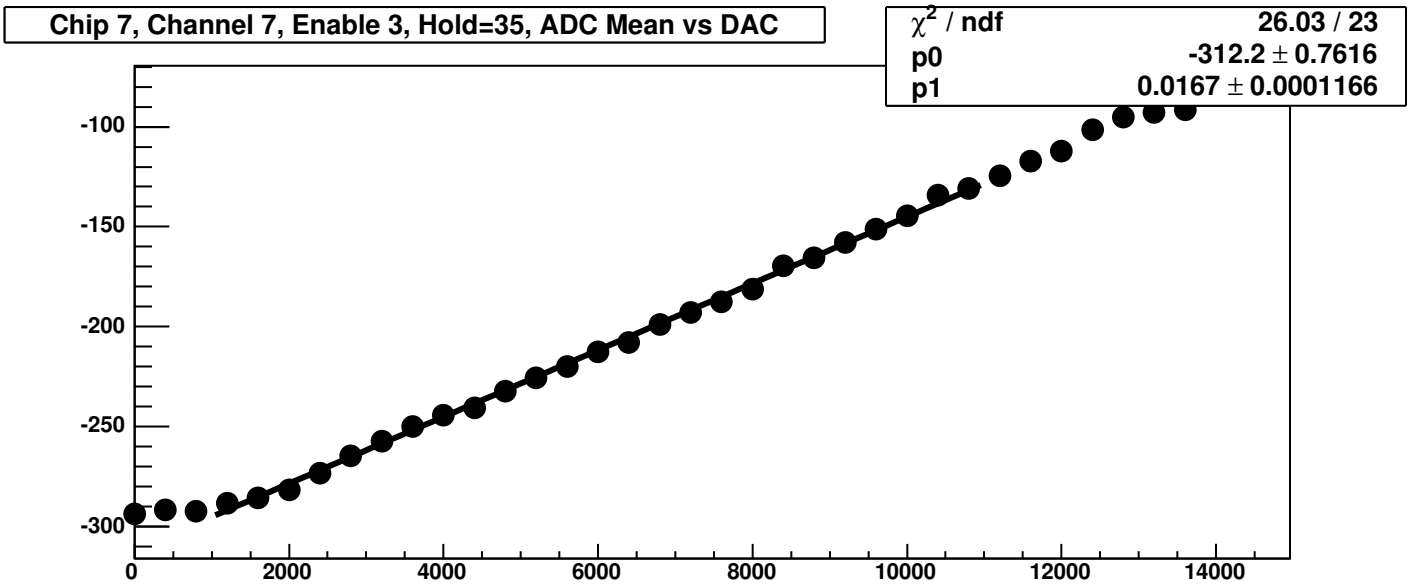

Chip 7, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

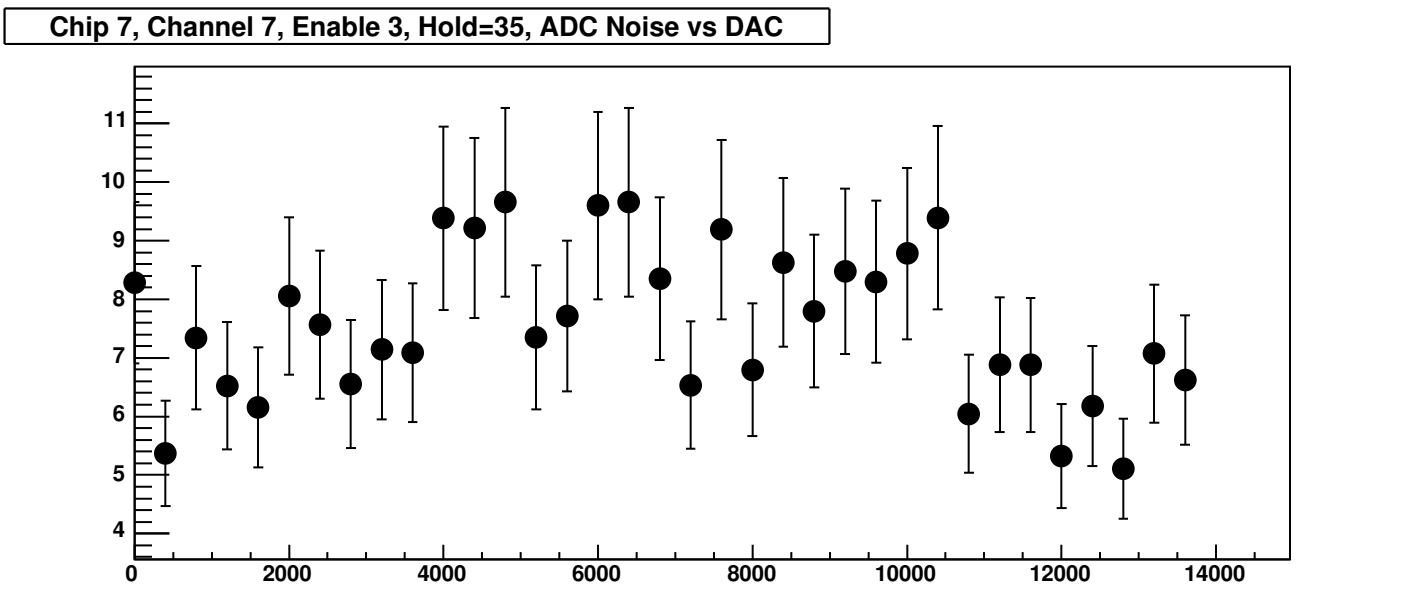

Chip 7, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

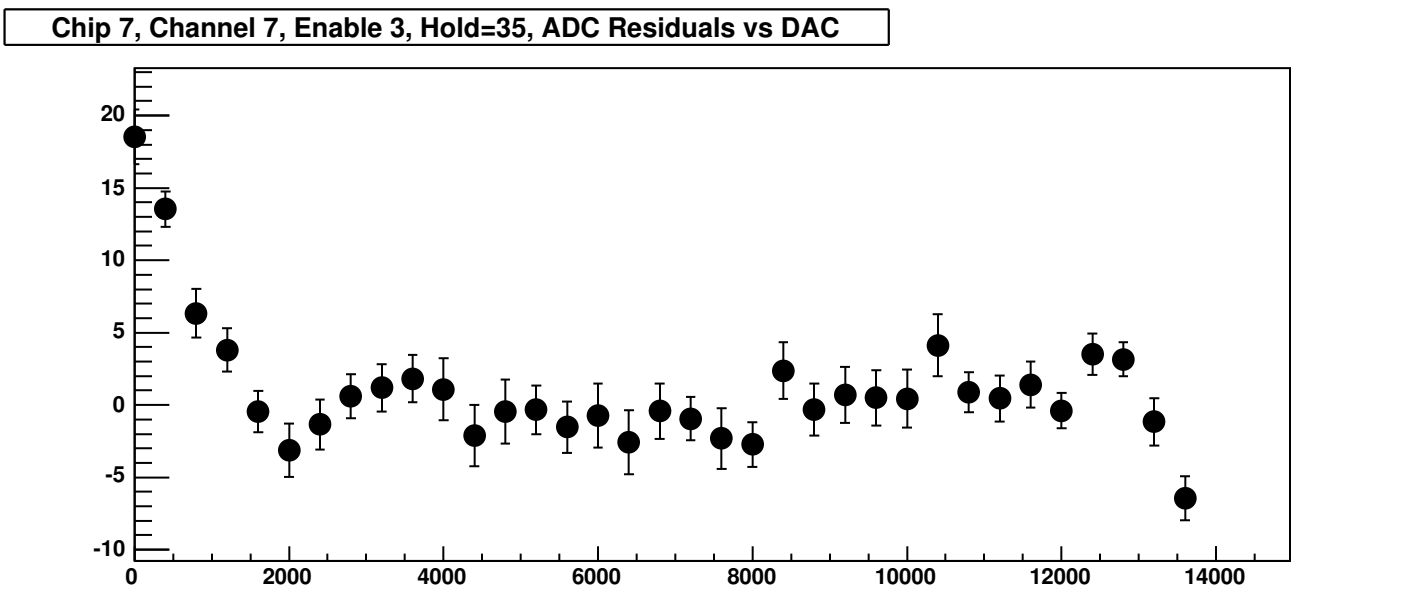

Chip 7, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 666 / 23
p0 -276.2 ± 1.79
p1 2.299 ± 0.0006533
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

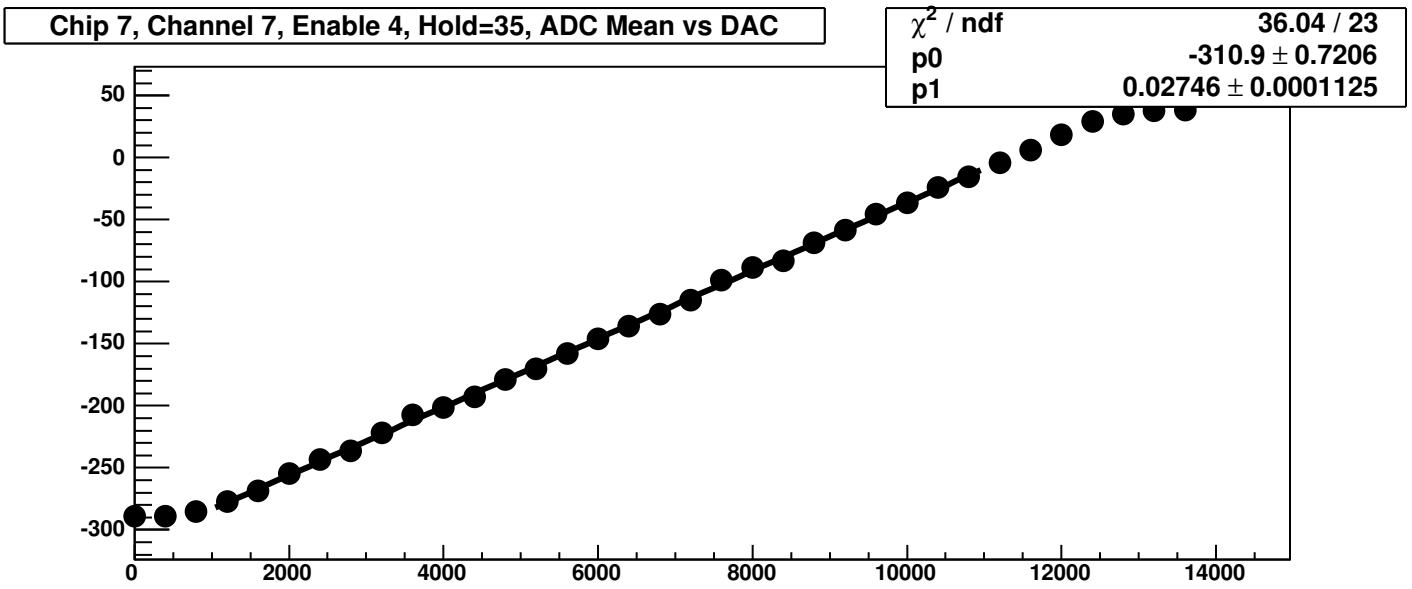

Chip 7, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

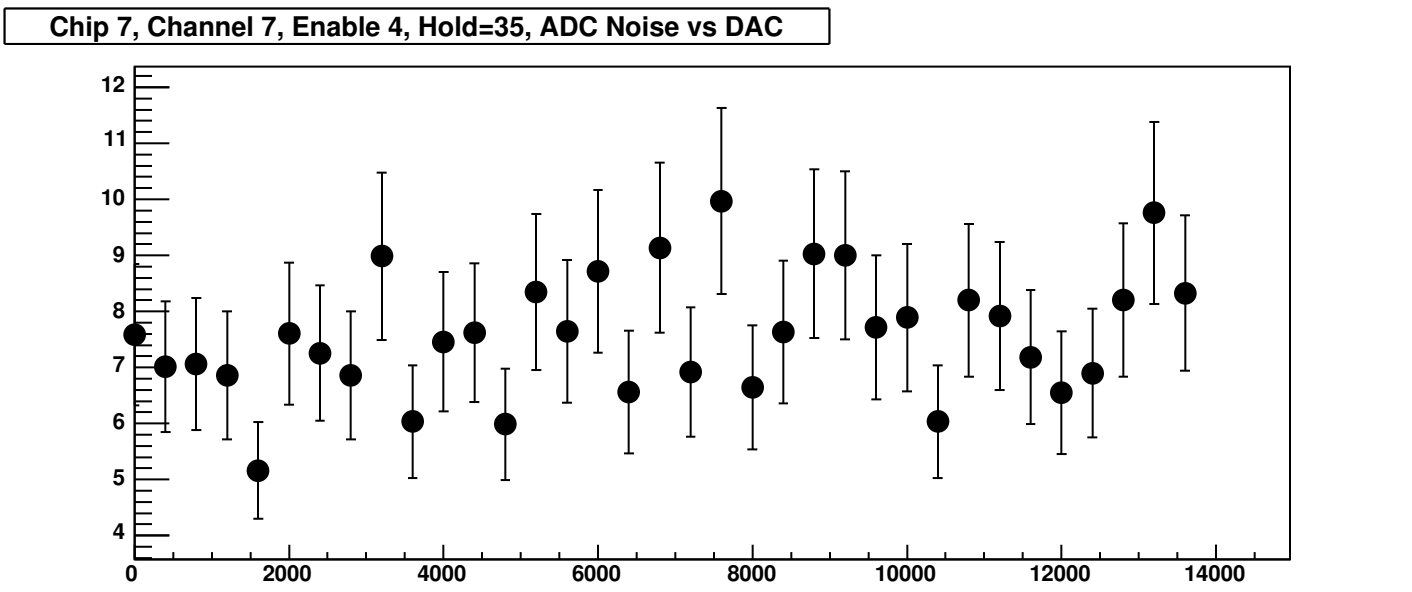

Chip 7, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

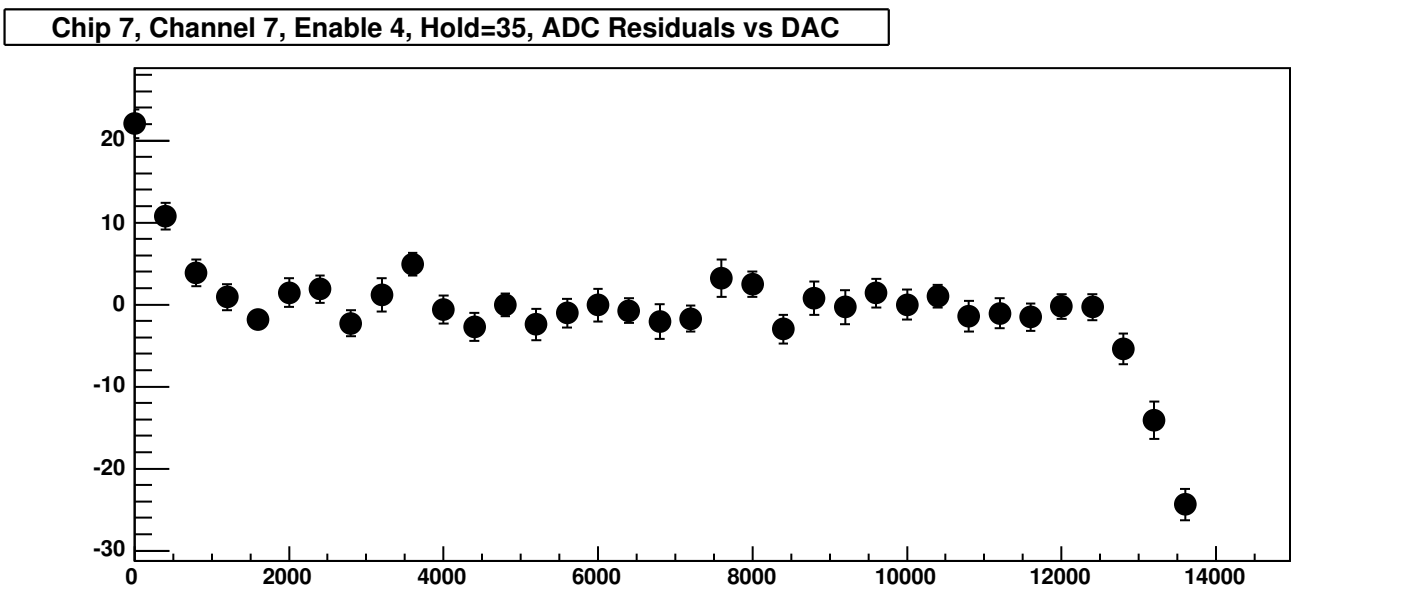

Chip 7, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.03 / 23
p0 -312.2 ± 0.7616
p1 0.0167 ± 0.0001166
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

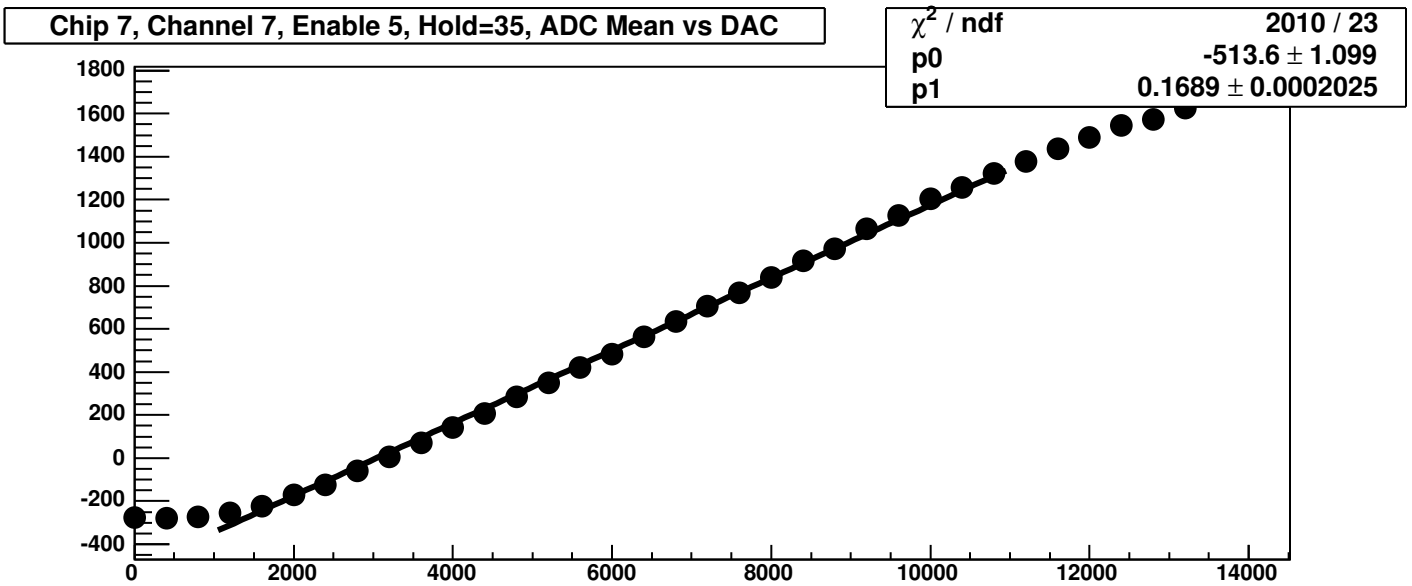

Chip 7, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

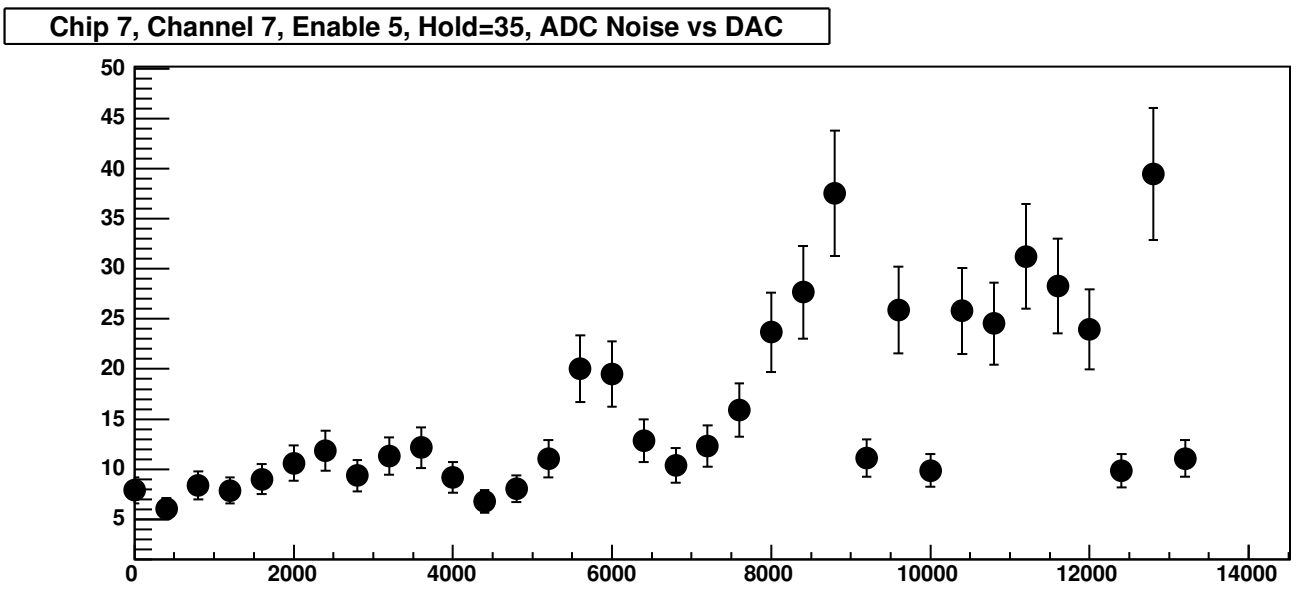

Chip 7, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

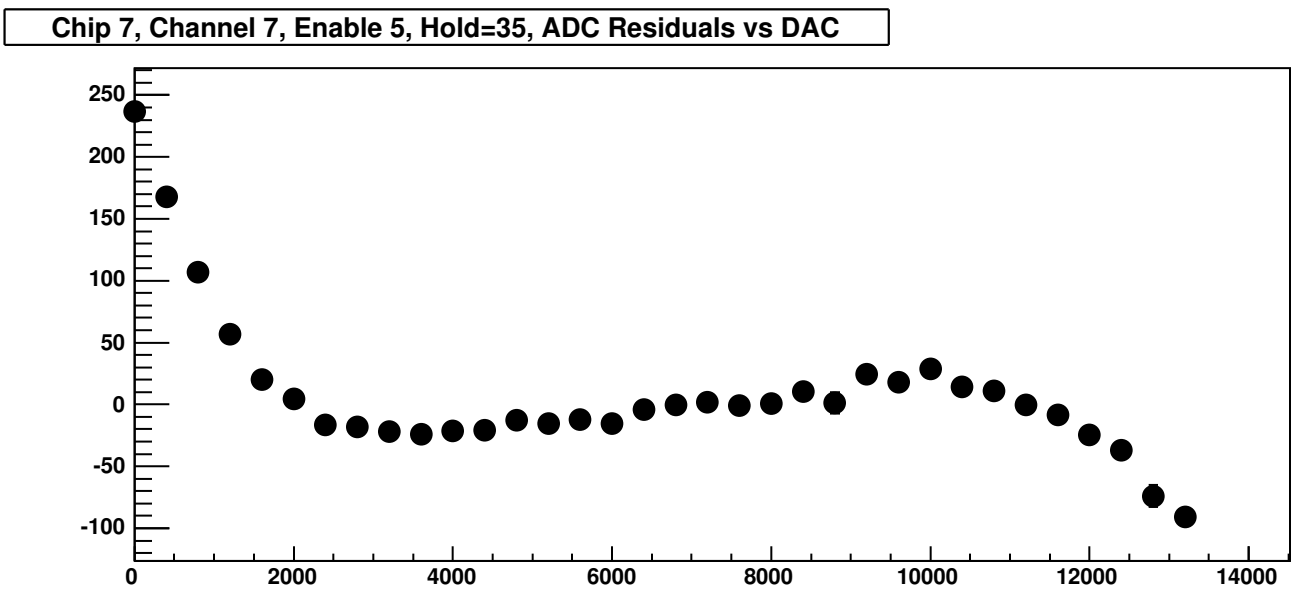

Chip 7, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 36.04 / 23
p0 -310.9 ± 0.7206
p1 0.02746 ± 0.0001125
50
0
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

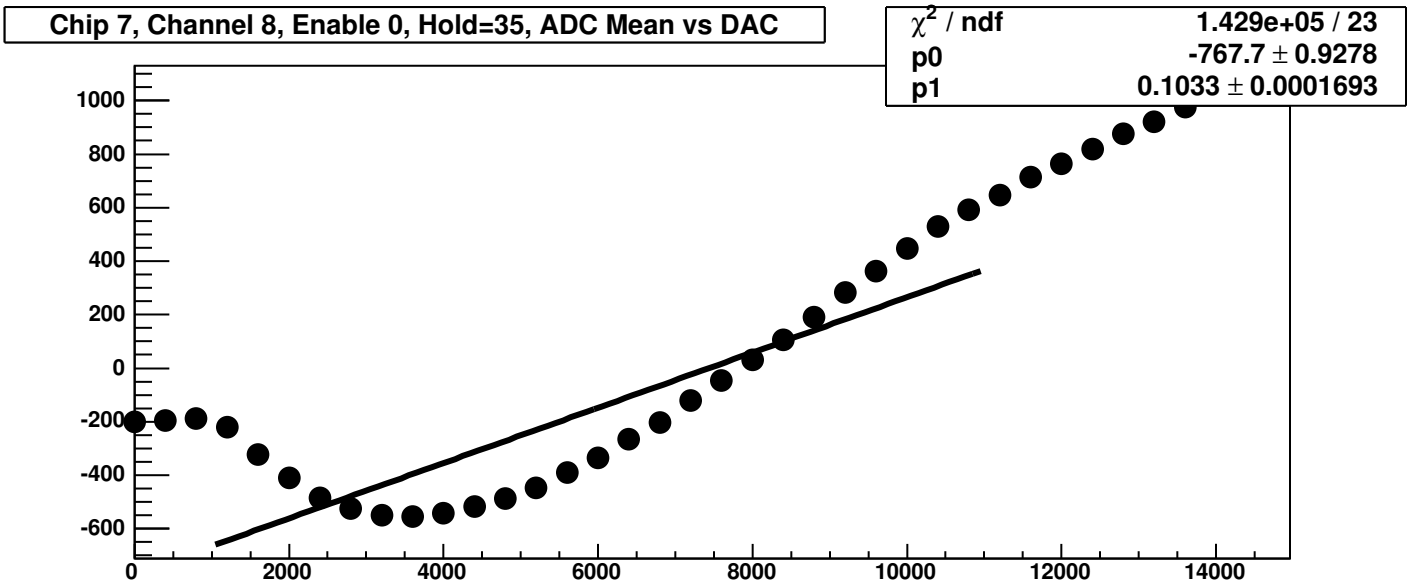

Chip 7, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

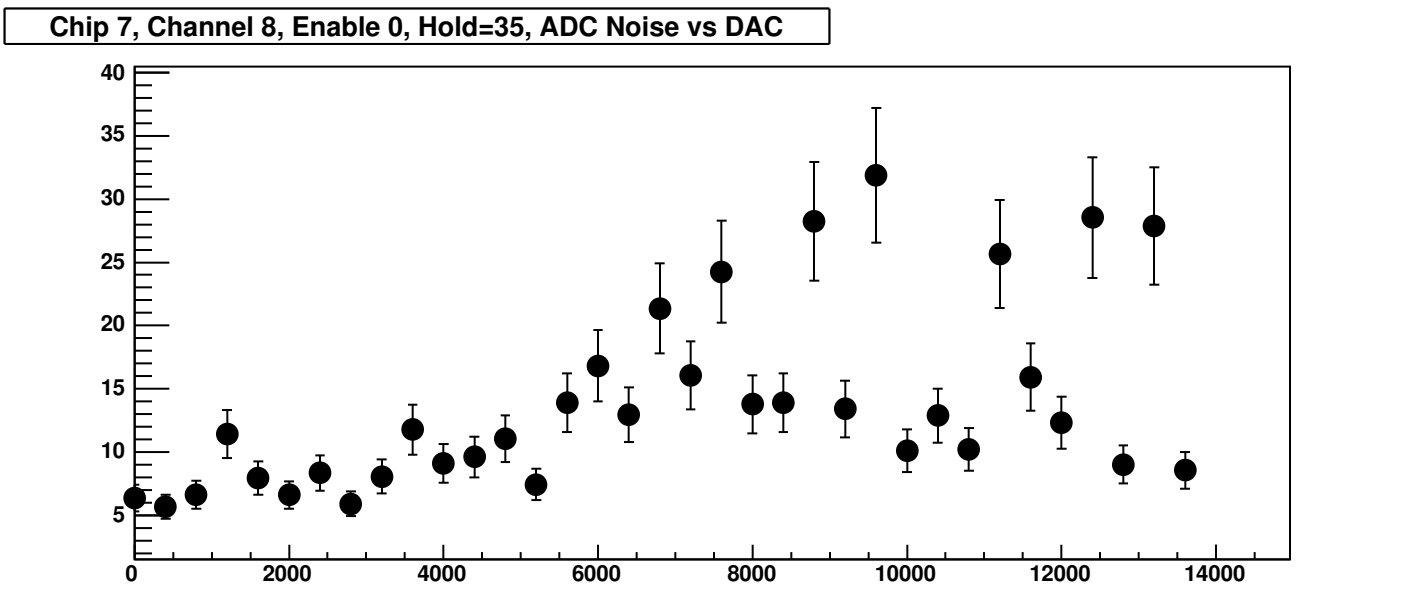

Chip 7, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

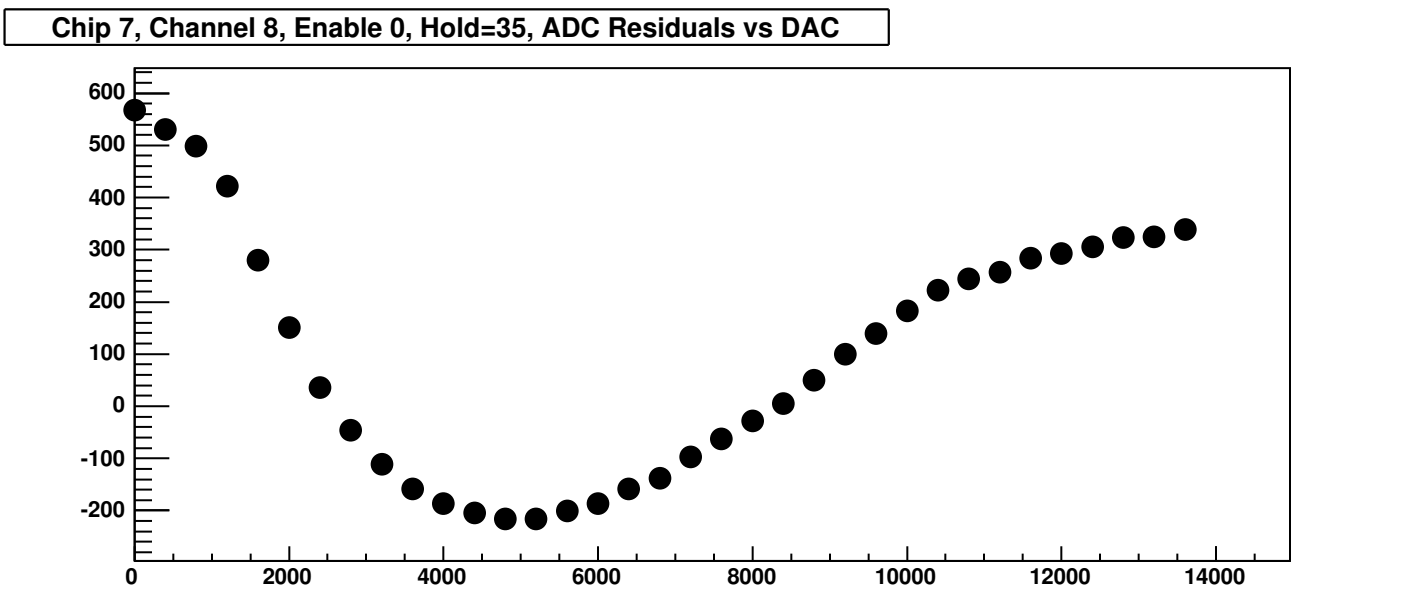

Chip 7, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2010 / 23
p0 -513.6 ± 1.099
p1 0.1689 ± 0.0002025
1800
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

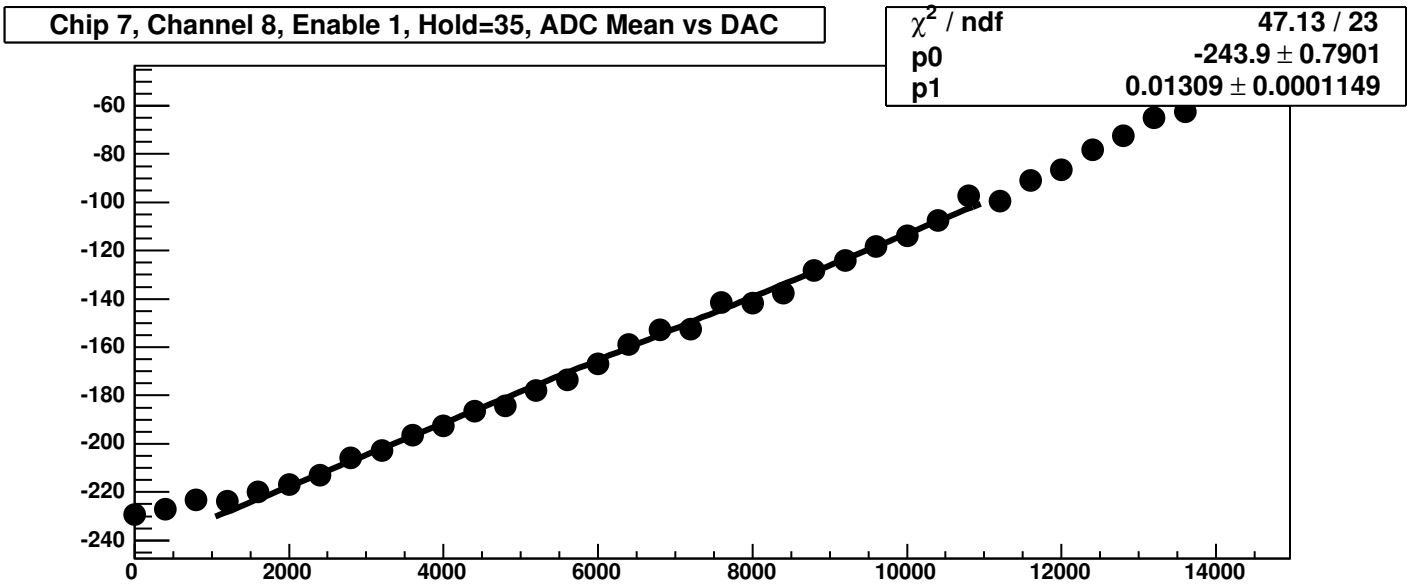

Chip 7, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

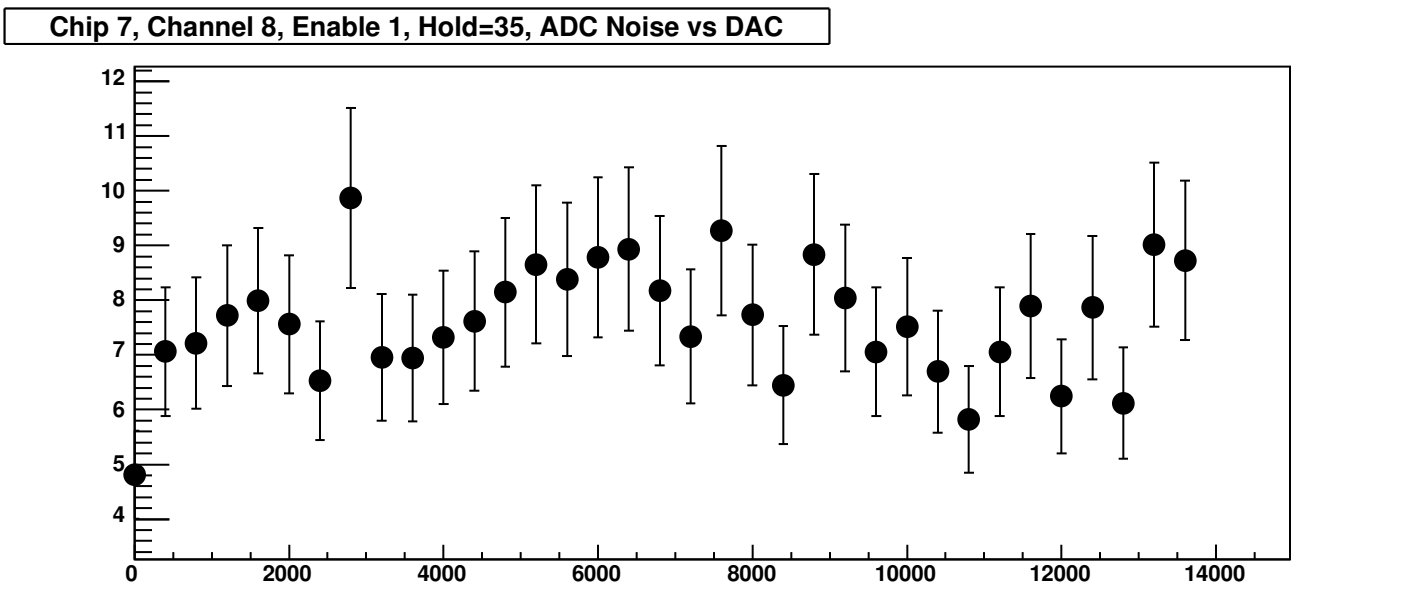

Chip 7, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

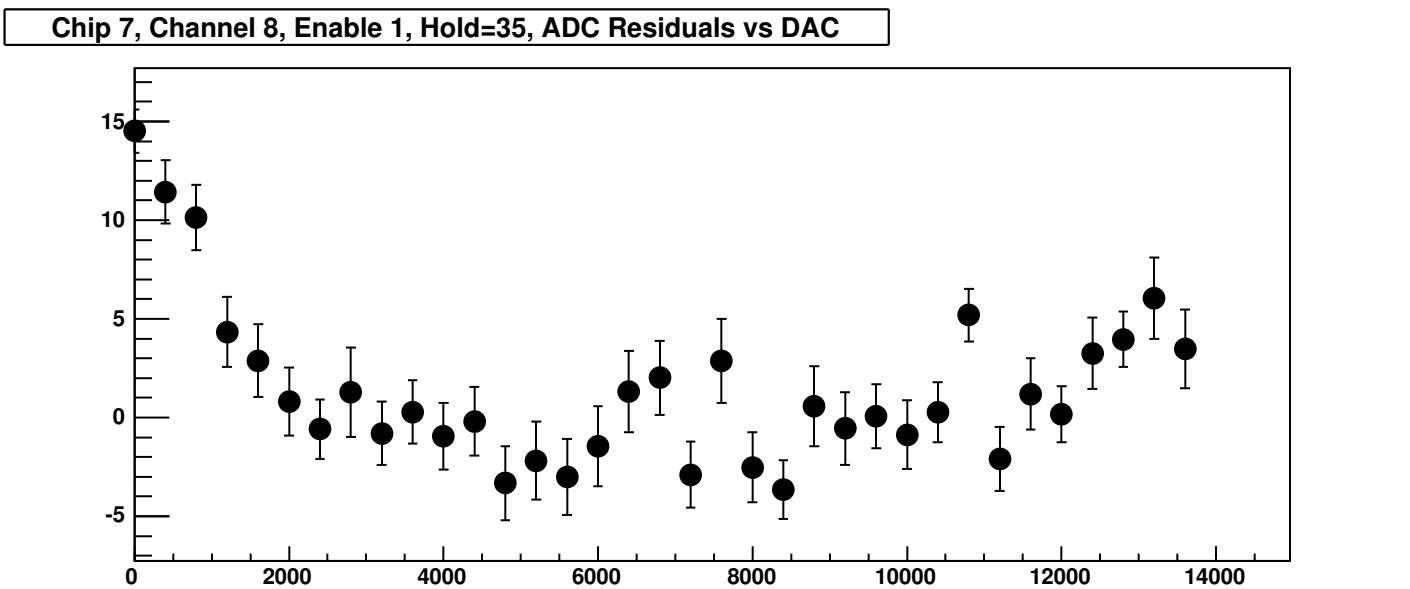

Chip 7, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.429e+05 / 23
p0 -767.7 ± 0.9278
p1 0.1033 ± 0.0001693
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

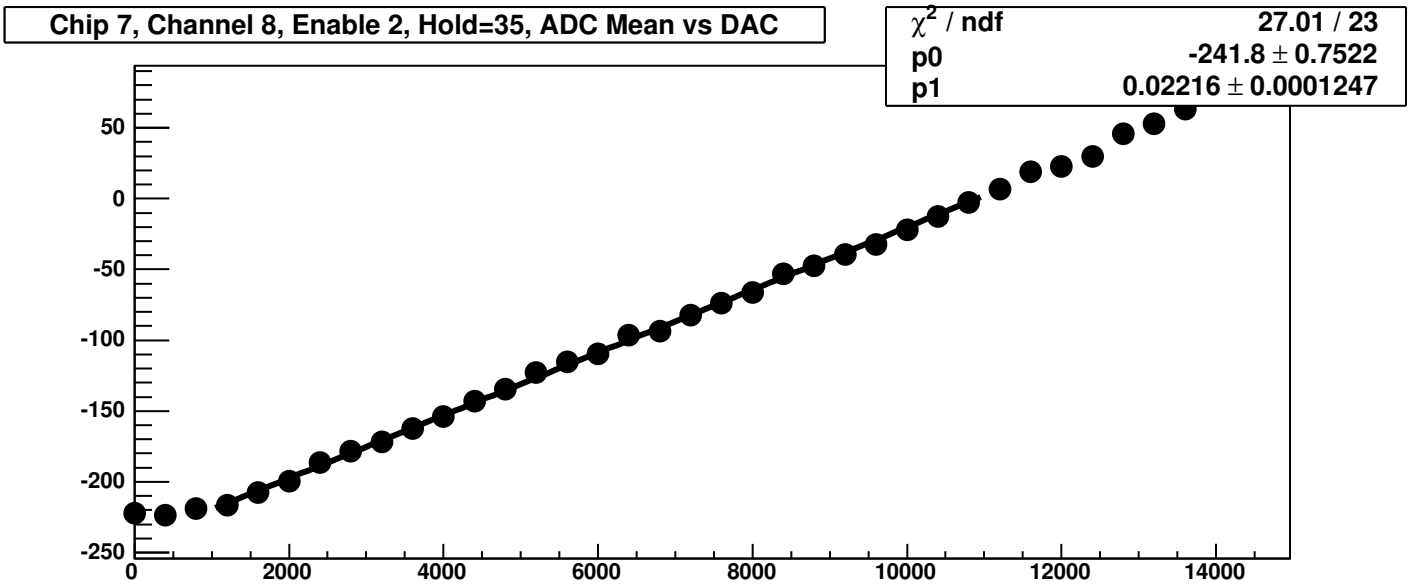

Chip 7, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

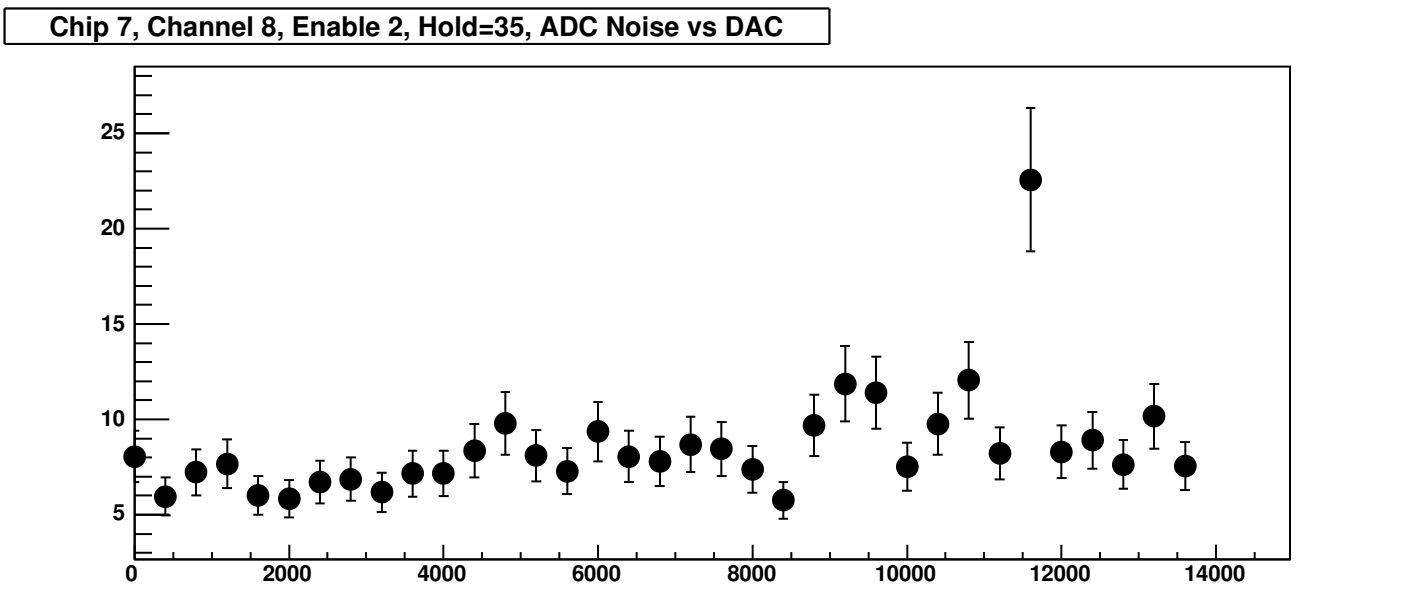

Chip 7, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

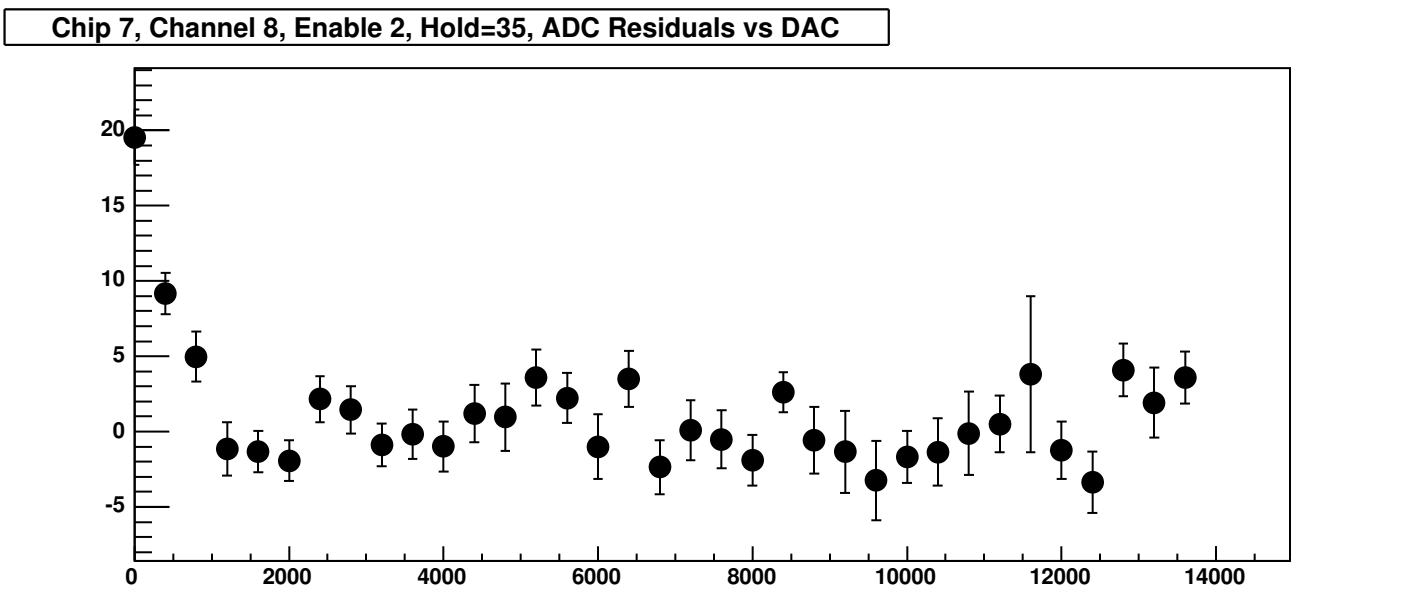

Chip 7, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 47.13 / 23
p0 -243.9 ± 0.7901
p1 0.01309 ± 0.0001149
-60
-80
-100
-120
-140
-160
-180
-200
-220
-240
0
2000
4000
6000
8000
10000
12000
14000

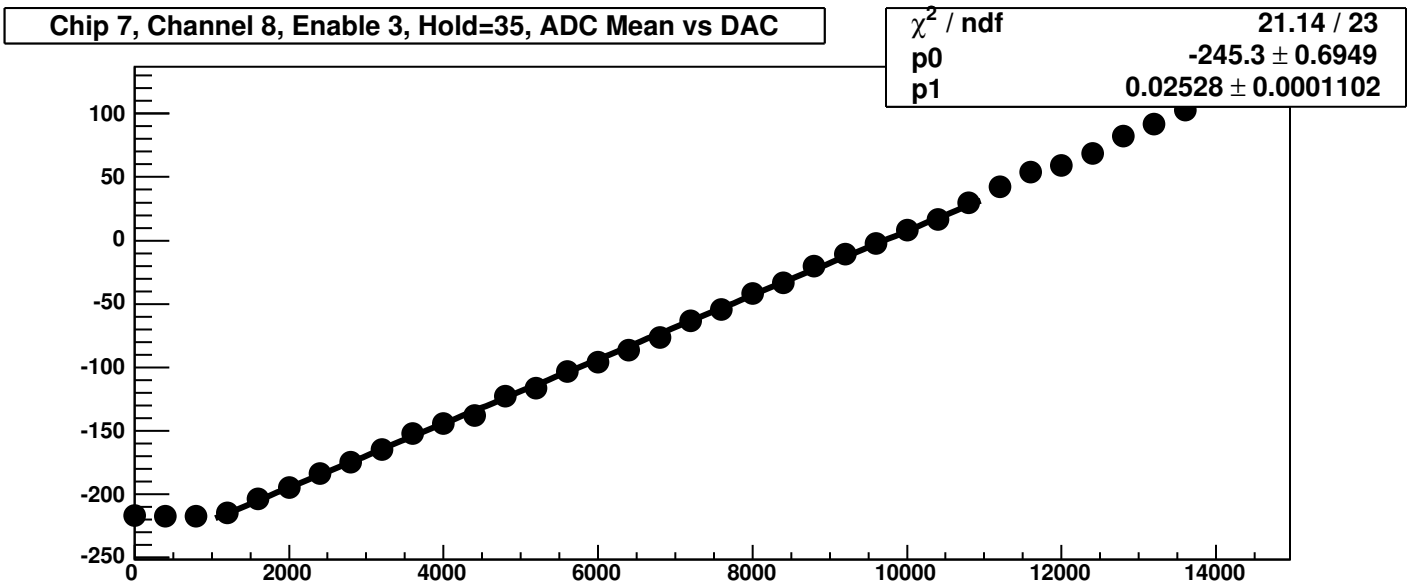

Chip 7, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

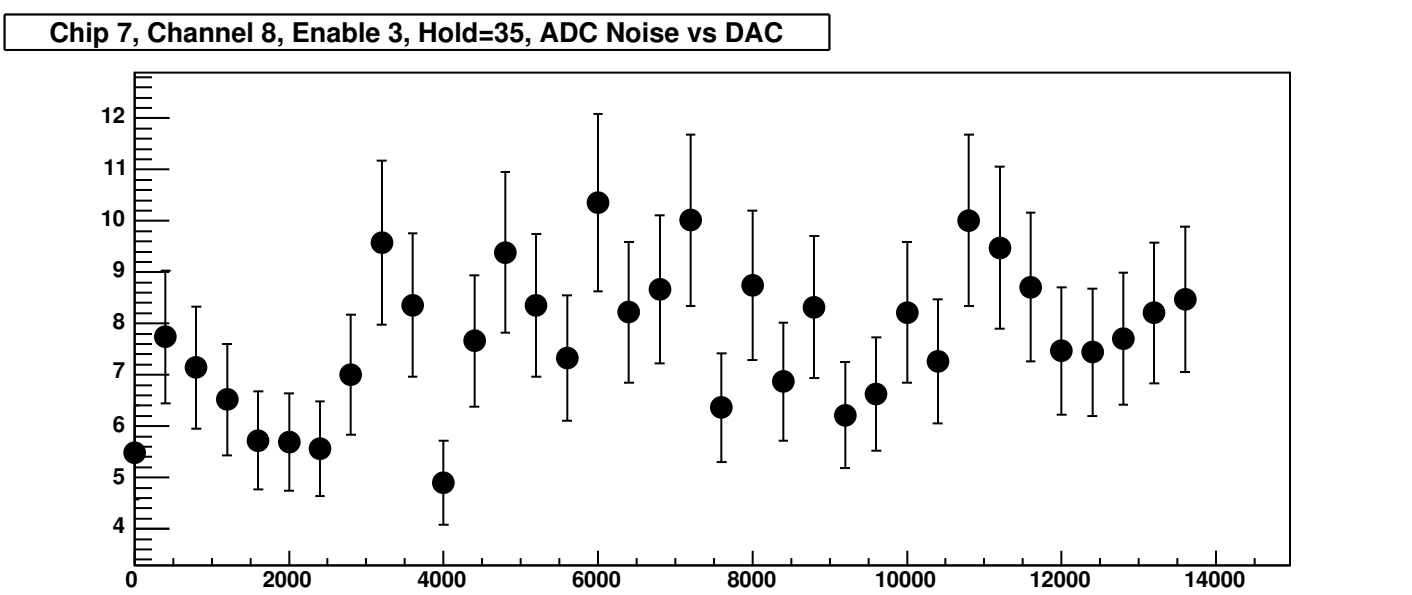

Chip 7, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

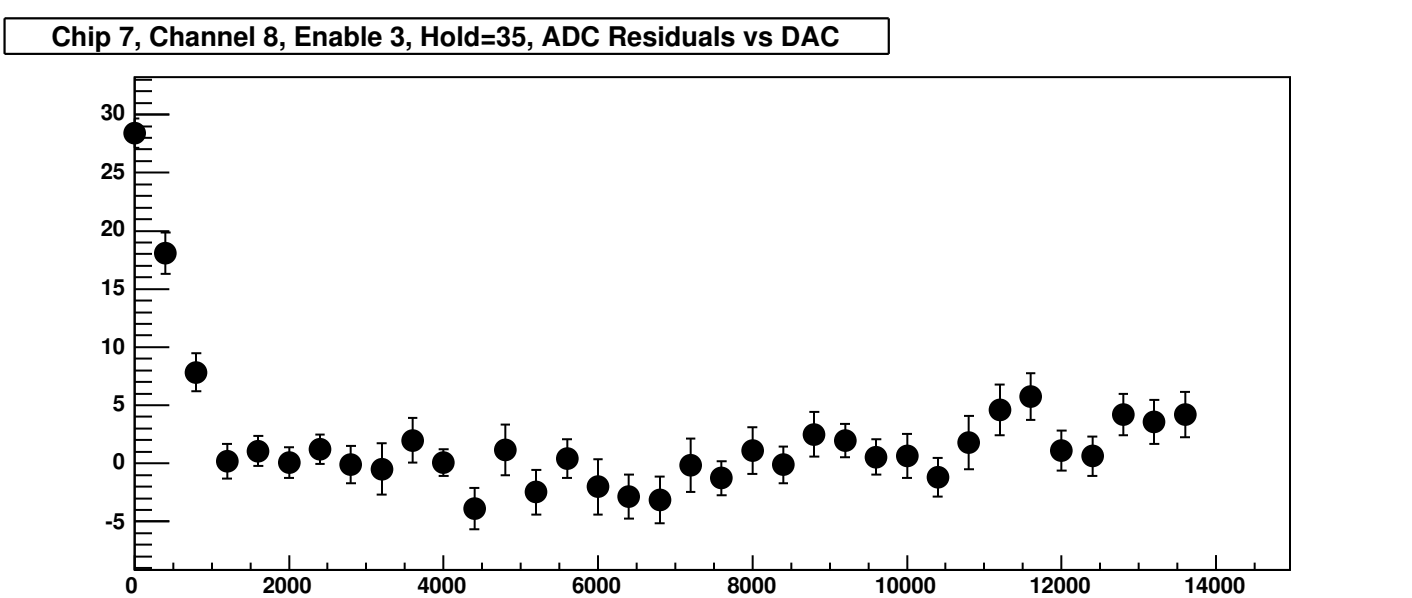

Chip 7, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 27.01 / 23
p0 -241.8 ± 0.7522
p1 0.02216 ± 0.0001247
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

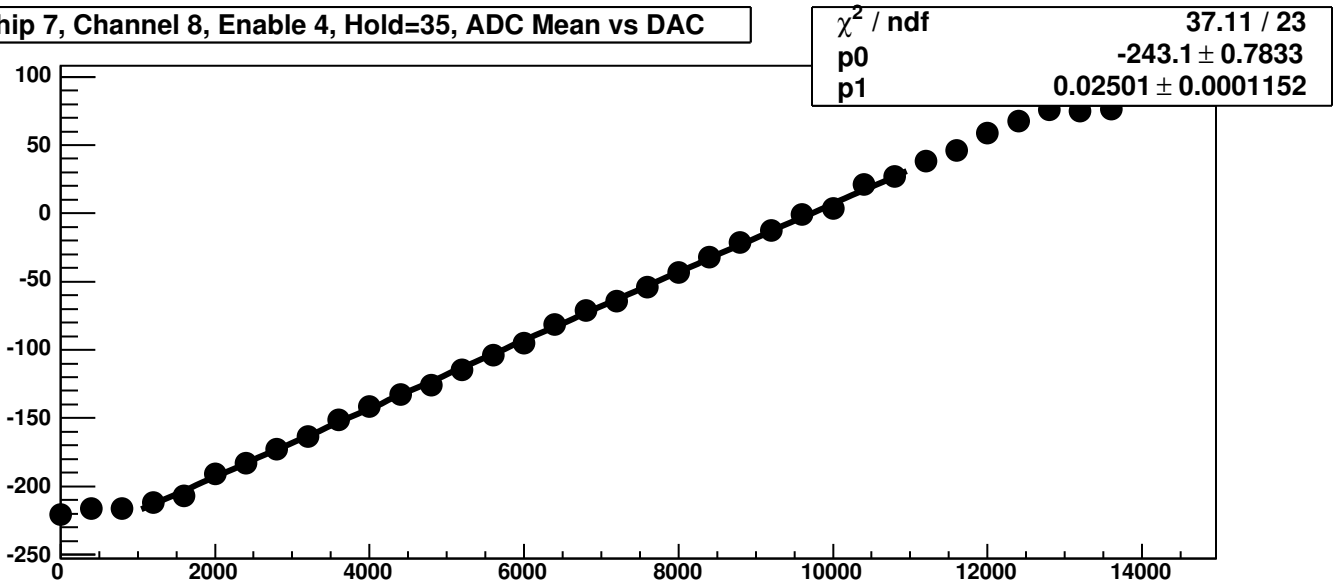

Chip 7, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.14 / 23
p0 -245.3 ± 0.6949
p1 0.02528 ± 0.0001102
100
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

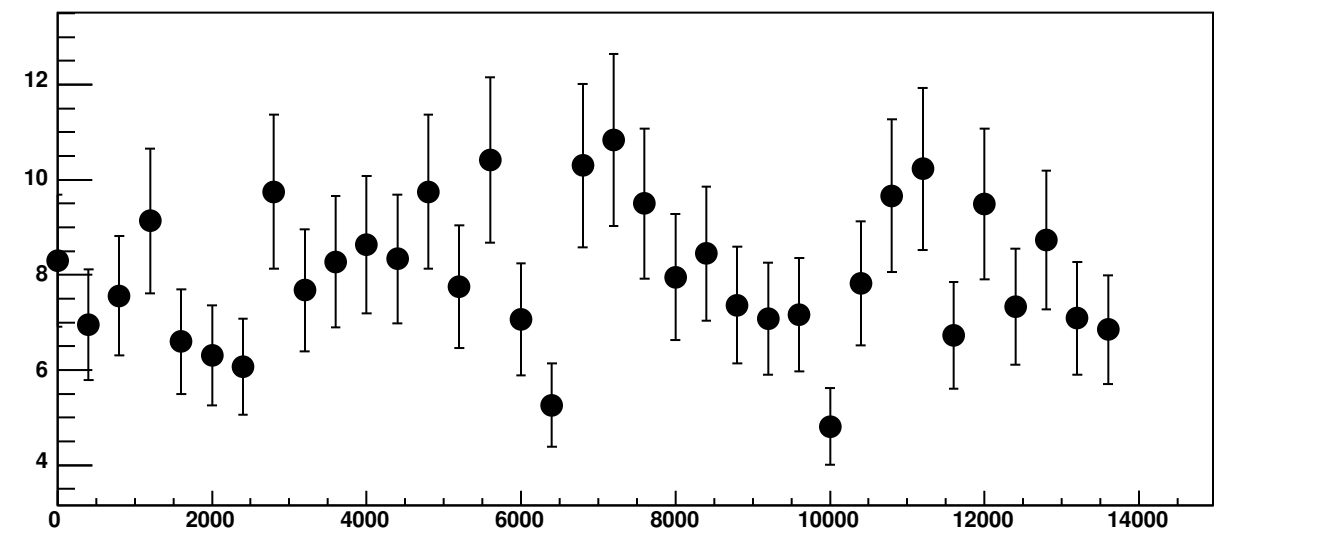

Chip 7, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

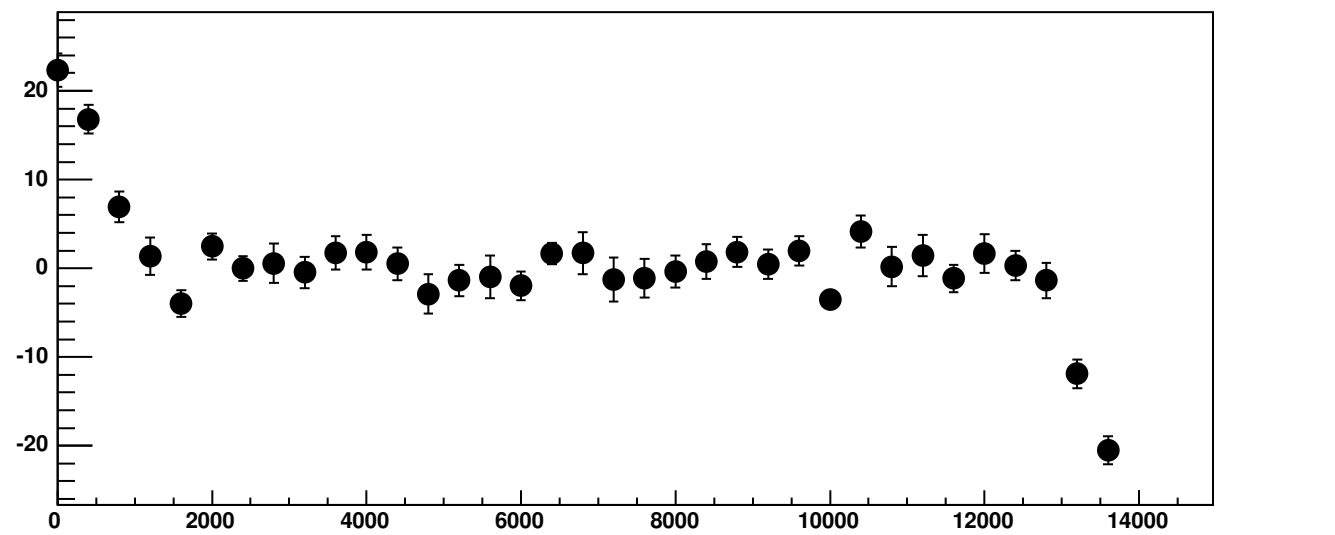
Chip 7, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

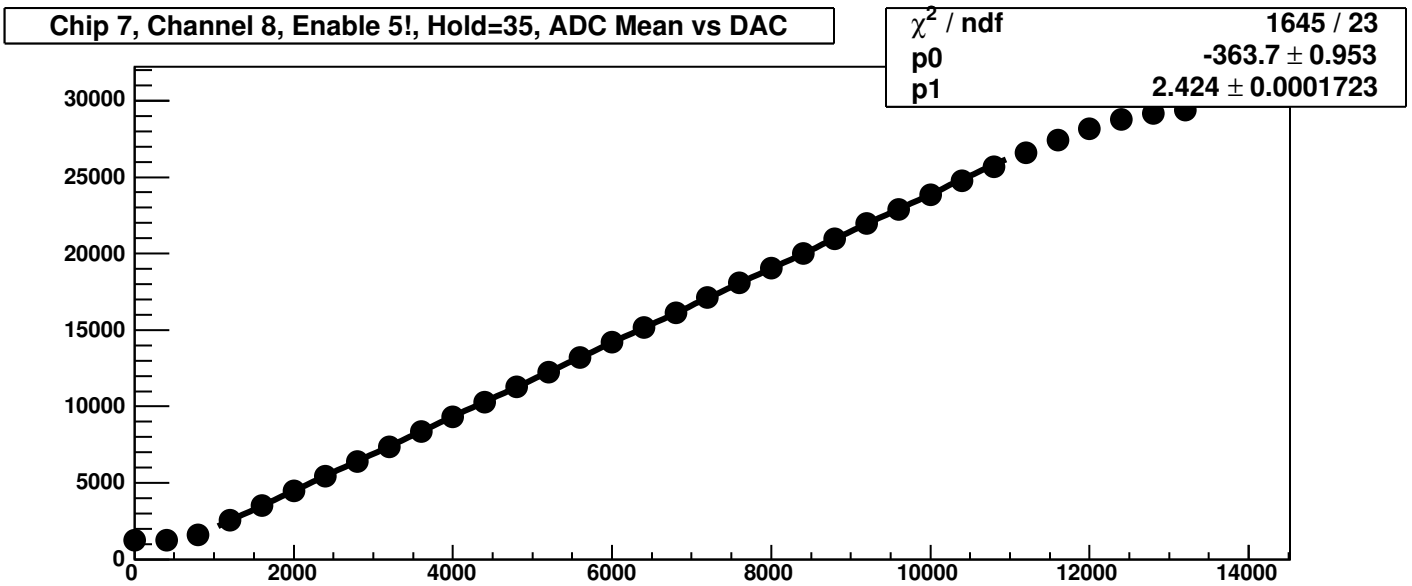


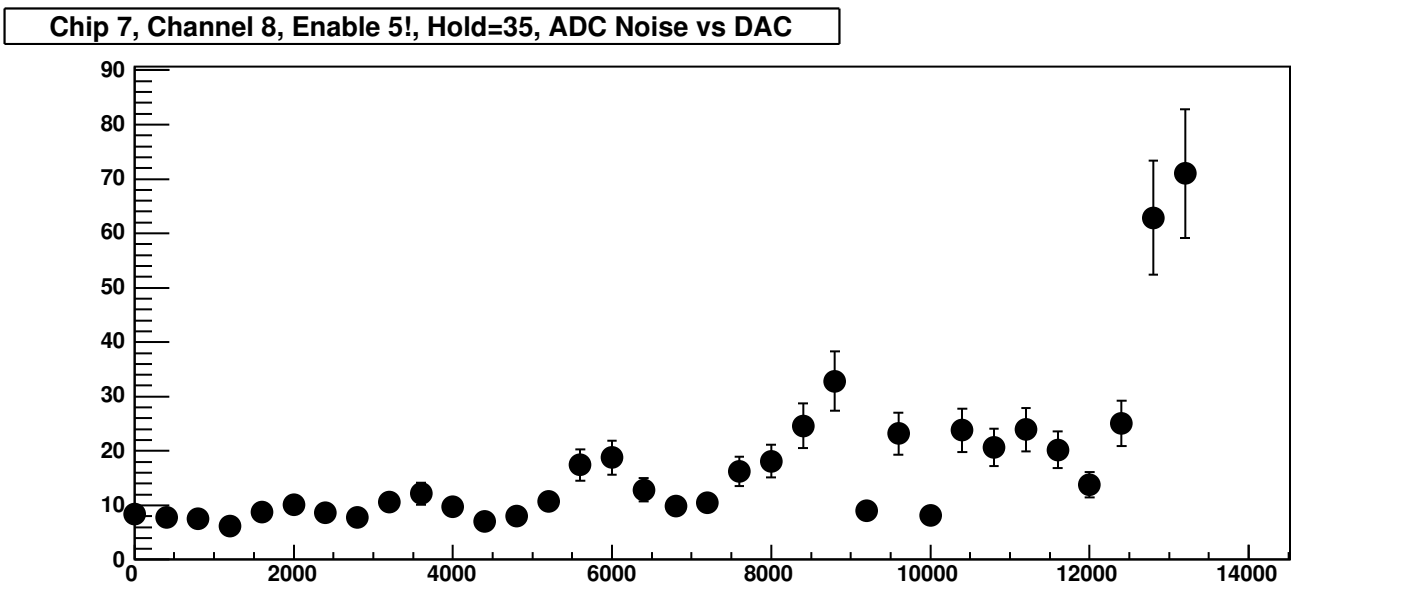
Chip 7, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC

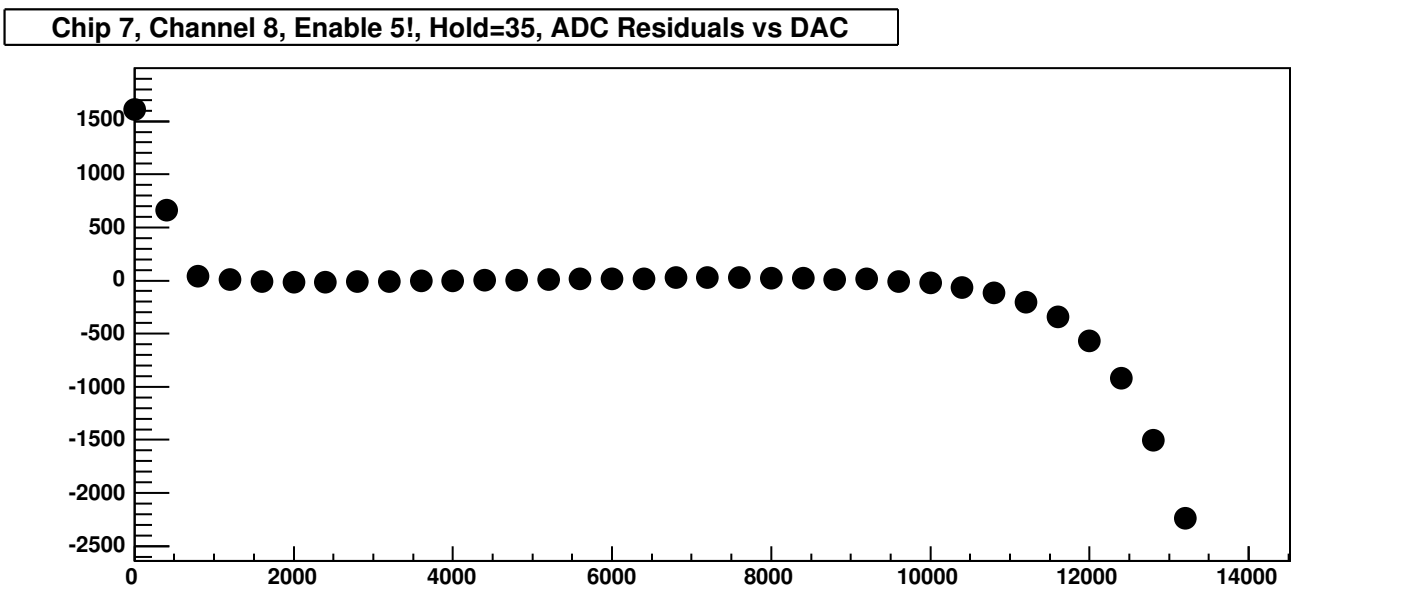


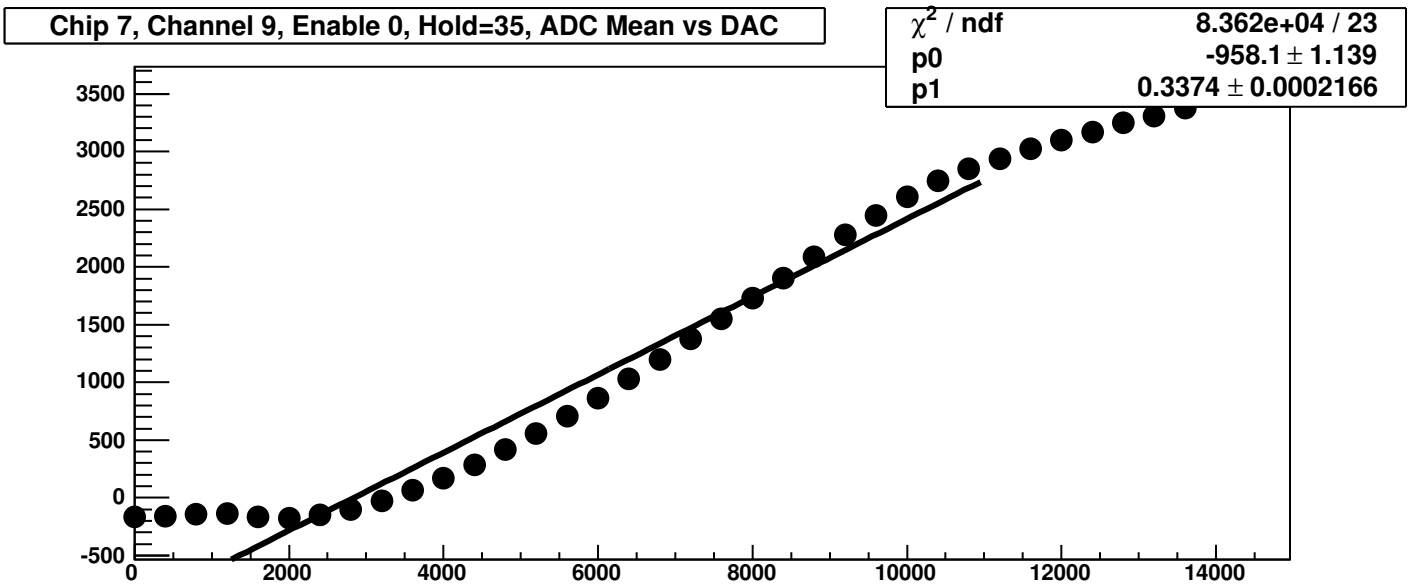
Chip 7, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

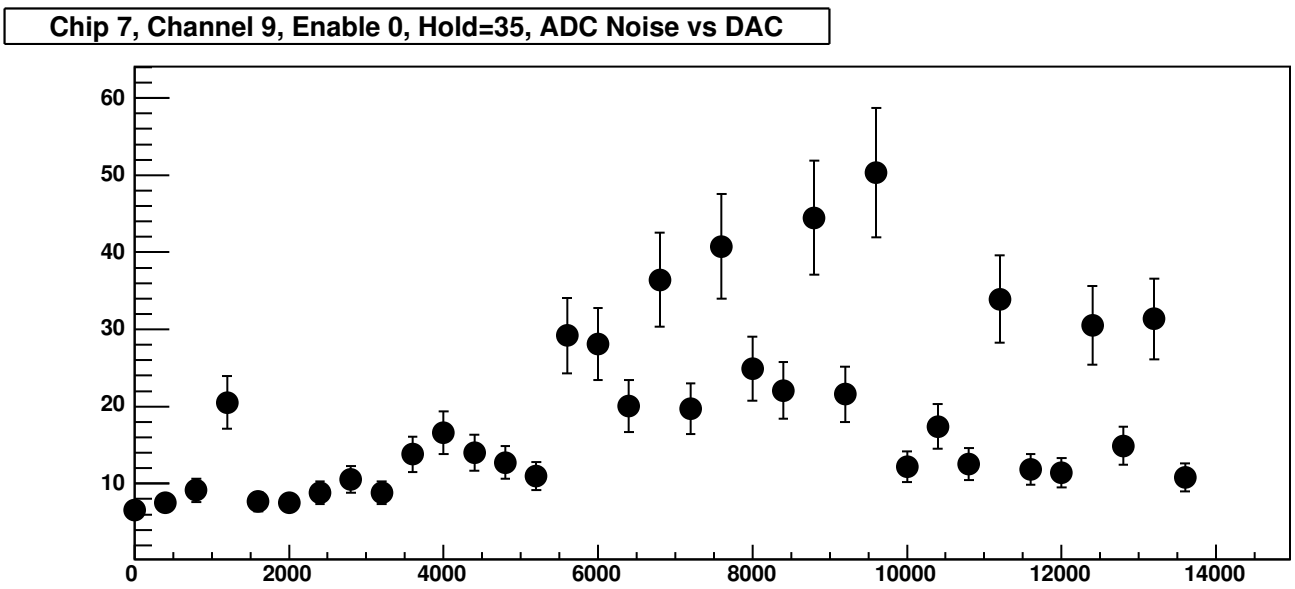


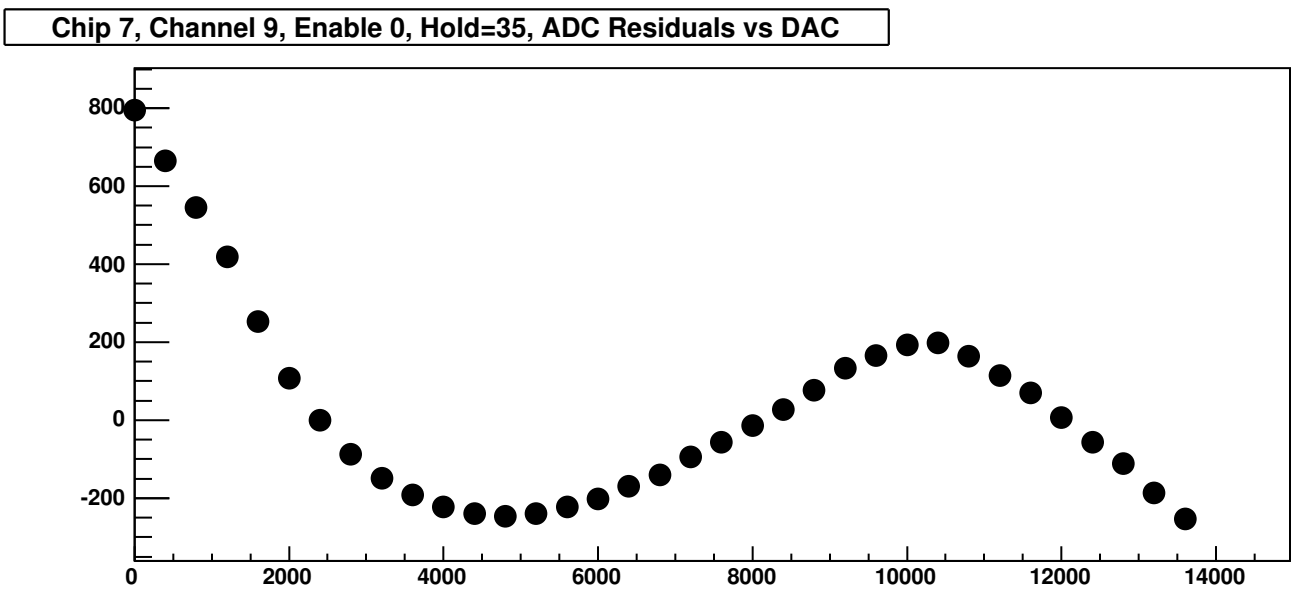

Chip 7, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1645 / 23
p0 -363.7 ± 0.953
p1 2.424 ± 0.0001723
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

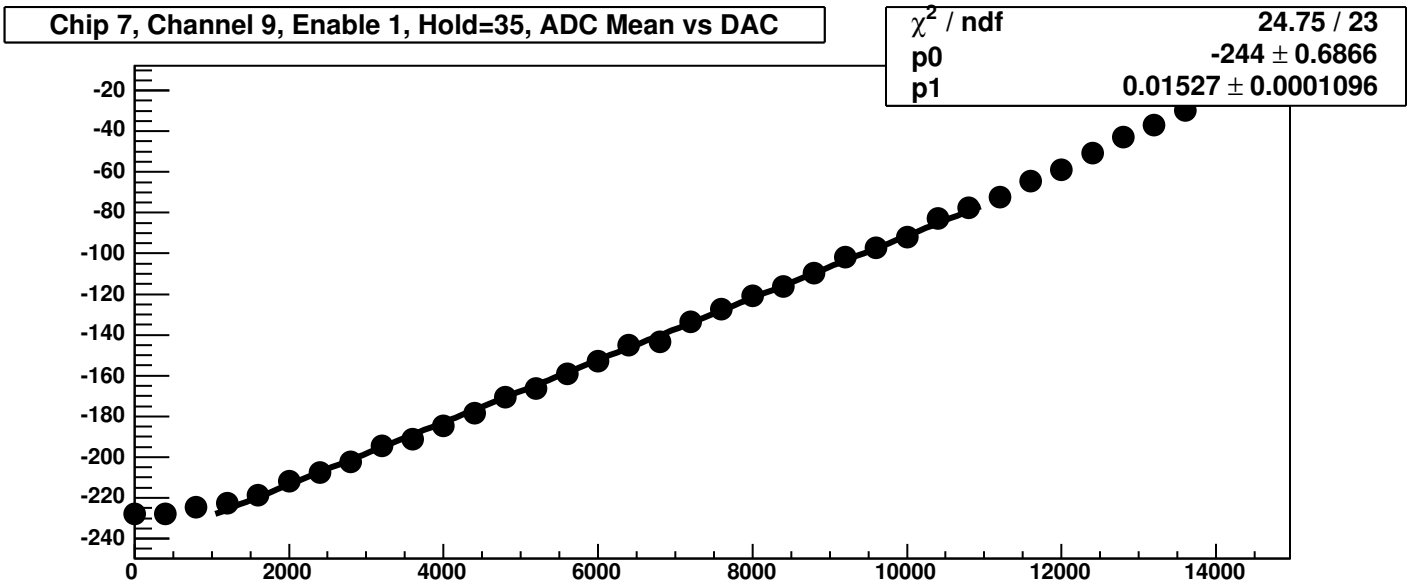

Chip 7, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

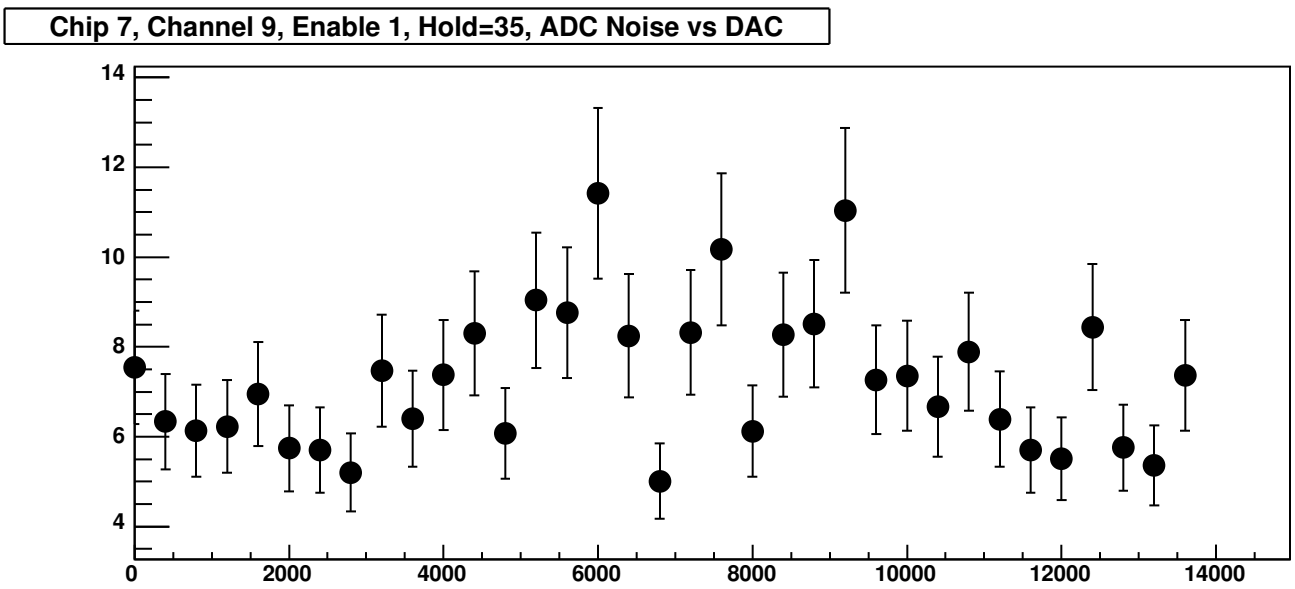

Chip 7, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

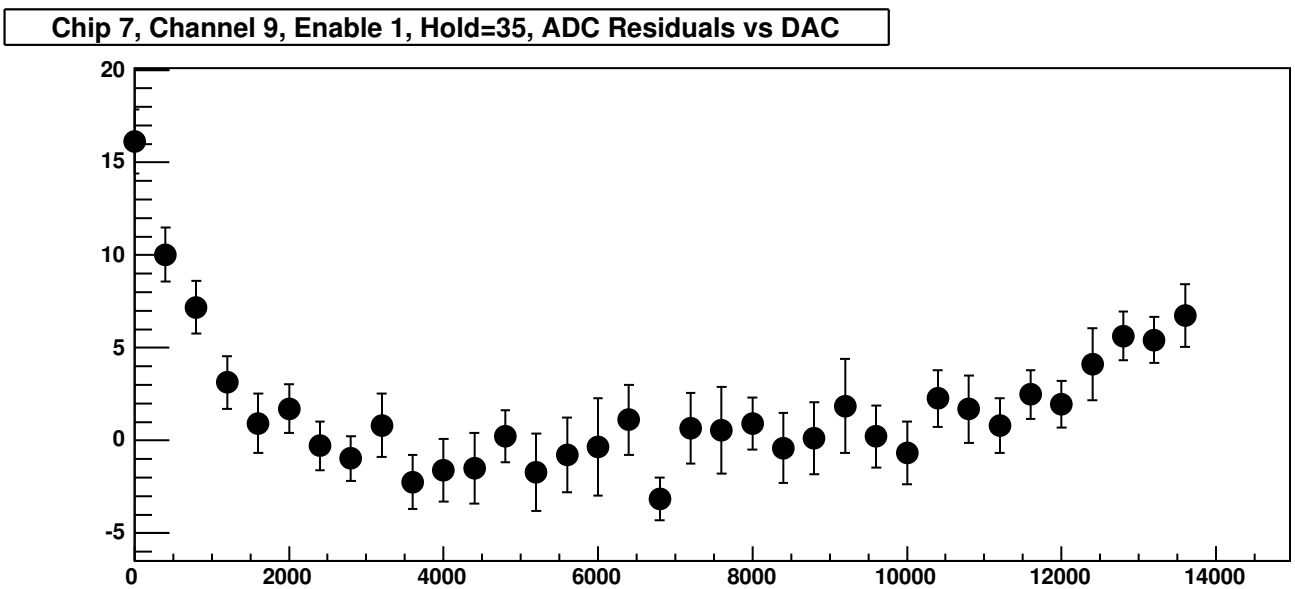

Chip 7, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 8.362e+04 / 23
p0 -958.1 ± 1.139
p1 0.3374 ± 0.0002166
3500
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

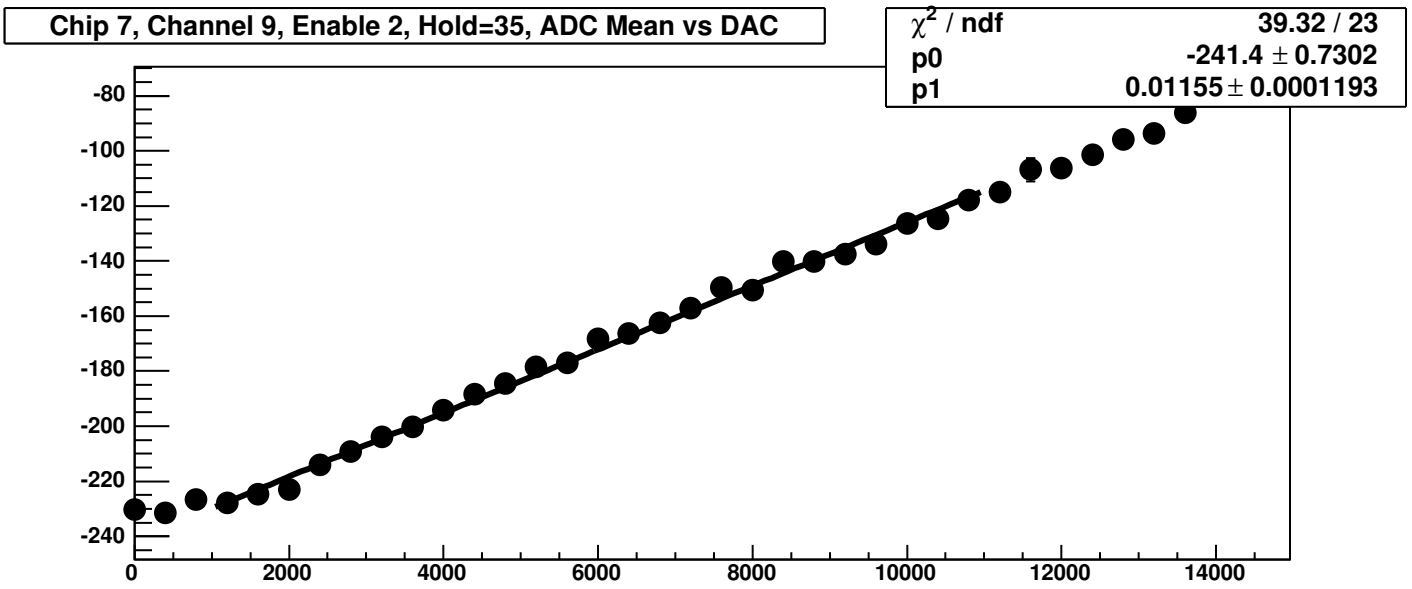

Chip 7, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

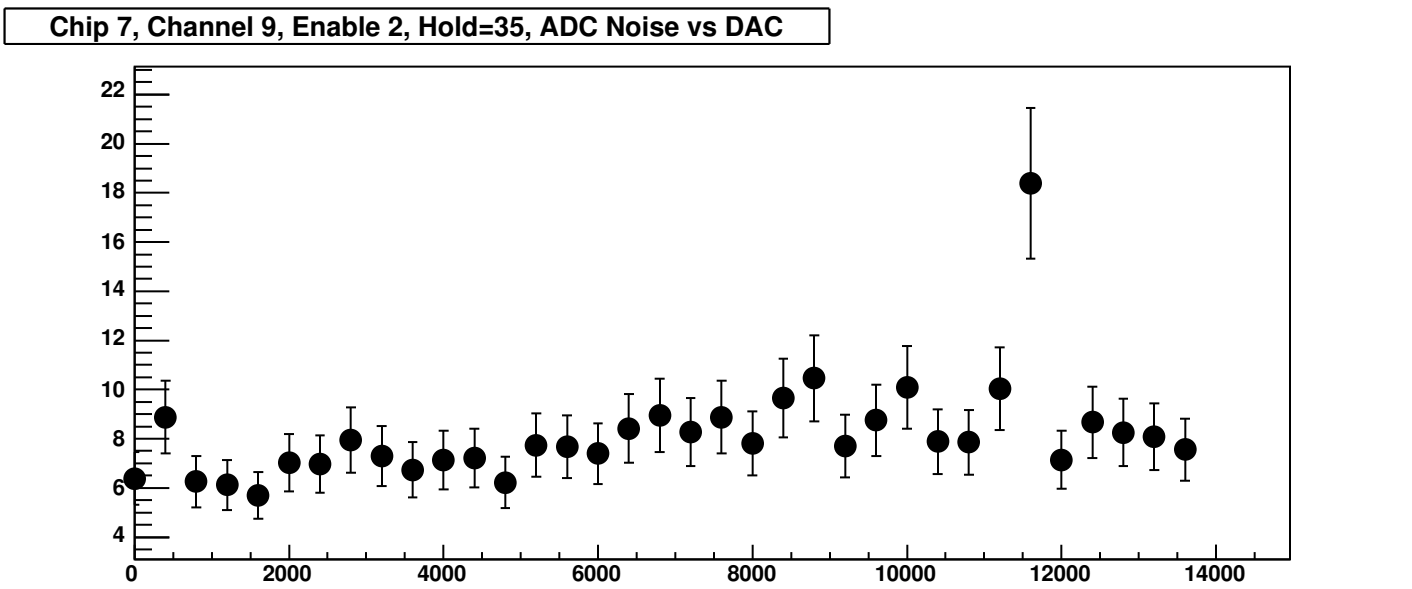

Chip 7, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

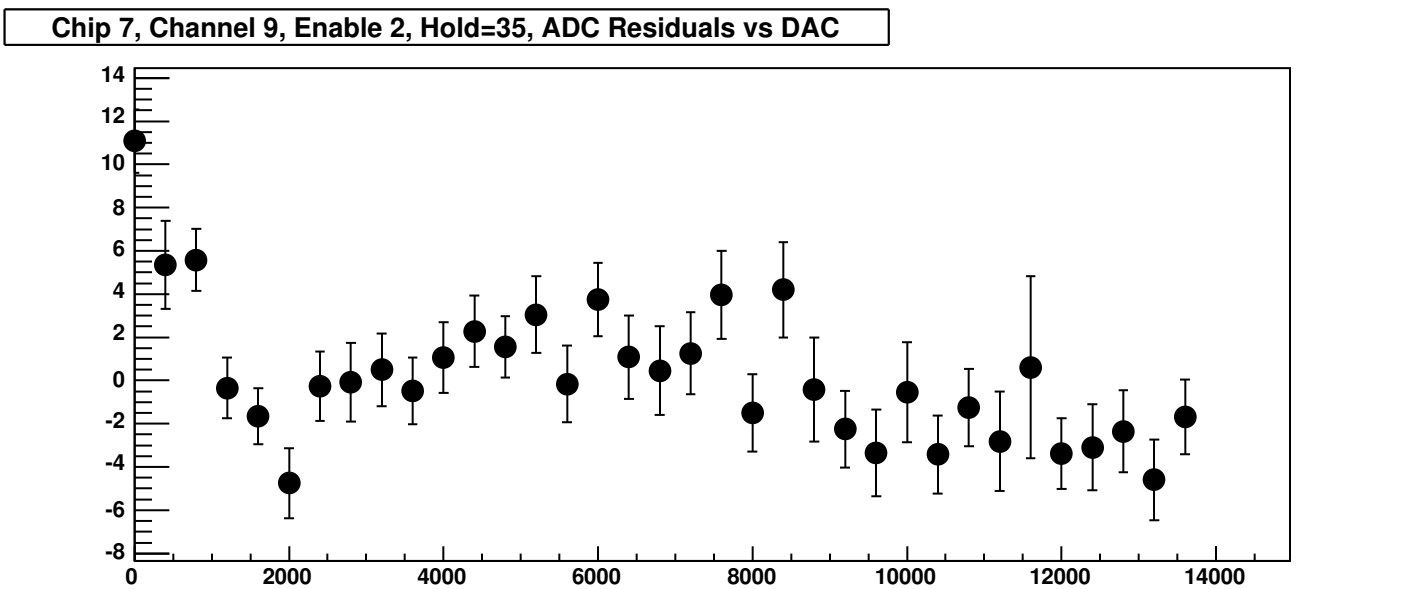

Chip 7, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 24.75 / 23
p0 -244 ± 0.6866
p1 0.01527 ± 0.0001096
-20
-40
-60
-80
-100
-120
-140
-160
-180
-200
-220
-240
0
2000
4000
6000
8000
10000
12000
14000

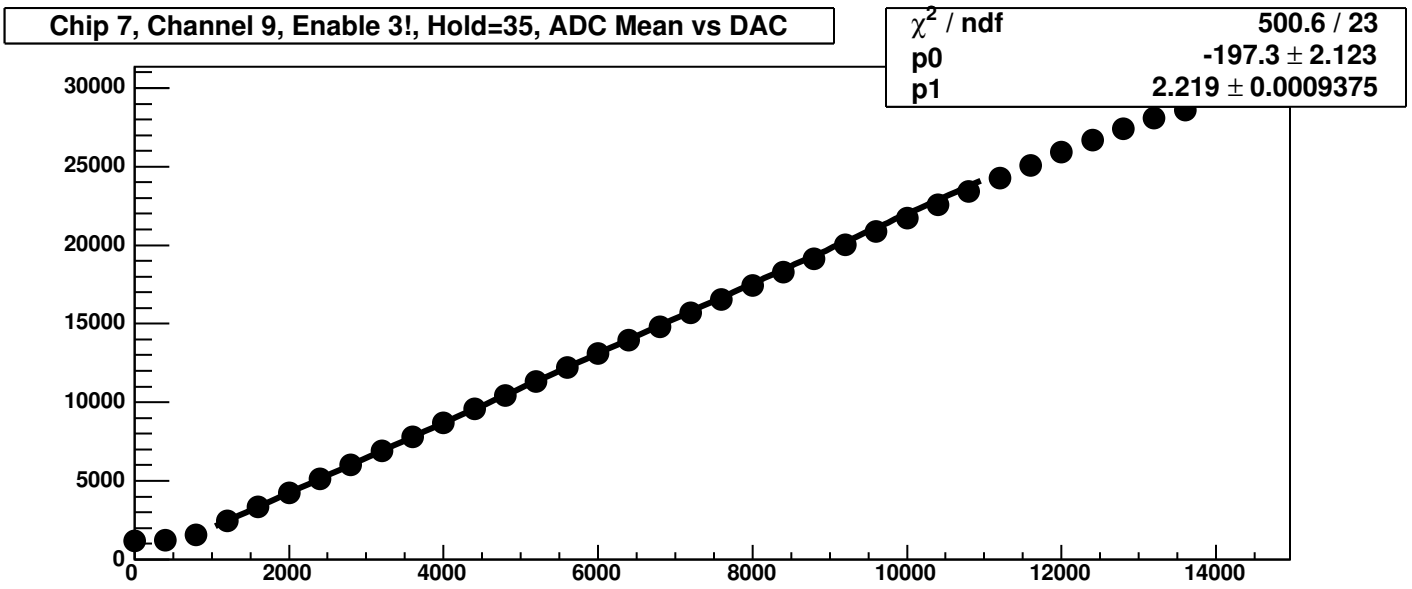

Chip 7, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

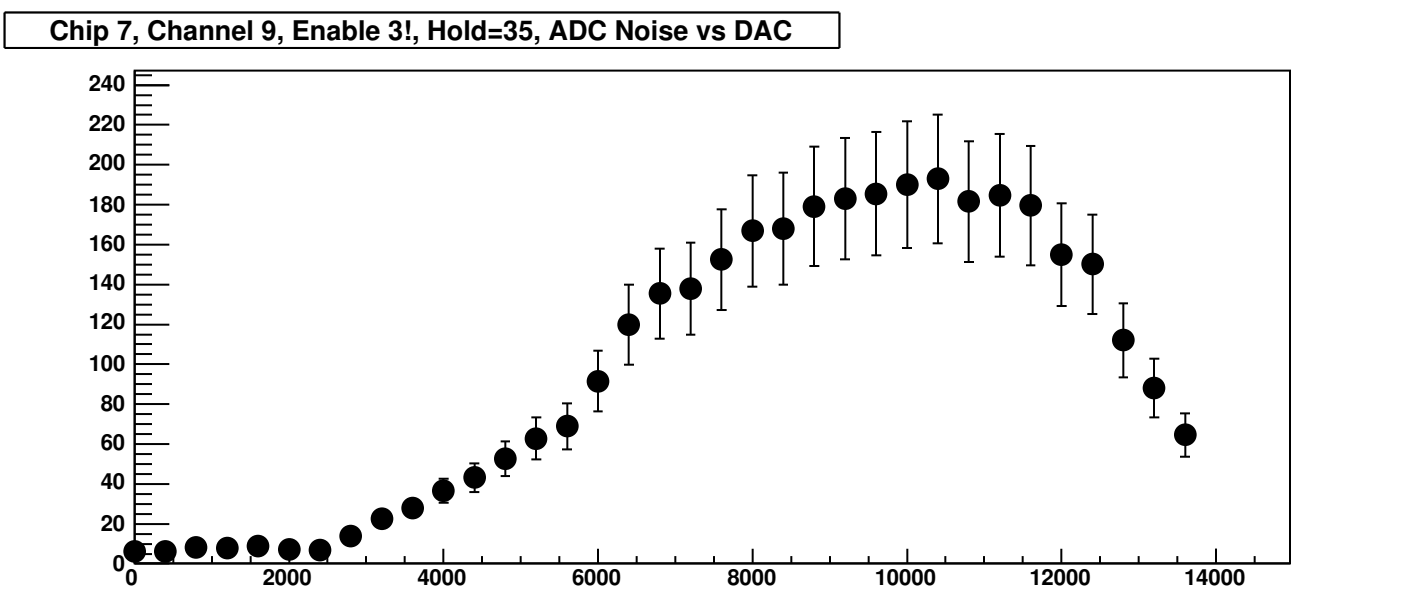

Chip 7, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

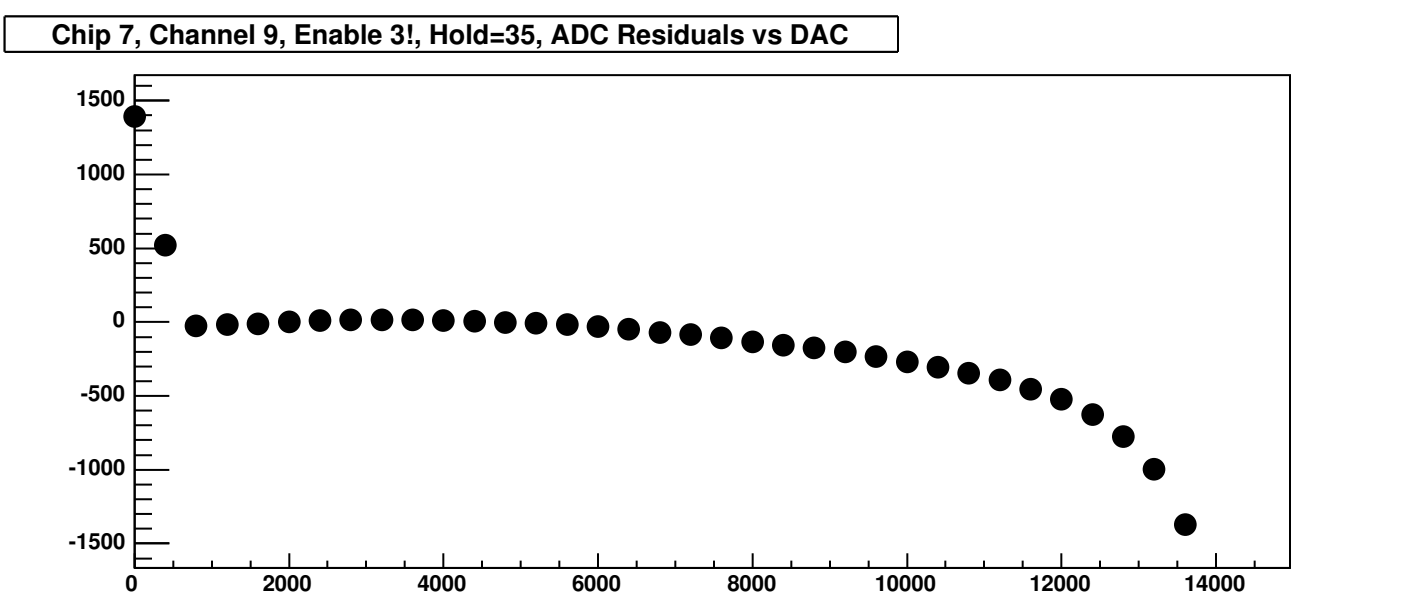

Chip 7, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 39.32 / 23
p0 -241.4 ± 0.7302
p1 0.01155 ± 0.0001193
-80
-100
-120
-140
-160
-180
-200
-220
-240
0
2000
4000
6000
8000
10000
12000
14000

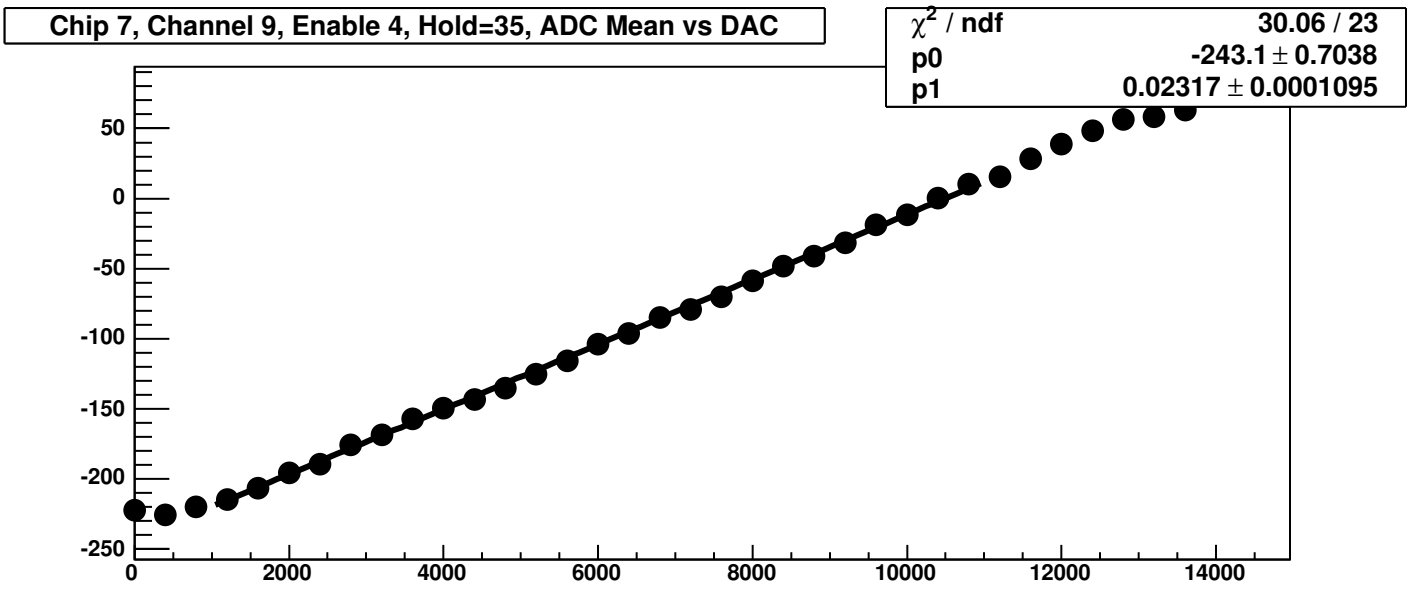

Chip 7, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

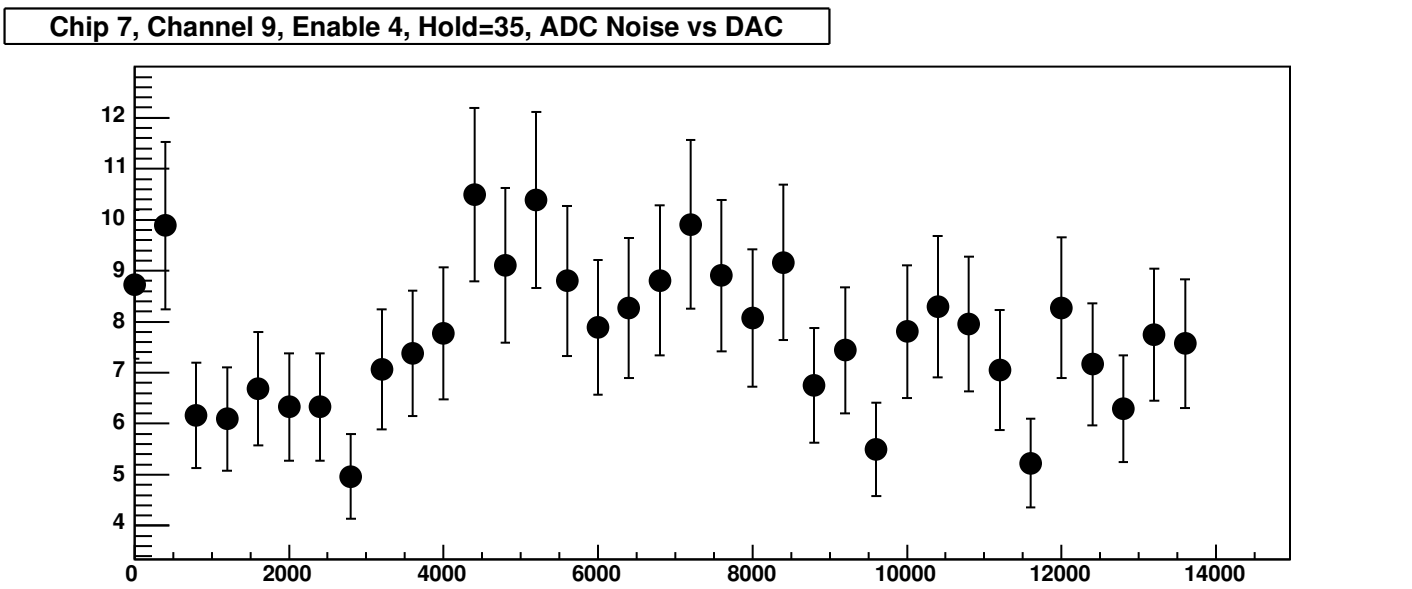

Chip 7, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

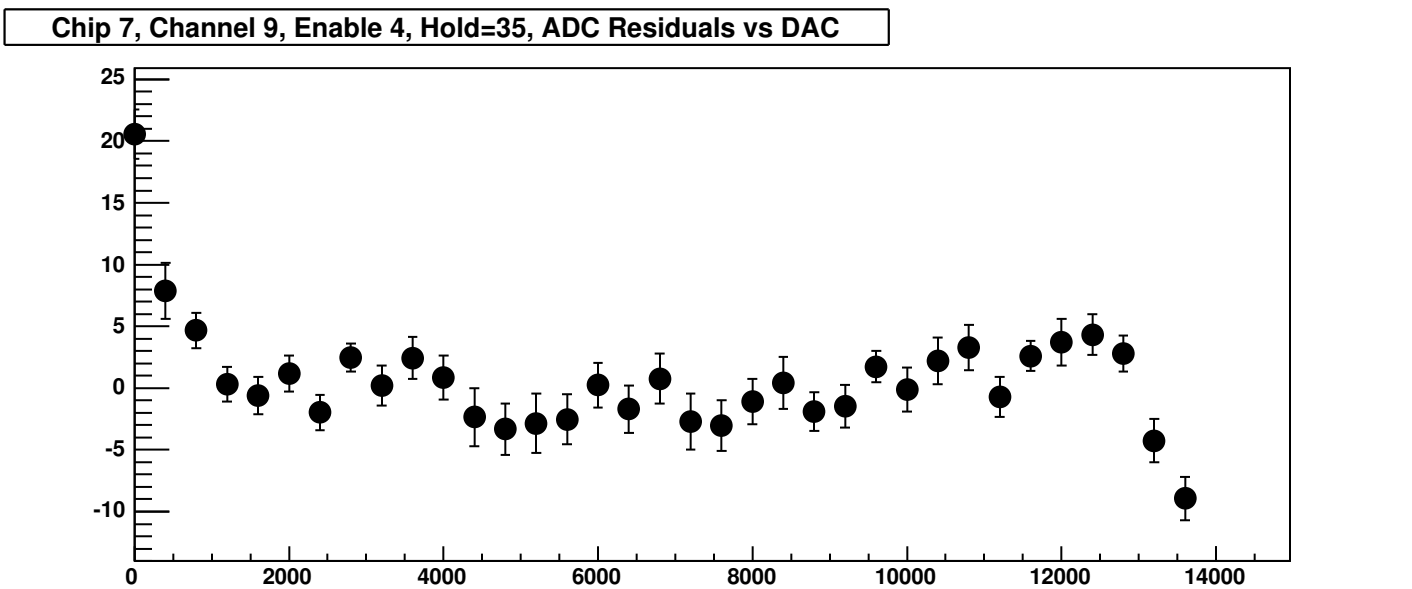

Chip 7, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 500.6 / 23
p0 -197.3 ± 2.123
p1 2.219 ± 0.0009375
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
240
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

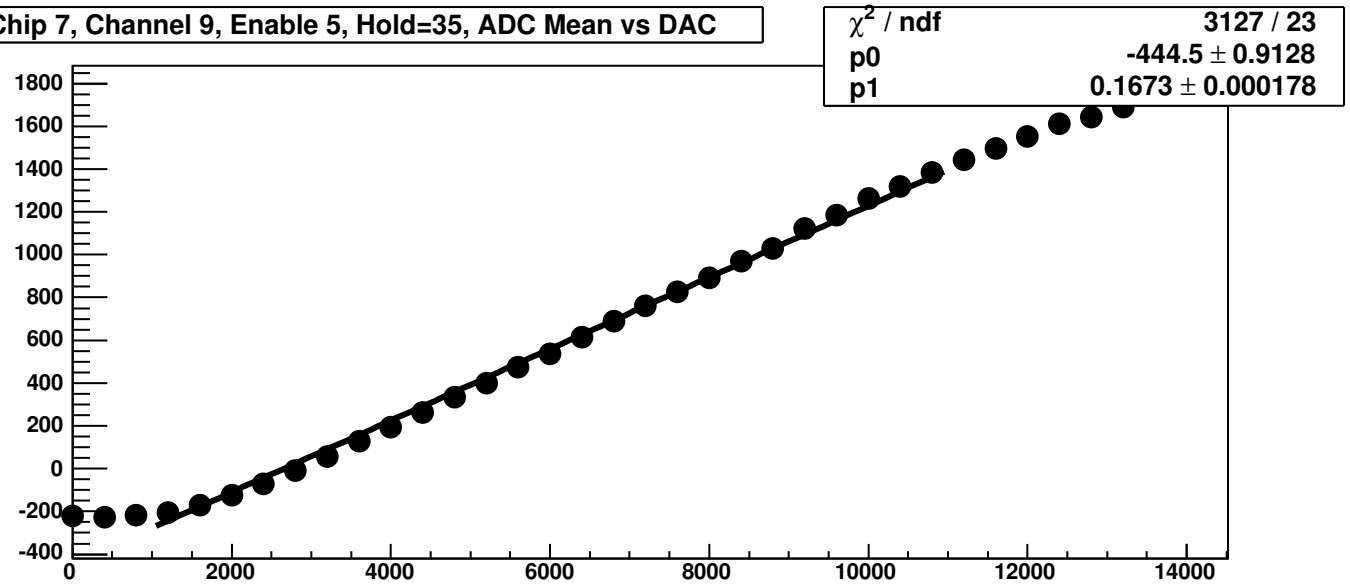

Chip 7, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.06 / 23
p0 -243.1 ± 0.7038
p1 0.02317 ± 0.0001095
50
0
-50
-100
-150
-200
-250
0
2000
4000
6000
8000
10000
12000
14000

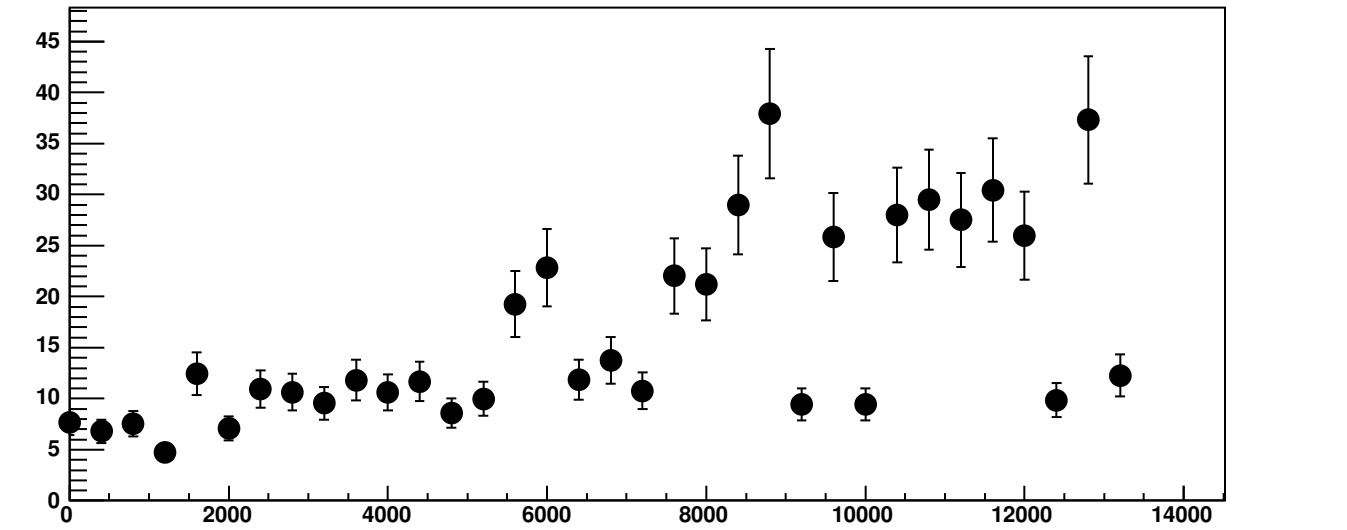

Chip 7, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

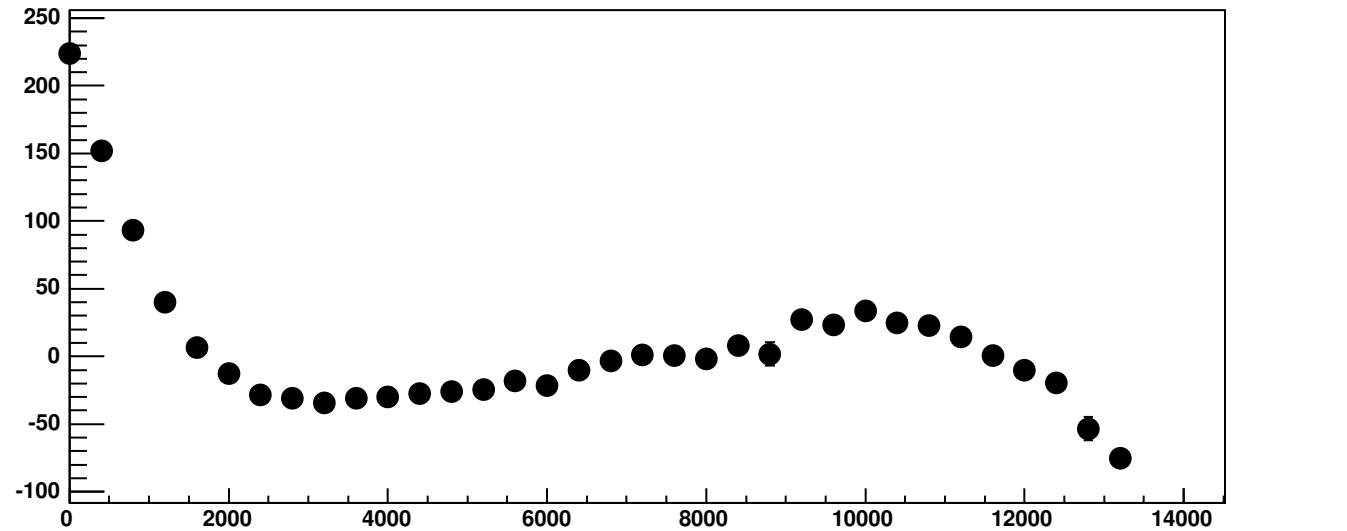
Chip 7, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

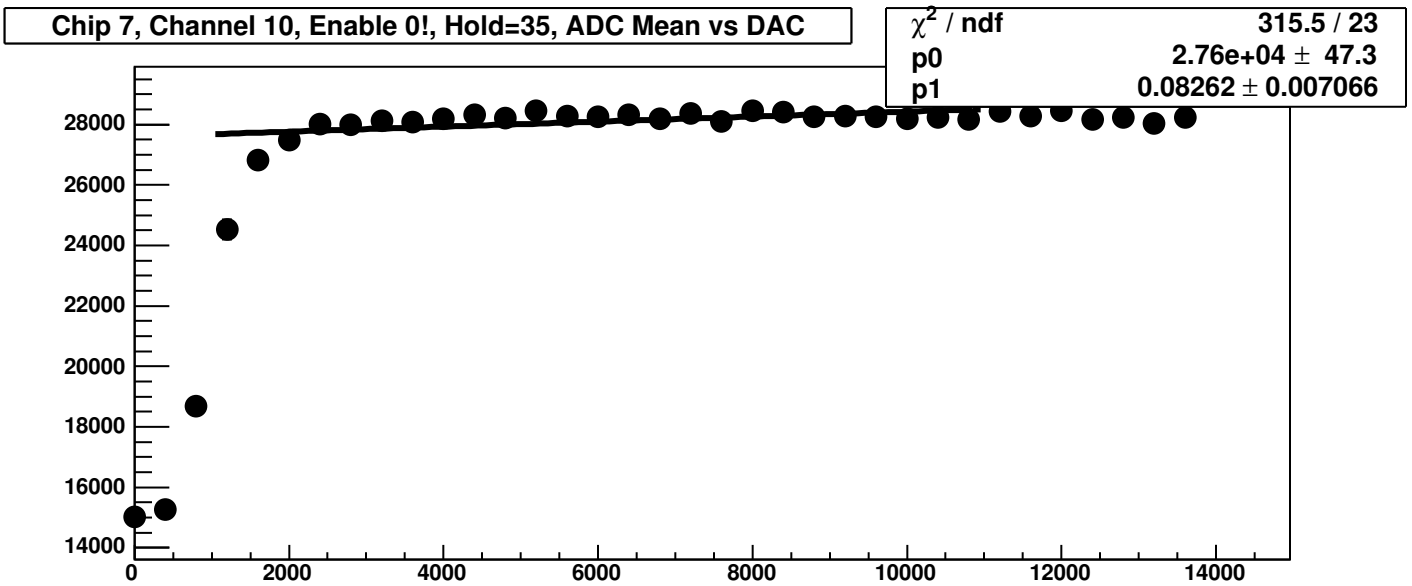


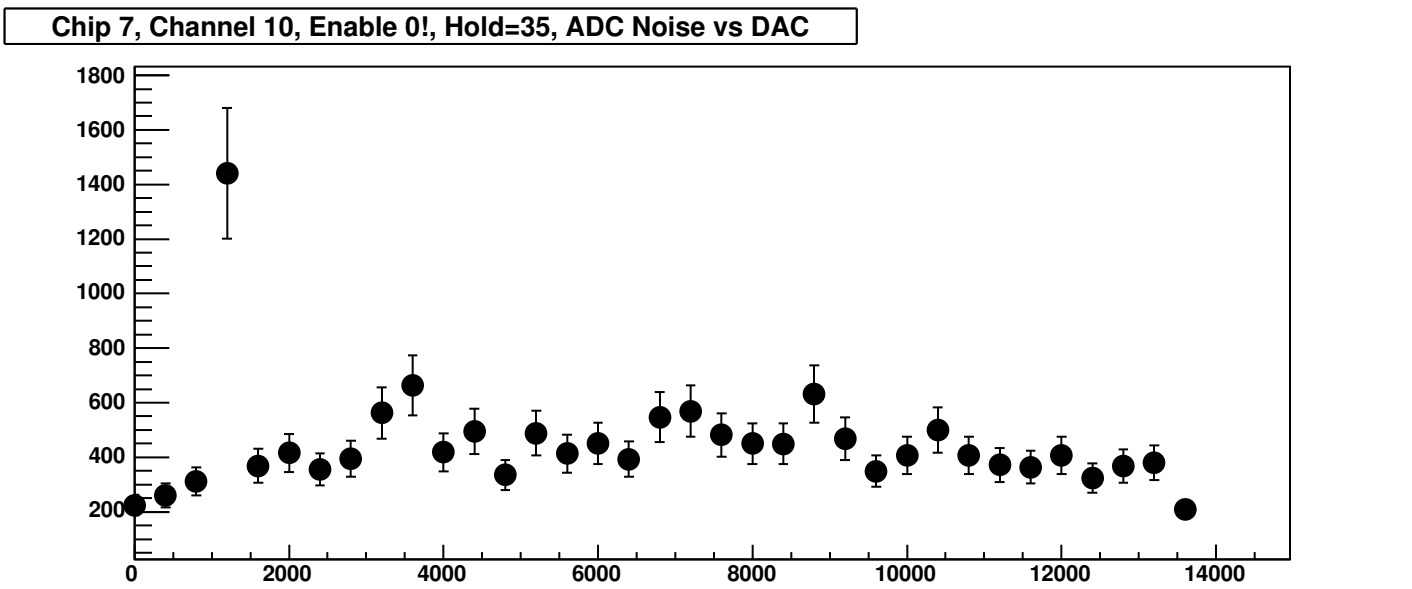
Chip 7, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

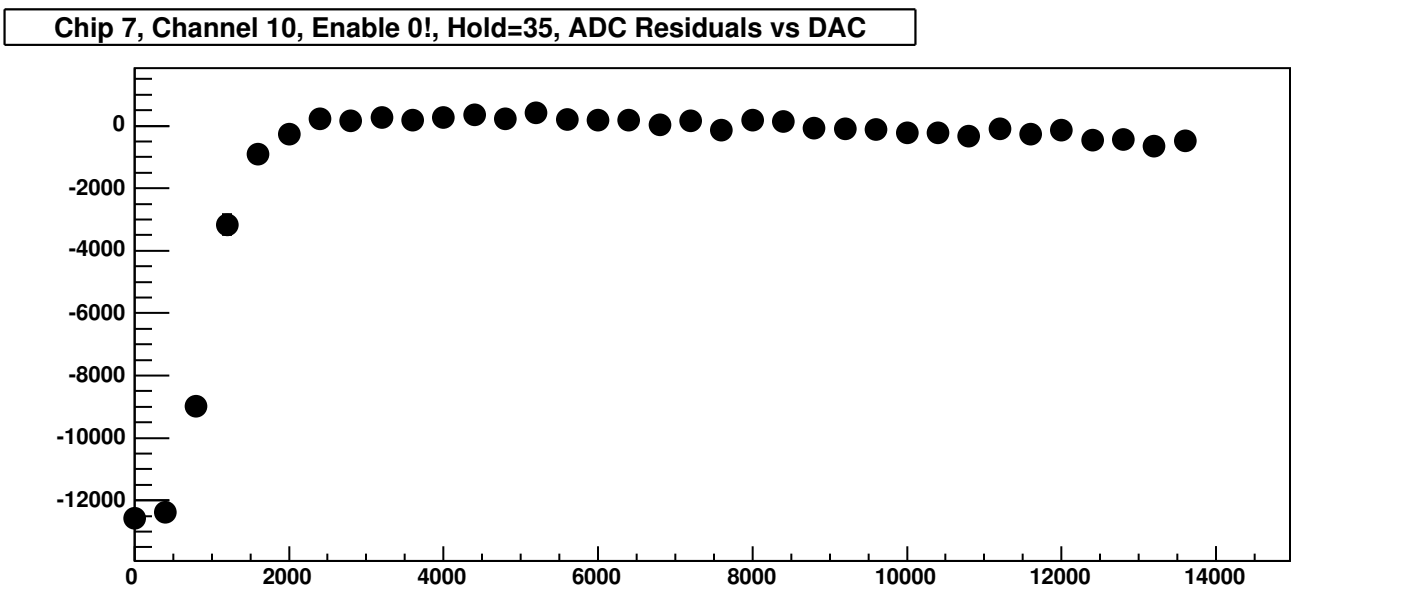


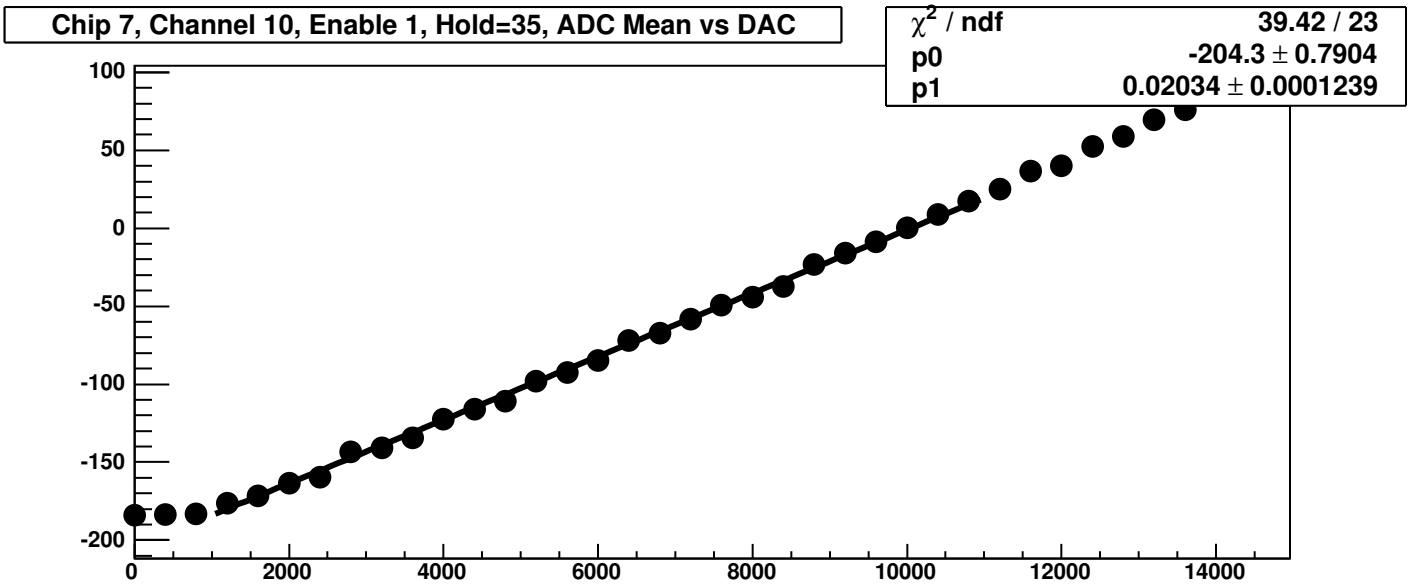
Chip 7, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

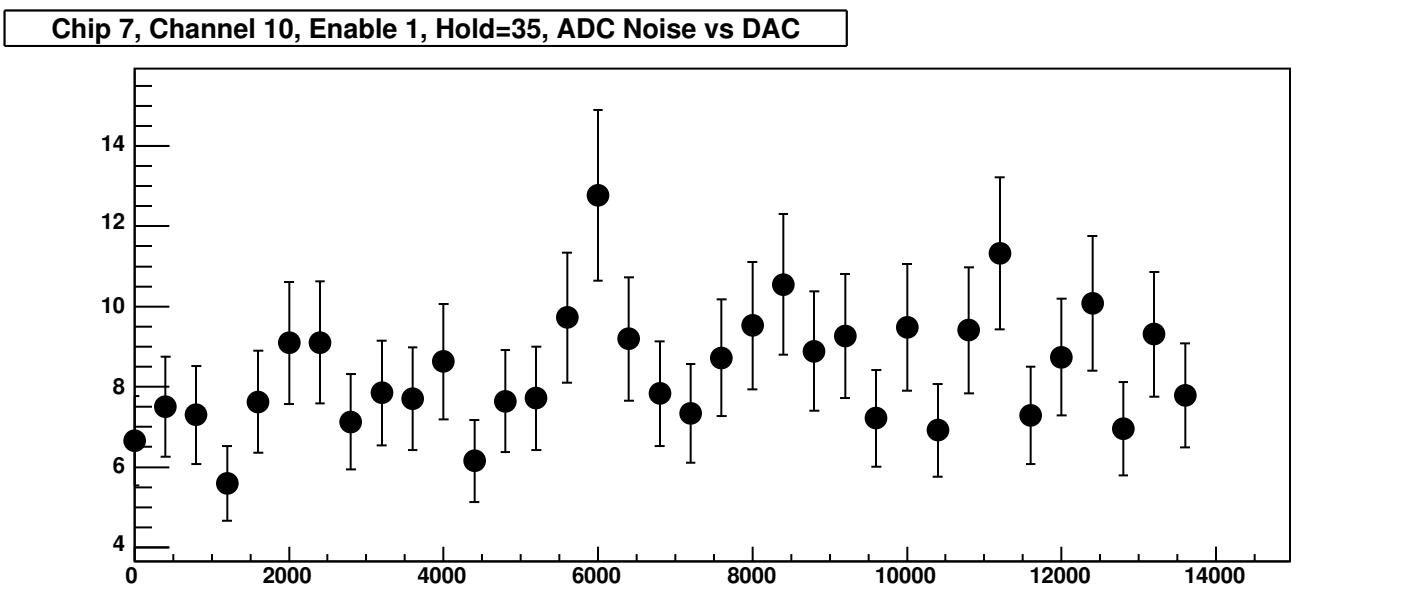


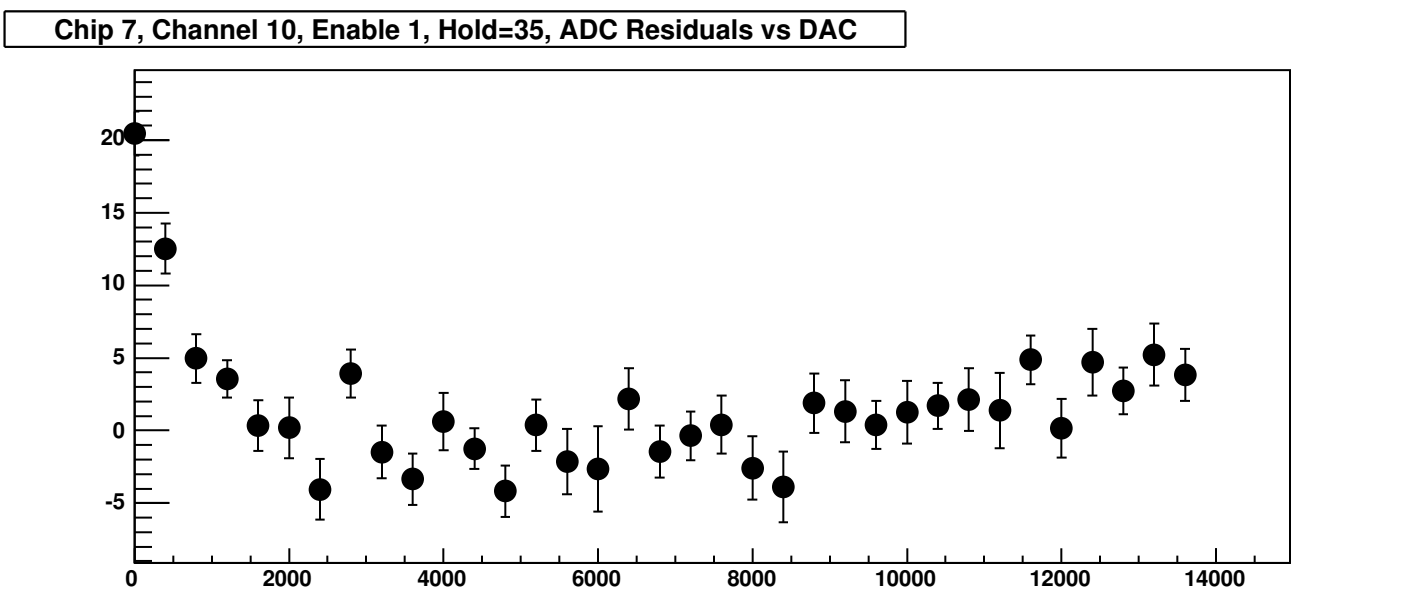

Chip 7, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 315.5 / 23
p0 2.76e+04 ± 47.3
p1 0.08262 ± 0.007066
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

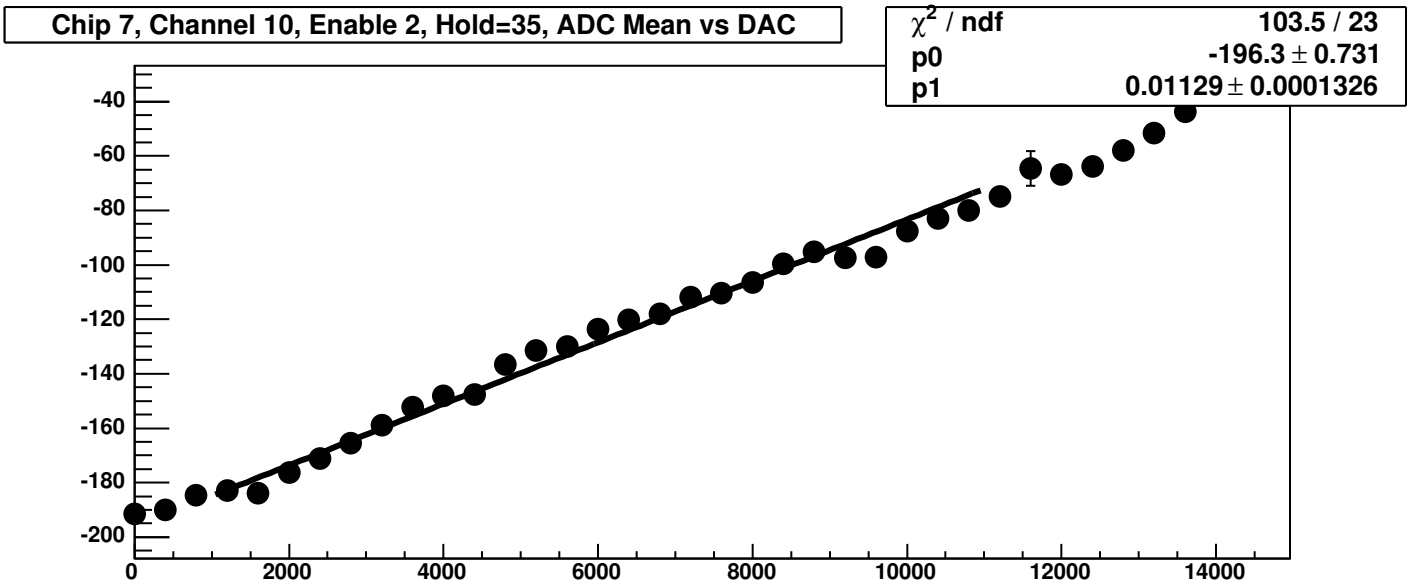

Chip 7, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

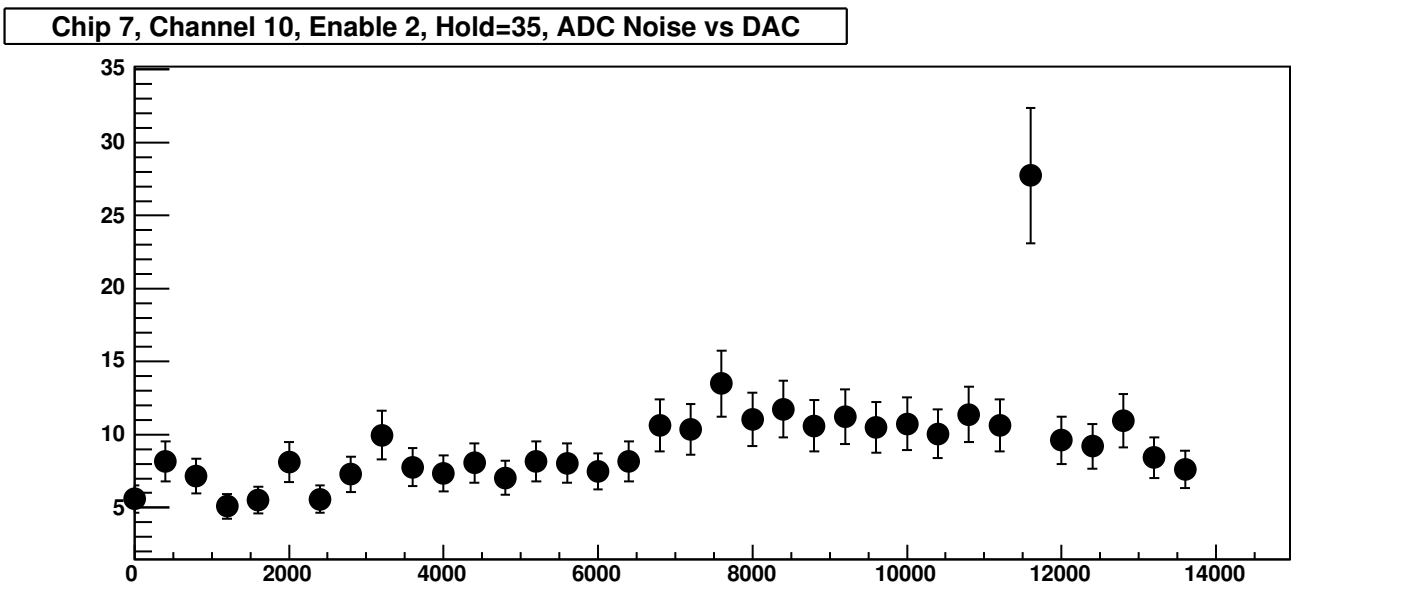

Chip 7, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
2000
4000
6000
8000
10000
12000
14000

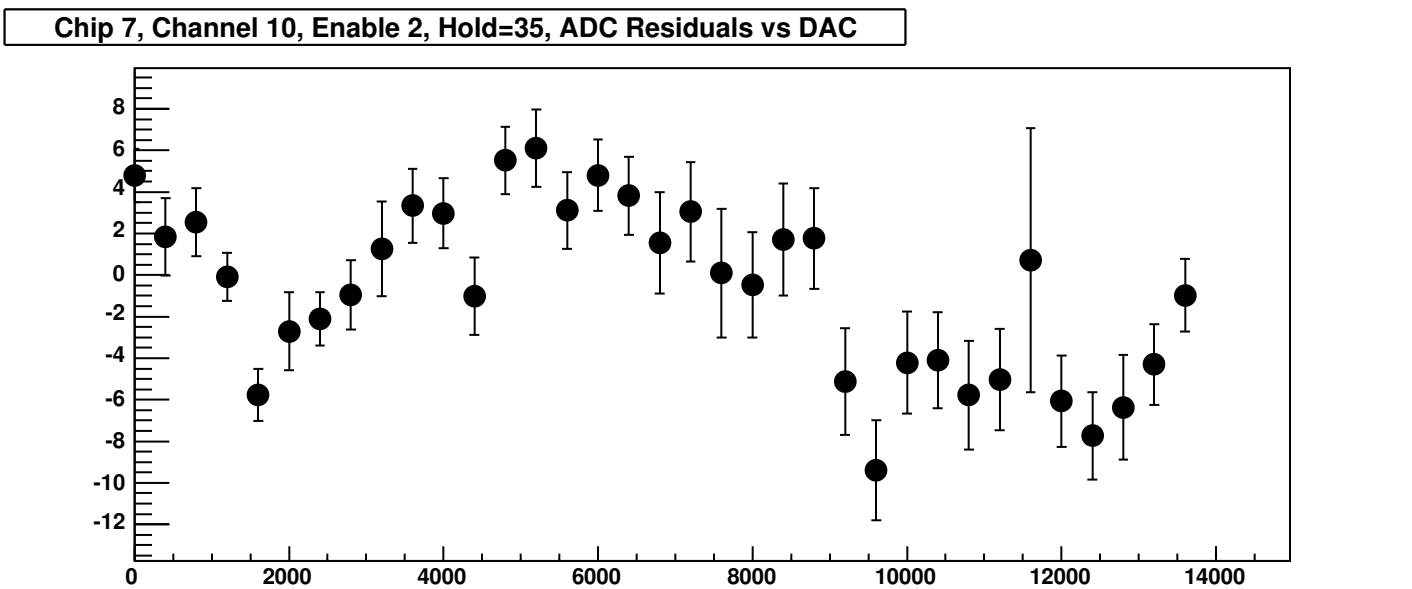

Chip 7, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 39.42 / 23
p0 -204.3 ± 0.7904
p1 0.02034 ± 0.0001239
100
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

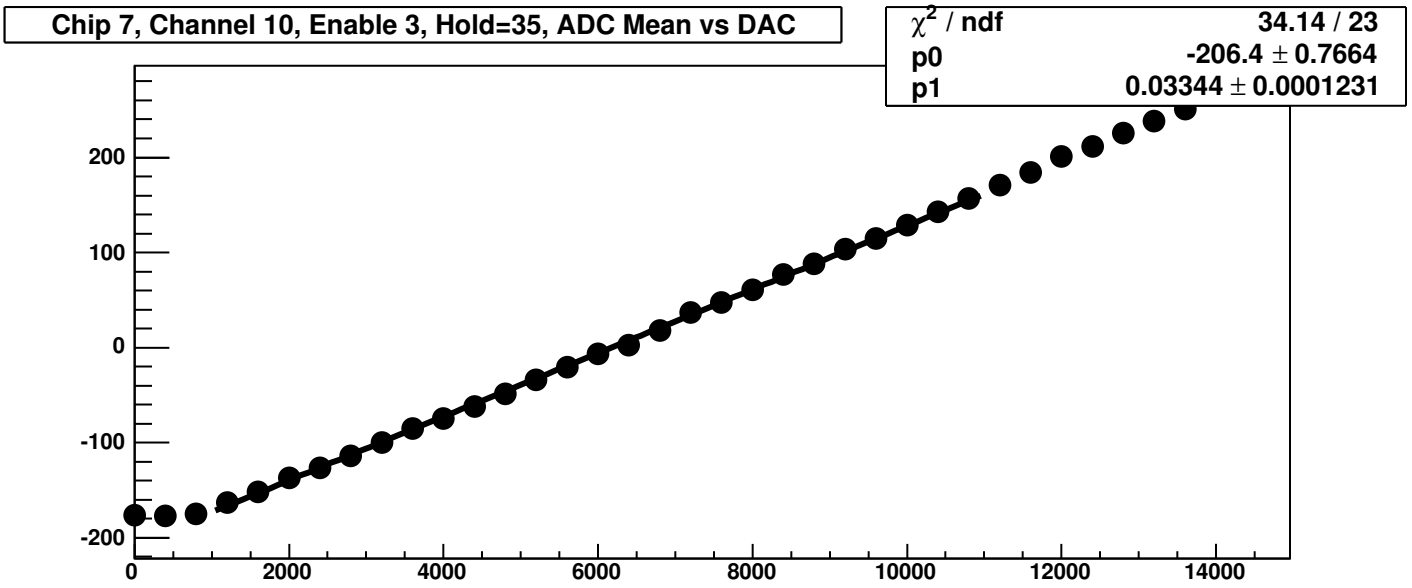

Chip 7, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

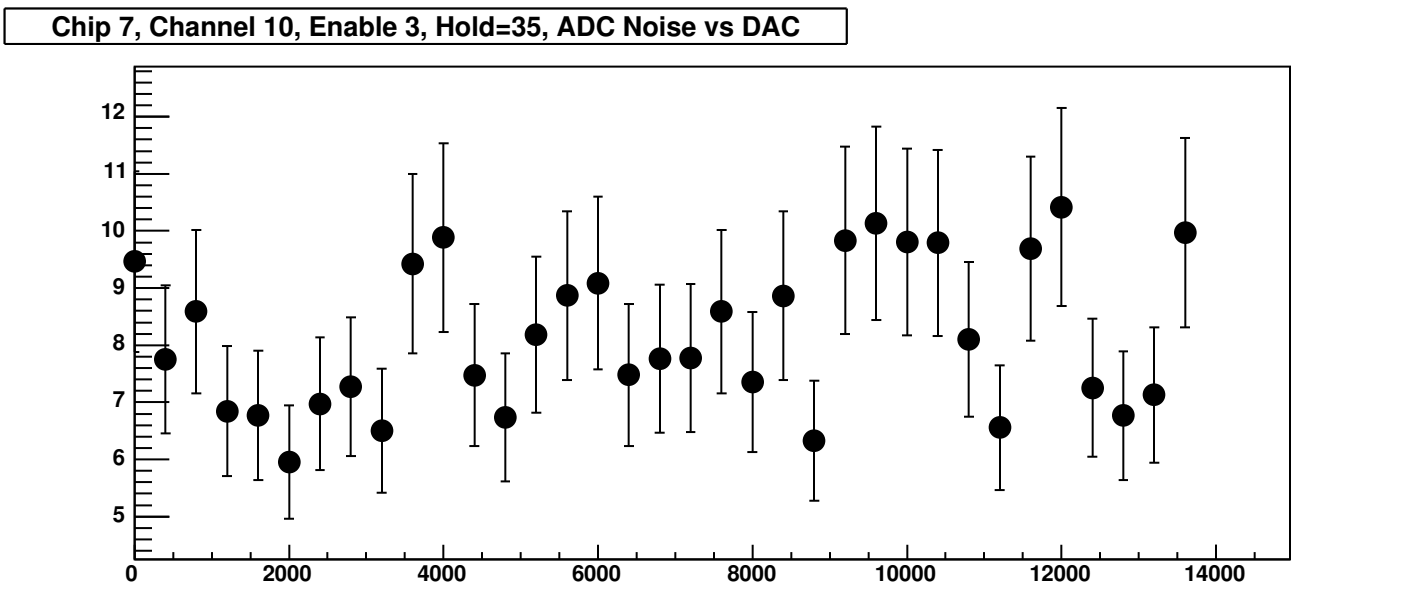

Chip 7, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

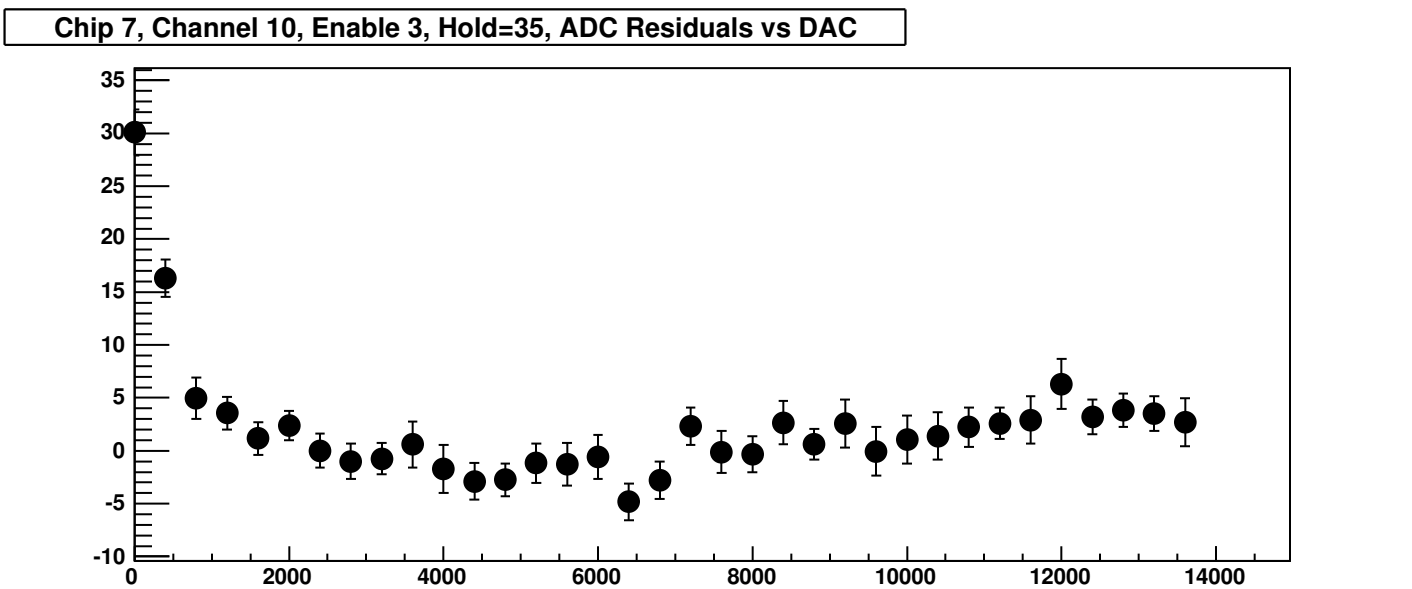

Chip 7, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 103.5 / 23
p0 -196.3 ± 0.731
p1 0.01129 ± 0.0001326
-40
-60
-80
-100
-120
-140
-160
-180
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

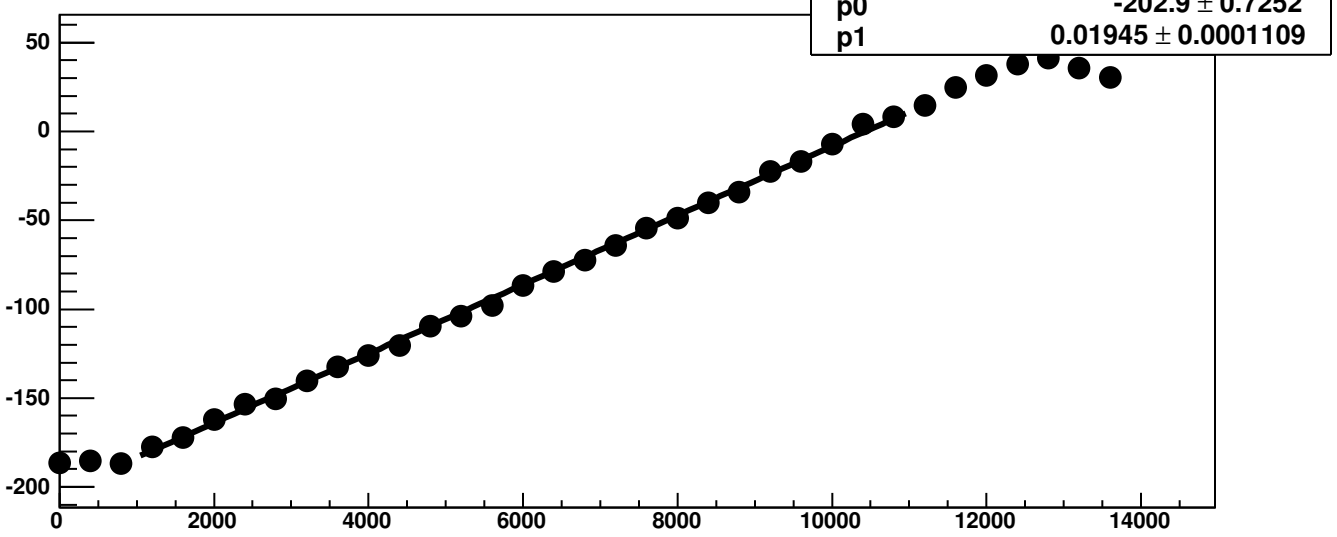

Chip 7, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 34.14 / 23
p0 -206.4 ± 0.7664
p1 0.03344 ± 0.0001231
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

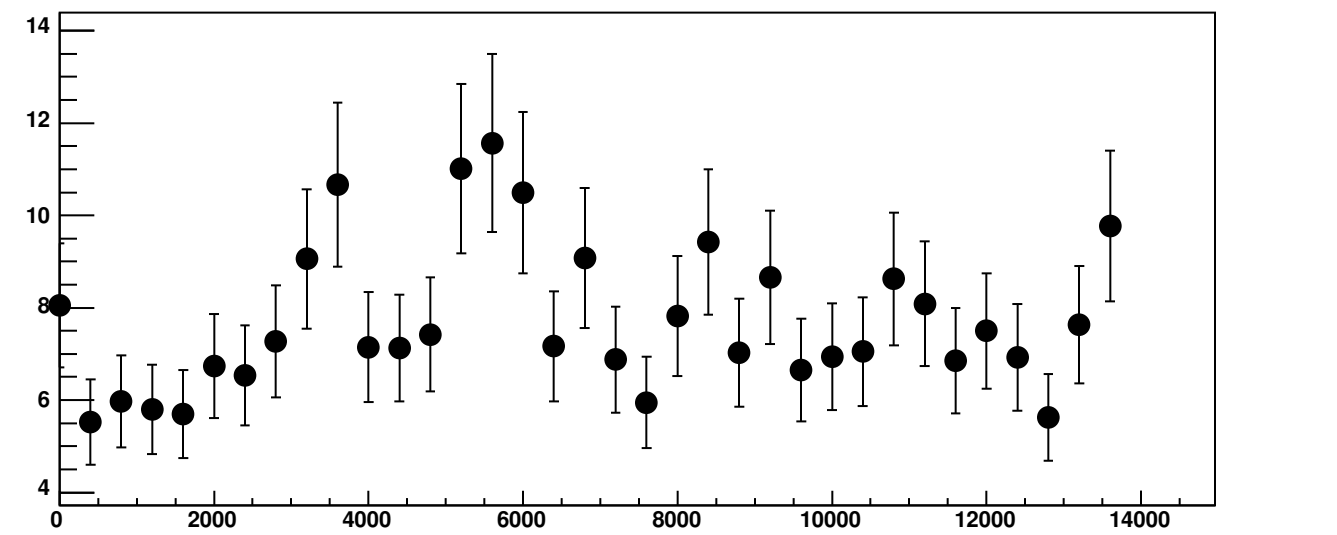

Chip 7, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

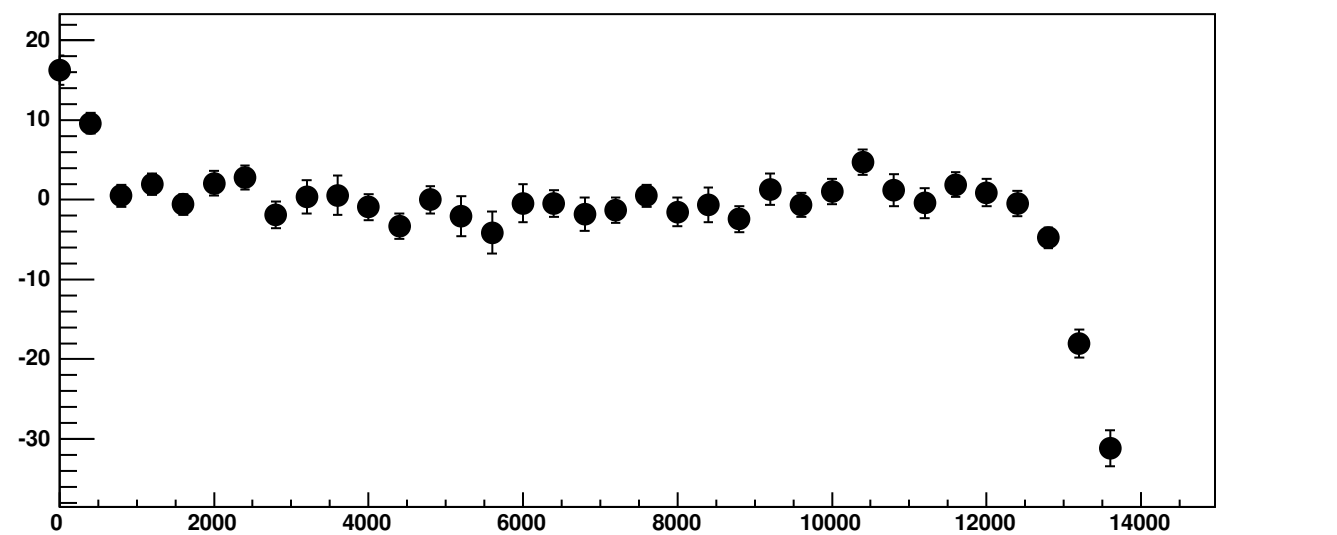
Chip 7, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

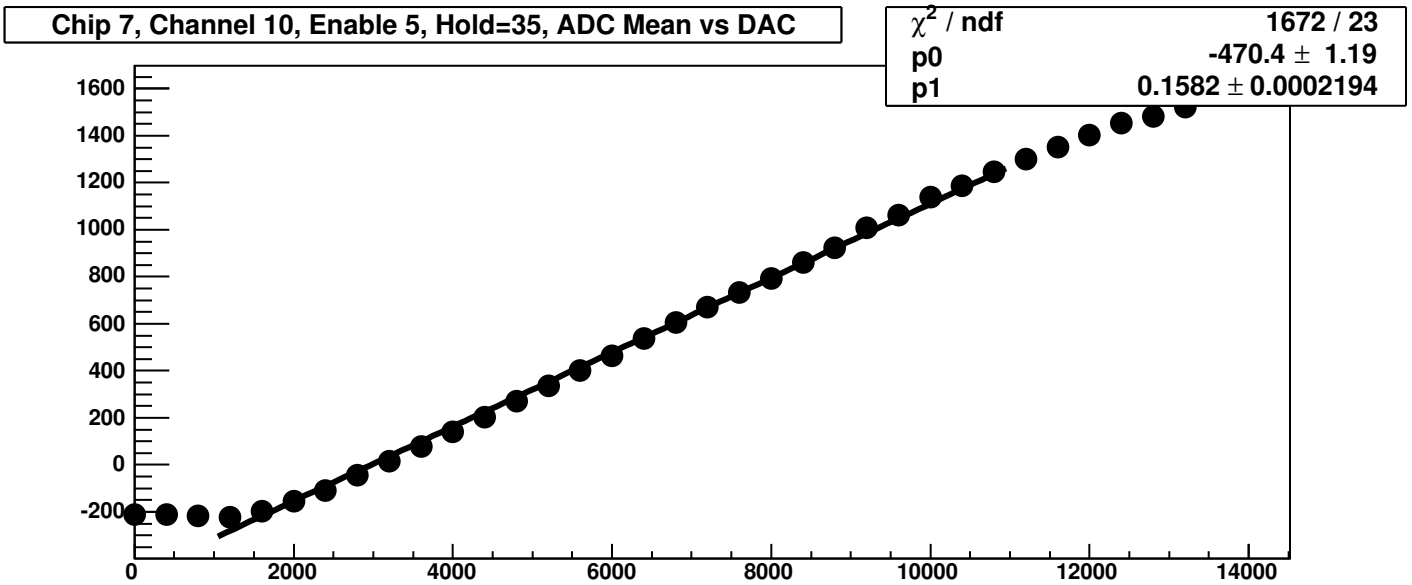


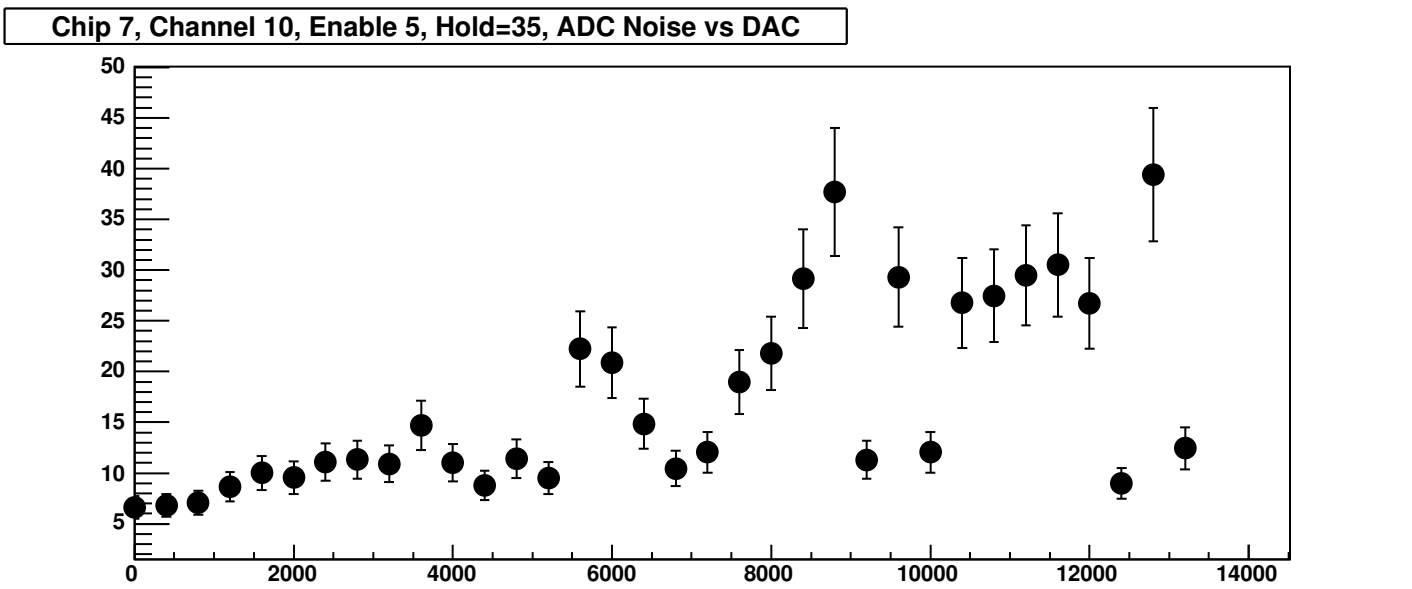
Chip 7, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

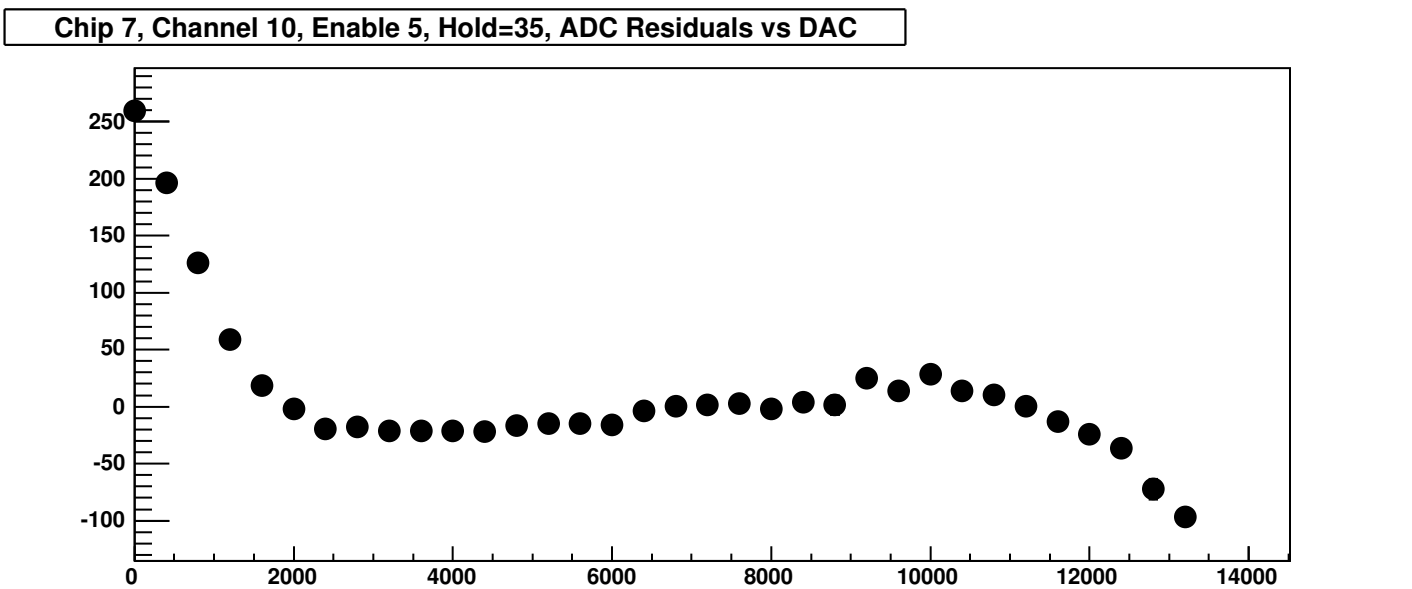


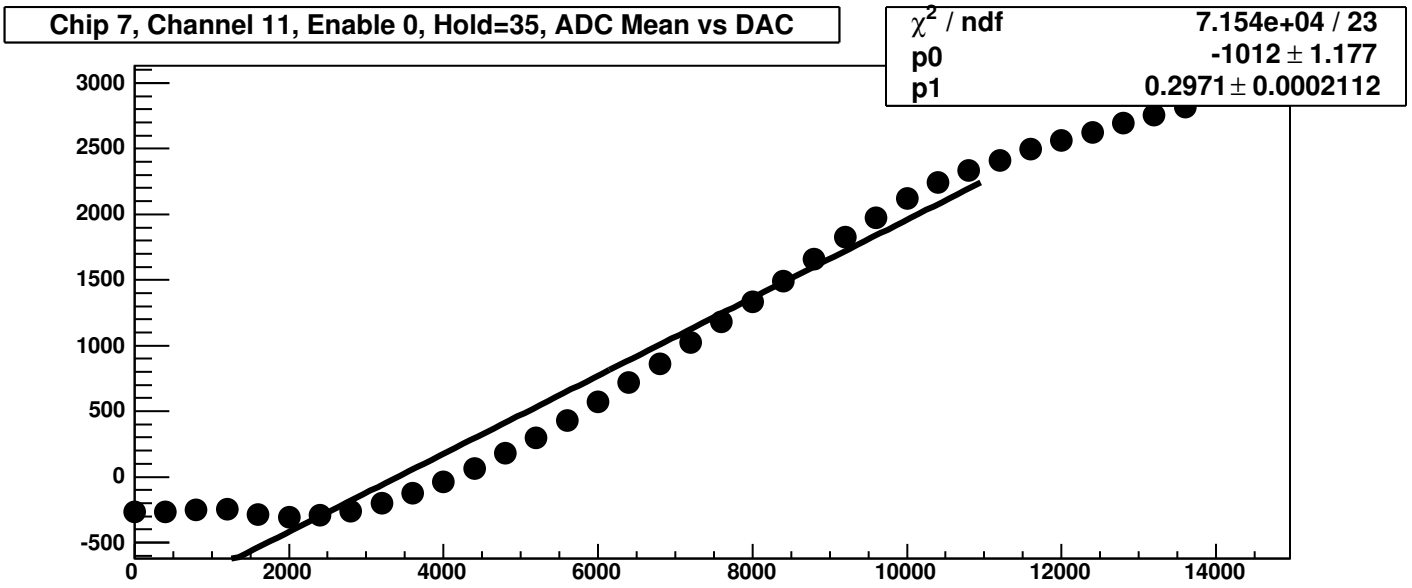
Chip 7, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

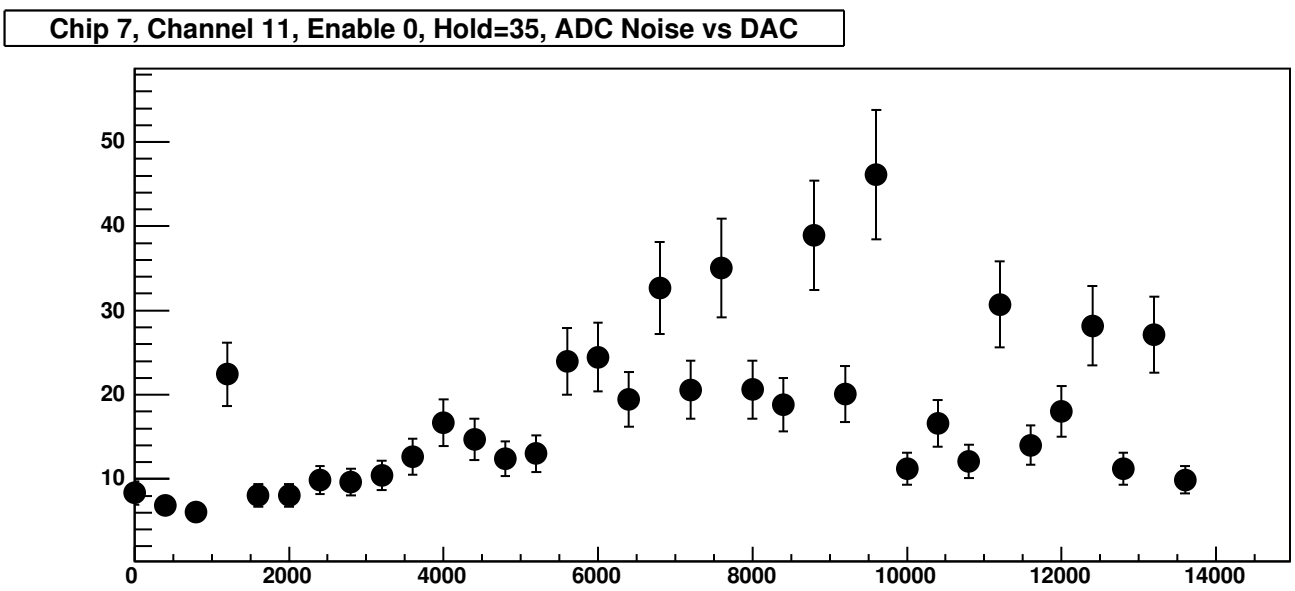


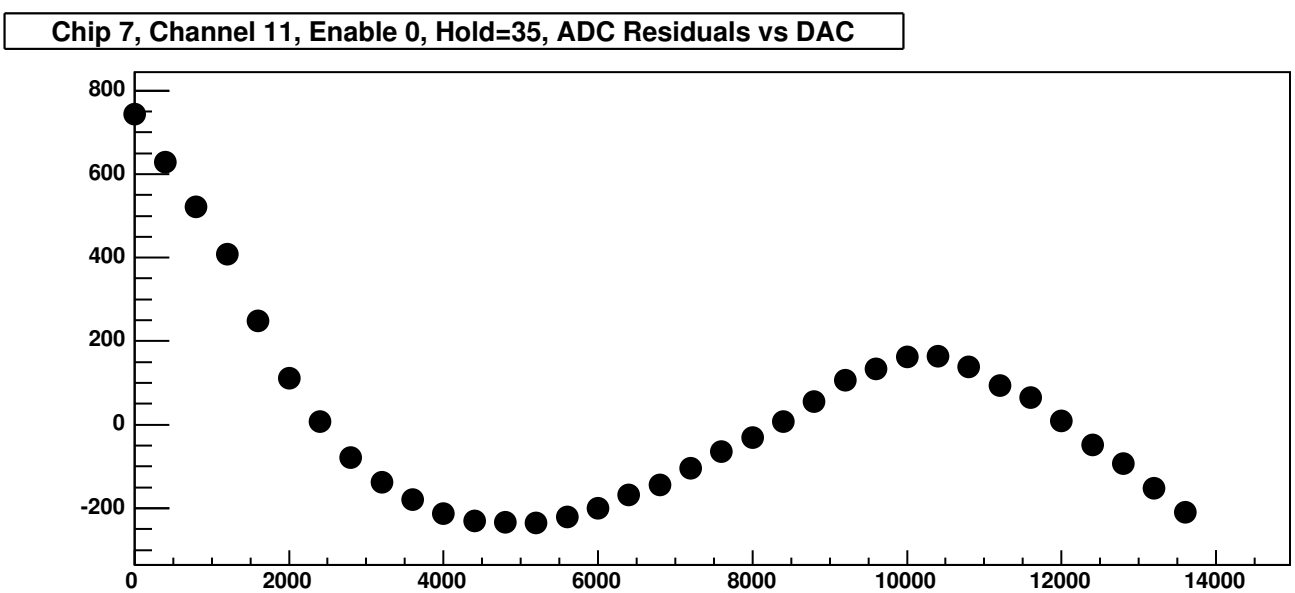

Chip 7, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1672 / 23
p0 -470.4 ± 1.19
p1 0.1582 ± 0.0002194
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

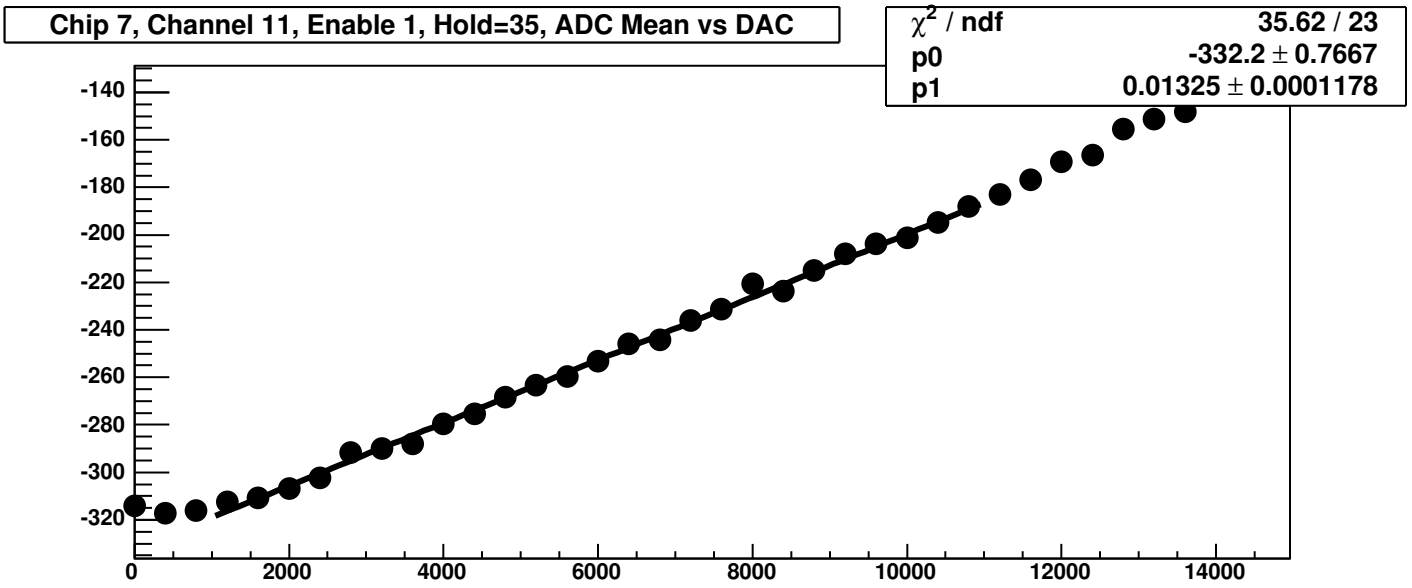

Chip 7, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

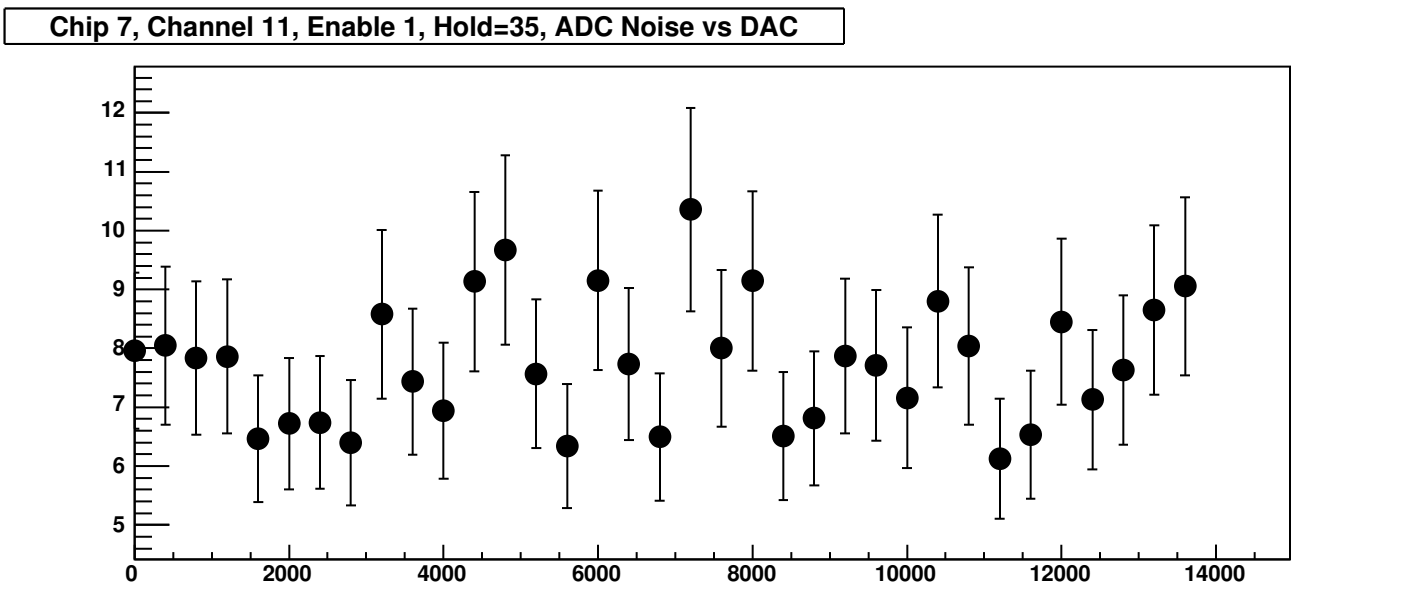

Chip 7, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

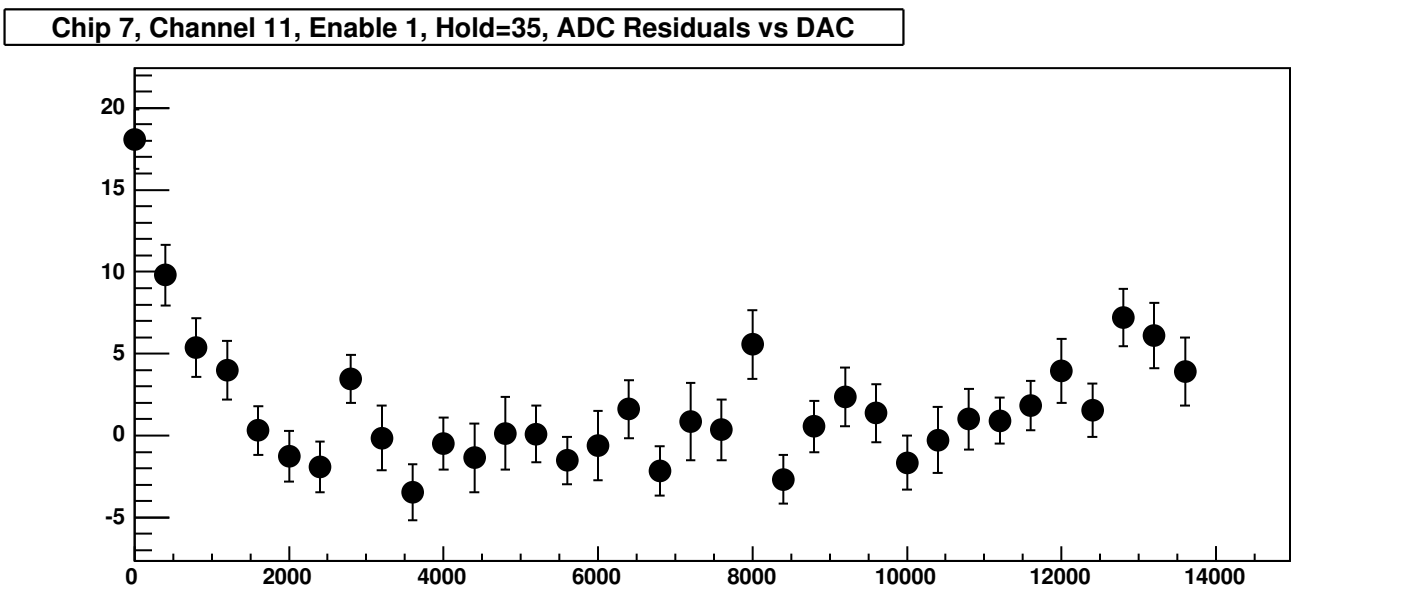

Chip 7, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 7.154e+04 / 23
p0 -1012 ± 1.177
p1 0.2971 ± 0.0002112
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

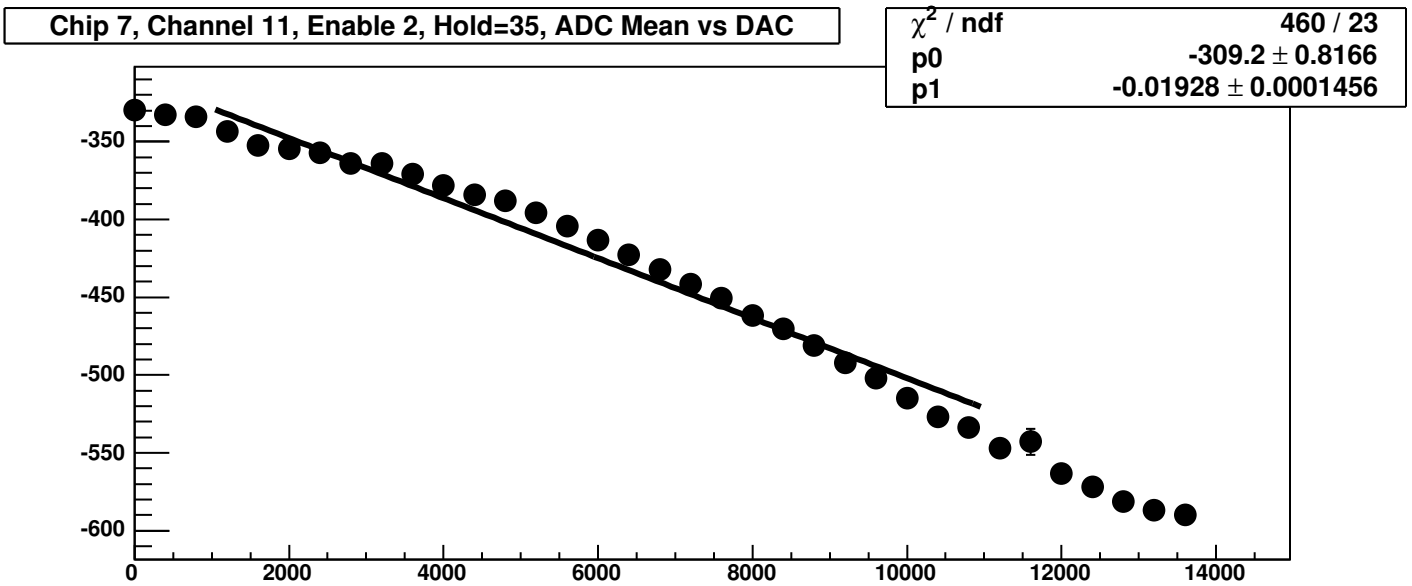

Chip 7, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

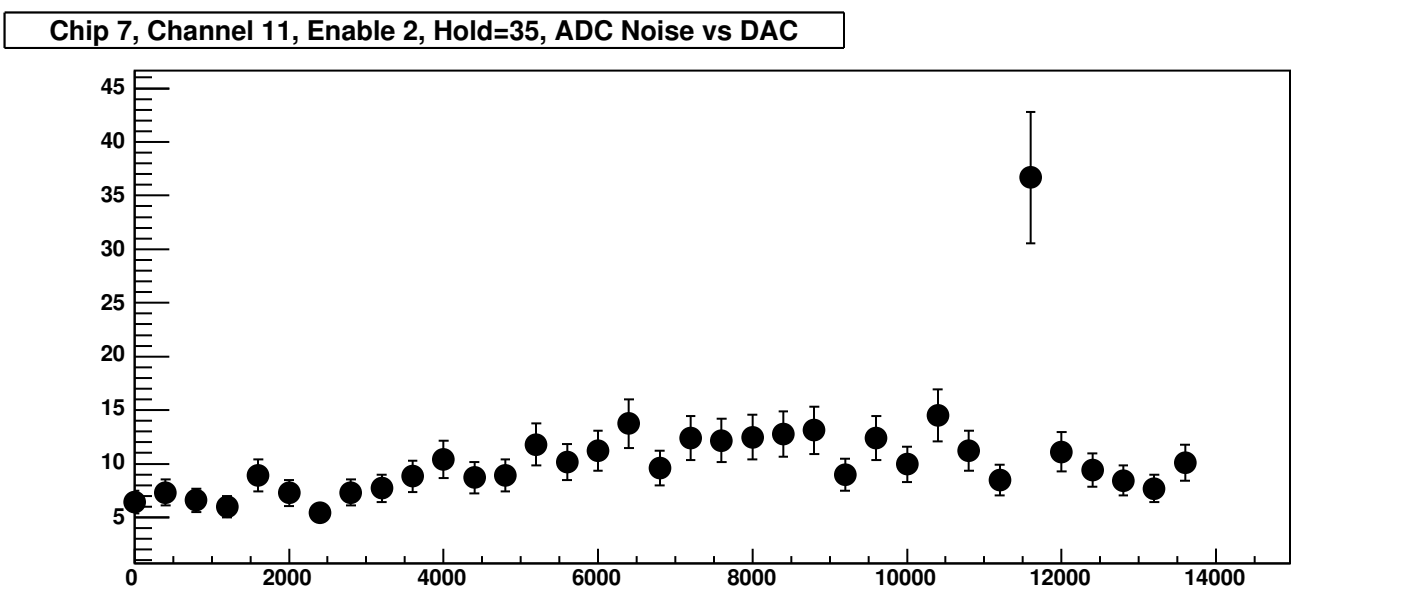

Chip 7, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

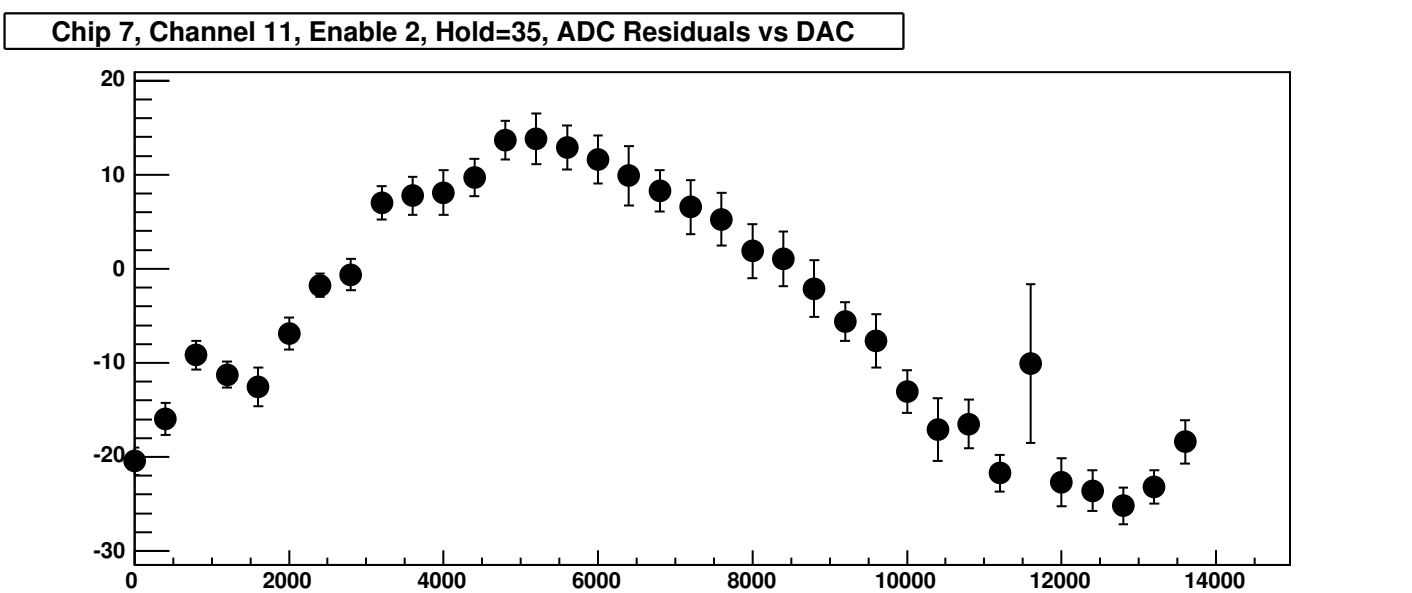

Chip 7, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 35.62 / 23
p0 -332.2 ± 0.7667
p1 0.01325 ± 0.0001178
-140
-160
-180
-200
-220
-240
-260
-280
-300
-320
0
2000
4000
6000
8000
10000
12000
14000

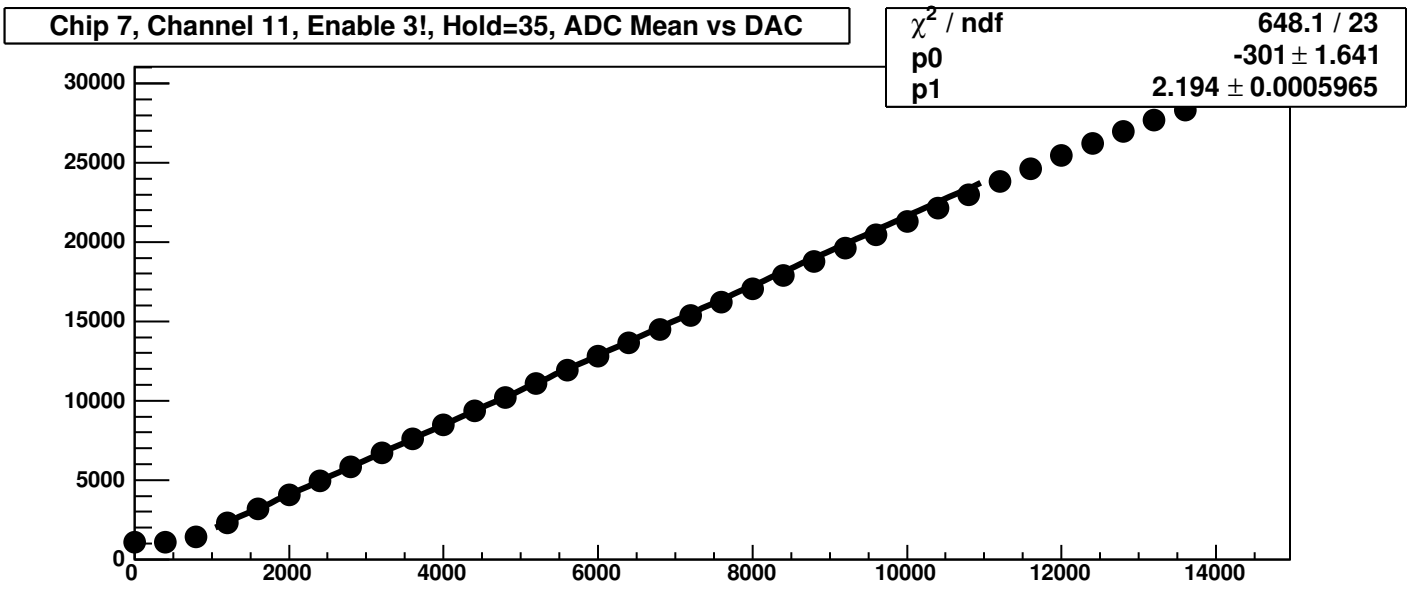

Chip 7, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

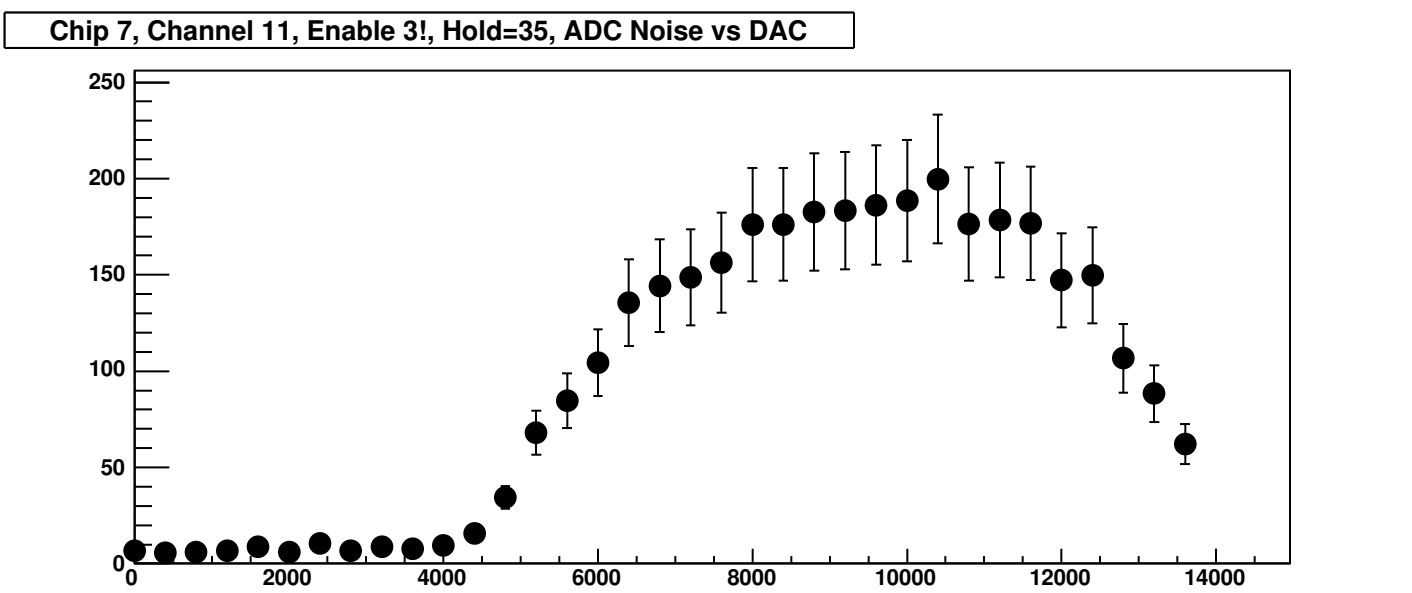

Chip 7, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

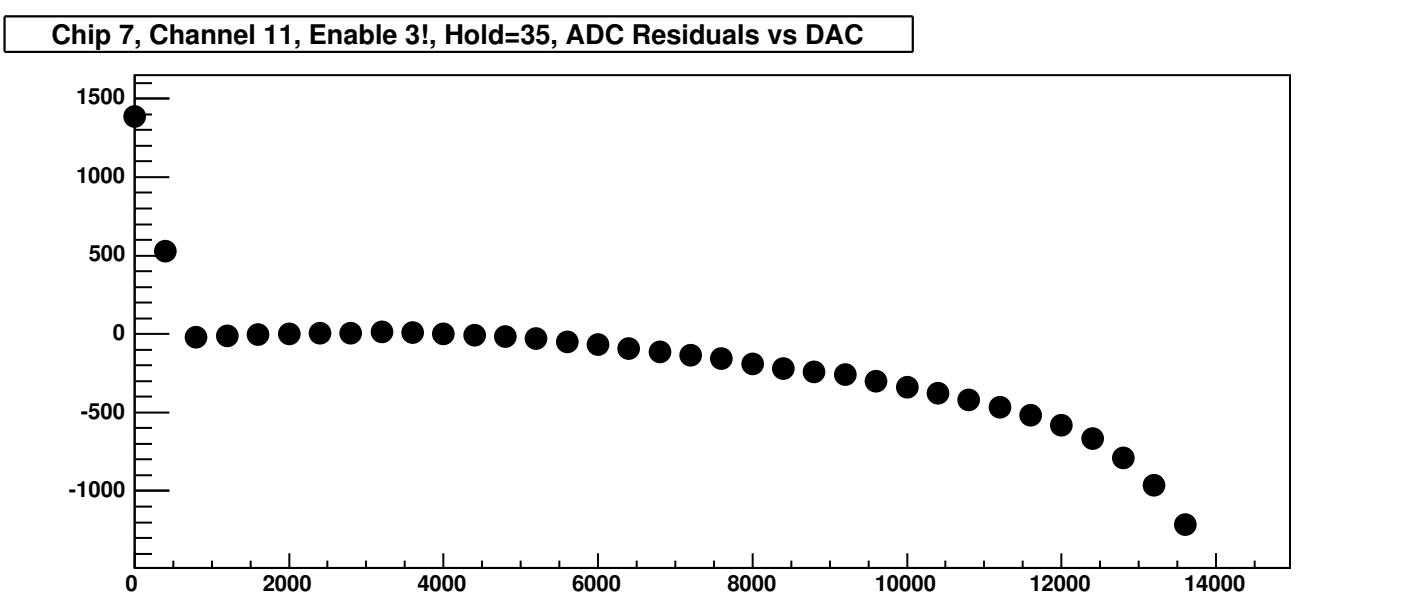

Chip 7, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 460 / 23
p0 -309.2 ± 0.8166
p1 -0.01928 ± 0.0001456
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

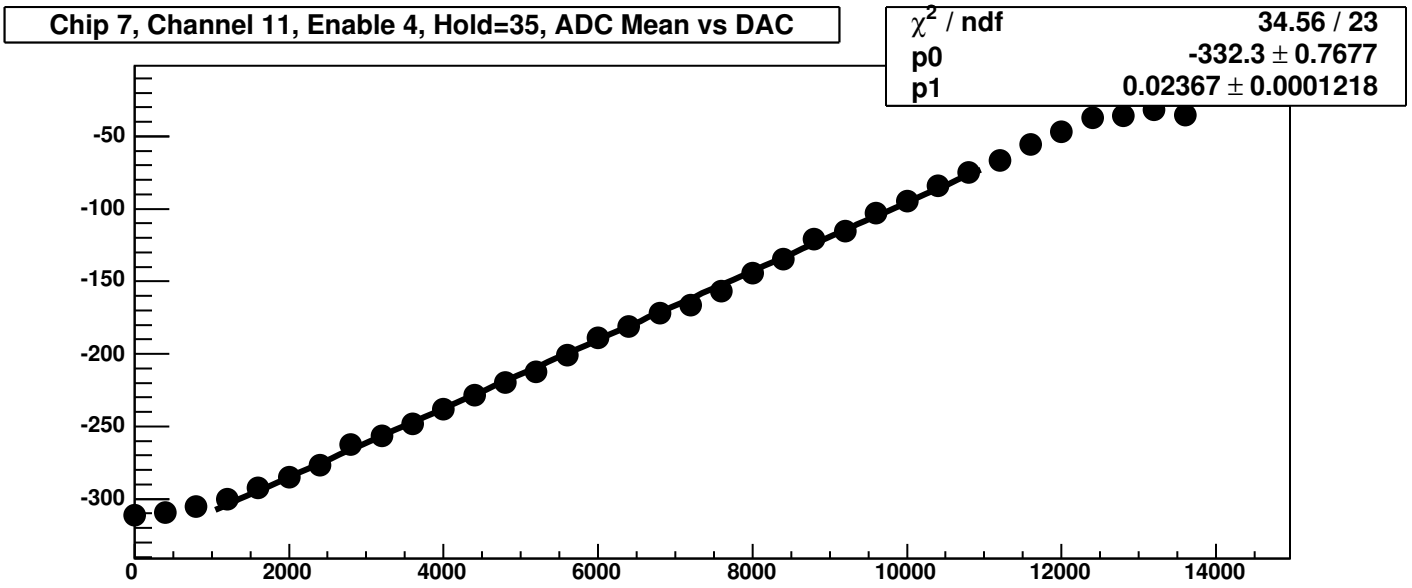

Chip 7, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

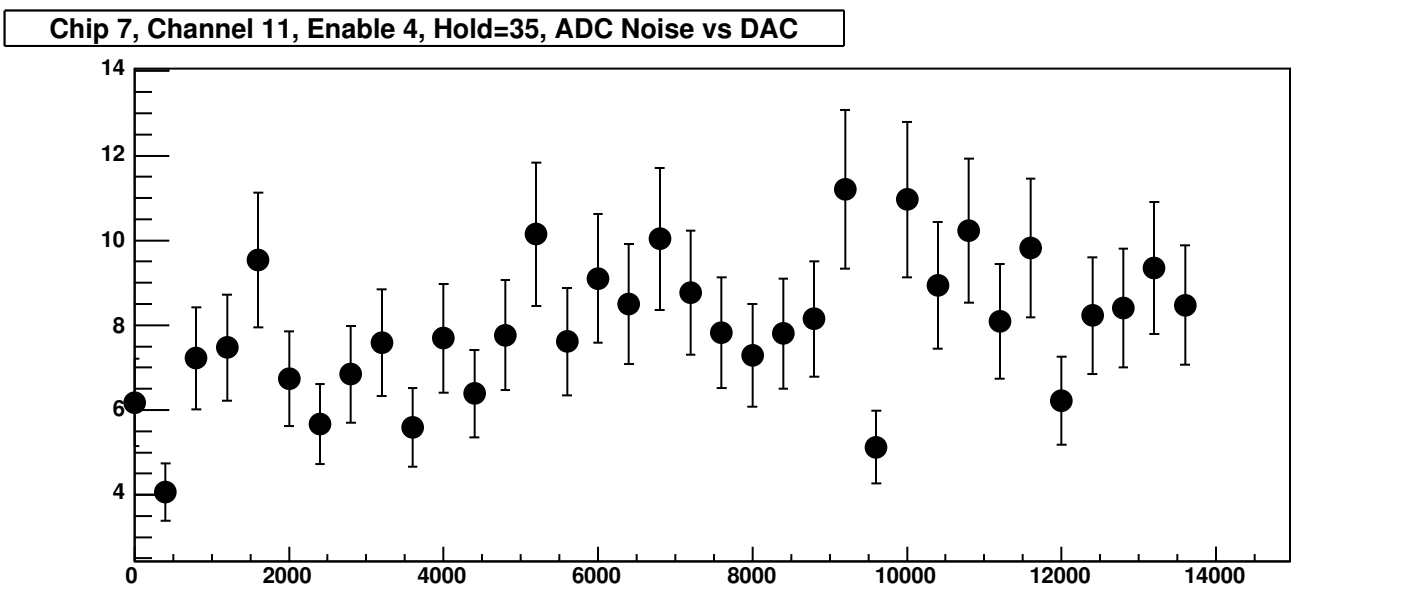

Chip 7, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

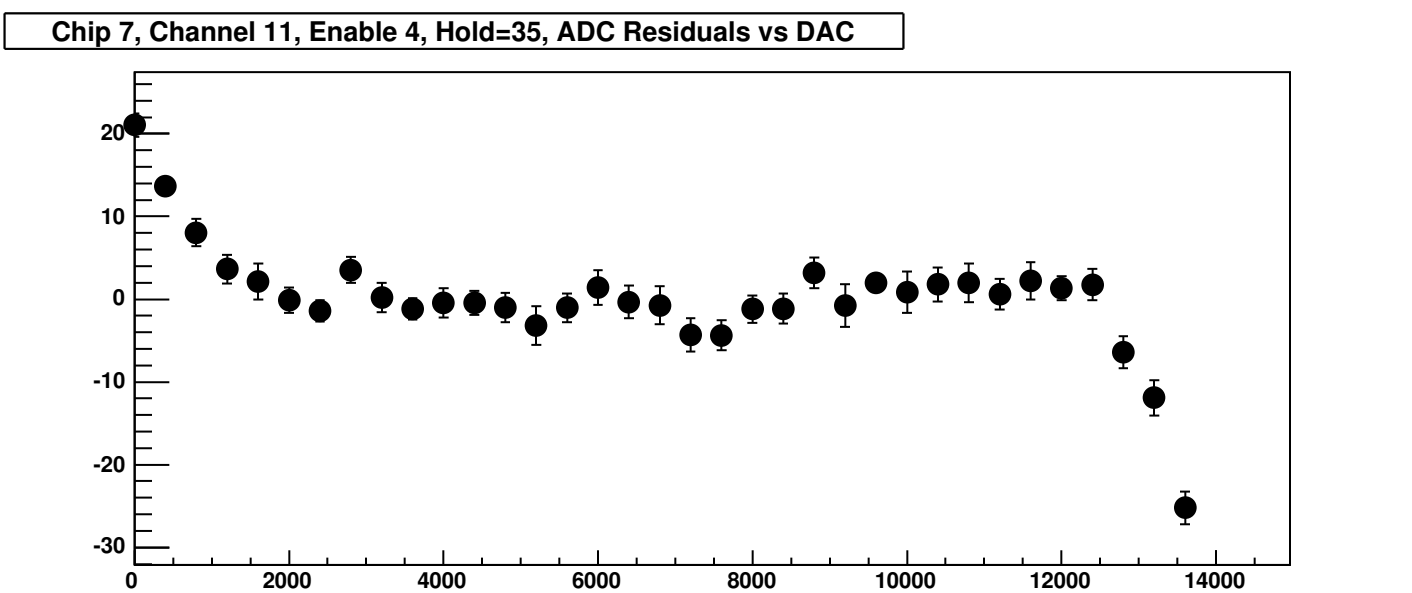

Chip 7, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 648.1 / 23
p0 -301 ± 1.641
p1 2.194 ± 0.0005965
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

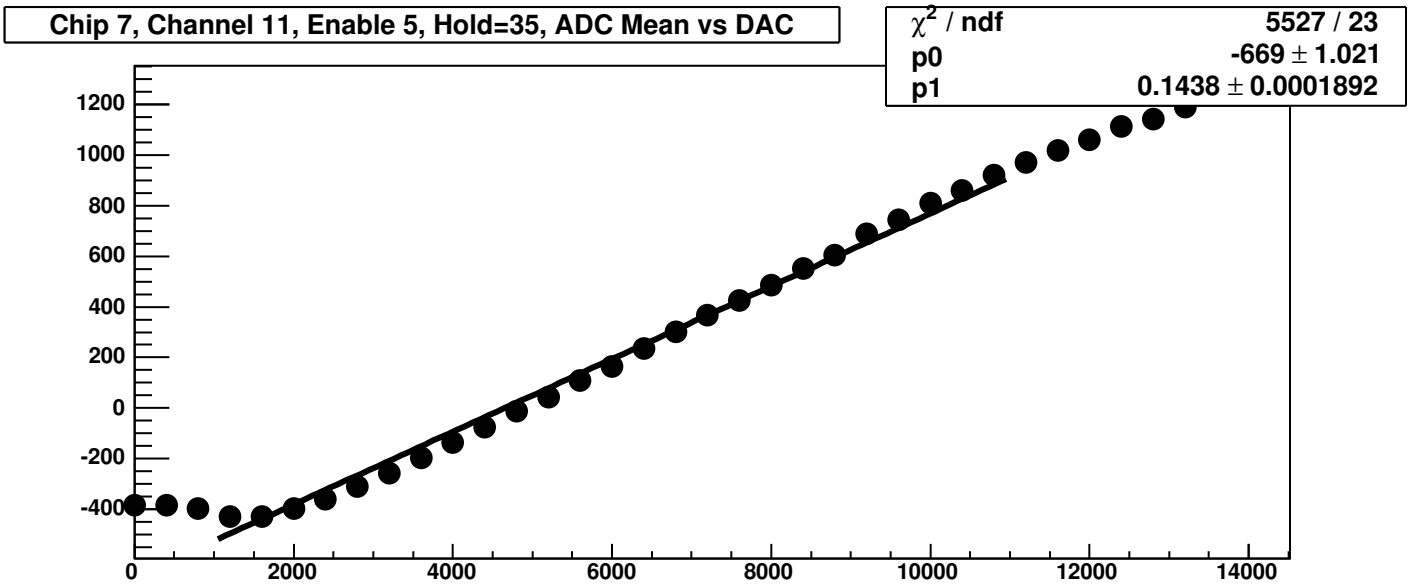

Chip 7, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

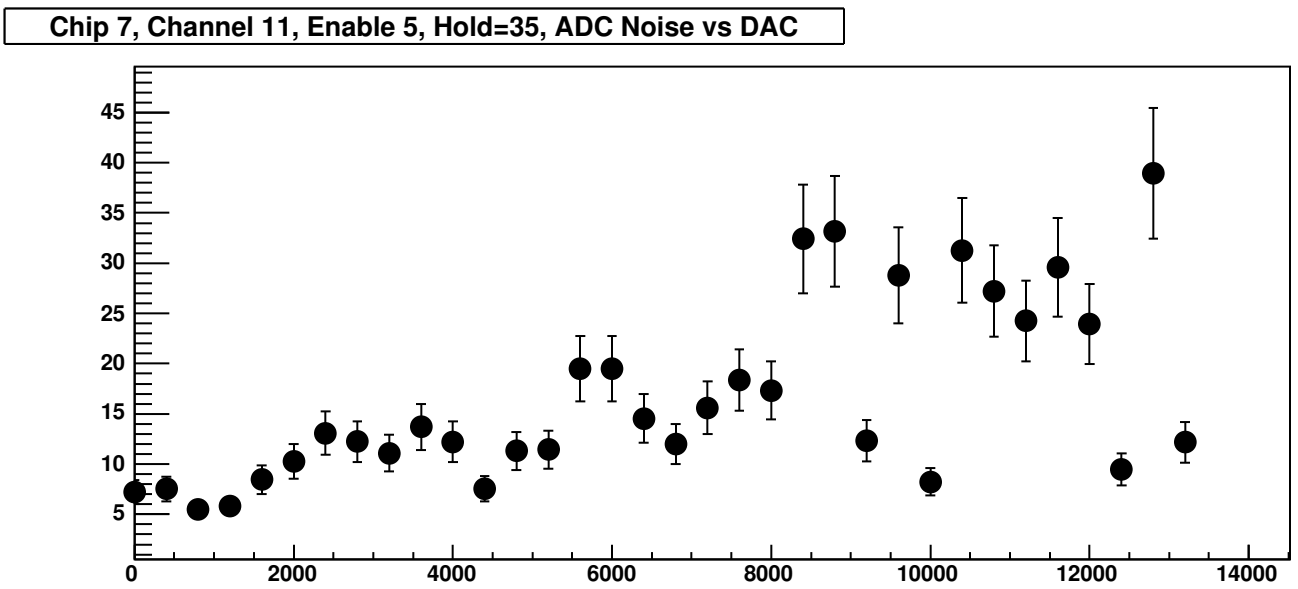

Chip 7, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

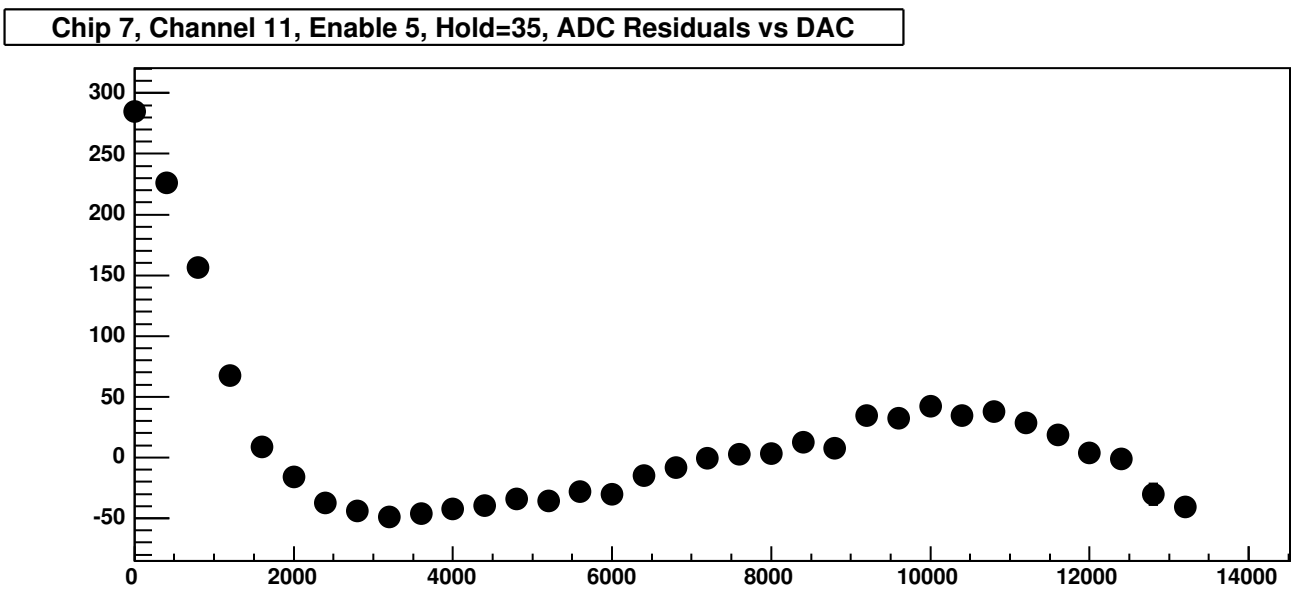

Chip 7, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 34.56 / 23
p0 -332.3 ± 0.7677
p1 0.02367 ± 0.0001218
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

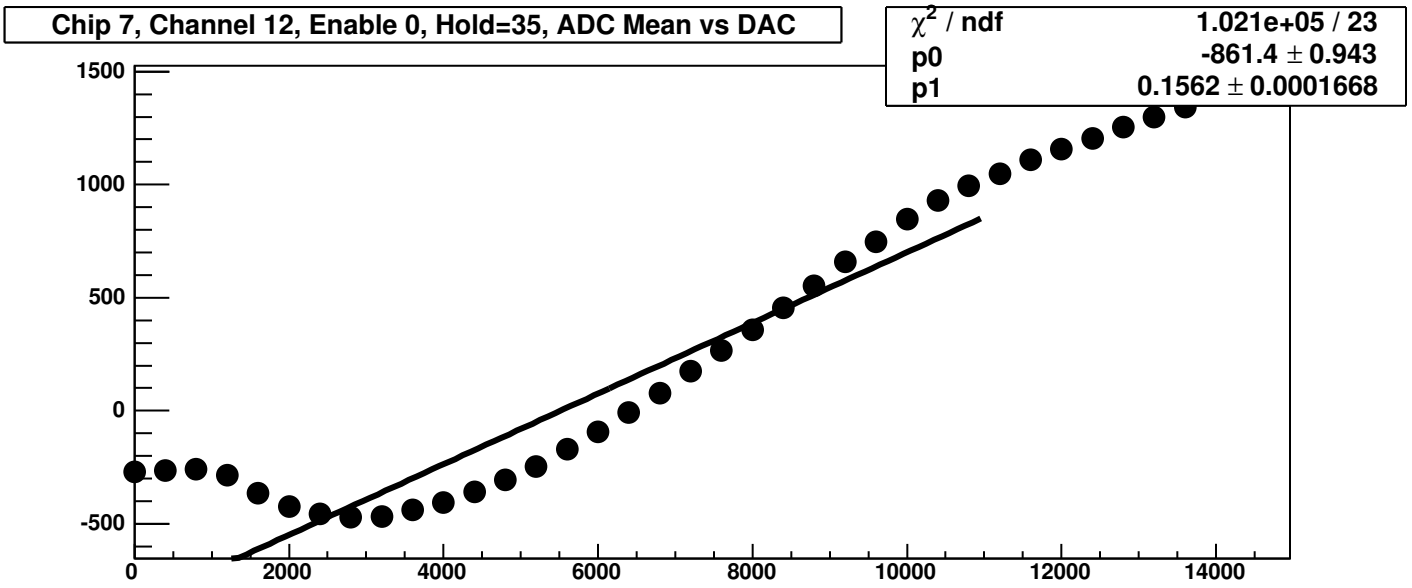

Chip 7, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

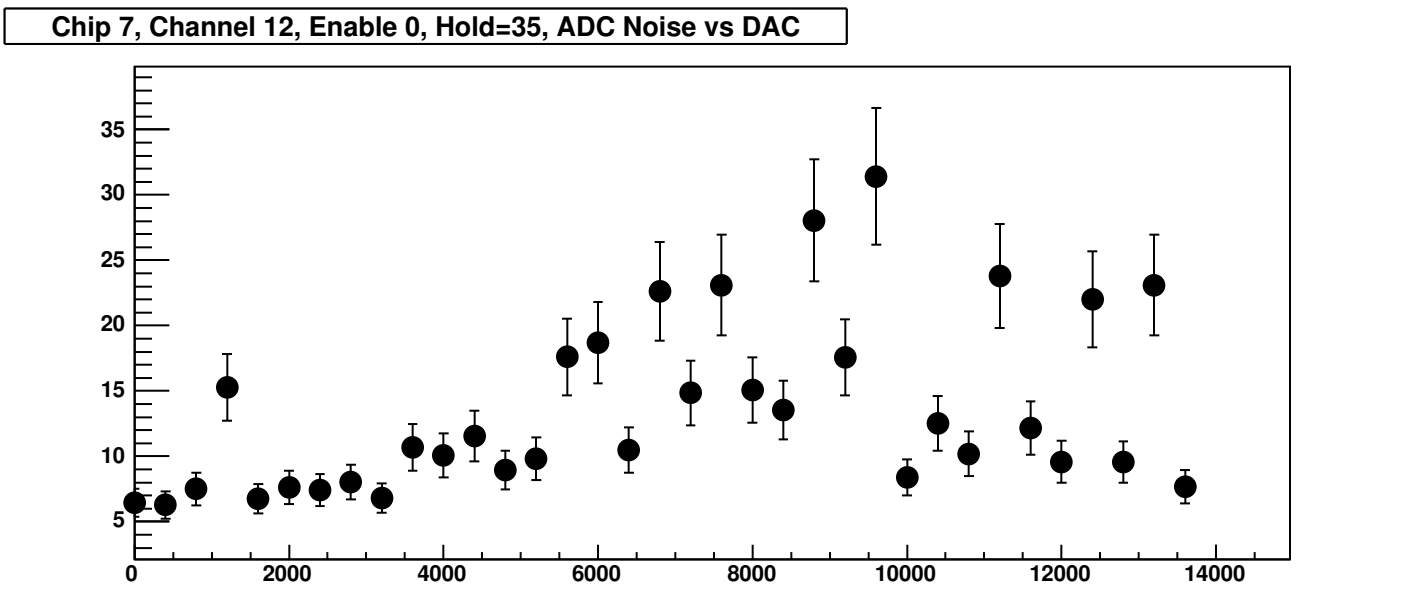

Chip 7, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

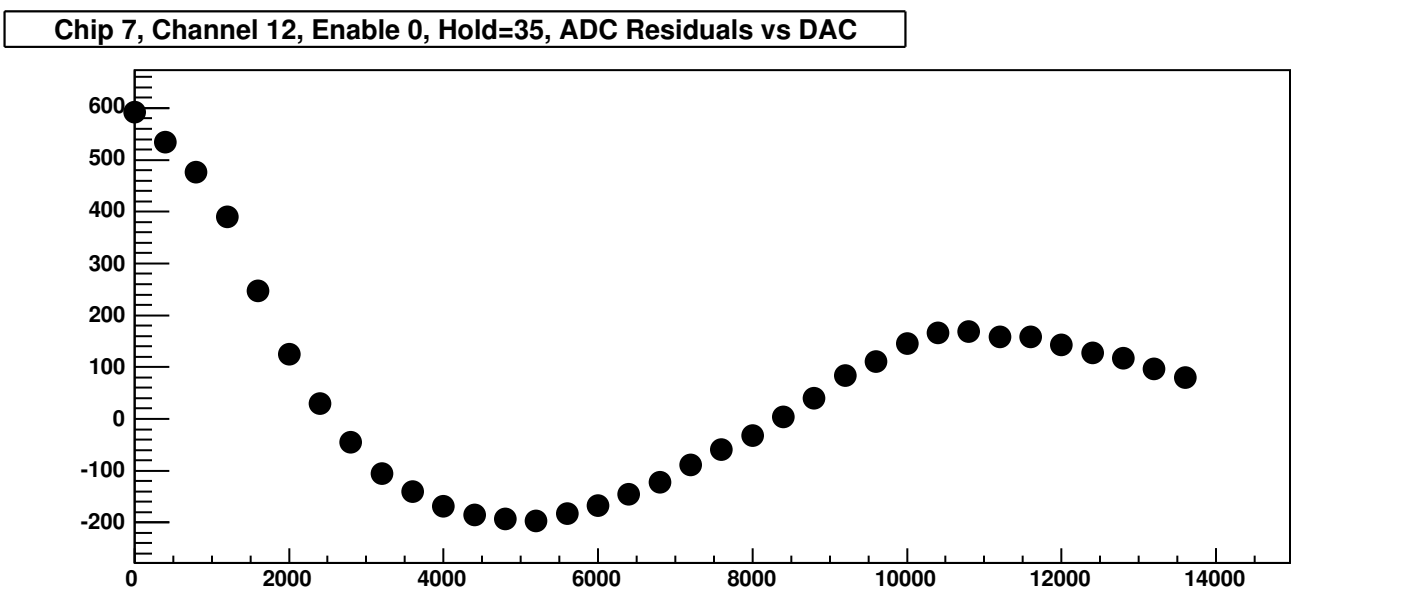

Chip 7, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5527 / 23
p0 -669 ± 1.021
p1 0.1438 ± 0.0001892
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

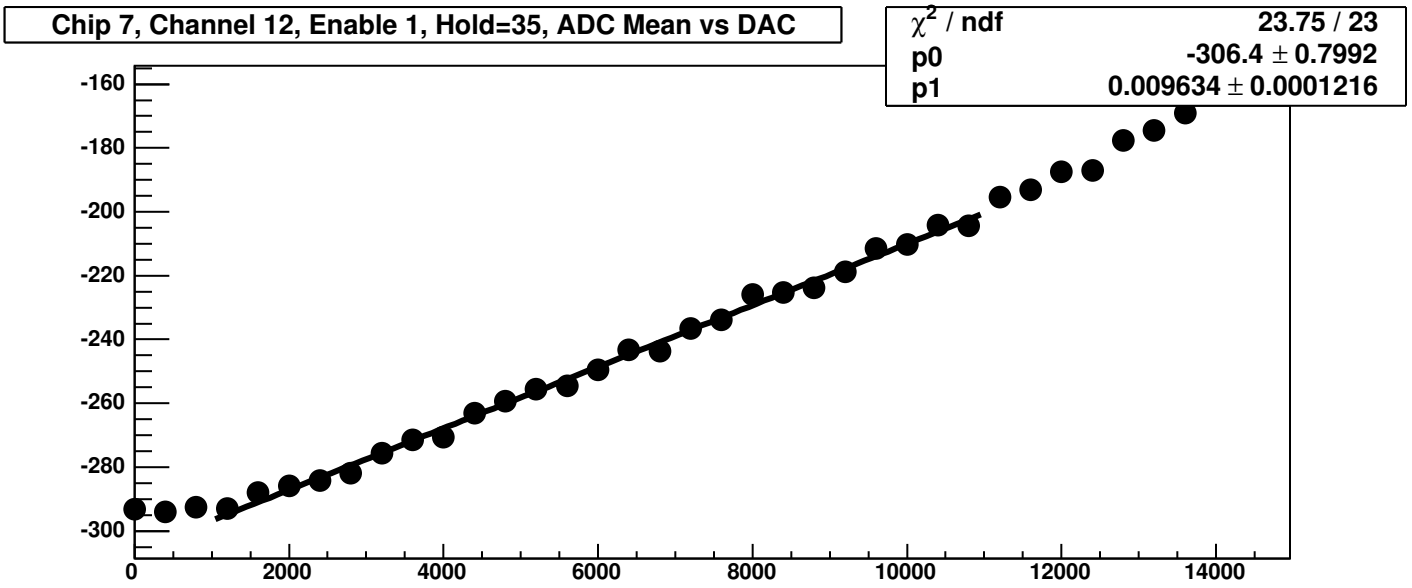

Chip 7, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

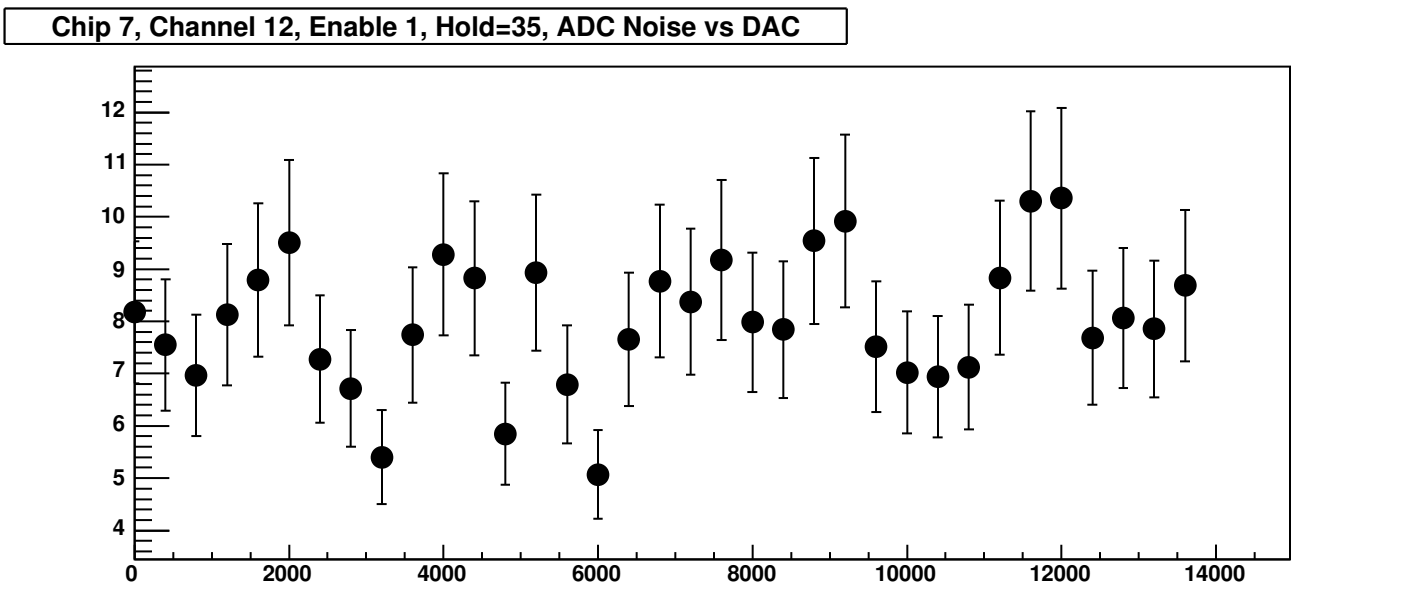

Chip 7, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

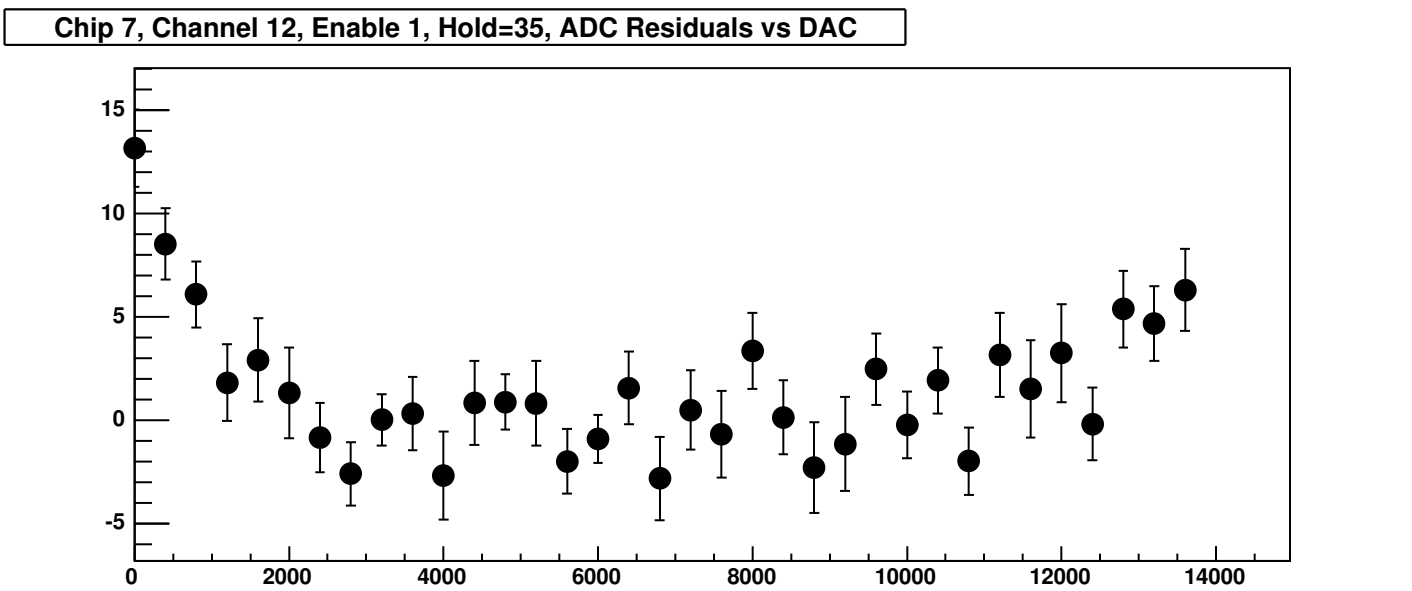

Chip 7, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.021e+05 / 23
p0 -861.4 ± 0.943
p1 0.1562 ± 0.0001668
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

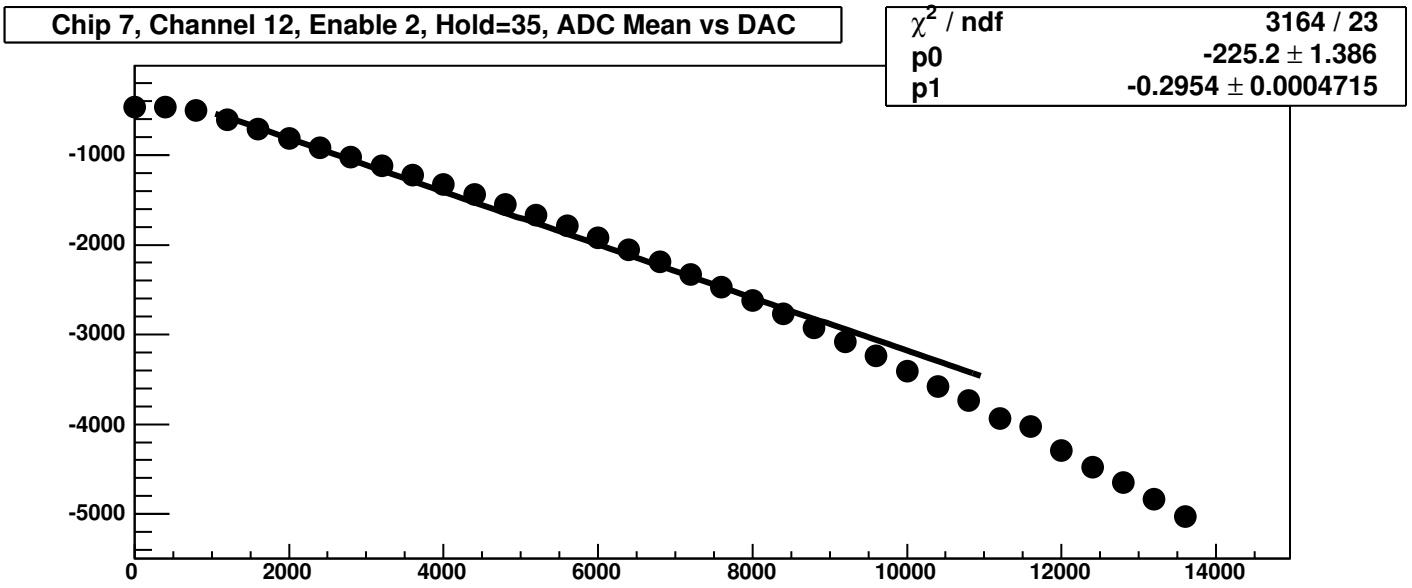

Chip 7, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

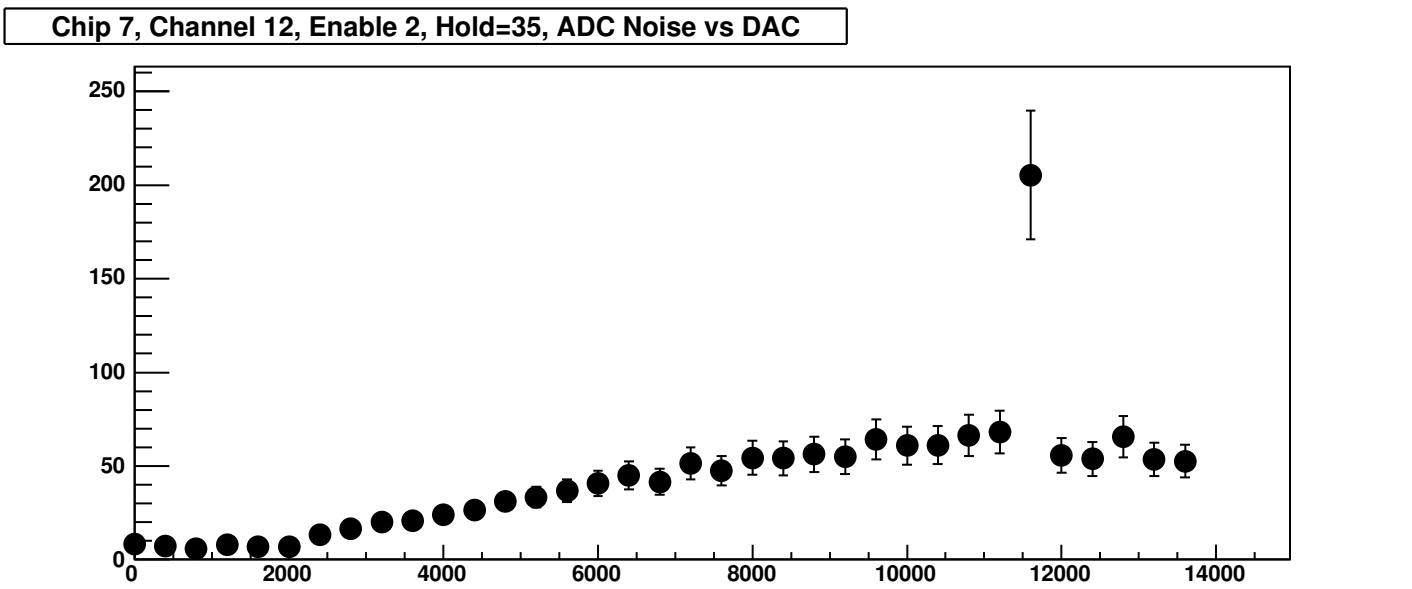

Chip 7, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

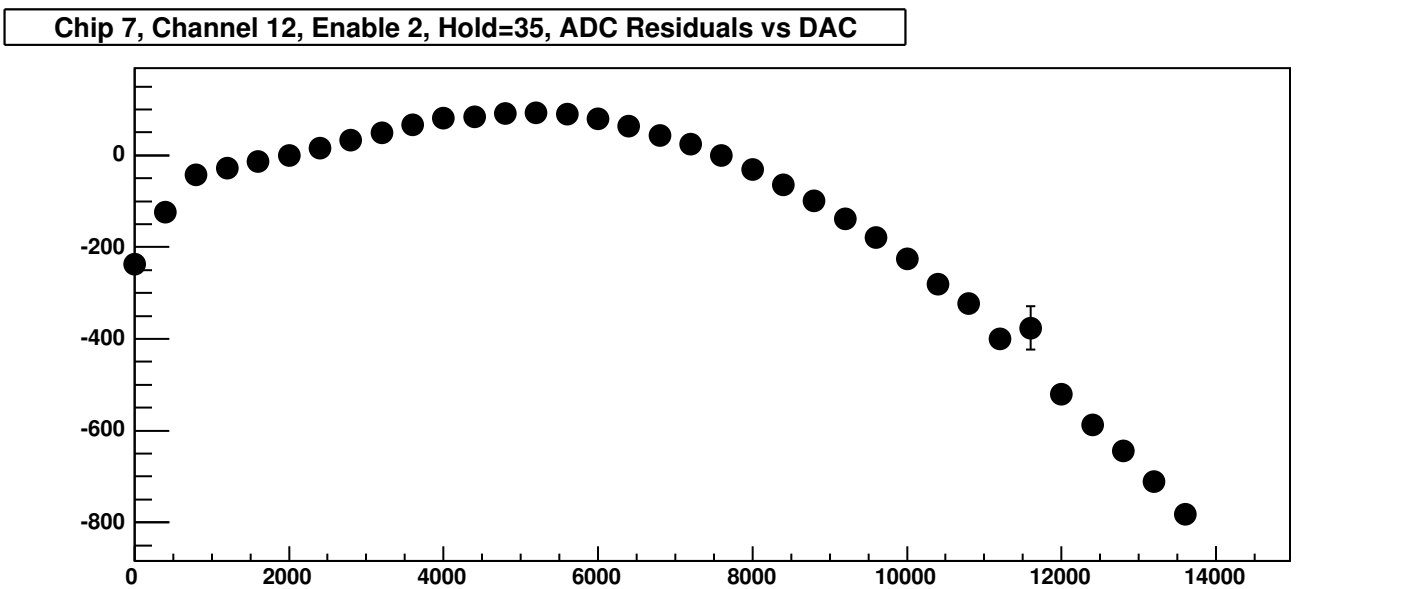

Chip 7, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 23.75 / 23
p0 -306.4 ± 0.7992
p1 0.009634 ± 0.0001216
-160
-180
-200
-220
-240
-260
-280
-300
0
2000
4000
6000
8000
10000
12000
14000

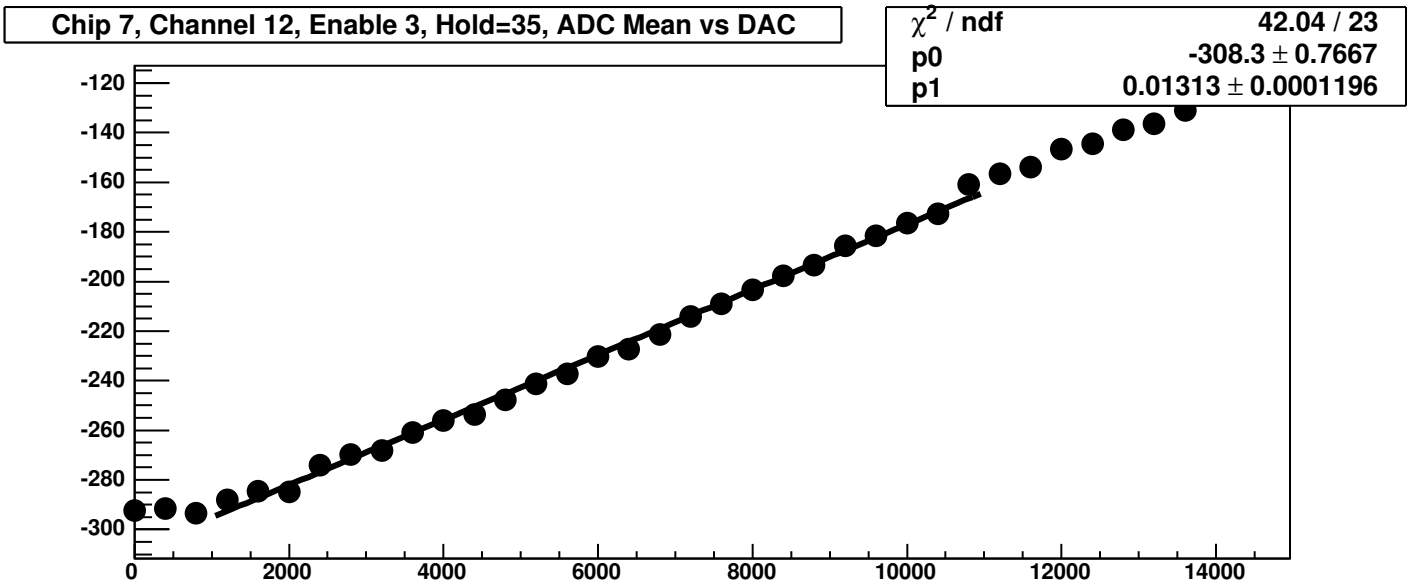

Chip 7, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

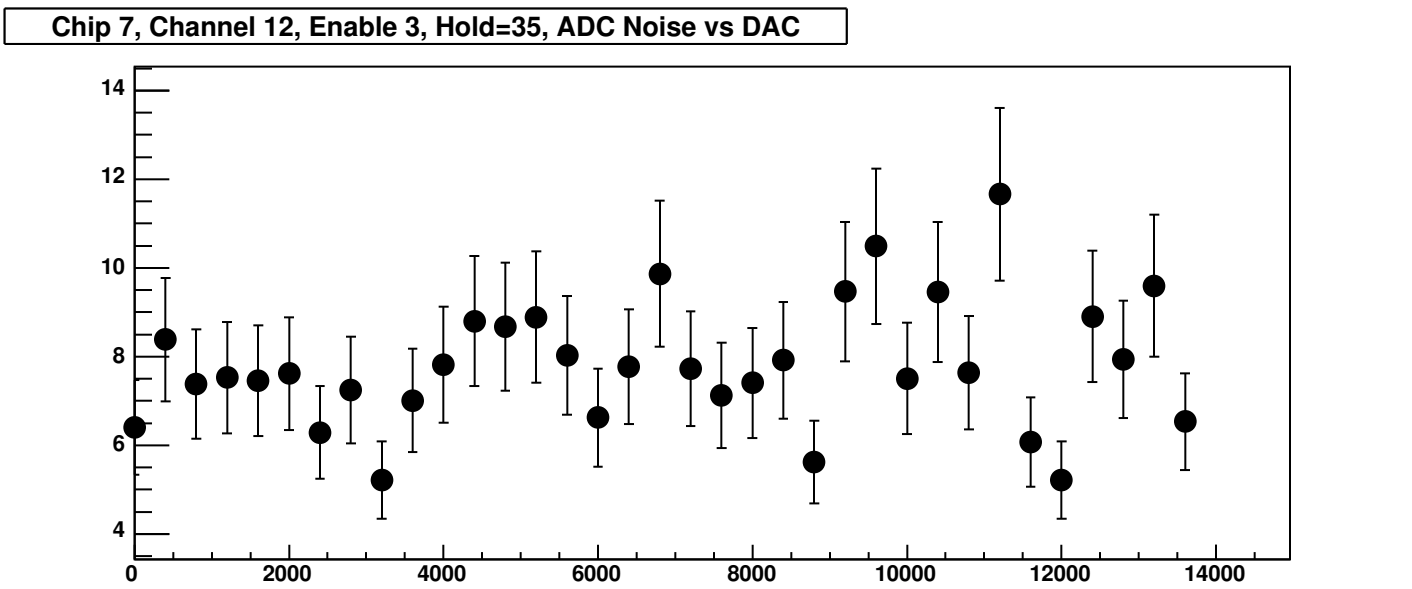

Chip 7, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

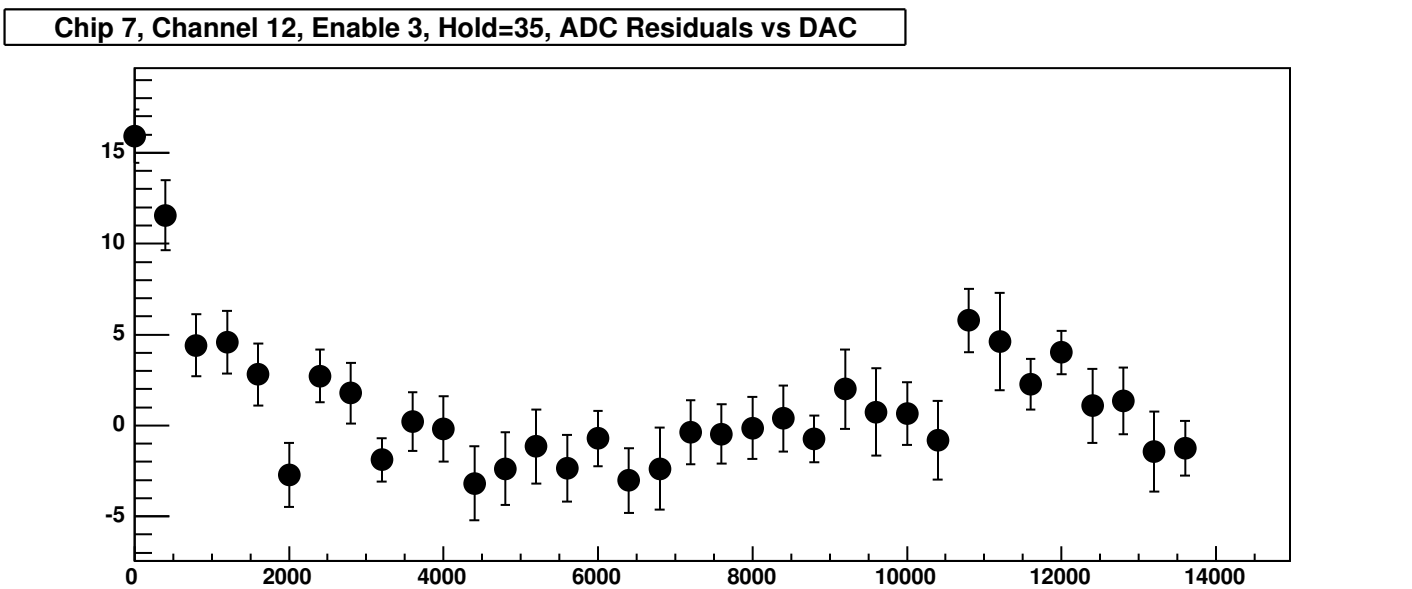

Chip 7, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 3164 / 23
p0 -225.2 ± 1.386
p1 -0.2954 ± 0.0004715
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000

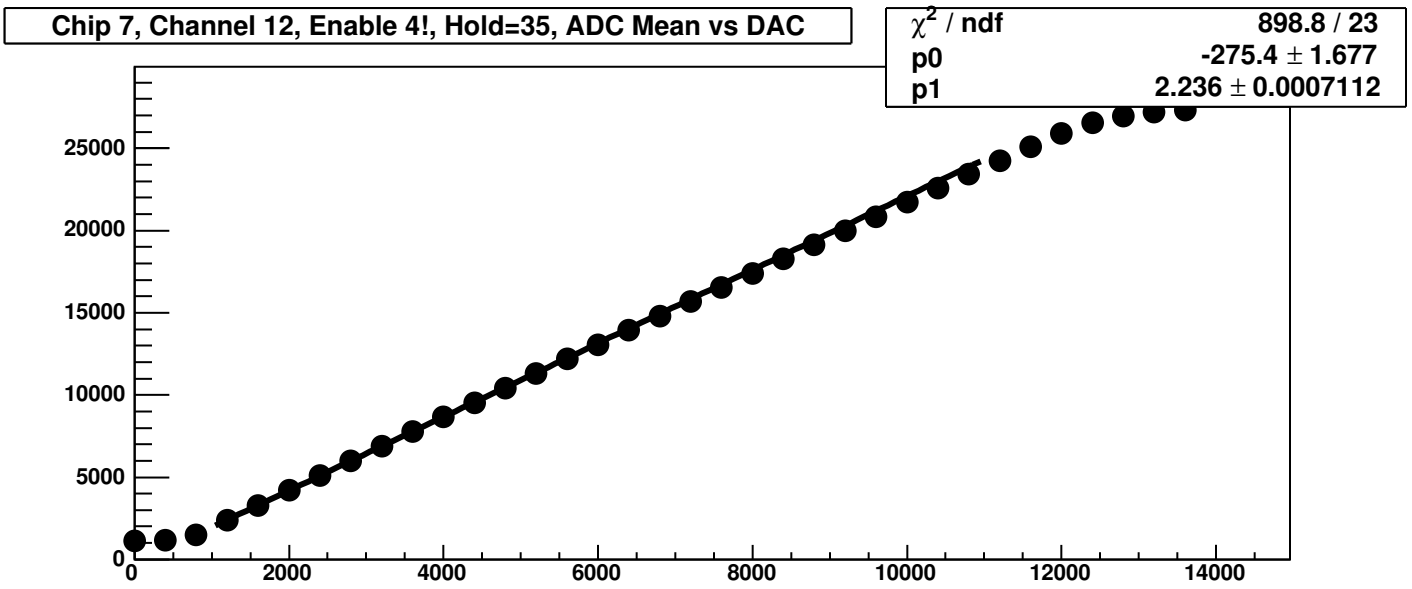

Chip 7, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

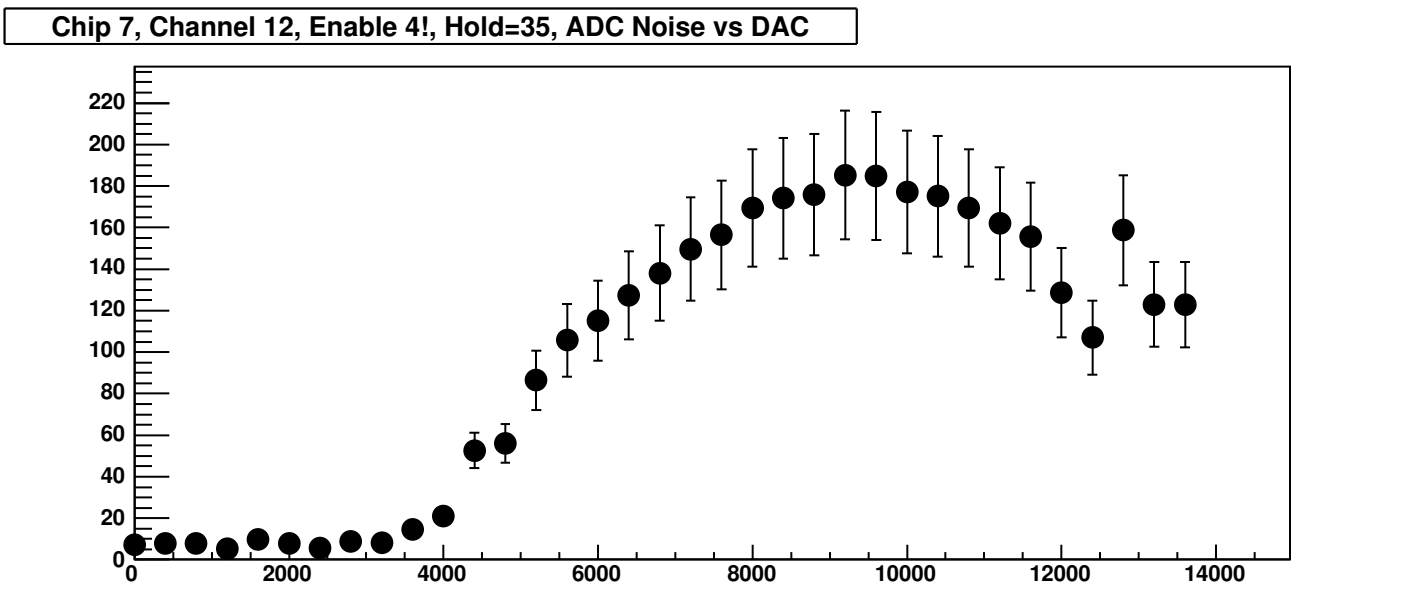

Chip 7, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

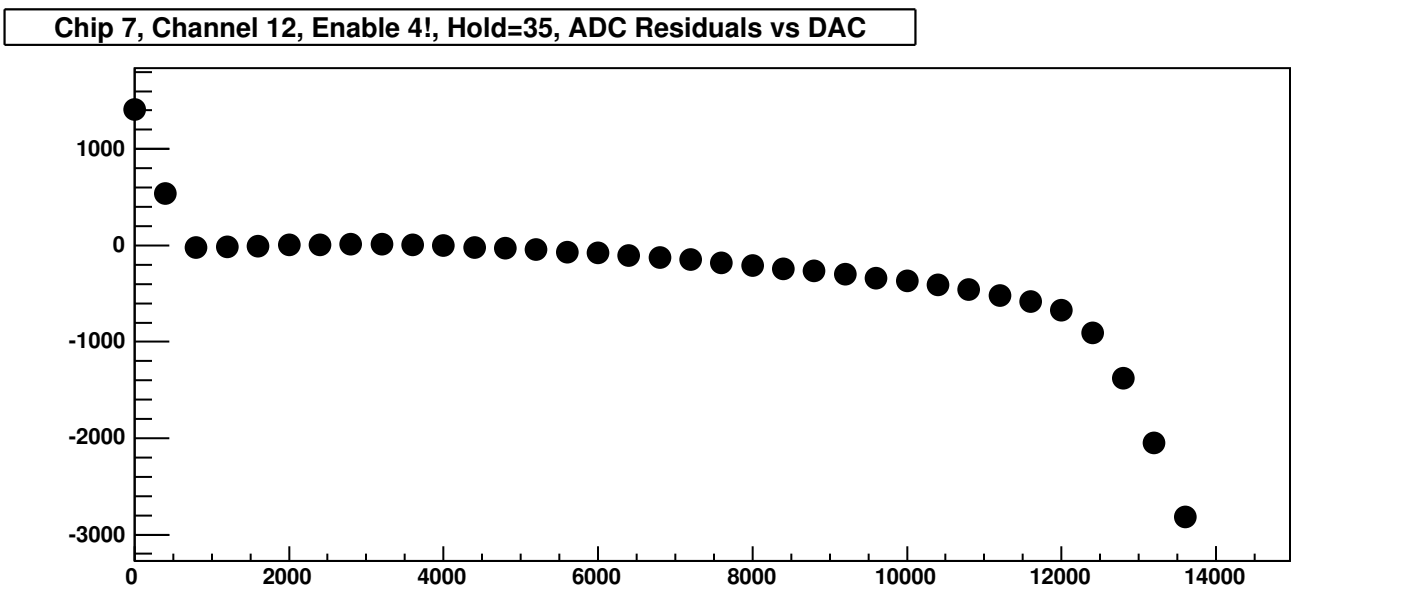

Chip 7, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 42.04 / 23
p0 -308.3 ± 0.7667
p1 0.01313 ± 0.0001196
-120
-140
-160
-180
-200
-220
-240
-260
-280
-300
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

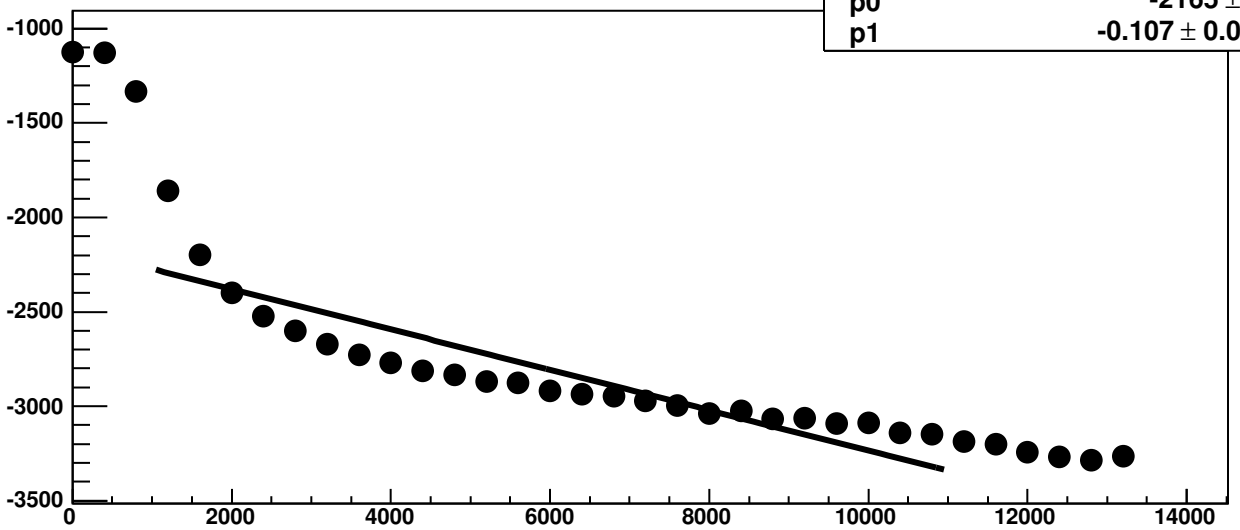

Chip 7, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 898.8 / 23
p0 -275.4 ± 1.677
p1 2.236 ± 0.0007112
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

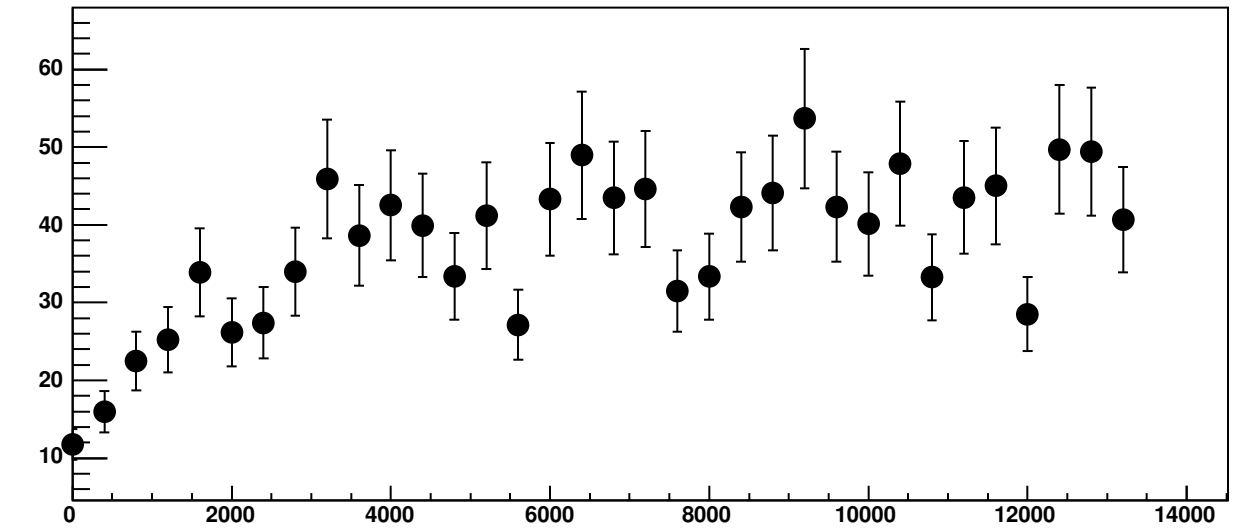

Chip 7, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

Chip 7, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

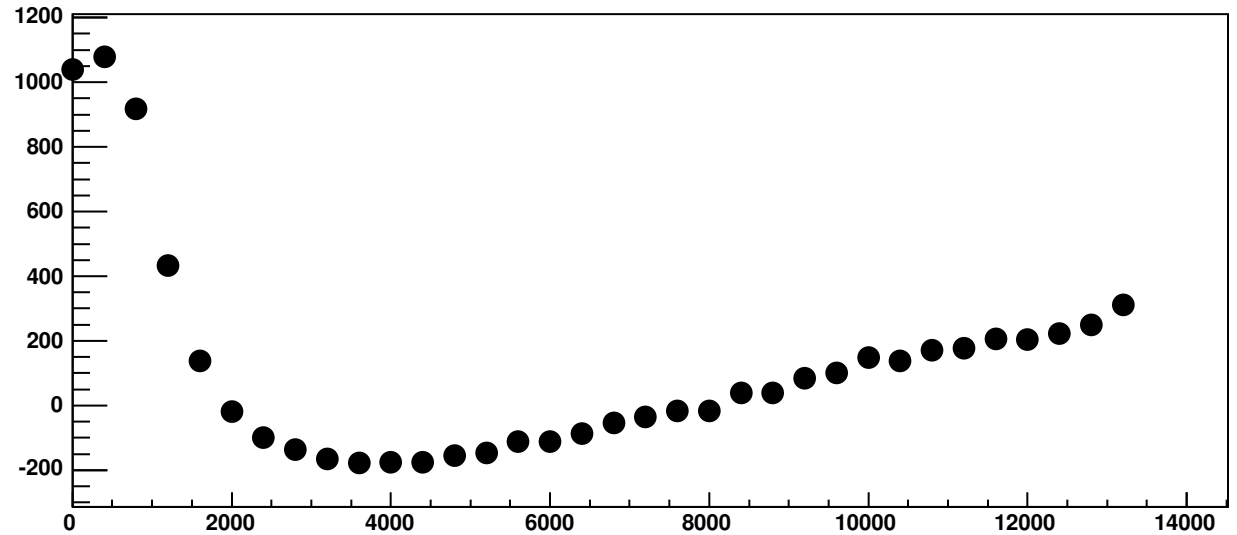
χ^2 / ndf	1.016e+04 / 23
p0	-2165 ± 3.386
p1	-0.107 ± 0.000553

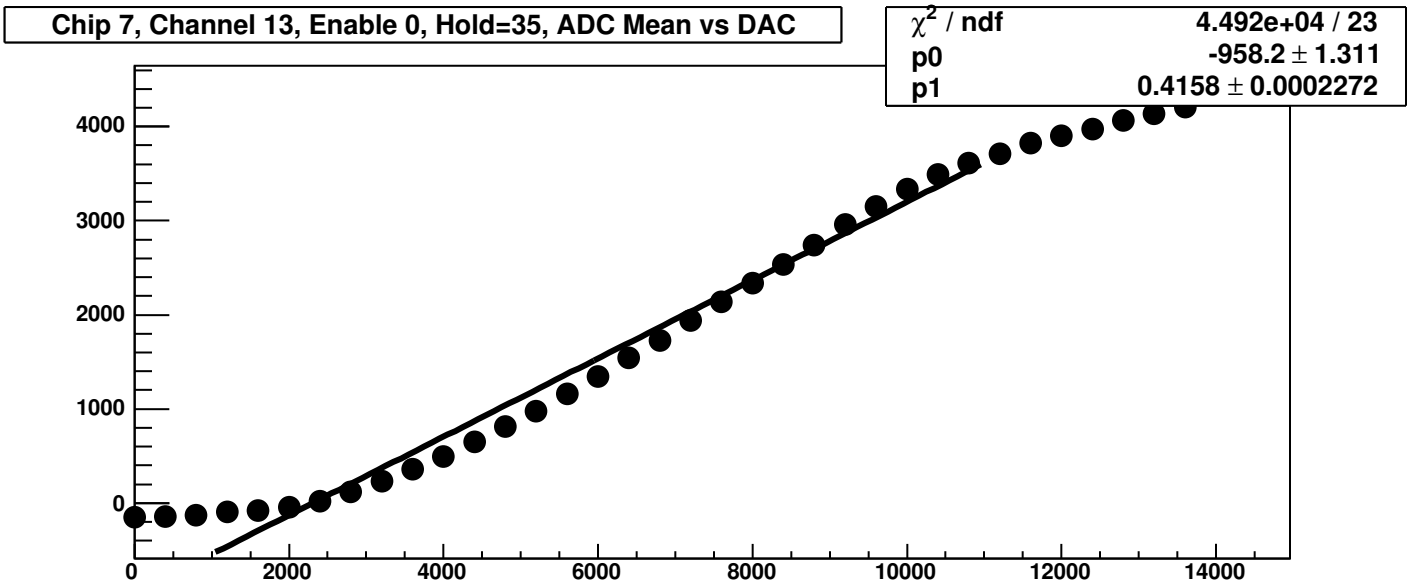


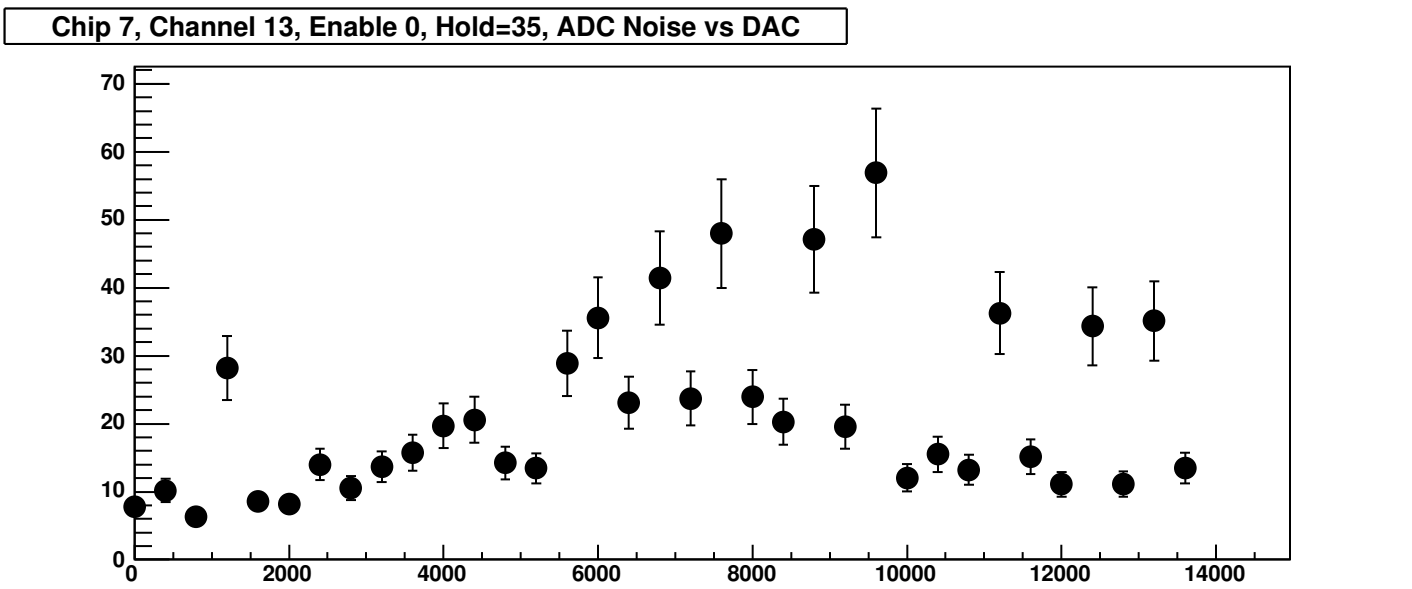
Chip 7, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

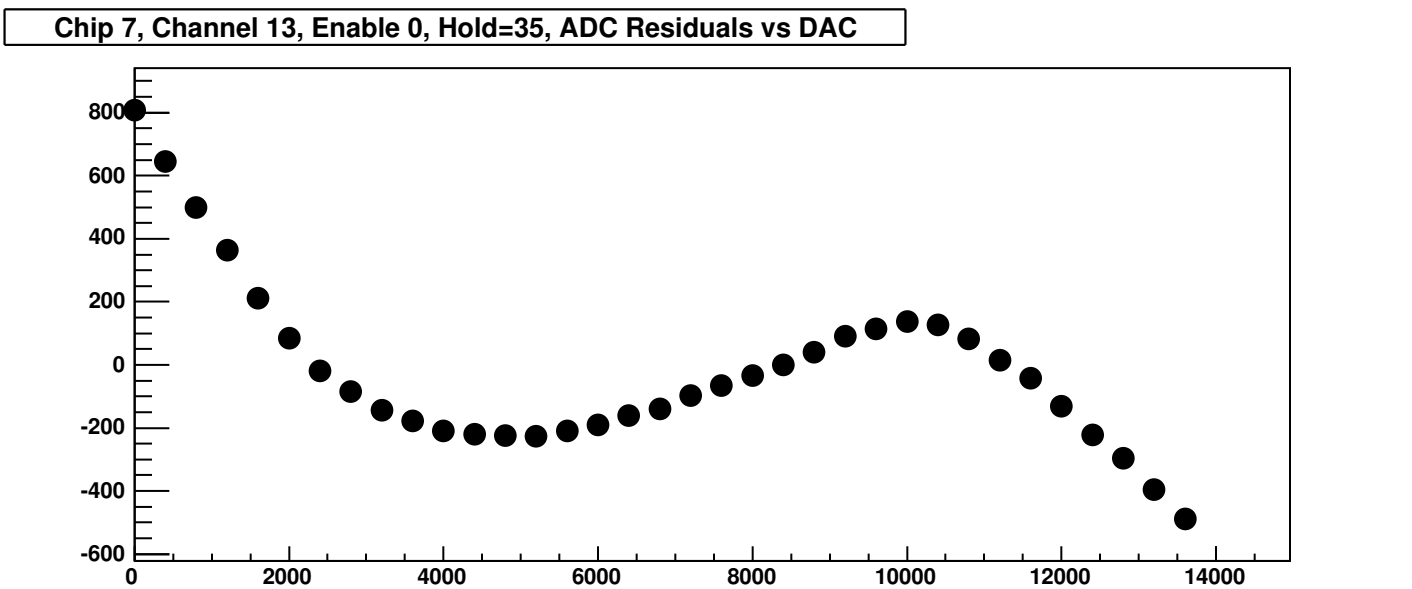


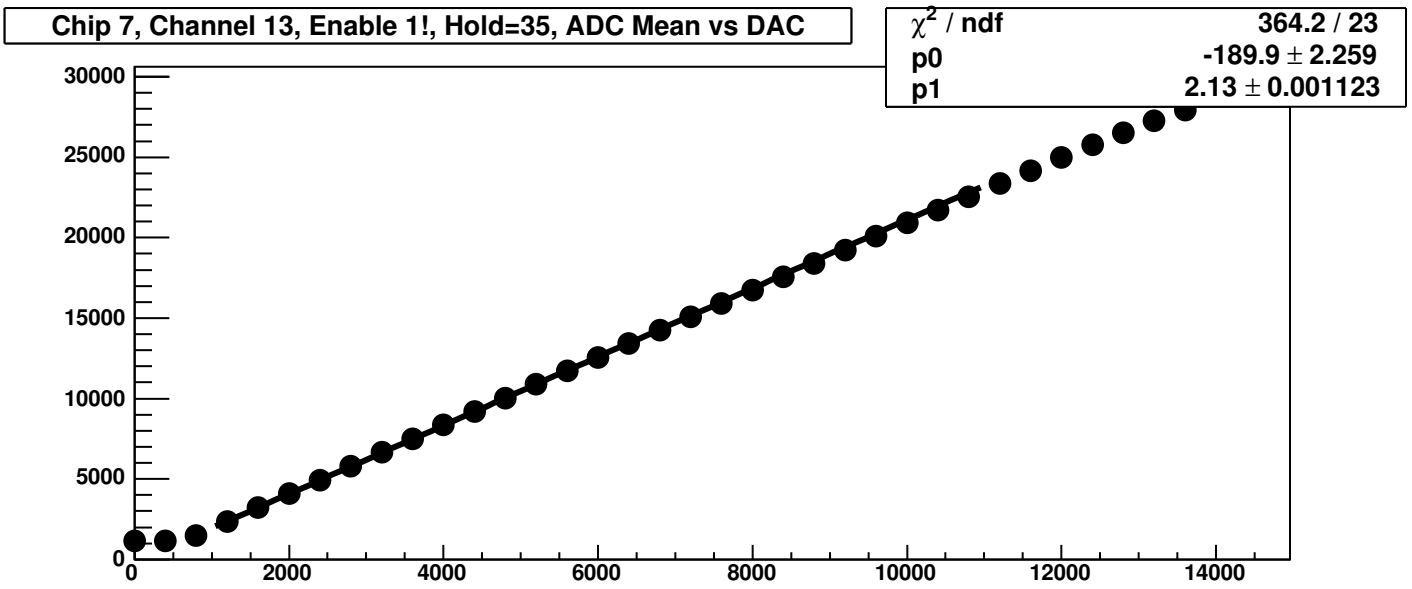
Chip 7, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

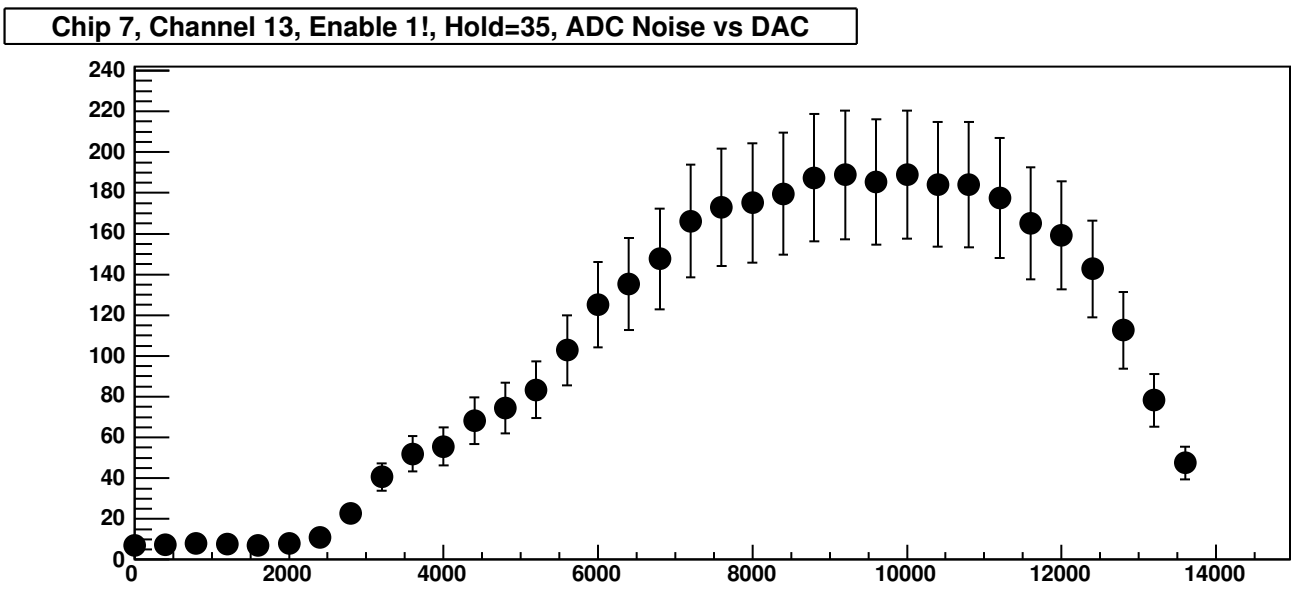


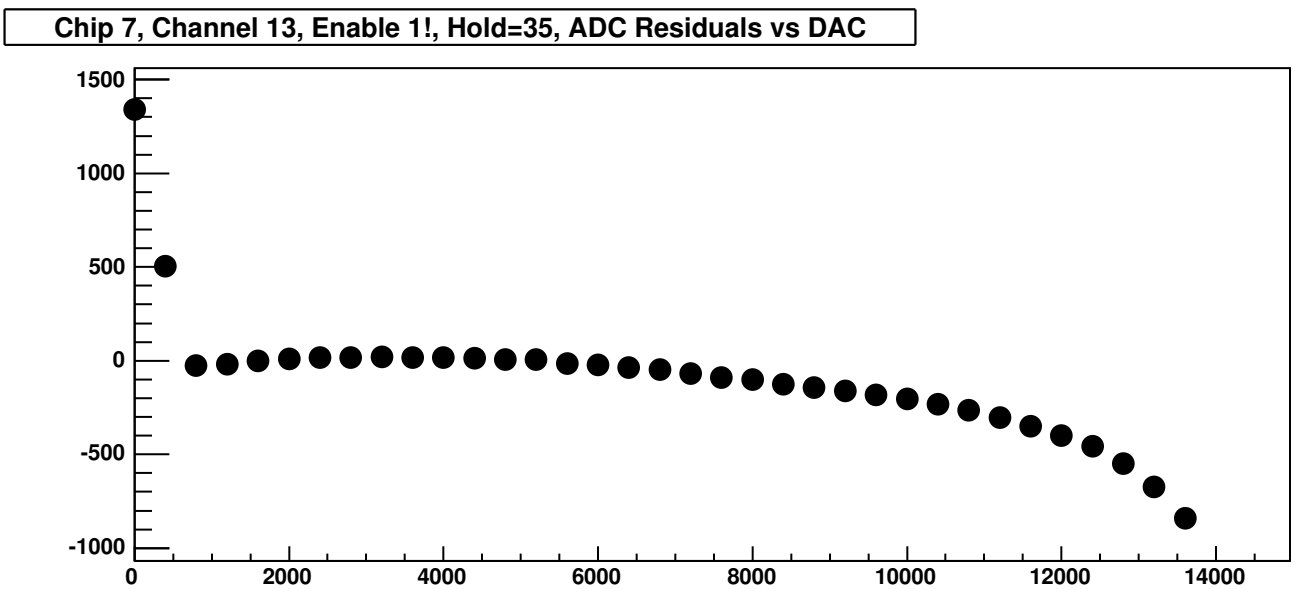

Chip 7, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.492e+04 / 23
p0 -958.2 ± 1.311
p1 0.4158 ± 0.0002272
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

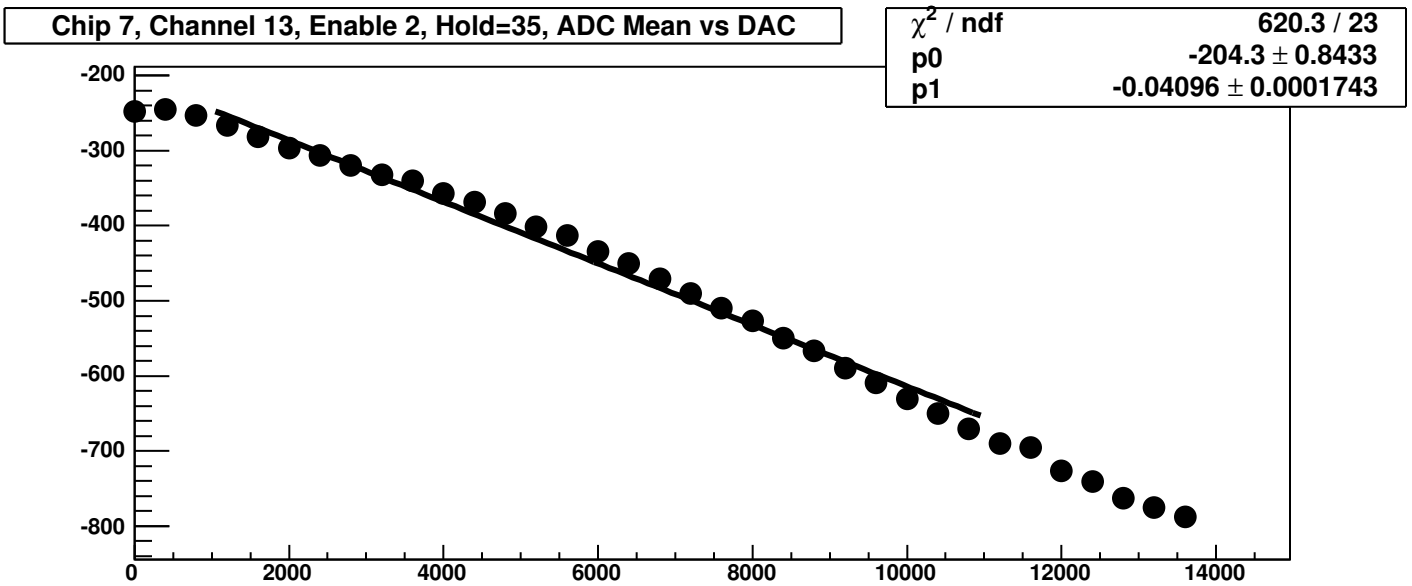

Chip 7, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

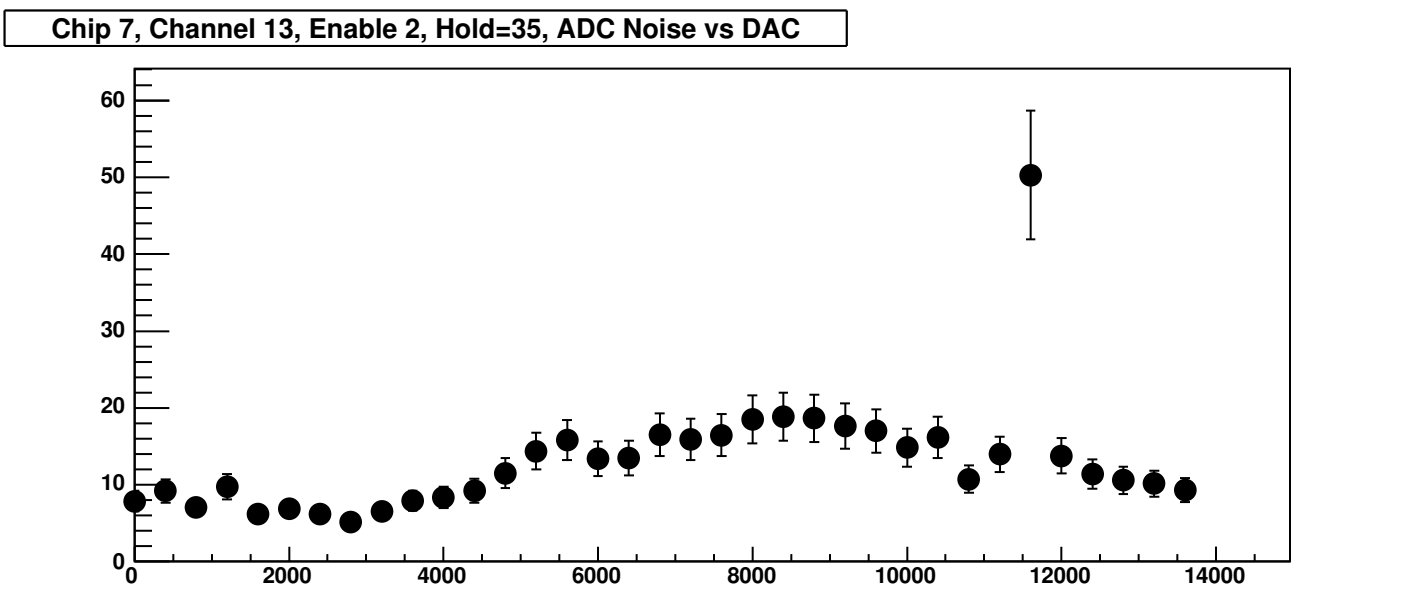

Chip 7, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

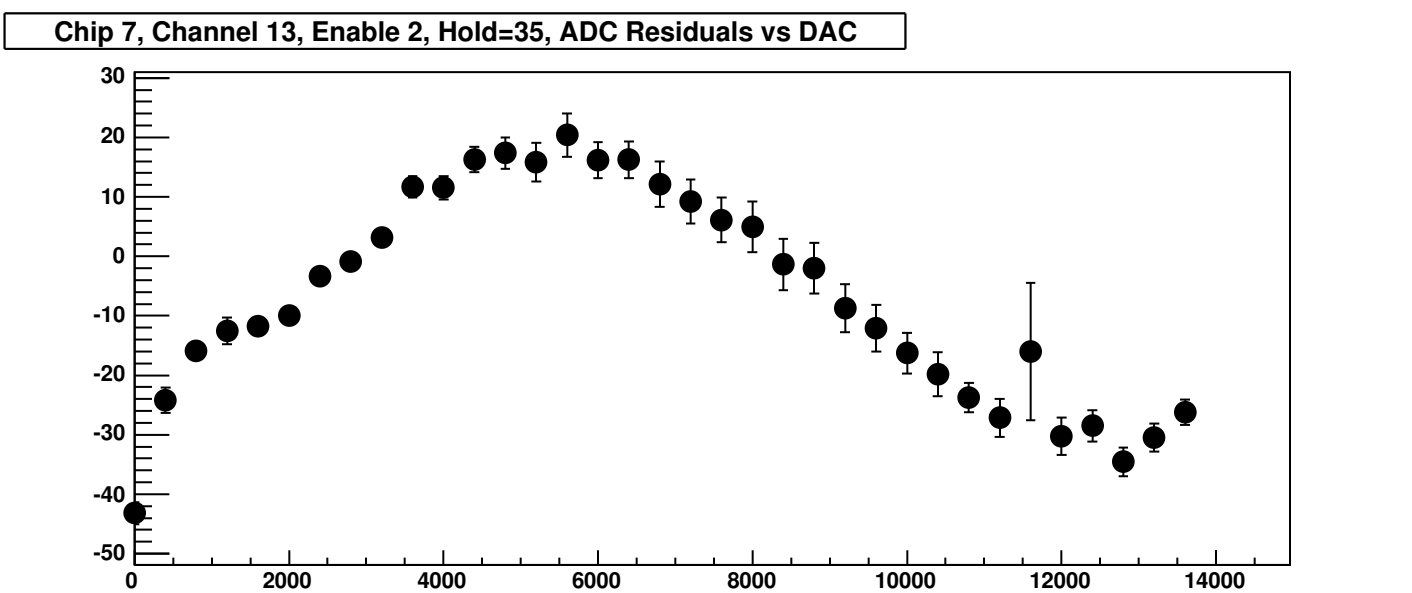

Chip 7, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 364.2 / 23
p0 -189.9 ± 2.259
p1 2.13 ± 0.001123
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

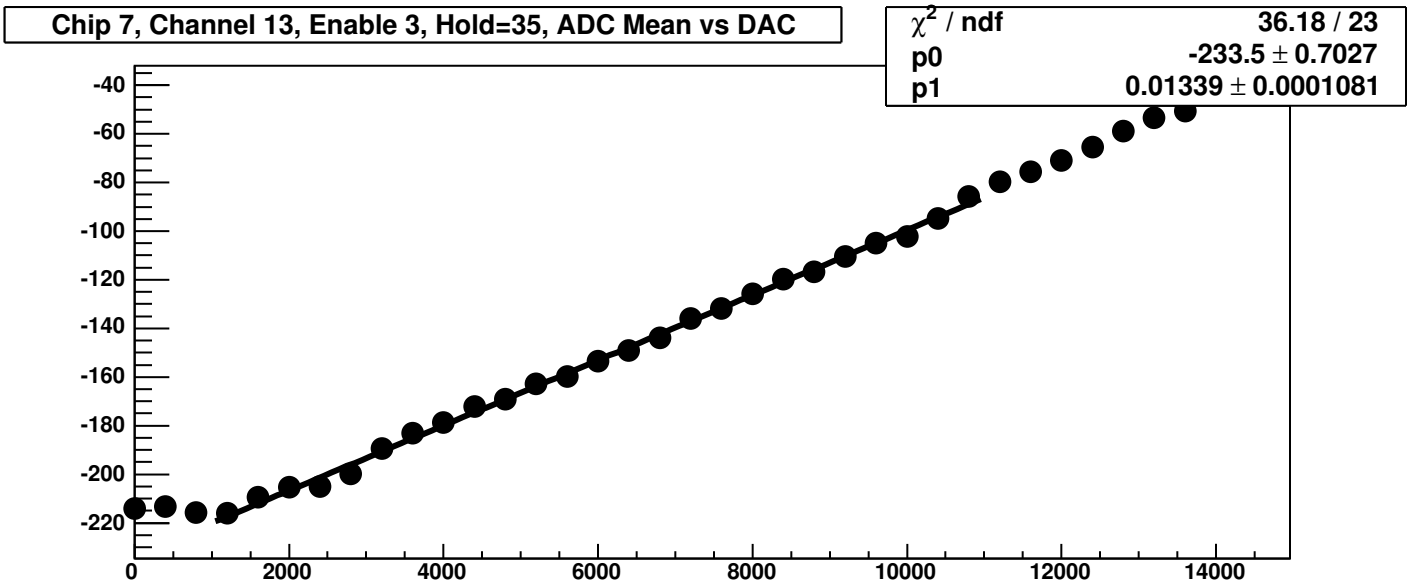

Chip 7, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
240
220
200
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

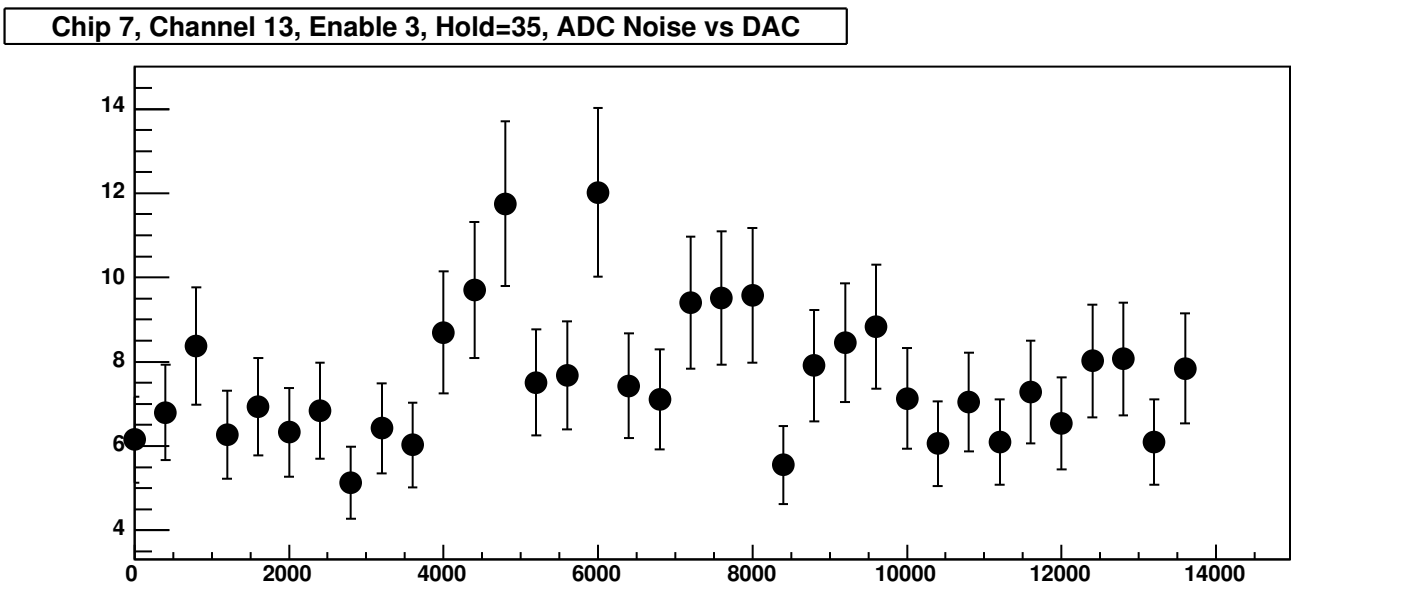

Chip 7, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

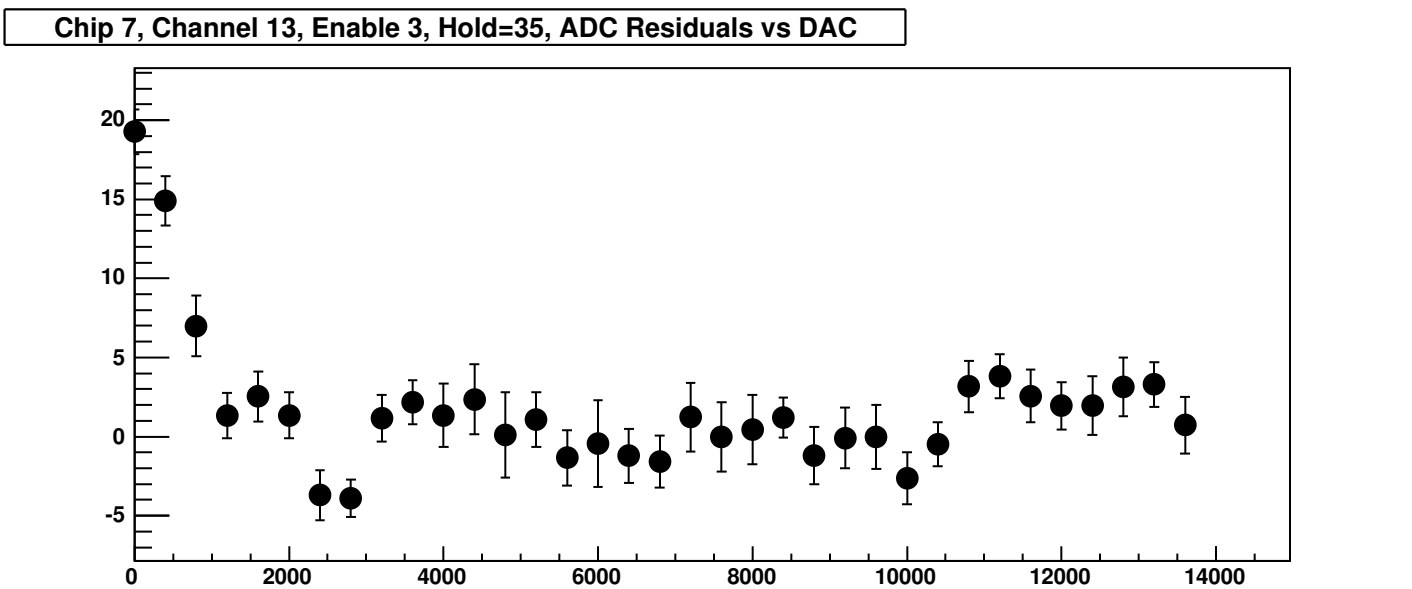

Chip 7, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 620.3 / 23
p0 -204.3 ± 0.8433
p1 -0.04096 ± 0.0001743
-200
-300
-400
-500
-600
-700
-800
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

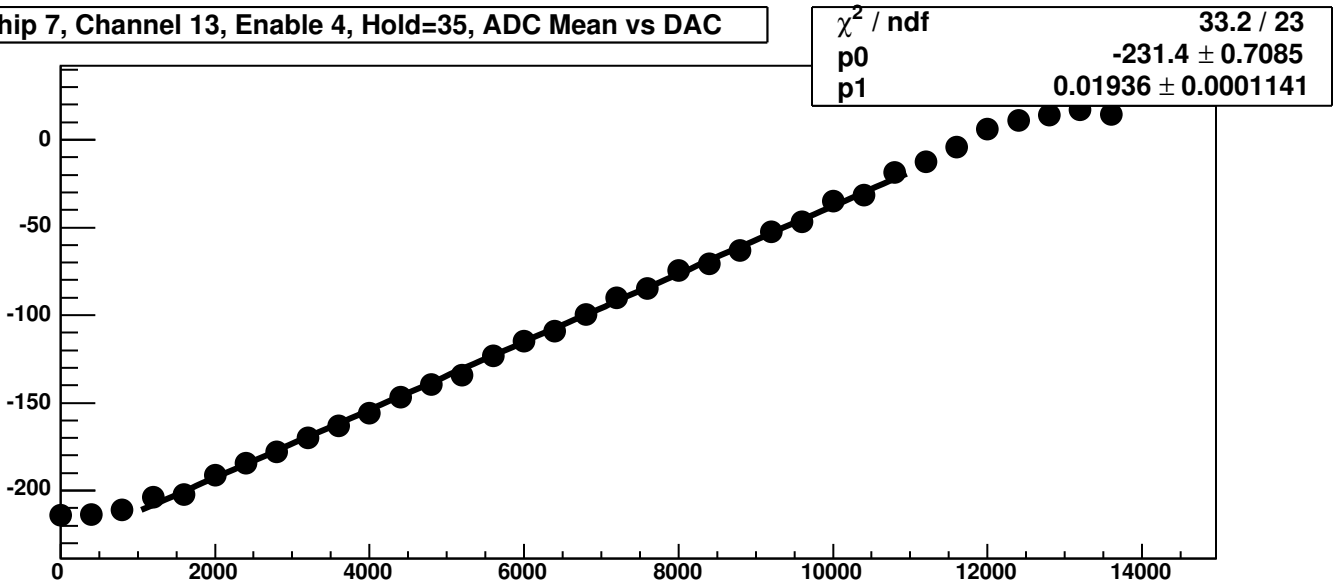

Chip 7, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.18 / 23
p0 -233.5 ± 0.7027
p1 0.01339 ± 0.0001081
-40
-60
-80
-100
-120
-140
-160
-180
-200
-220
0
2000
4000
6000
8000
10000
12000
14000

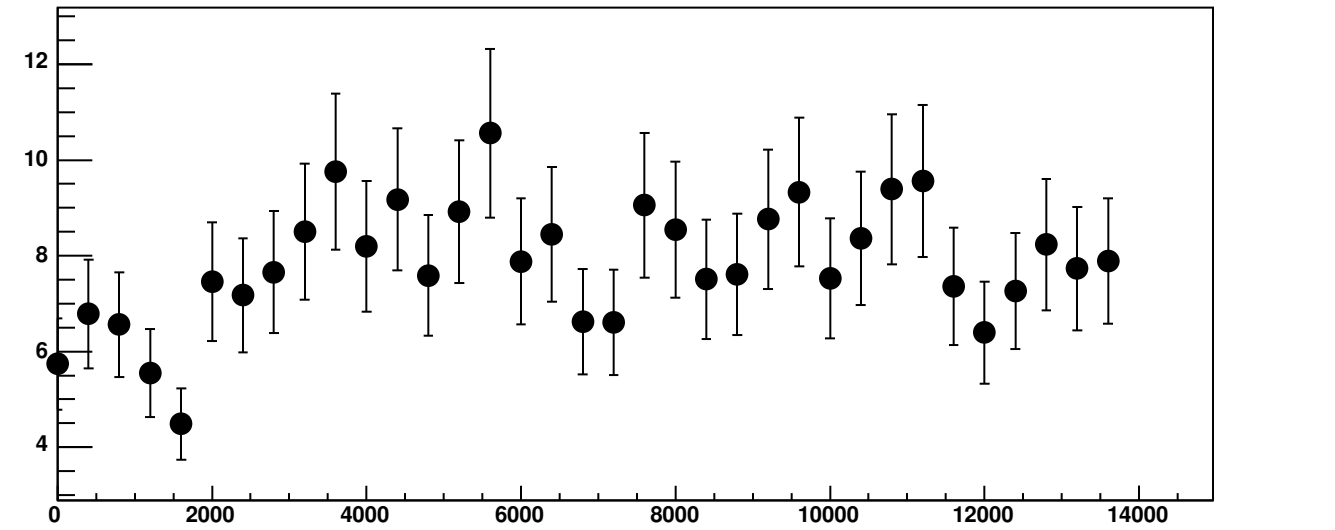

Chip 7, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

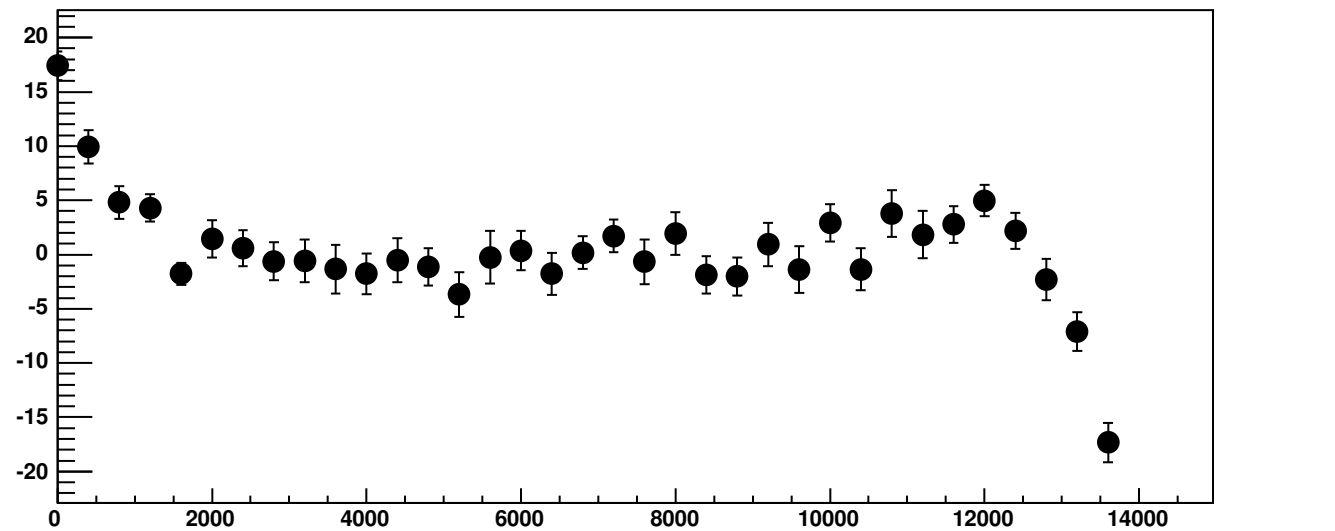
Chip 7, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC

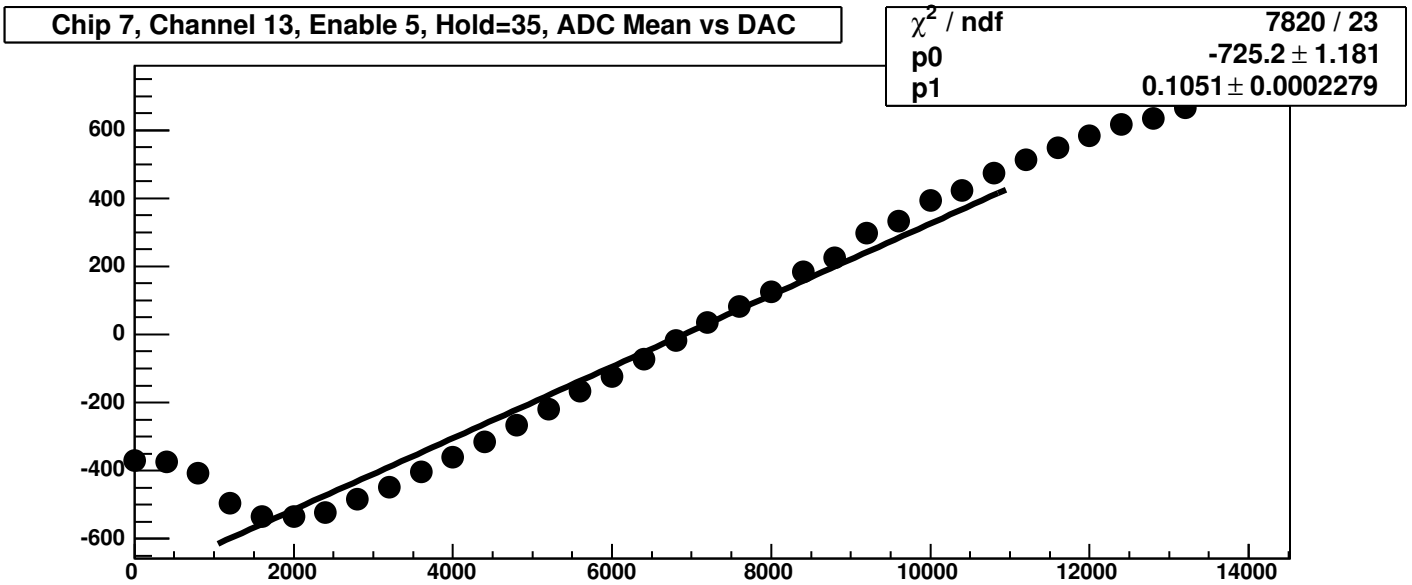


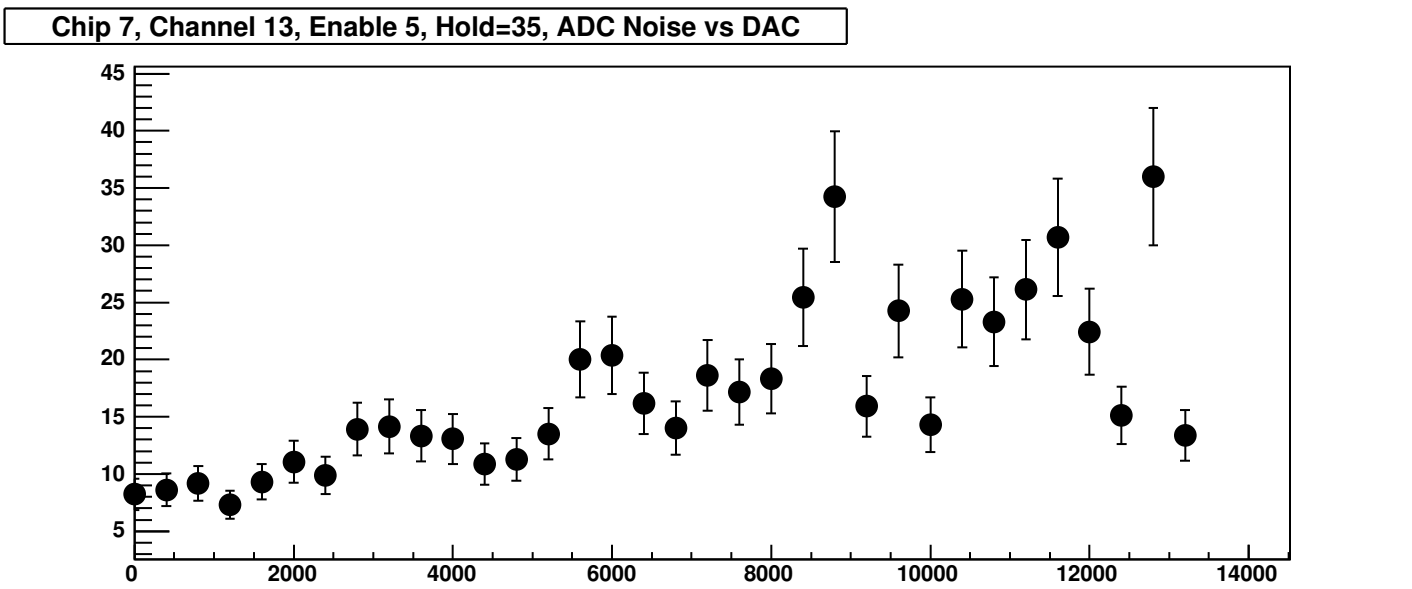
Chip 7, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC

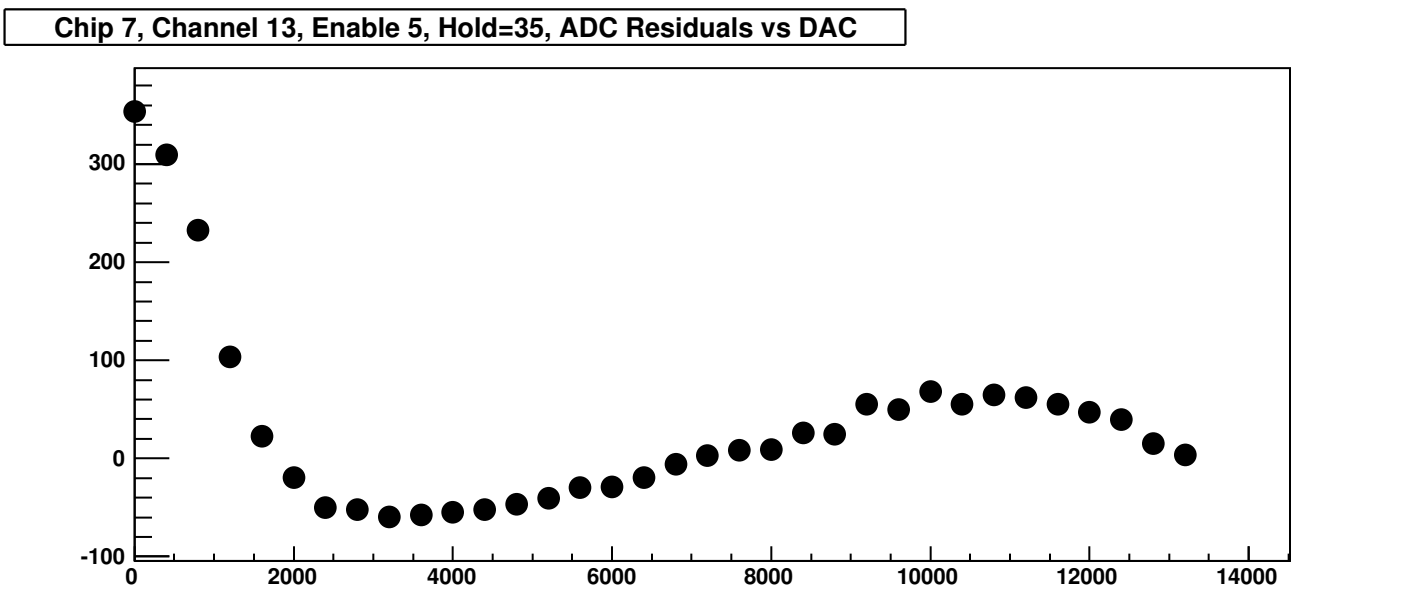


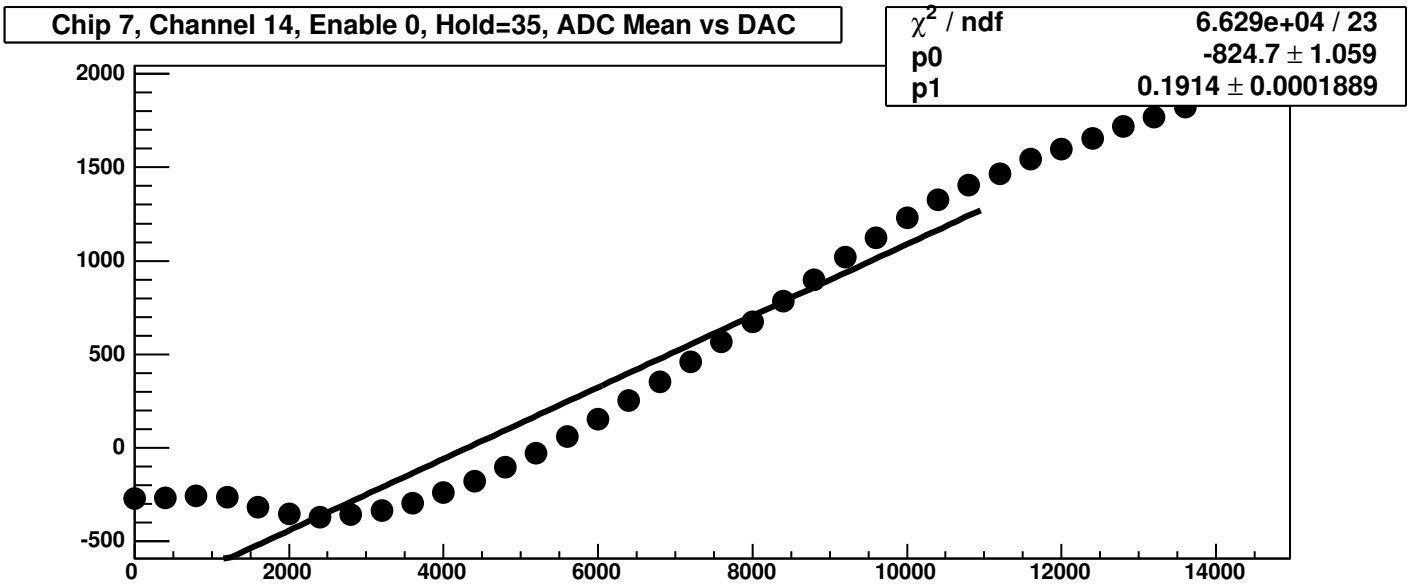
Chip 7, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC

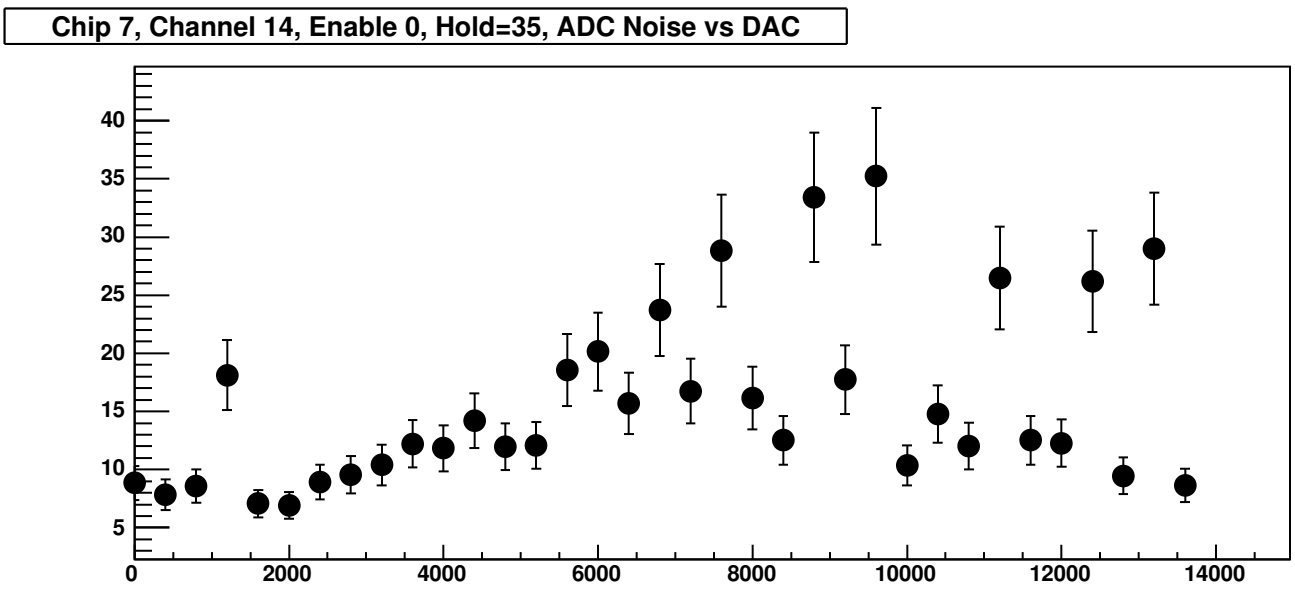


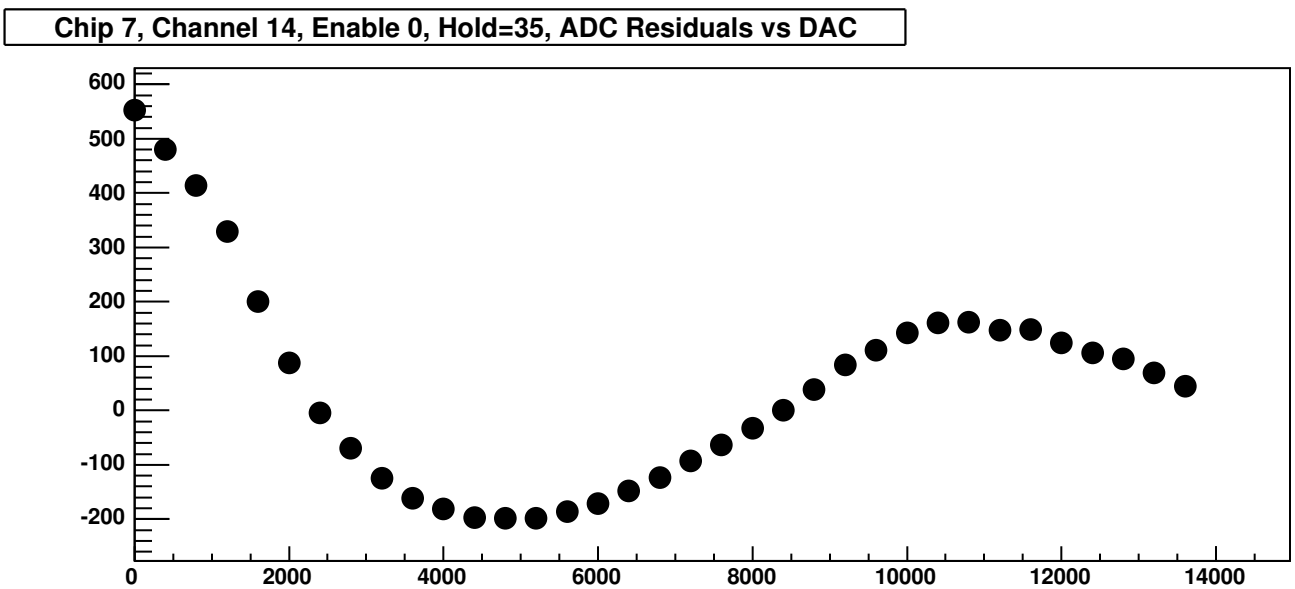

Chip 7, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ^2 / ndf 7820 / 23
p0 -725.2 ± 1.181
p1 0.1051 ± 0.0002279
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

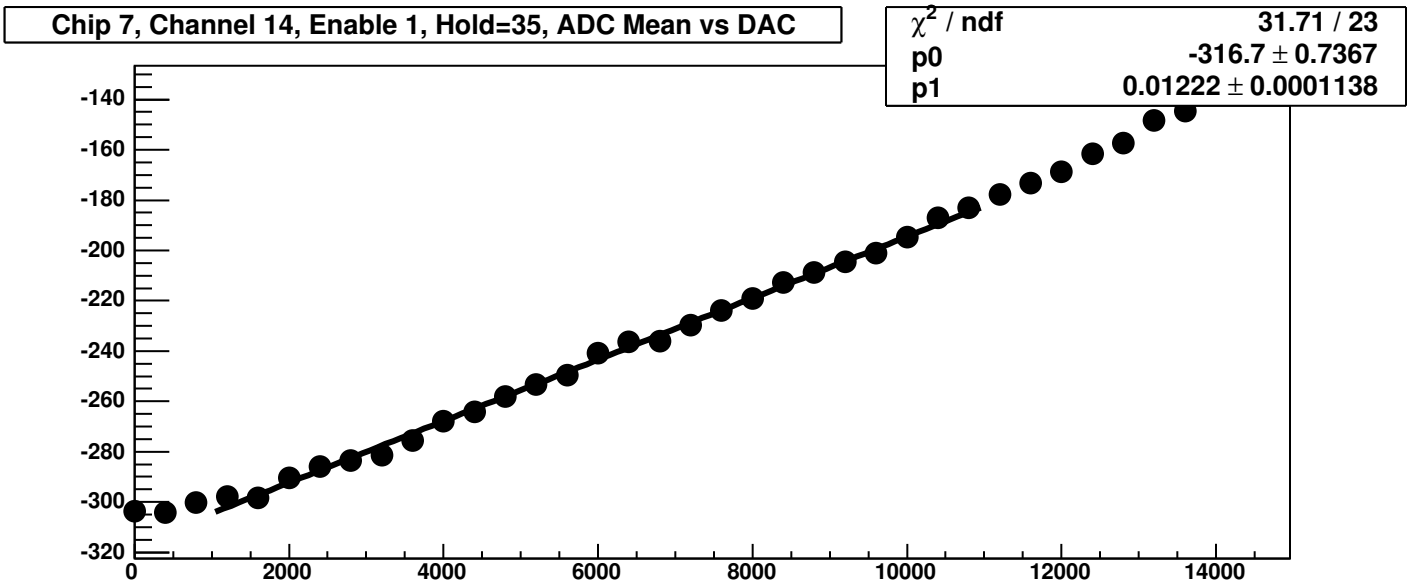

Chip 7, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

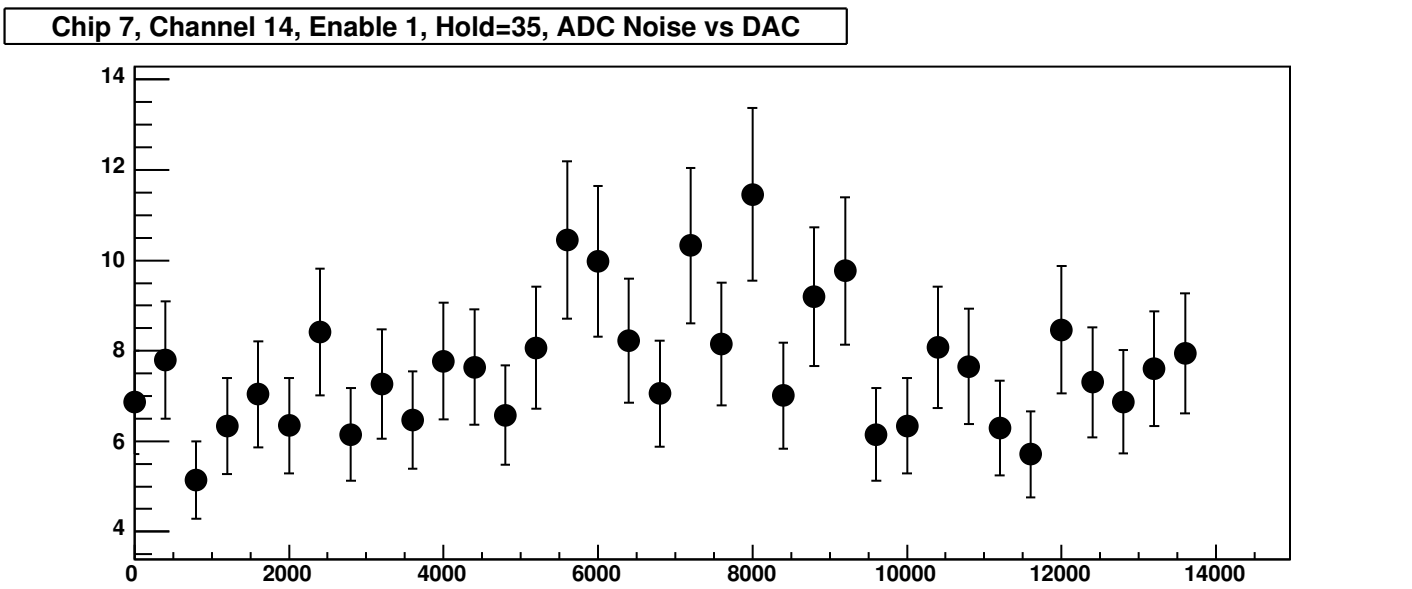

Chip 7, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

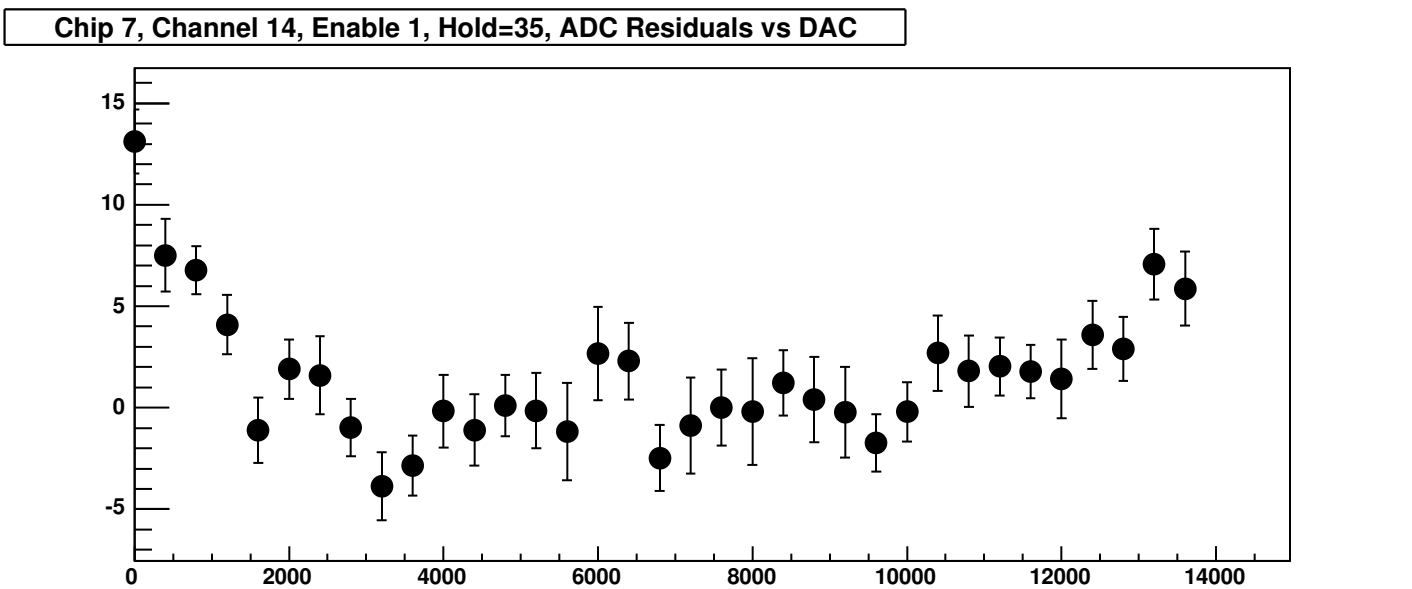

Chip 7, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 6.629e+04 / 23
p0 -824.7 ± 1.059
p1 0.1914 ± 0.0001889
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

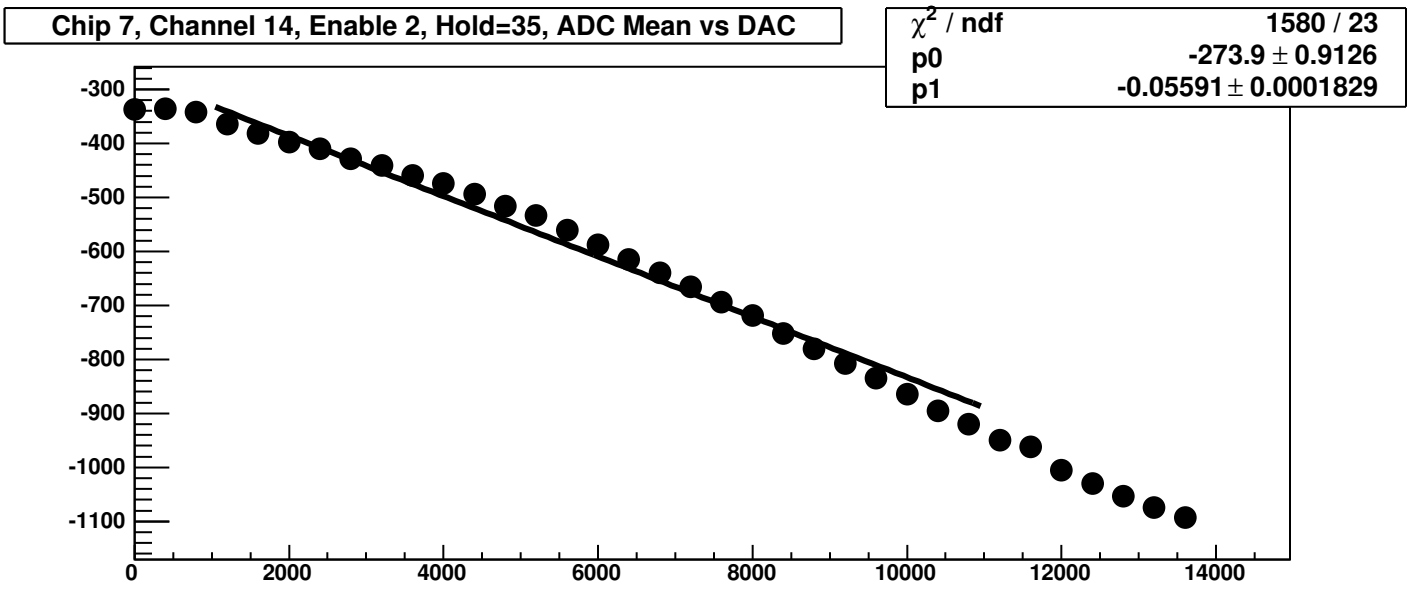

Chip 7, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

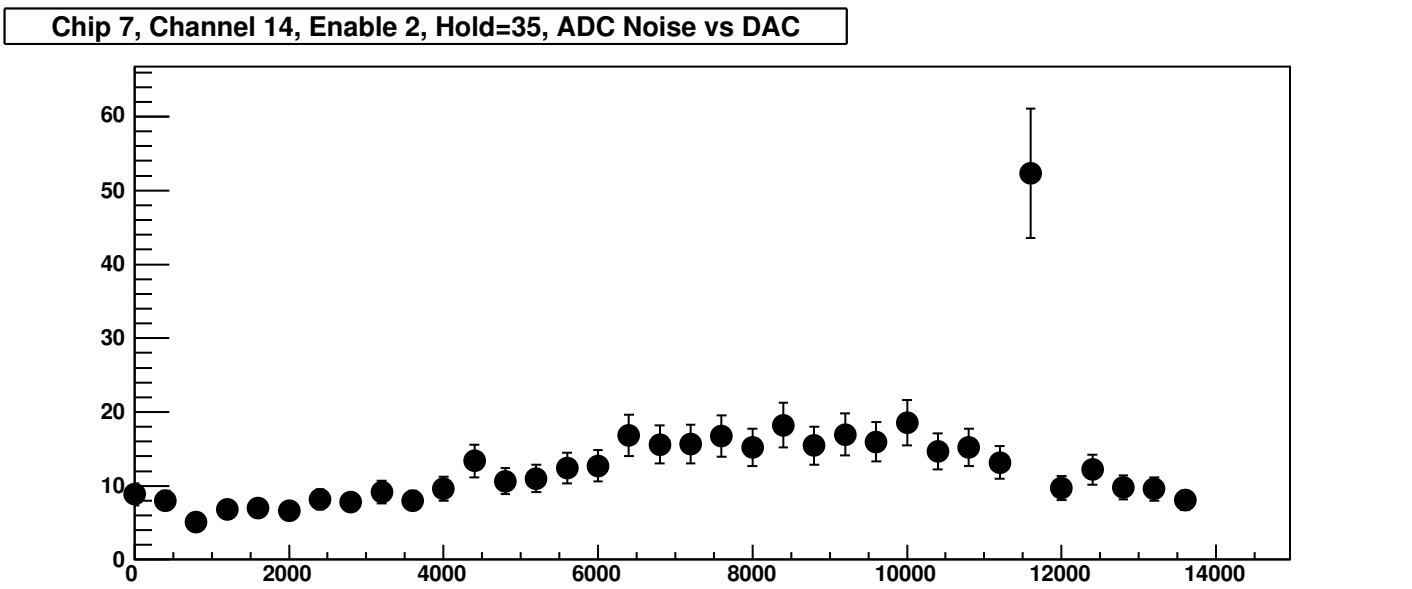

Chip 7, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

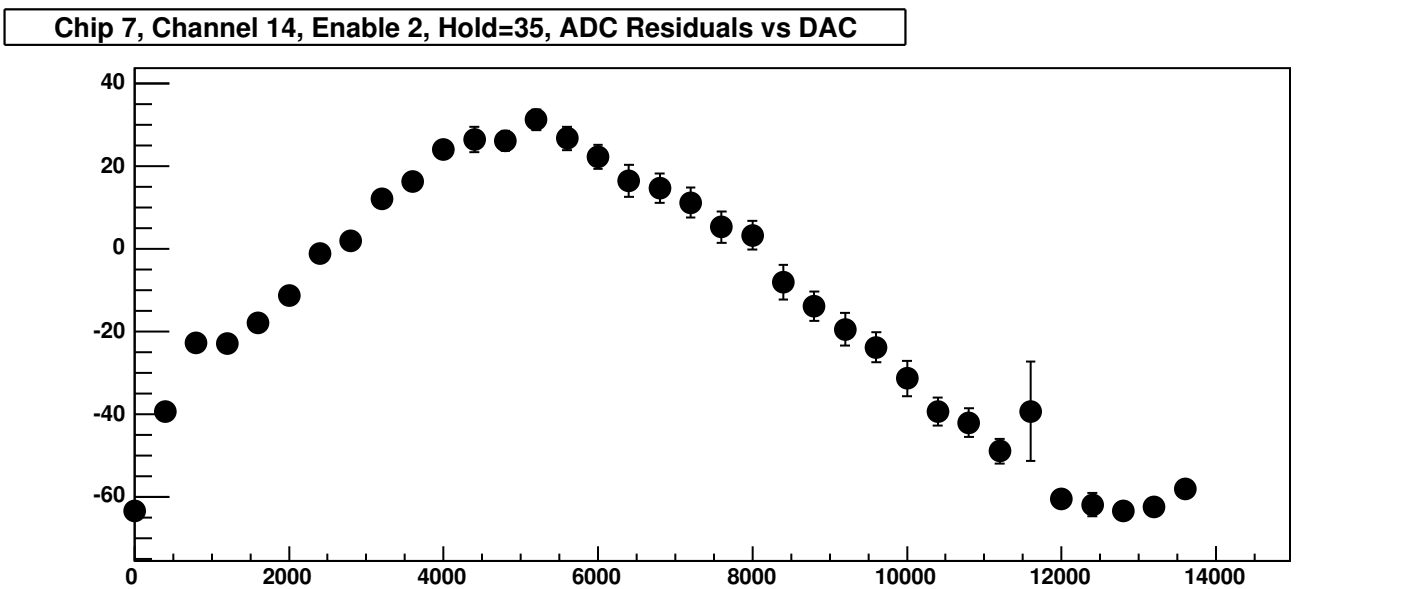

Chip 7, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.71 / 23
p0 -316.7 ± 0.7367
p1 0.01222 ± 0.0001138
-140
-160
-180
-200
-220
-240
-260
-280
-300
-320
0
2000
4000
6000
8000
10000
12000
14000

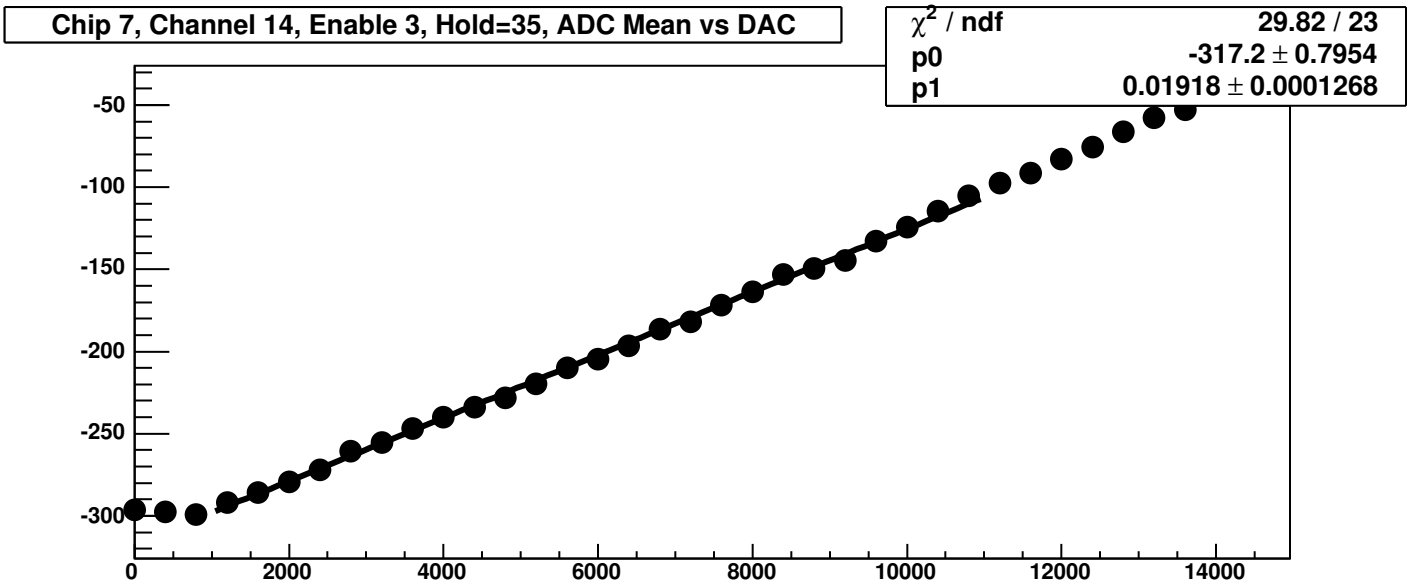

Chip 7, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

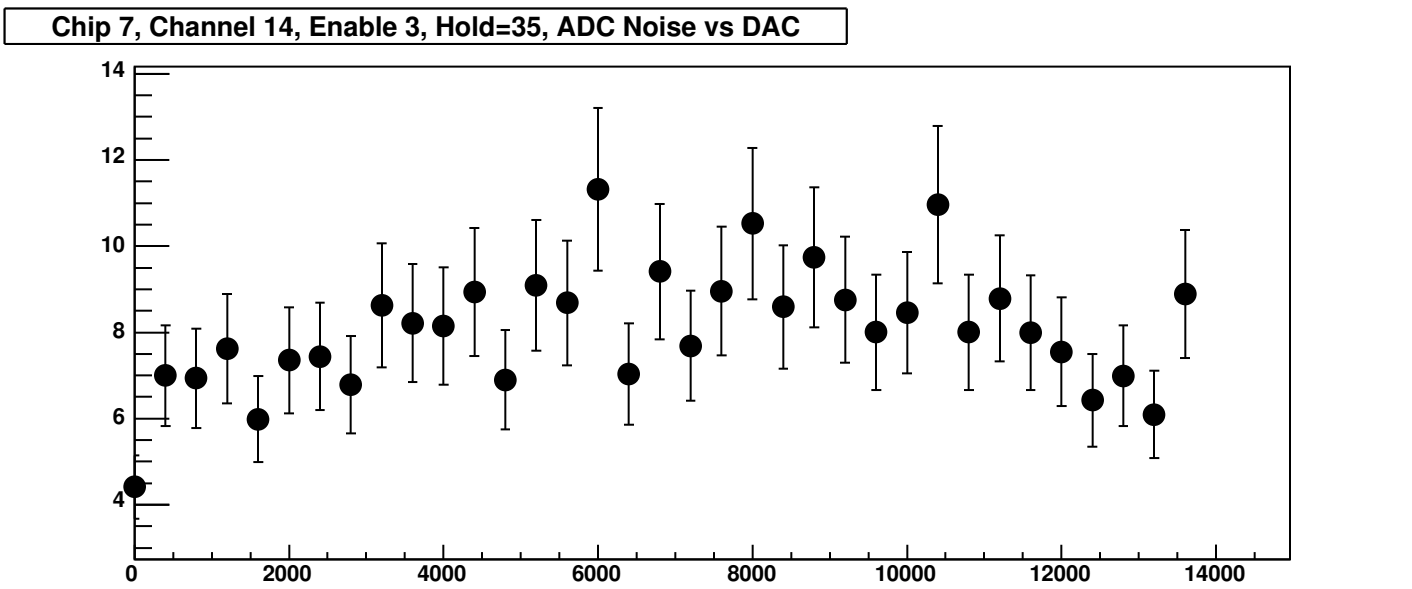

Chip 7, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

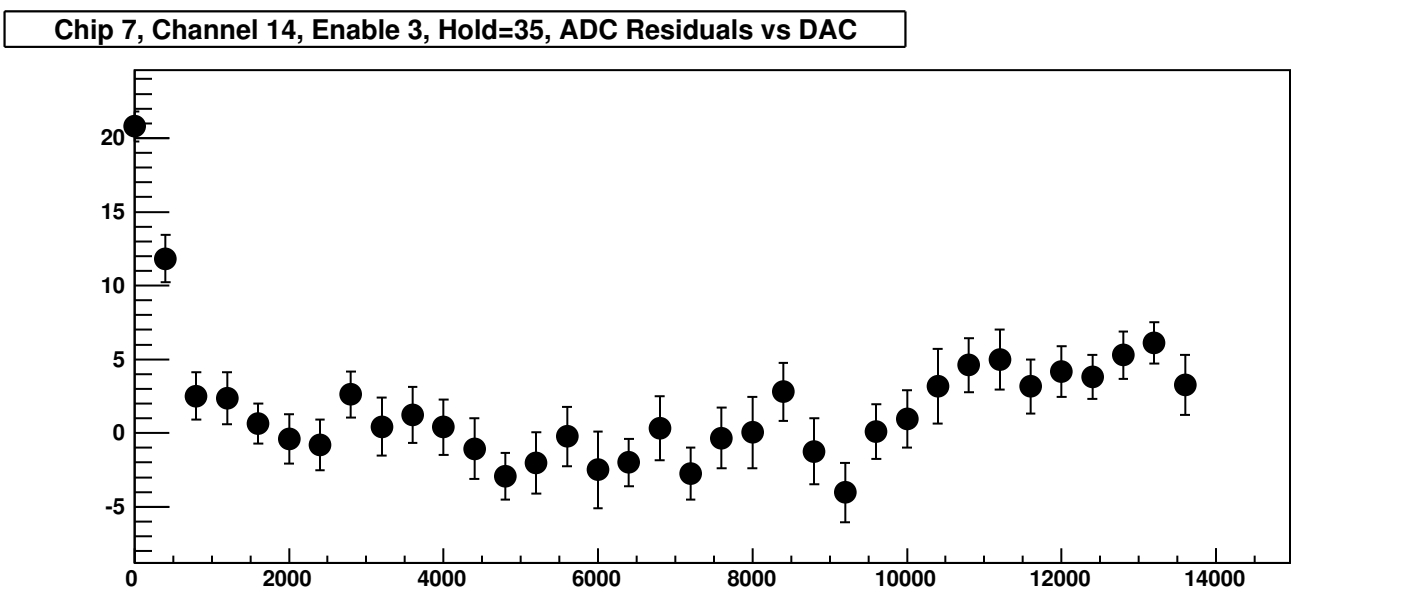

Chip 7, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1580 / 23
p0 -273.9 ± 0.9126
p1 -0.05591 ± 0.0001829
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
2000
4000
6000
8000
10000
12000
14000

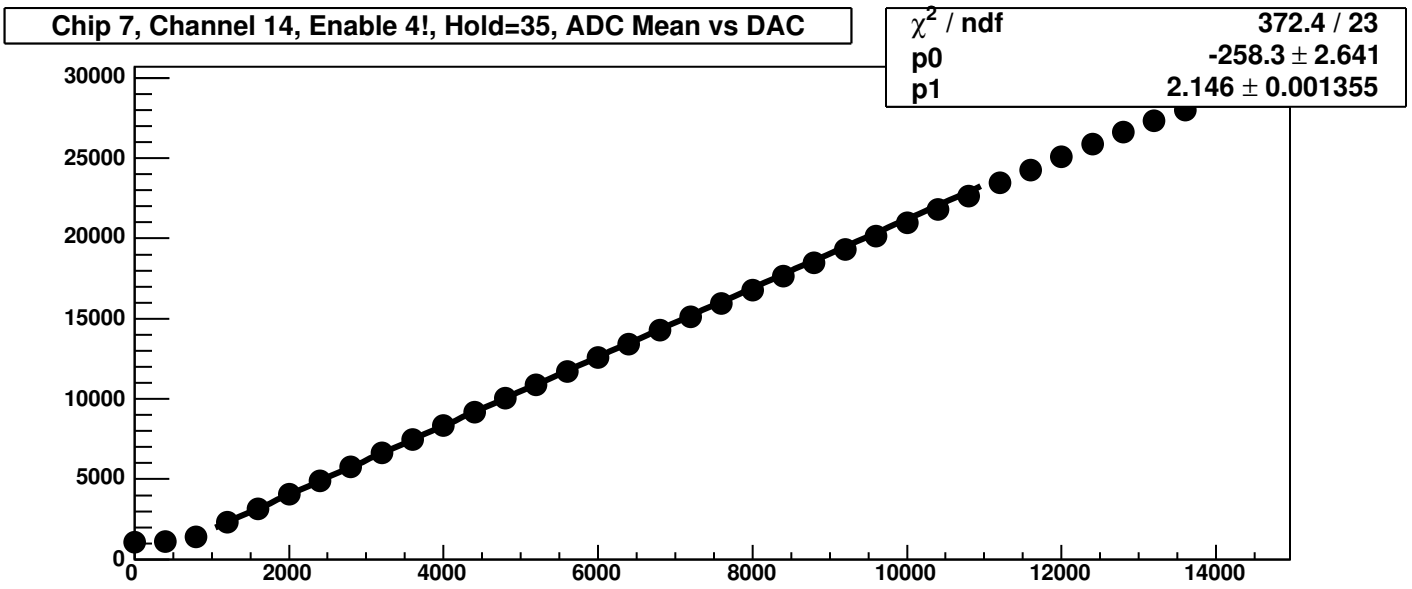

Chip 7, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

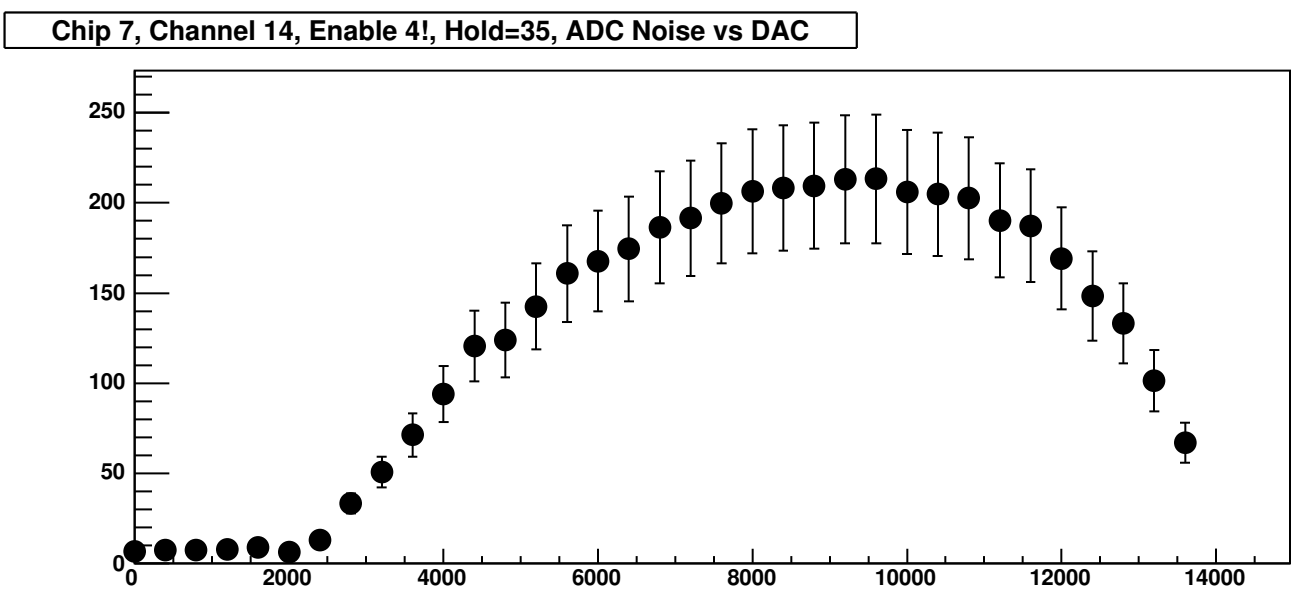

Chip 7, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
40
20
0
-20
-40
-60
0
2000
4000
6000
8000
10000
12000
14000

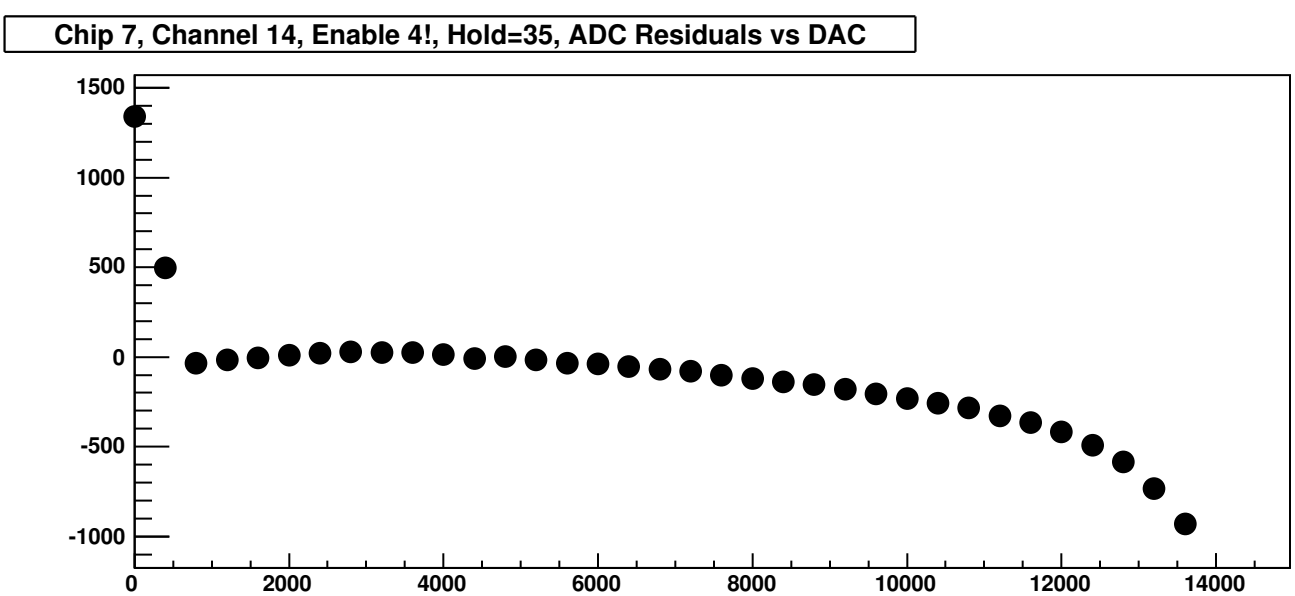

Chip 7, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 29.82 / 23
p0 -317.2 ± 0.7954
p1 0.01918 ± 0.0001268
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

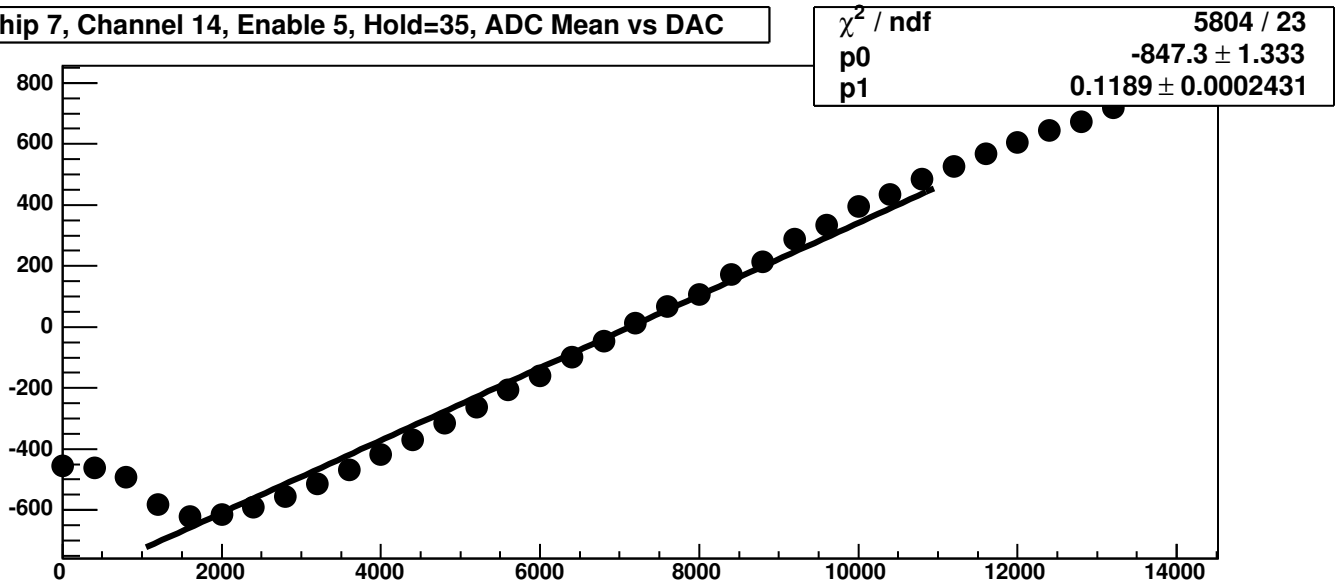

Chip 7, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 372.4 / 23
p0 -258.3 ± 2.641
p1 2.146 ± 0.001355
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

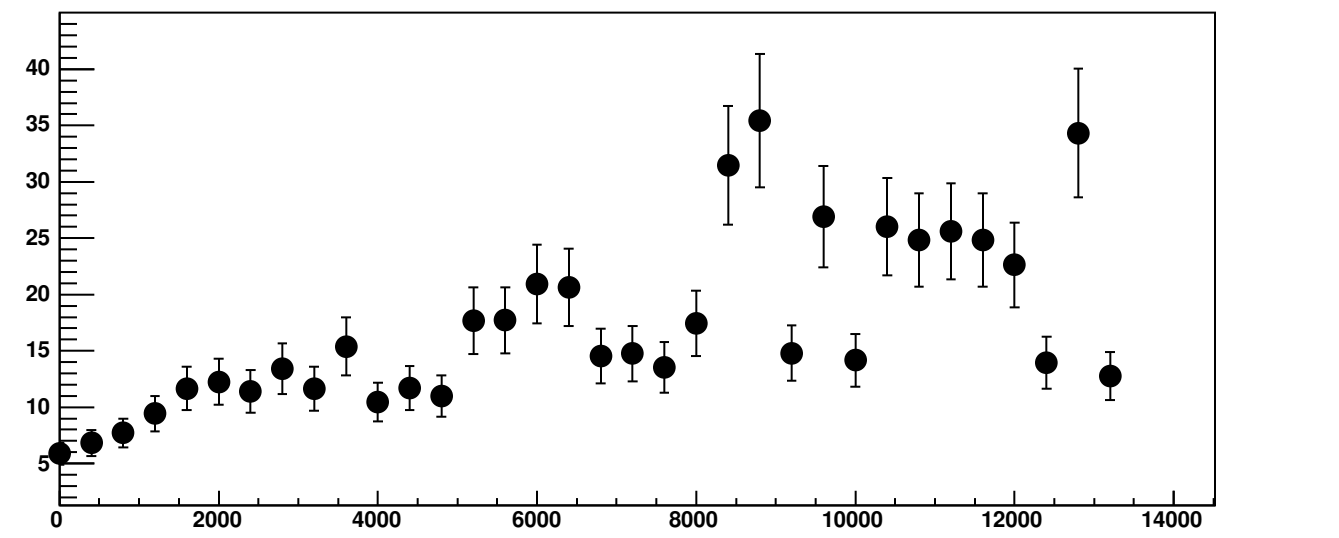

Chip 7, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

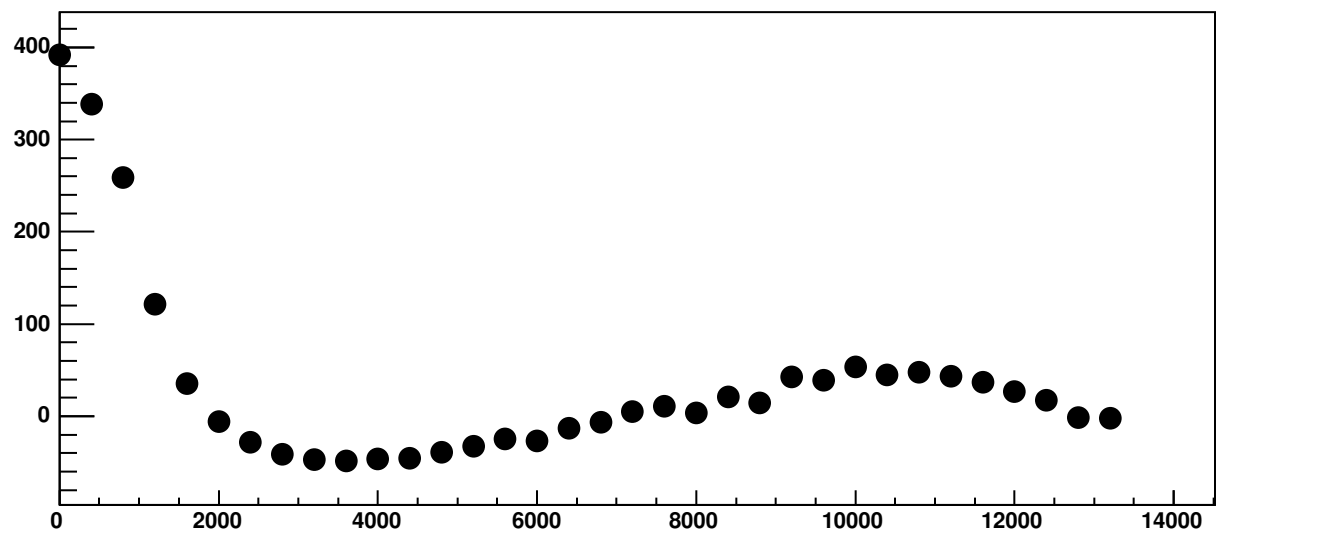
Chip 7, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

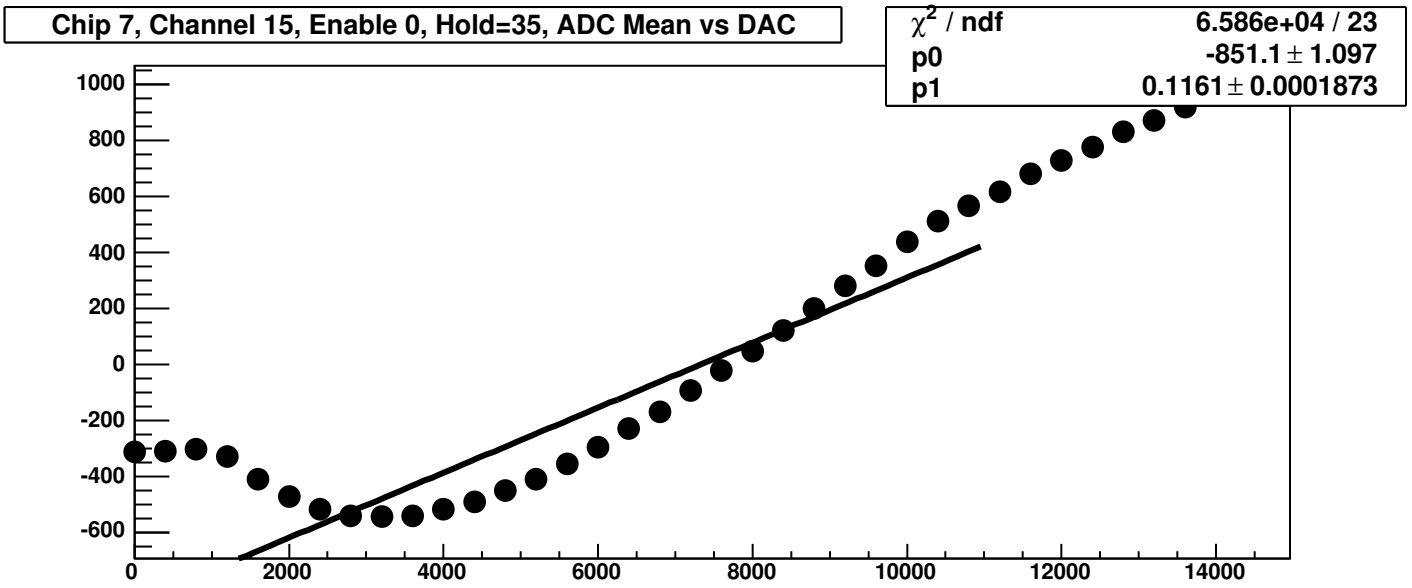


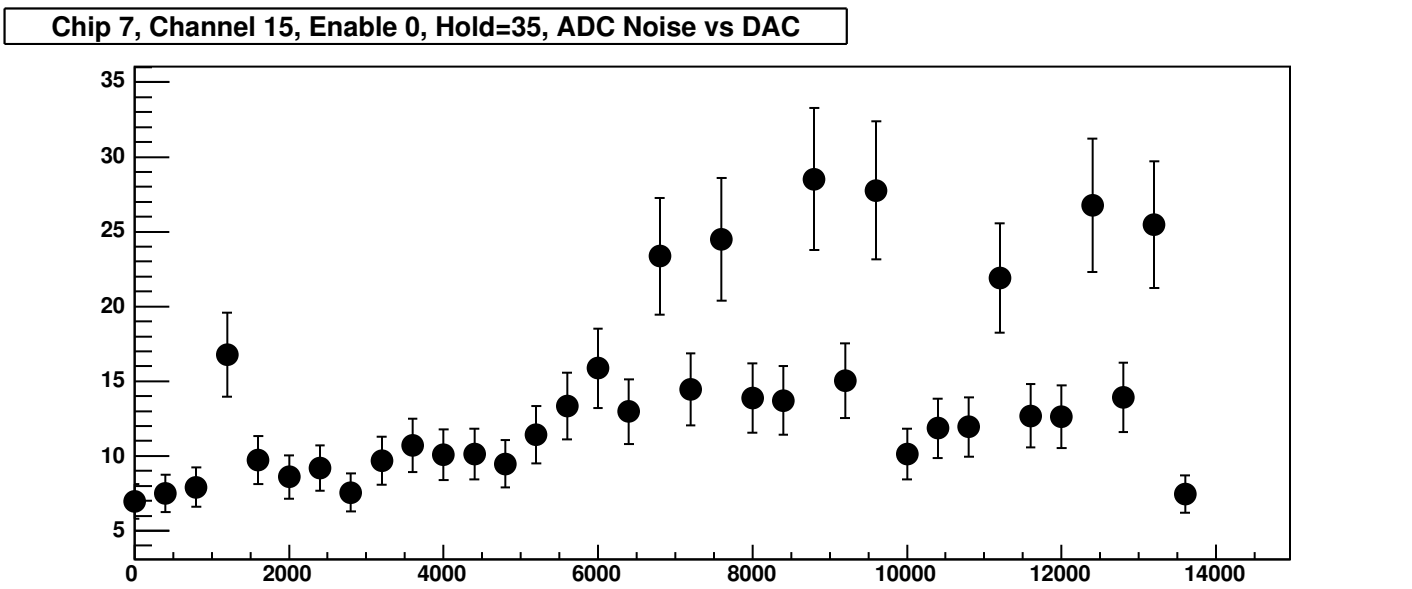
Chip 7, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC

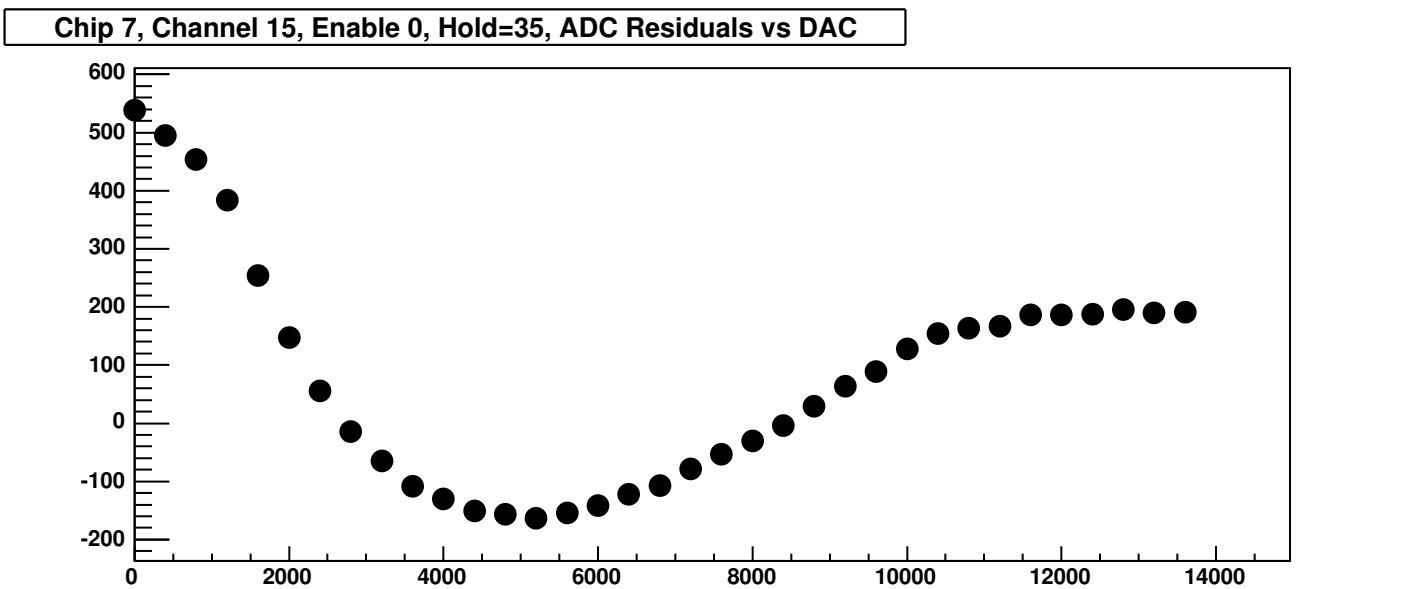


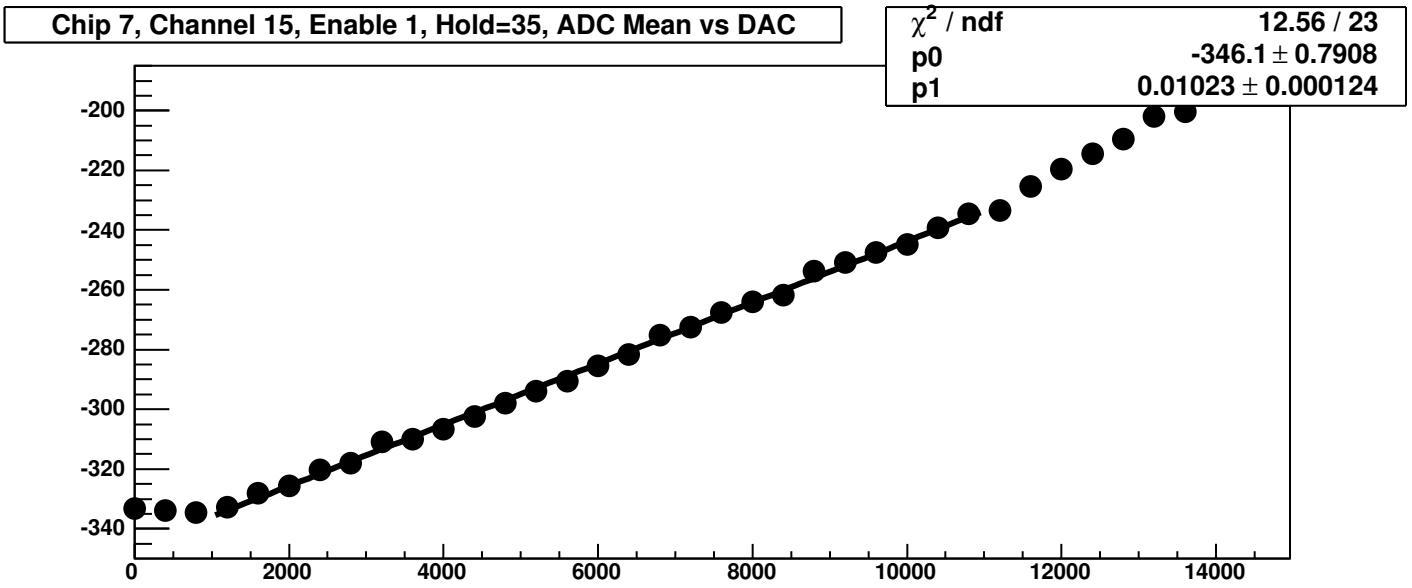
Chip 7, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

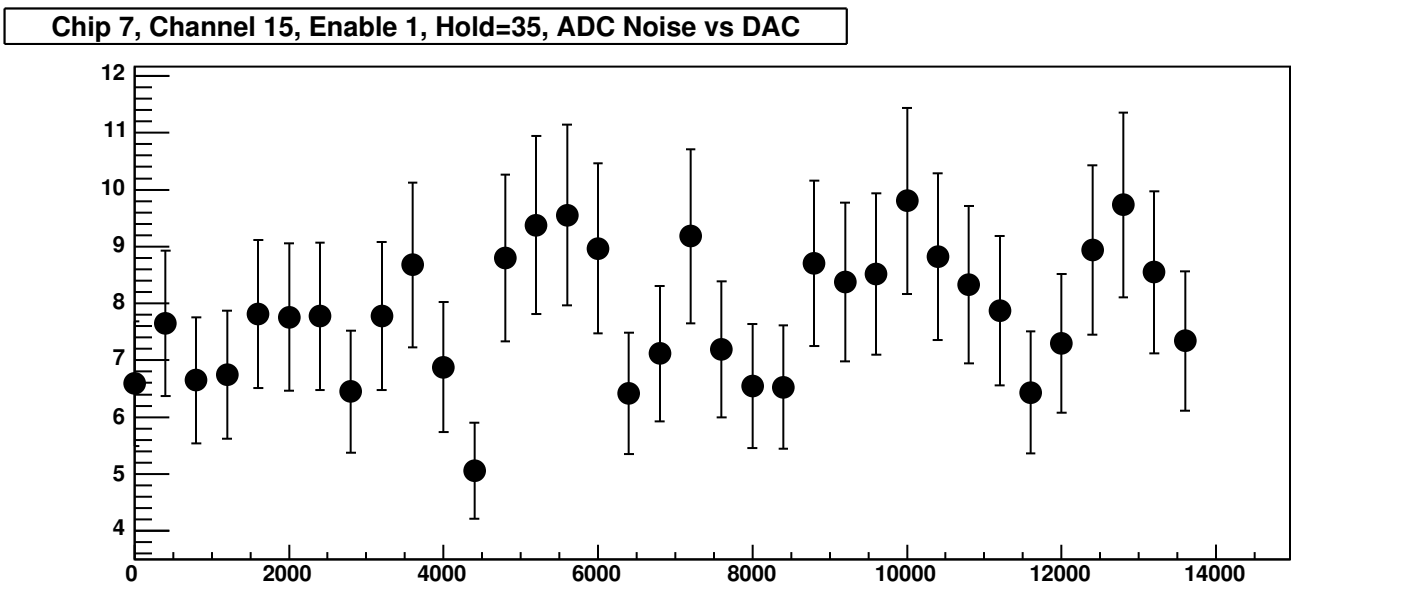


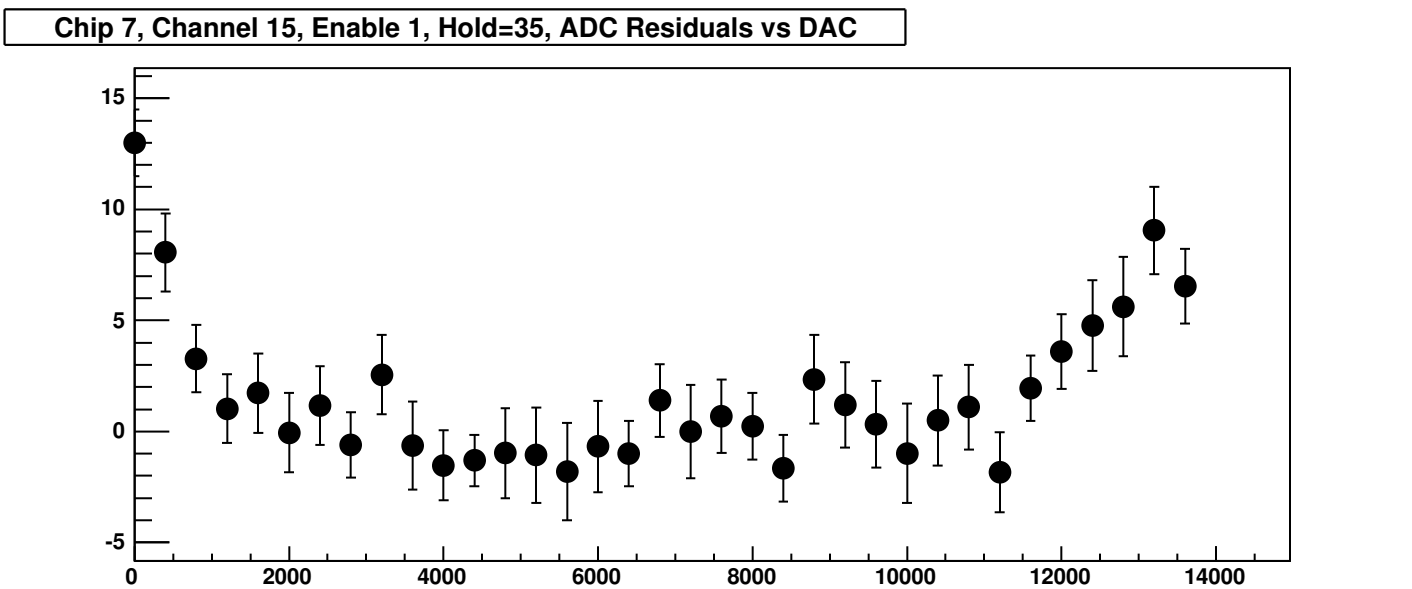

Chip 7, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 6.586e+04 / 23
p0 -851.1 ± 1.097
p1 0.1161 ± 0.0001873
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

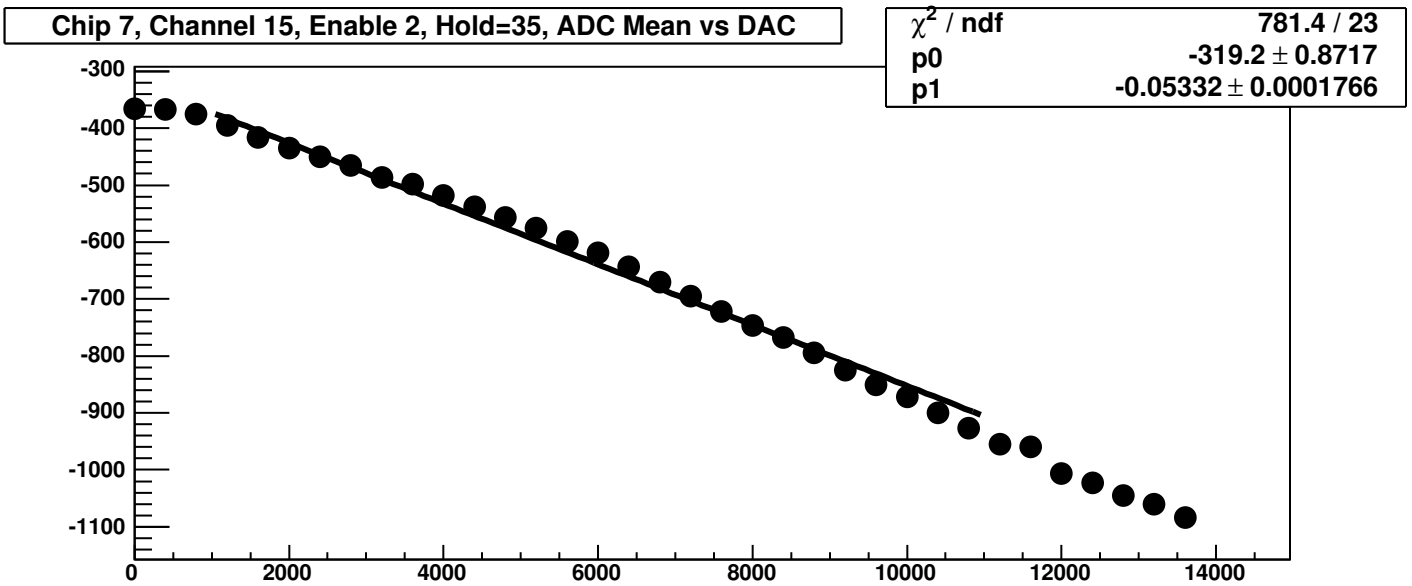

Chip 7, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

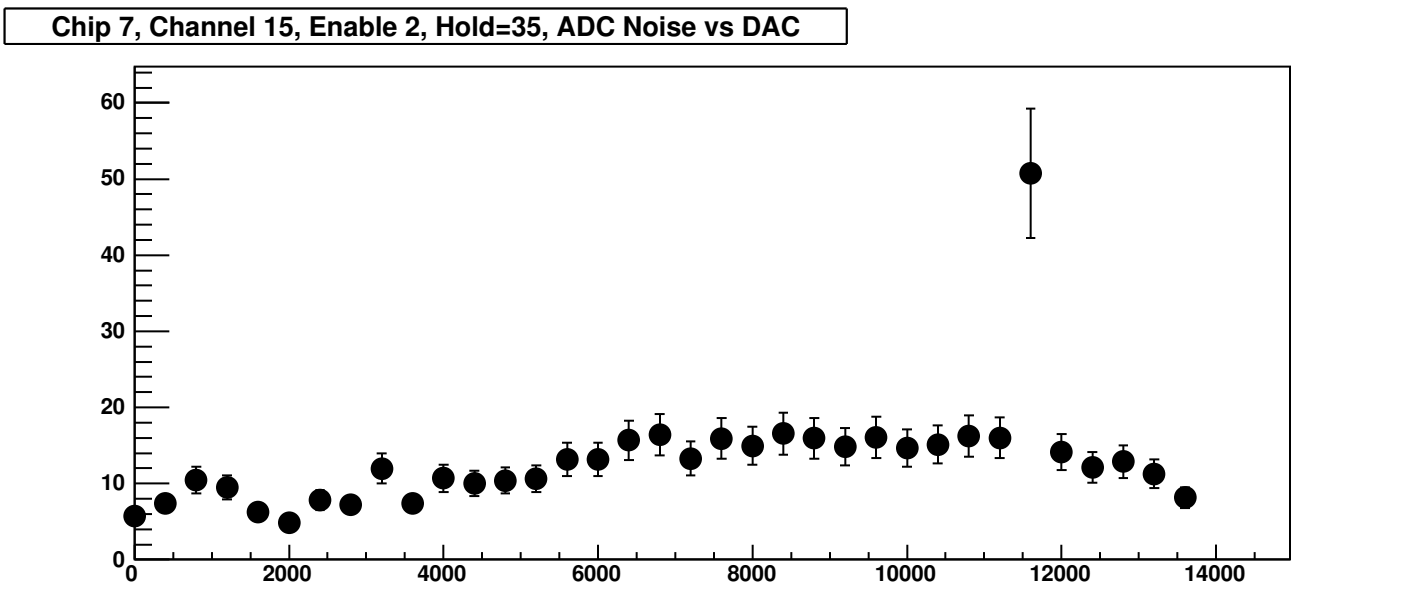

Chip 7, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

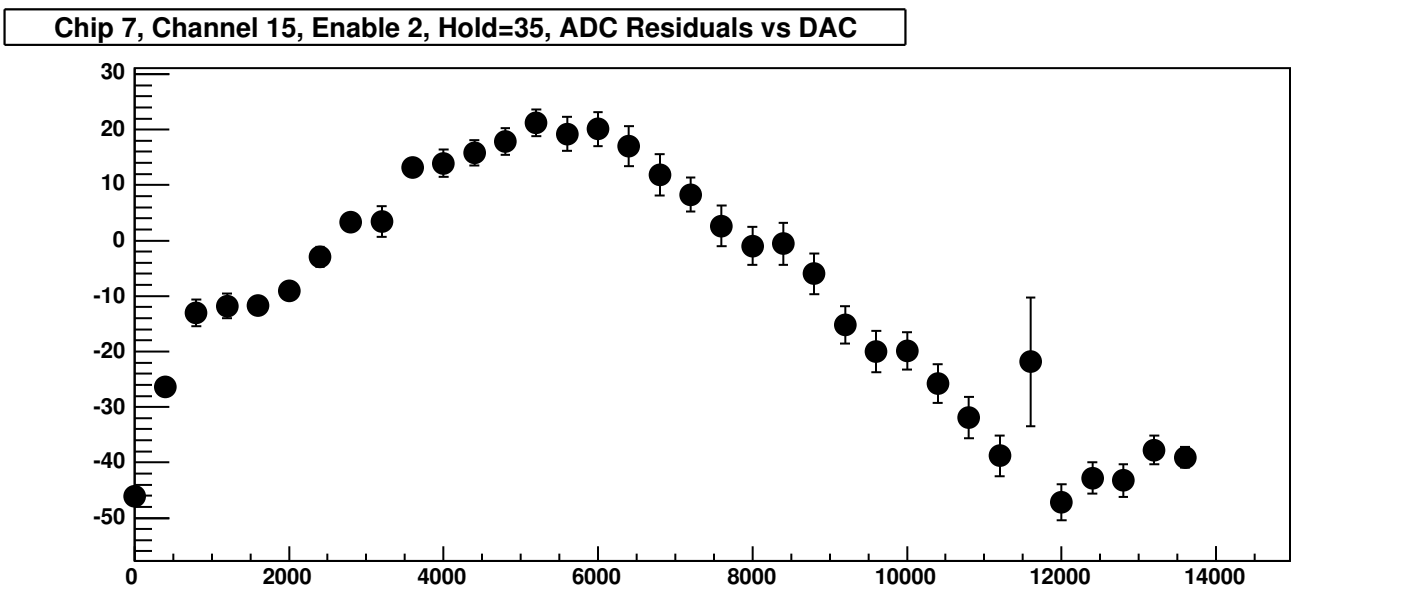

Chip 7, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 12.56 / 23
p0 -346.1 ± 0.7908
p1 0.01023 ± 0.000124
-200
-220
-240
-260
-280
-300
-320
-340
0
2000
4000
6000
8000
10000
12000
14000

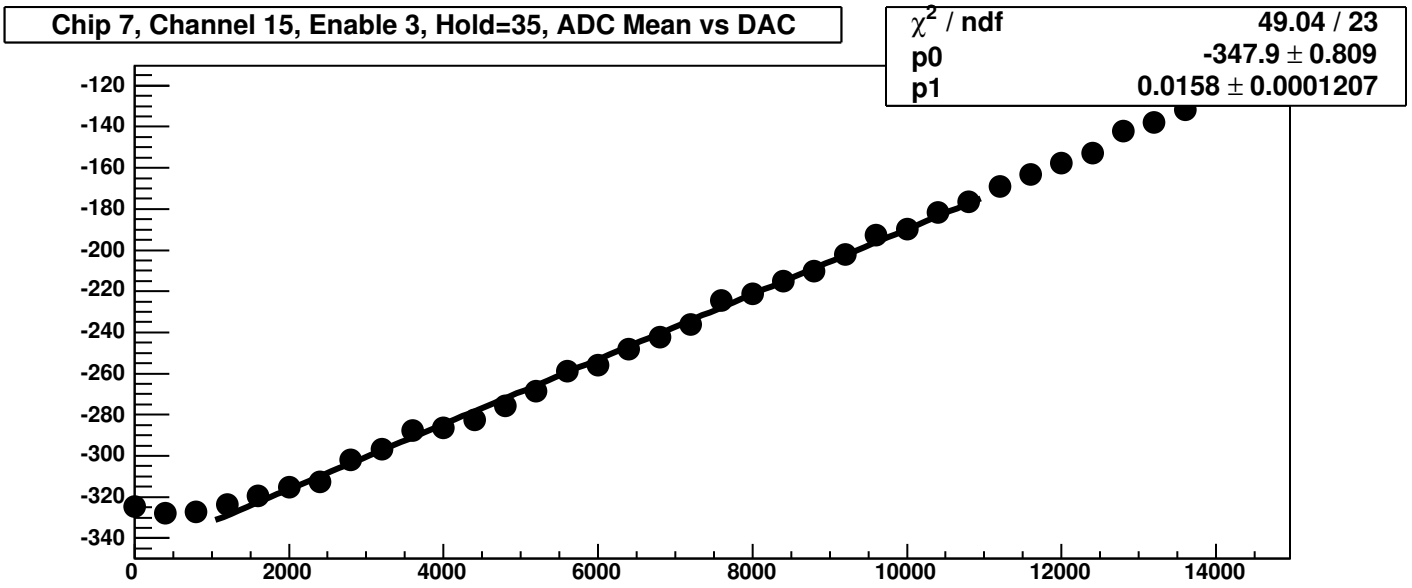

Chip 7, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

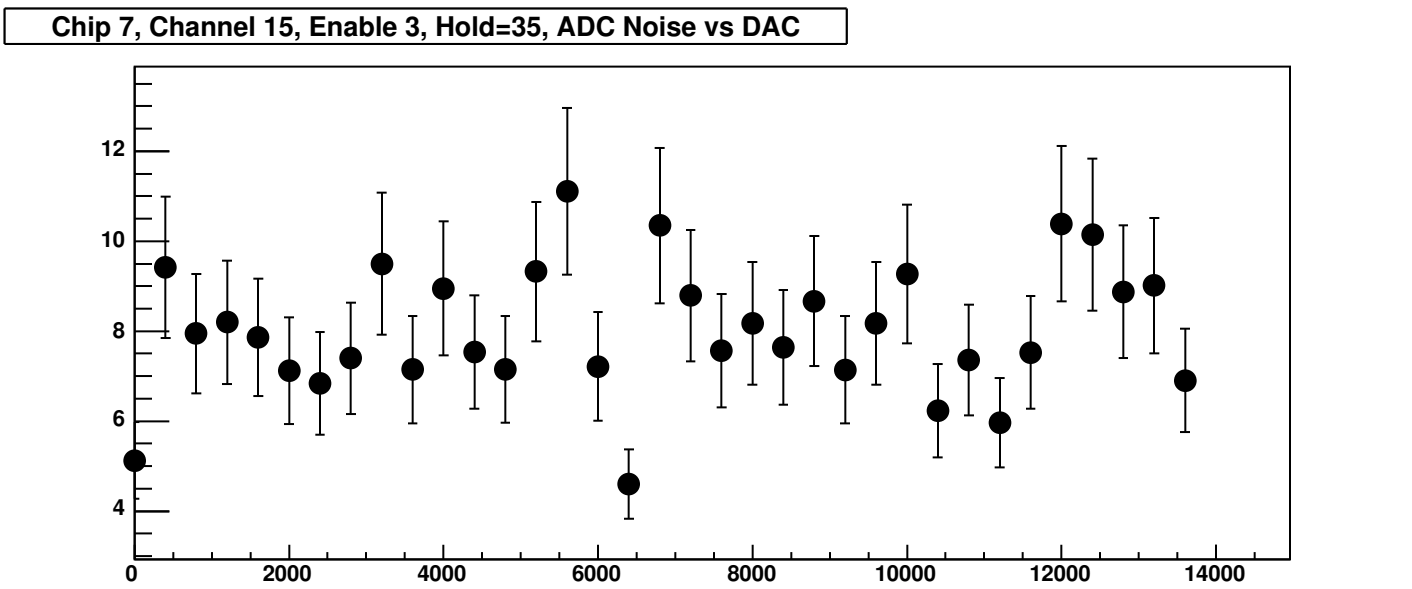

Chip 7, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

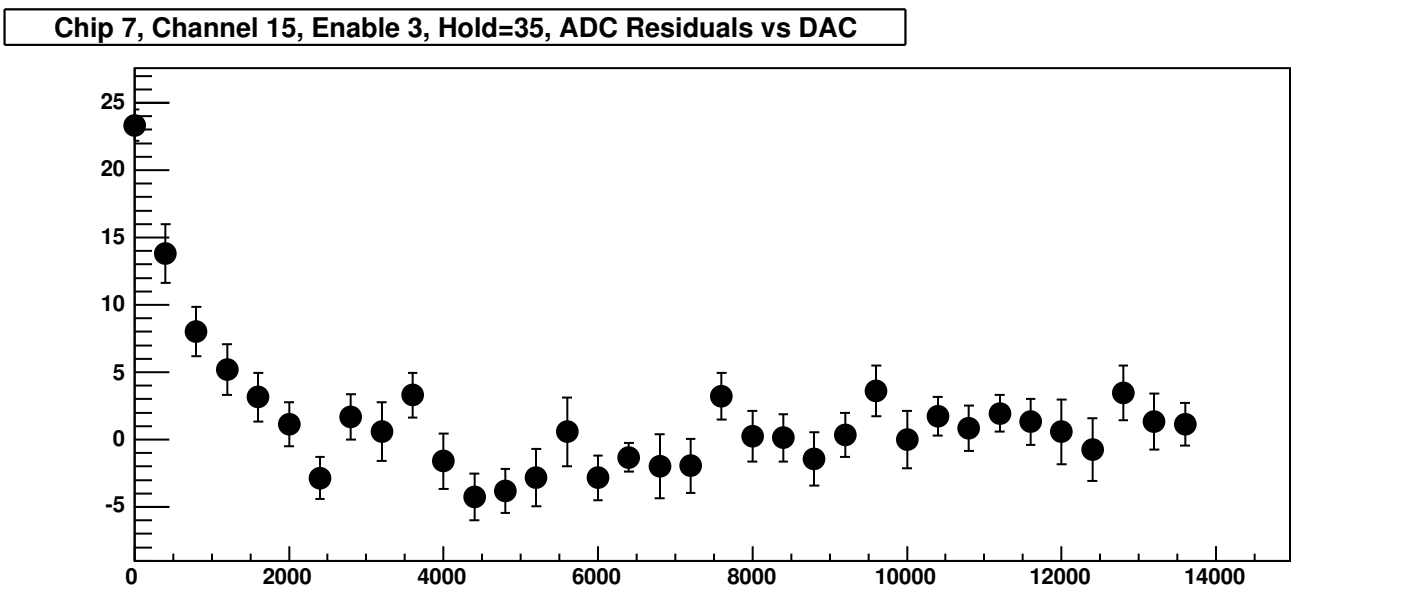

Chip 7, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 781.4 / 23
p0 -319.2 ± 0.8717
p1 -0.05332 ± 0.0001766
-300
-400
-500
-600
-700
-800
-900
-1000
-1100
0
2000
4000
6000
8000
10000
12000
14000

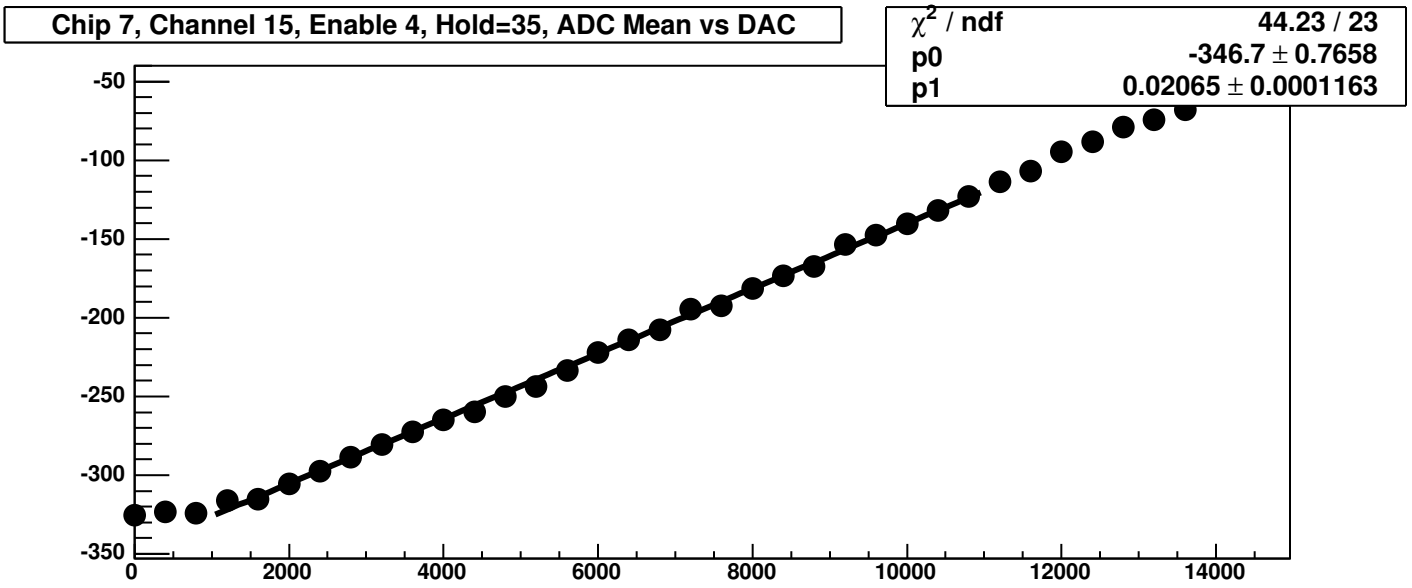

Chip 7, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

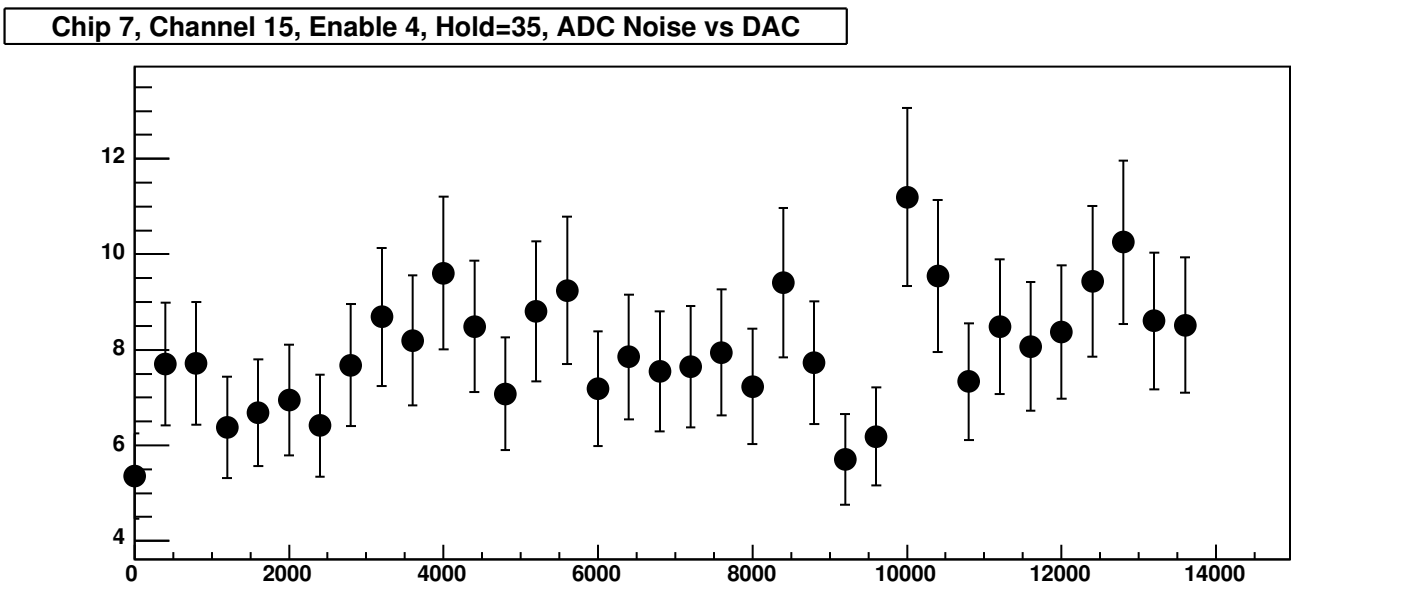

Chip 7, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

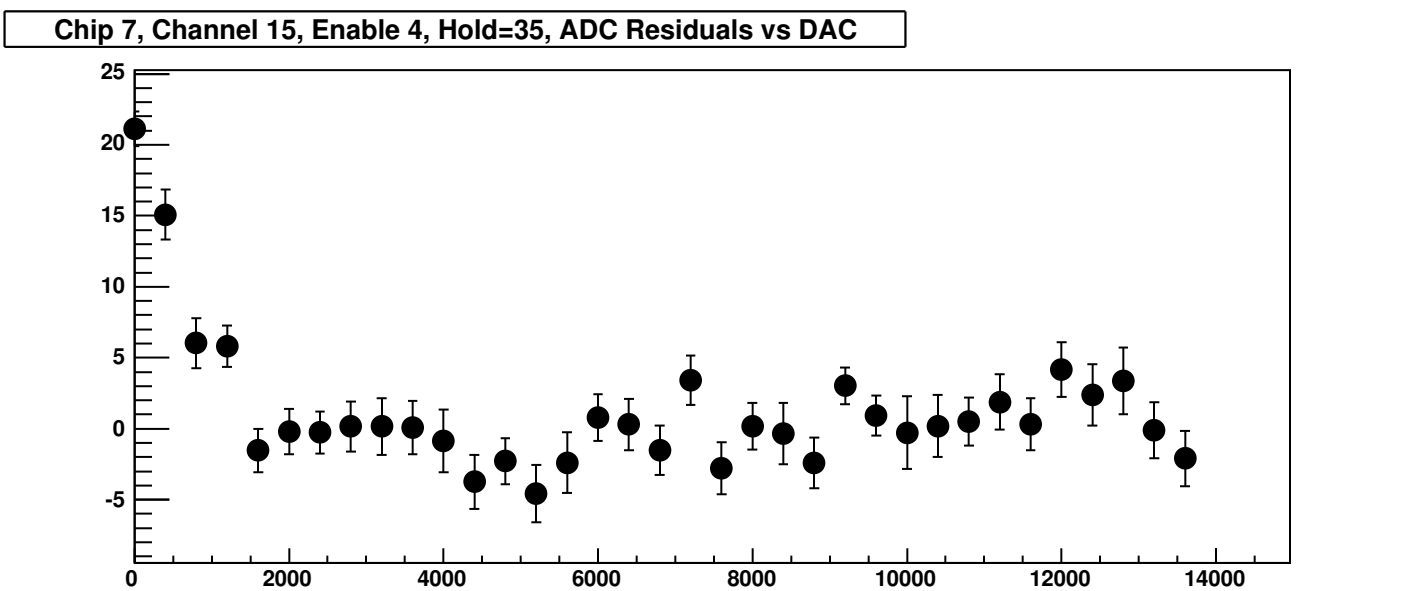

Chip 7, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 49.04 / 23
p0 -347.9 ± 0.809
p1 0.0158 ± 0.0001207
-120
-140
-160
-180
-200
-220
-240
-260
-280
-300
-320
-340
0
2000
4000
6000
8000
10000
12000
14000

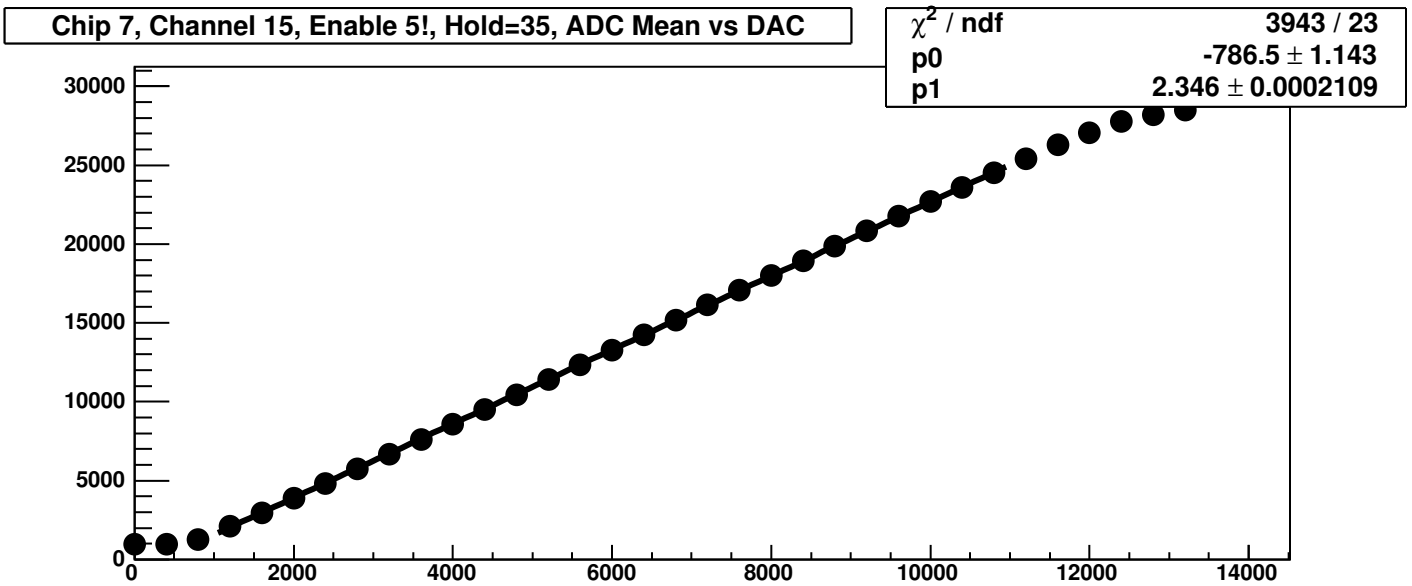

Chip 7, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

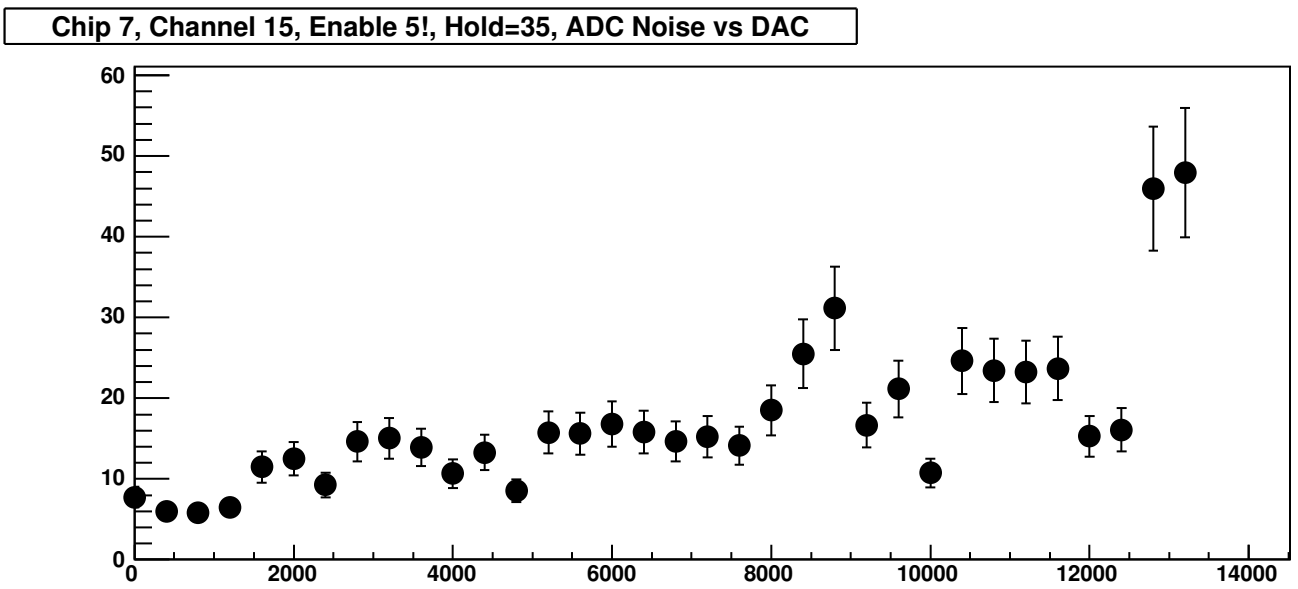

Chip 7, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

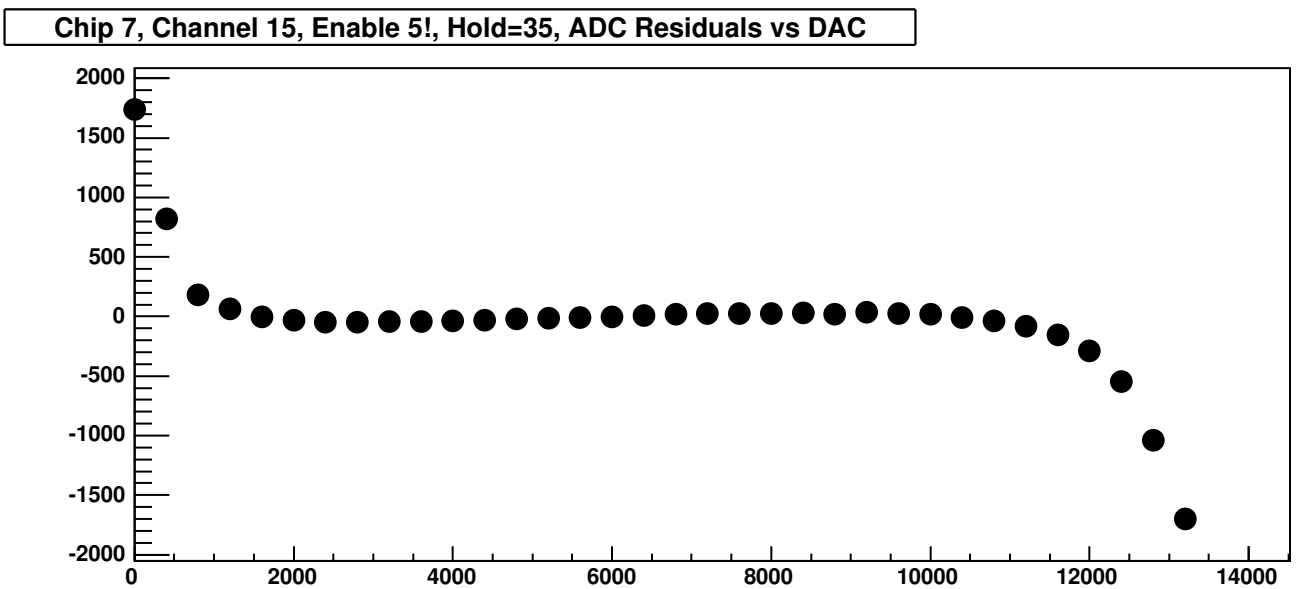

Chip 7, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 44.23 / 23
p0 -346.7 ± 0.7658
p1 0.02065 ± 0.0001163
-50
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

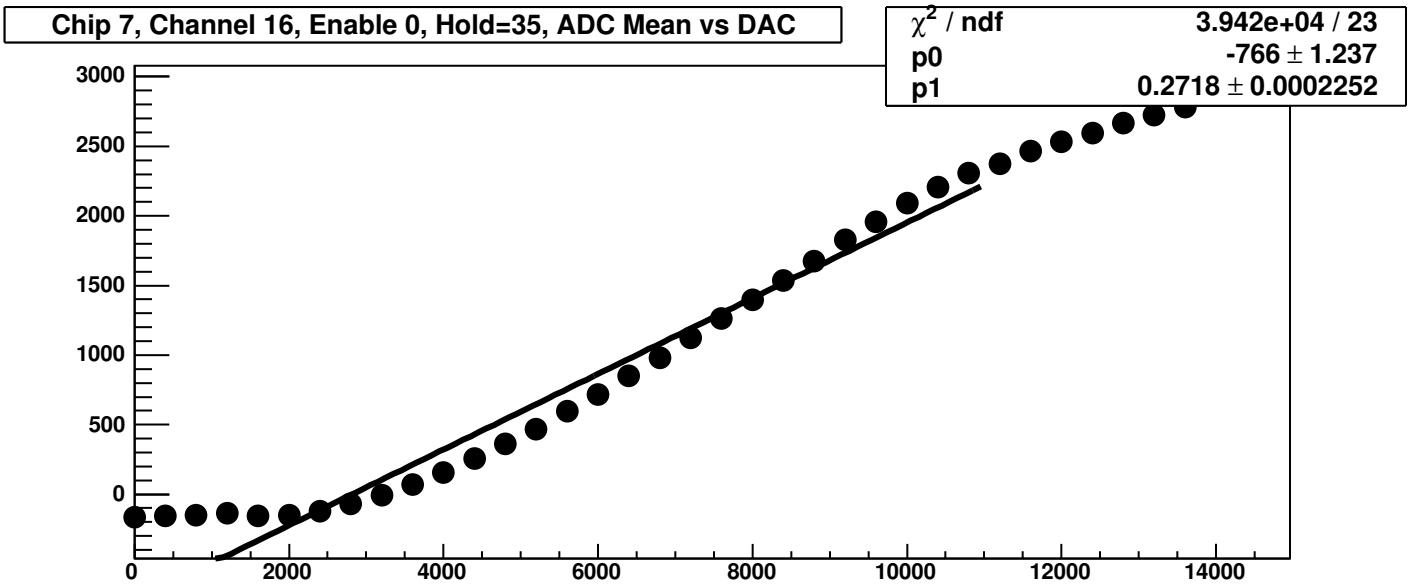

Chip 7, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

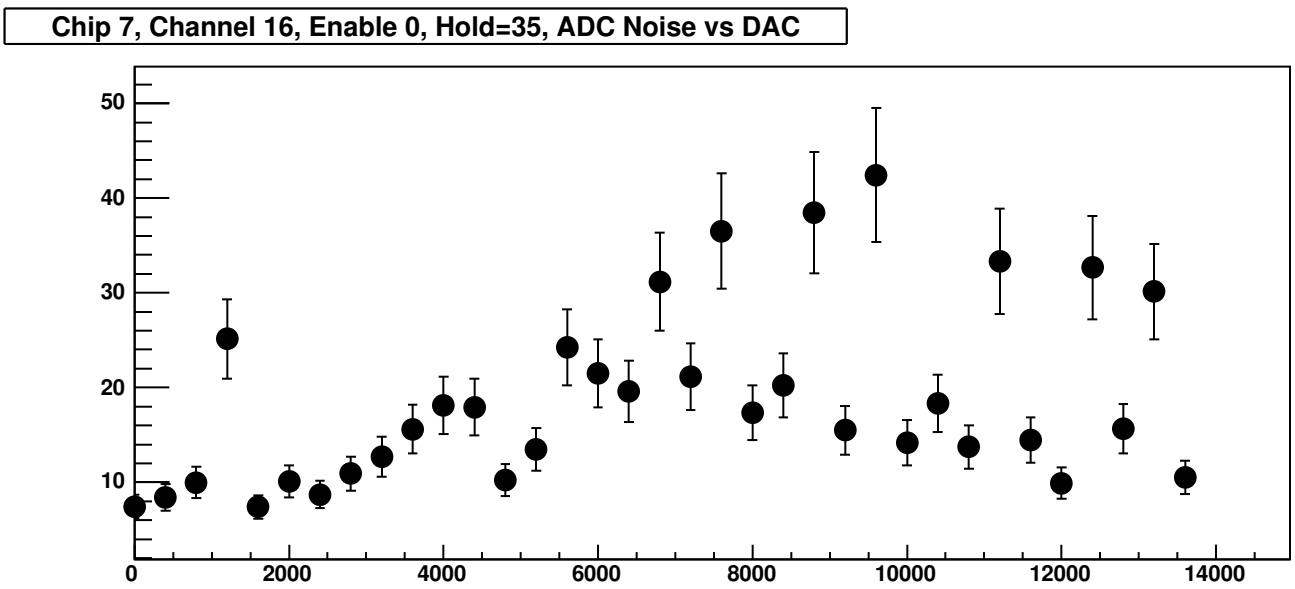

Chip 7, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

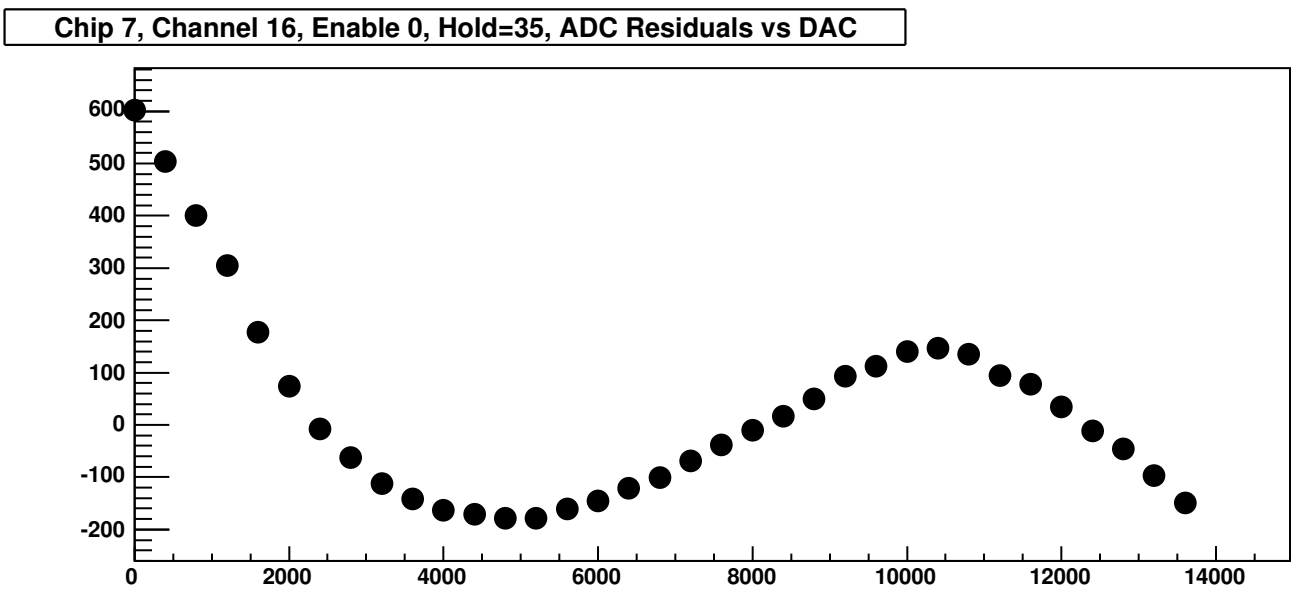

Chip 7, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3943 / 23
p0 -786.5 ± 1.143
p1 2.346 ± 0.0002109
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

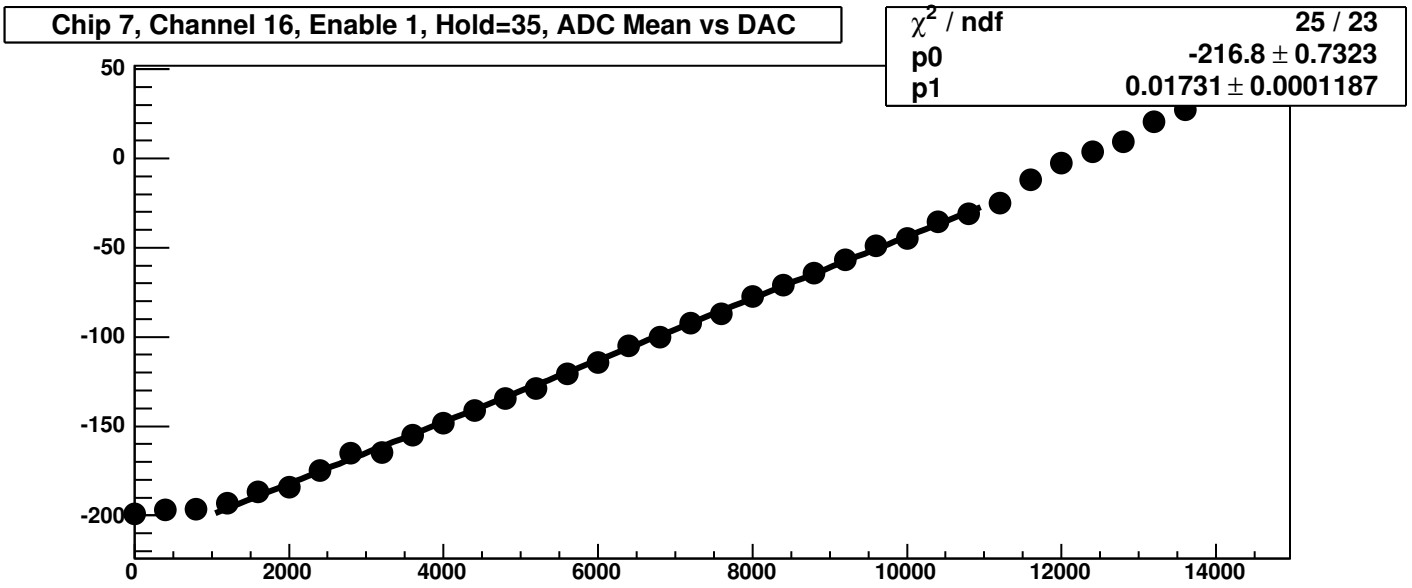

Chip 7, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

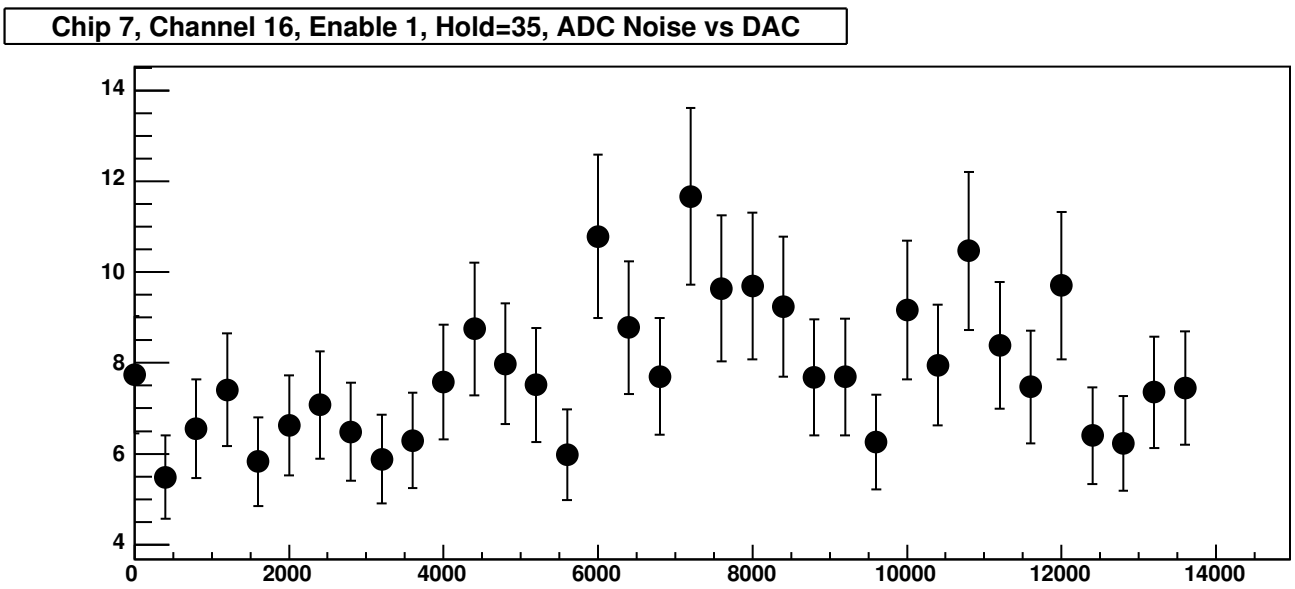

Chip 7, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

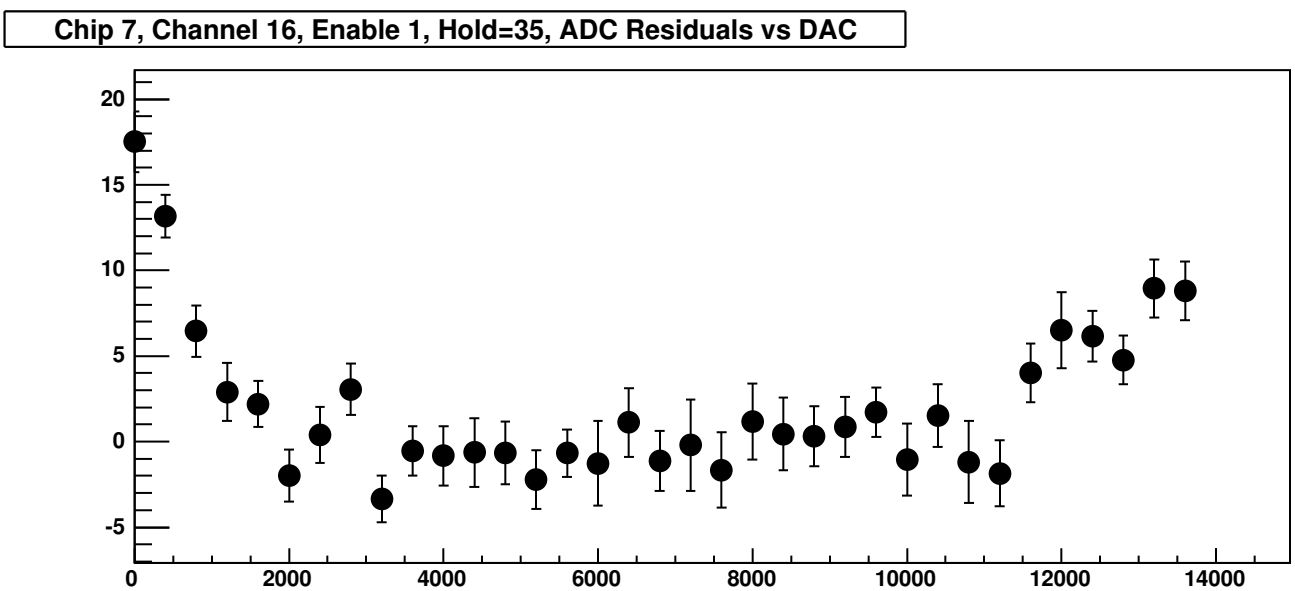

Chip 7, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.942e+04 / 23
p0 -766 ± 1.237
p1 0.2718 ± 0.0002252
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

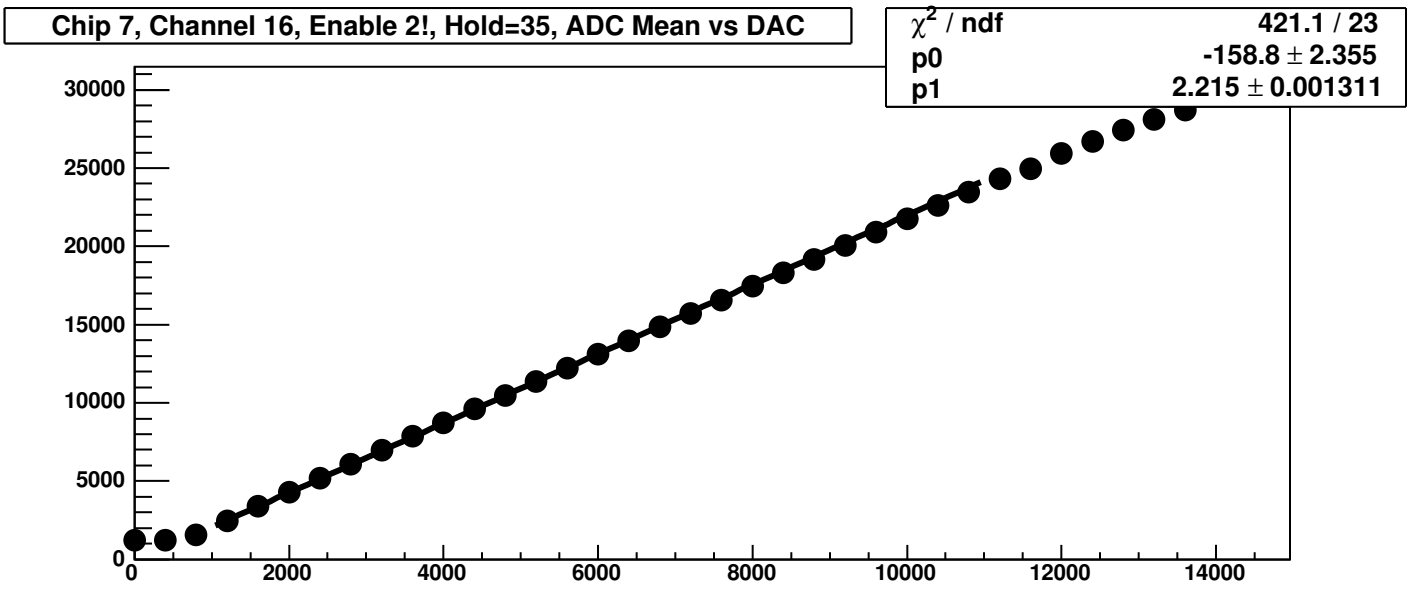

Chip 7, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

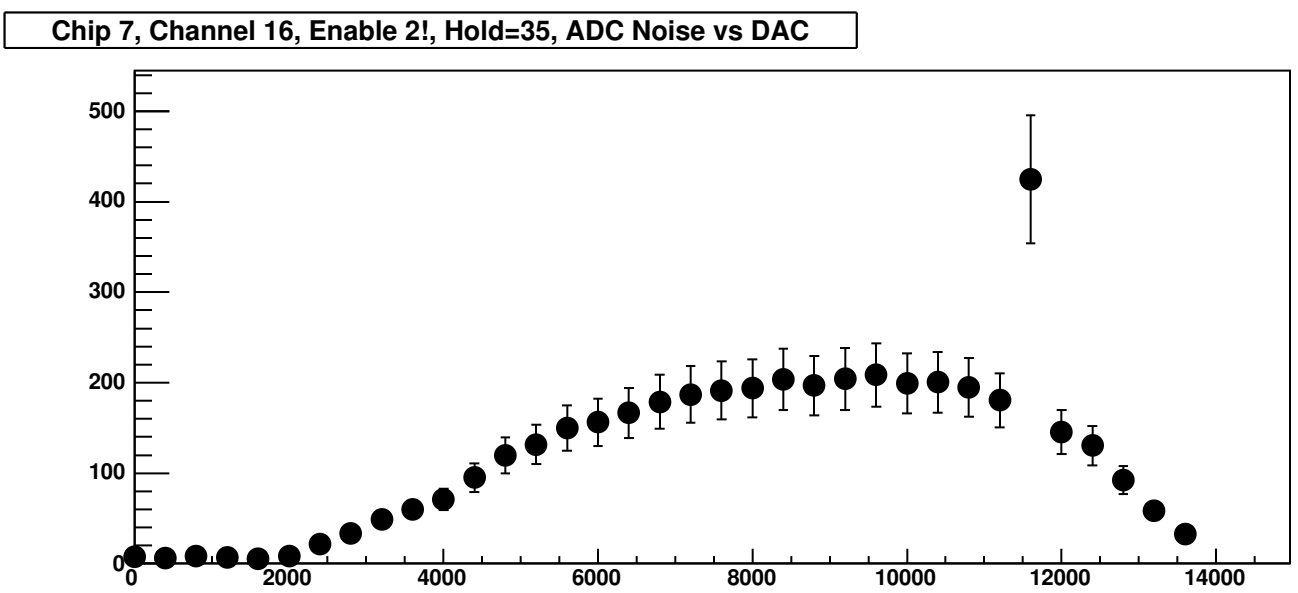

Chip 7, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

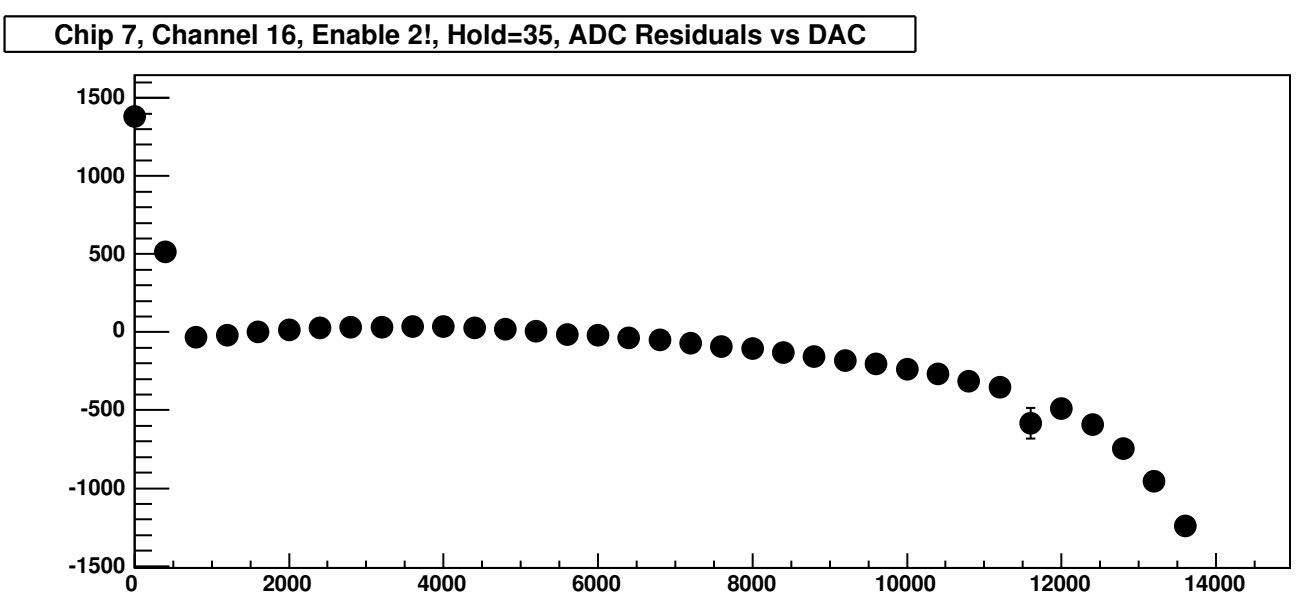

Chip 7, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 25 / 23
p0 -216.8 ± 0.7323
p1 0.01731 ± 0.0001187
50
0
-50
-100
-150
-200
0
2000
4000
6000
8000
10000
12000
14000

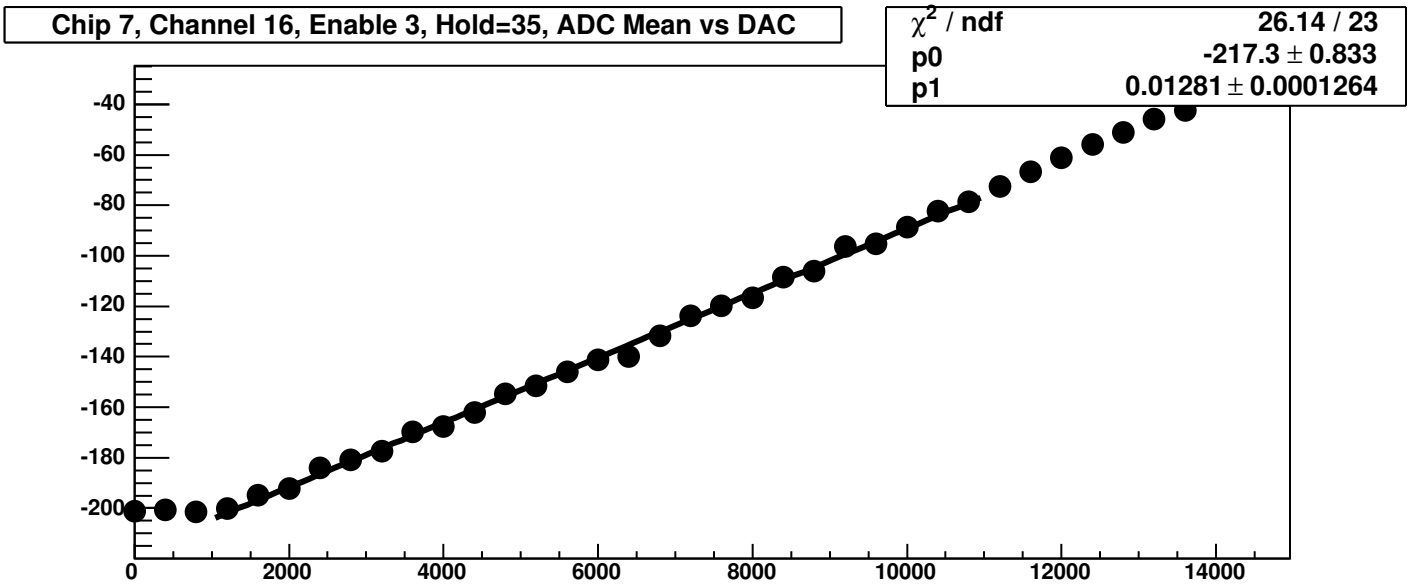

Chip 7, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

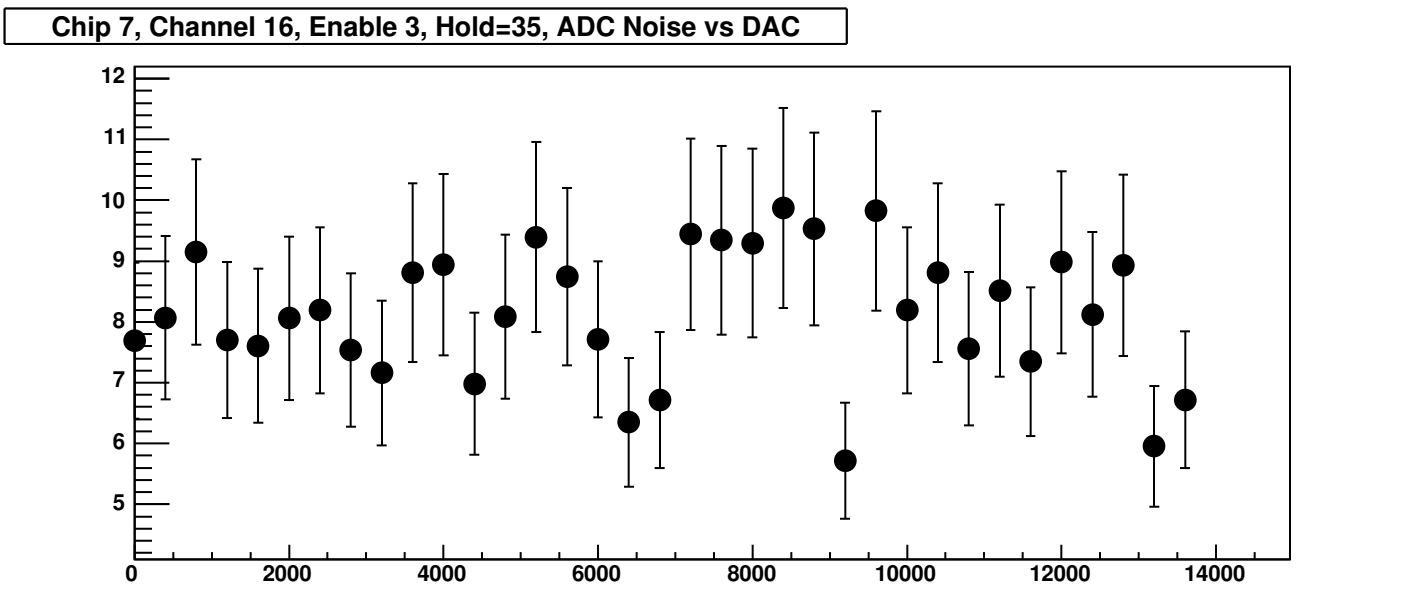

Chip 7, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

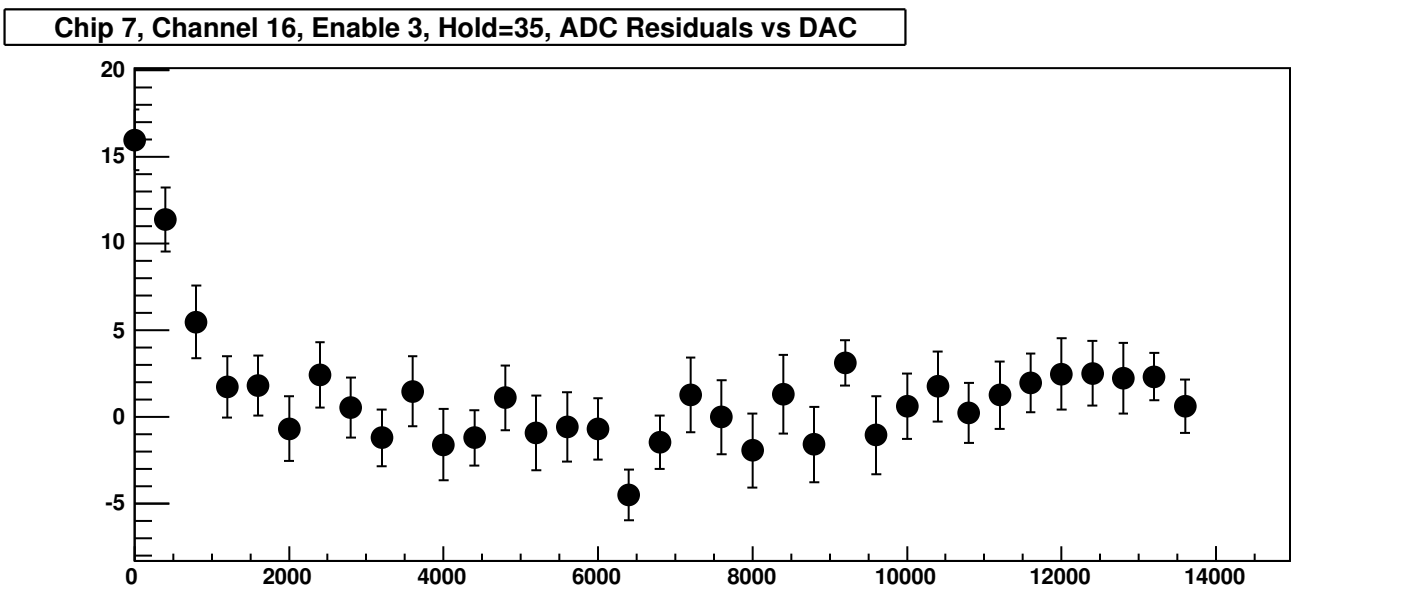

Chip 7, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 421.1 / 23
p0 -158.8 ± 2.355
p1 2.215 ± 0.001311
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

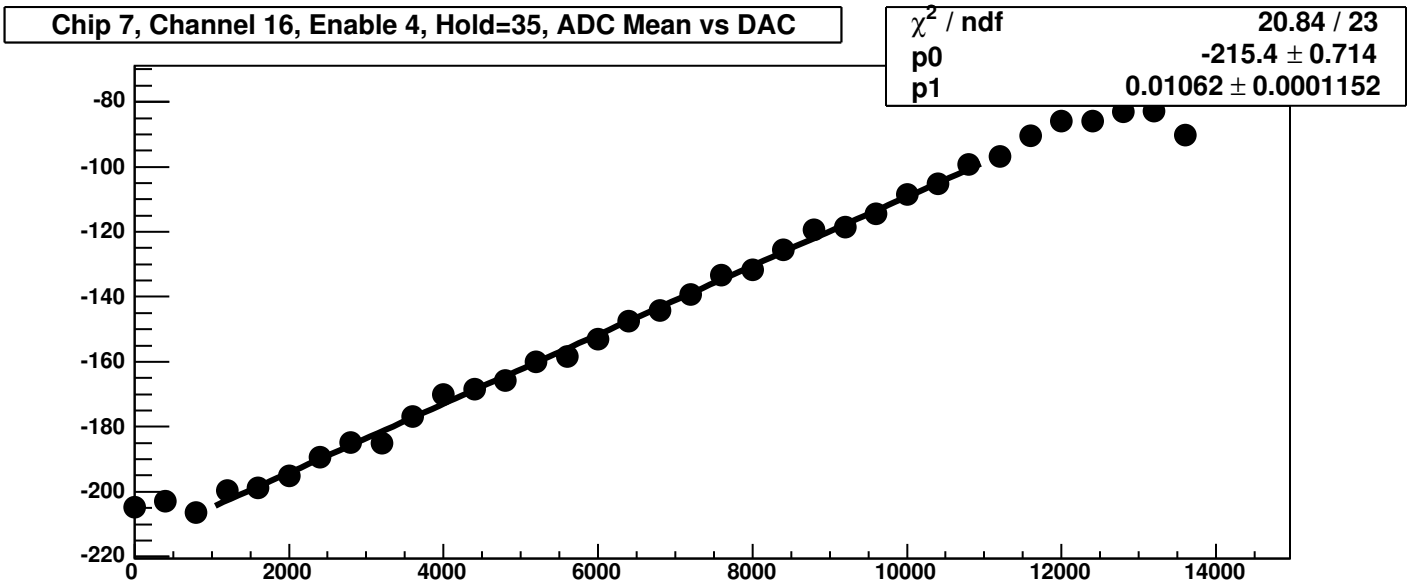

Chip 7, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

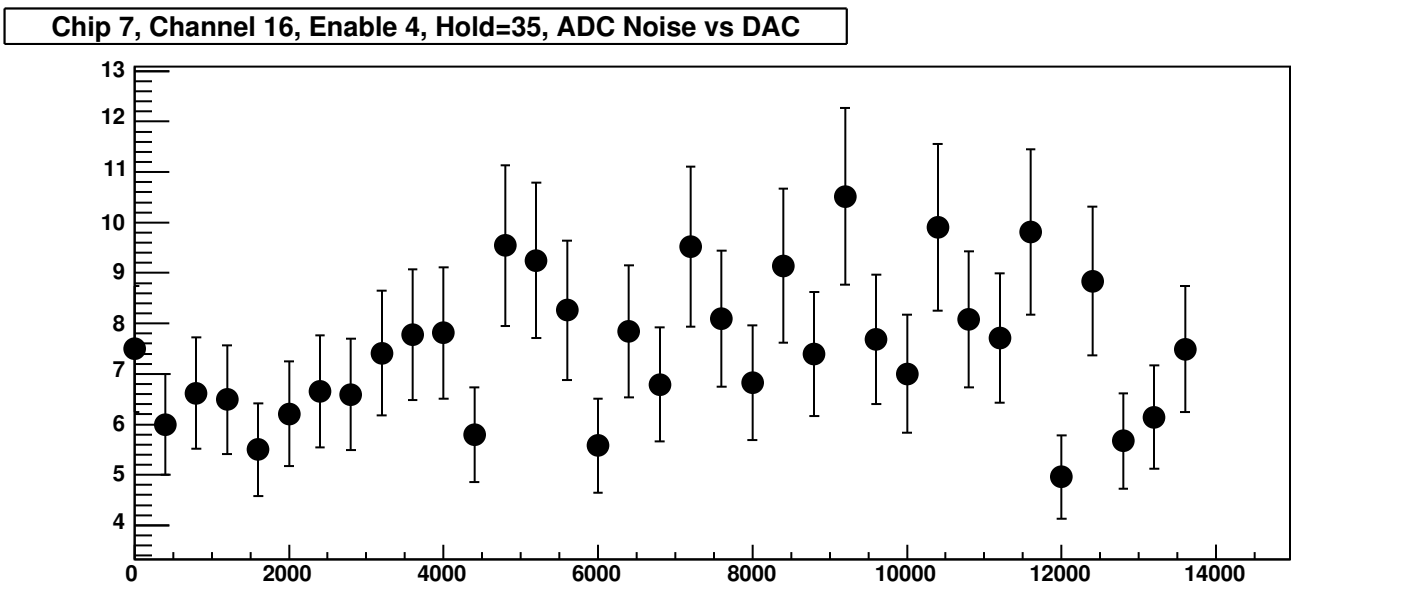

Chip 7, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

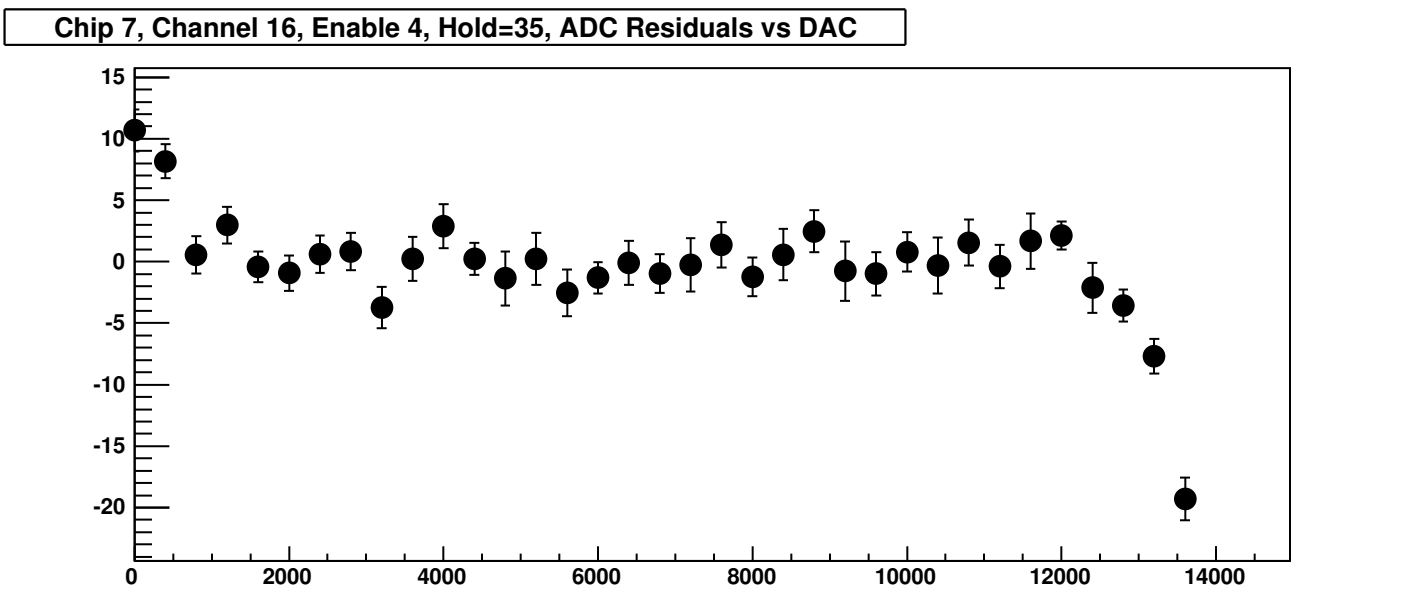

Chip 7, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.14 / 23
p0 -217.3 ± 0.833
p1 0.01281 ± 0.0001264
-40
-60
-80
-100
-120
-140
-160
-180
-200
0
2000
4000
6000
8000
10000
12000
14000

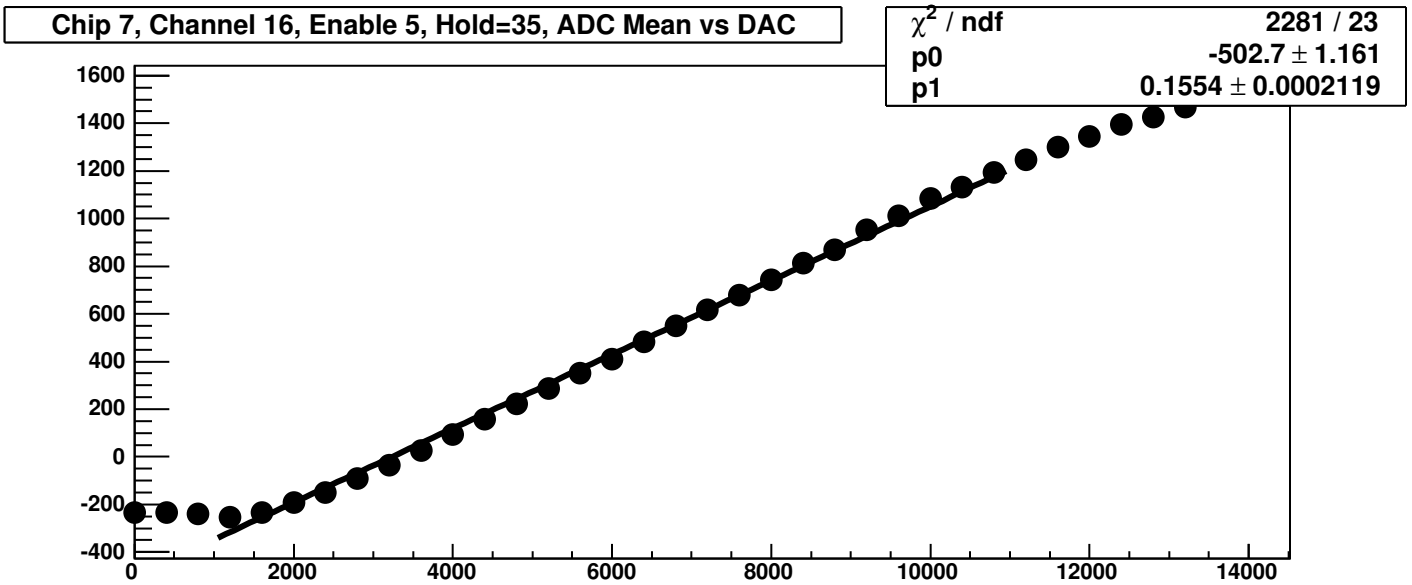

Chip 7, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

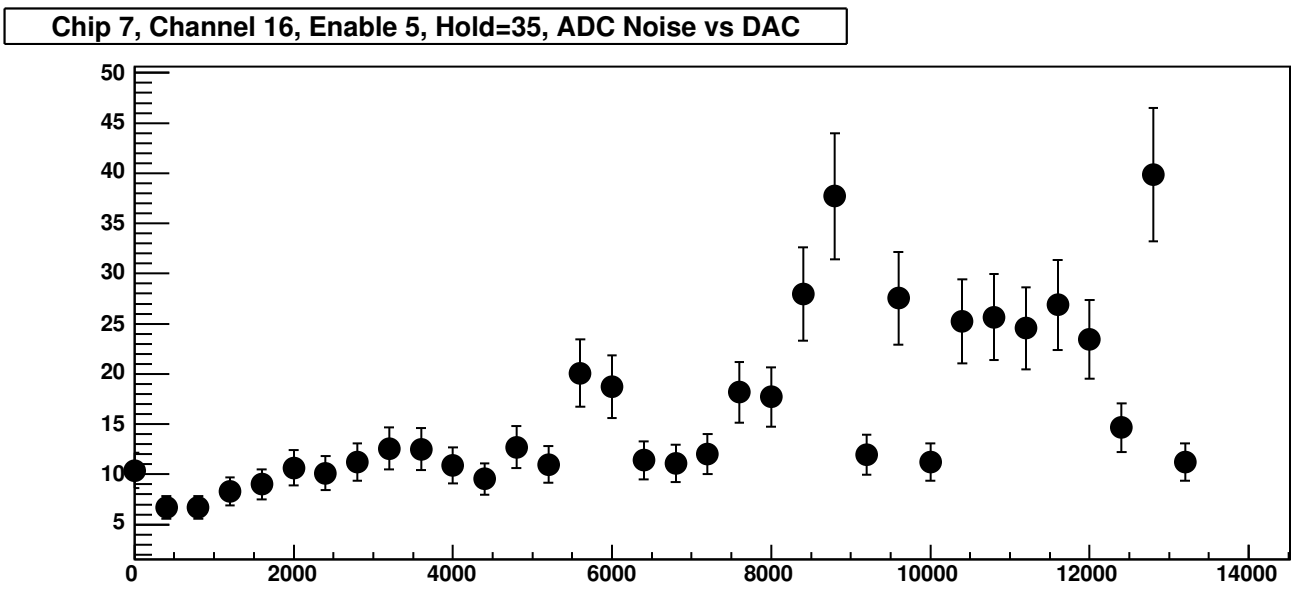

Chip 7, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

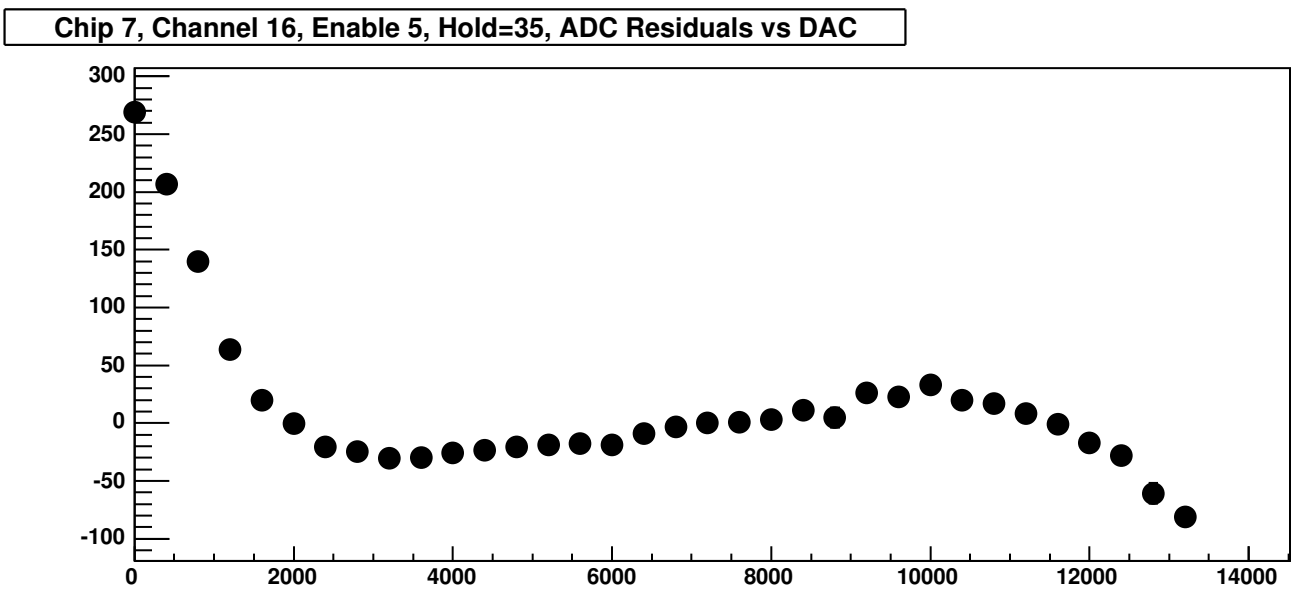

Chip 7, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 20.84 / 23
p0 -215.4 ± 0.714
p1 0.01062 ± 0.0001152
-80
-100
-120
-140
-160
-180
-200
-220
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

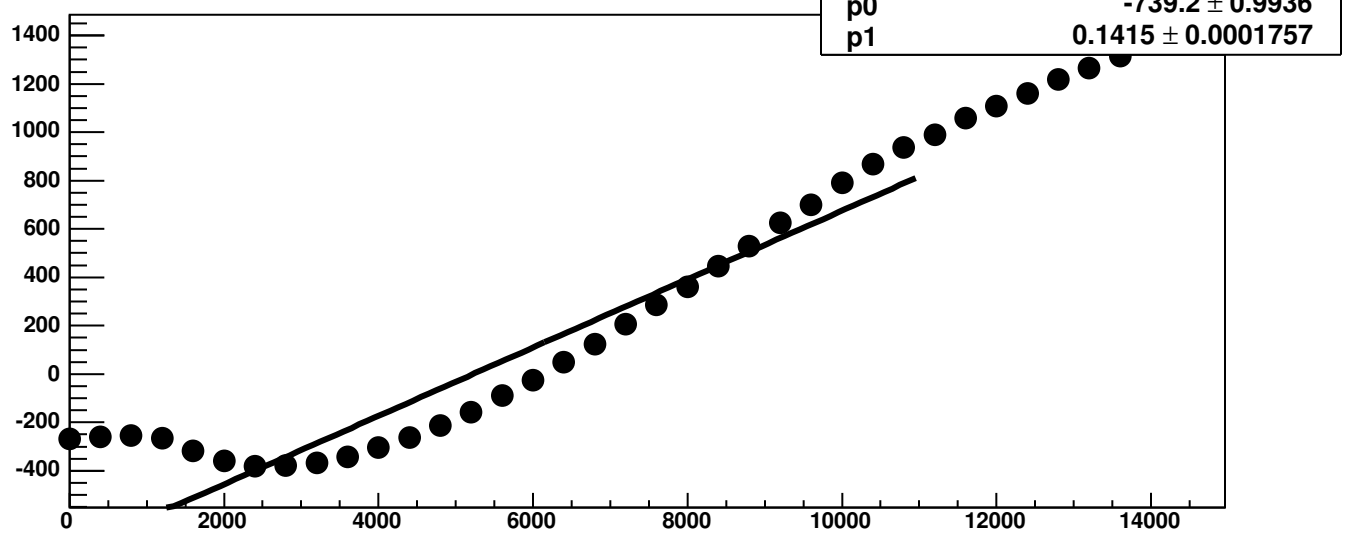

Chip 7, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2281 / 23
p0 -502.7 ± 1.161
p1 0.1554 ± 0.0002119
1600
1400
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

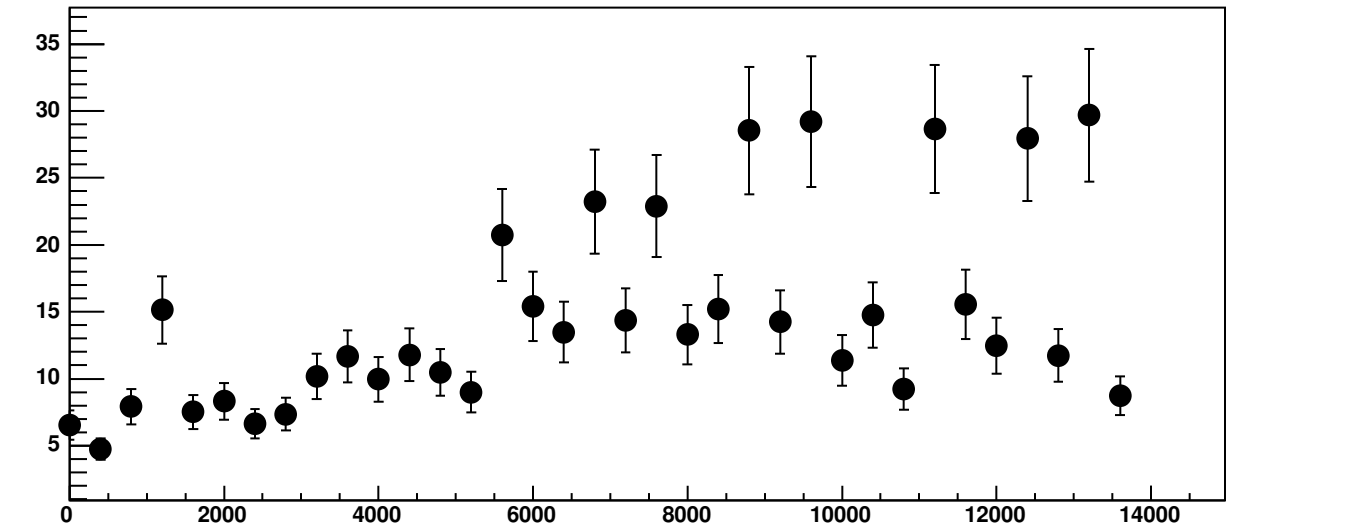

Chip 7, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 7, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

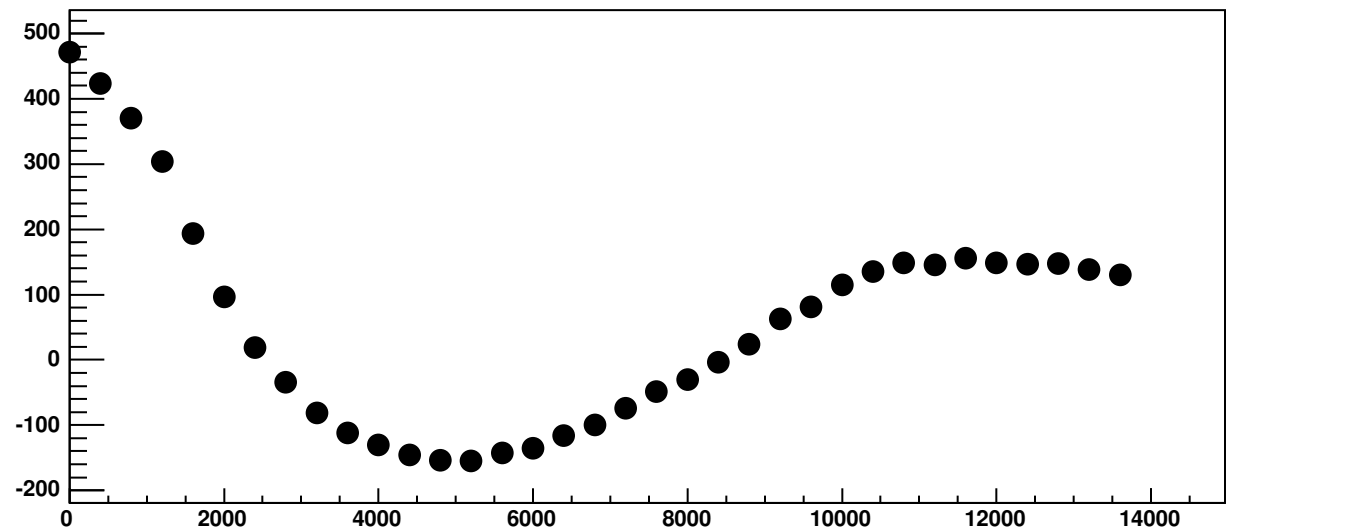
Chip 7, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC

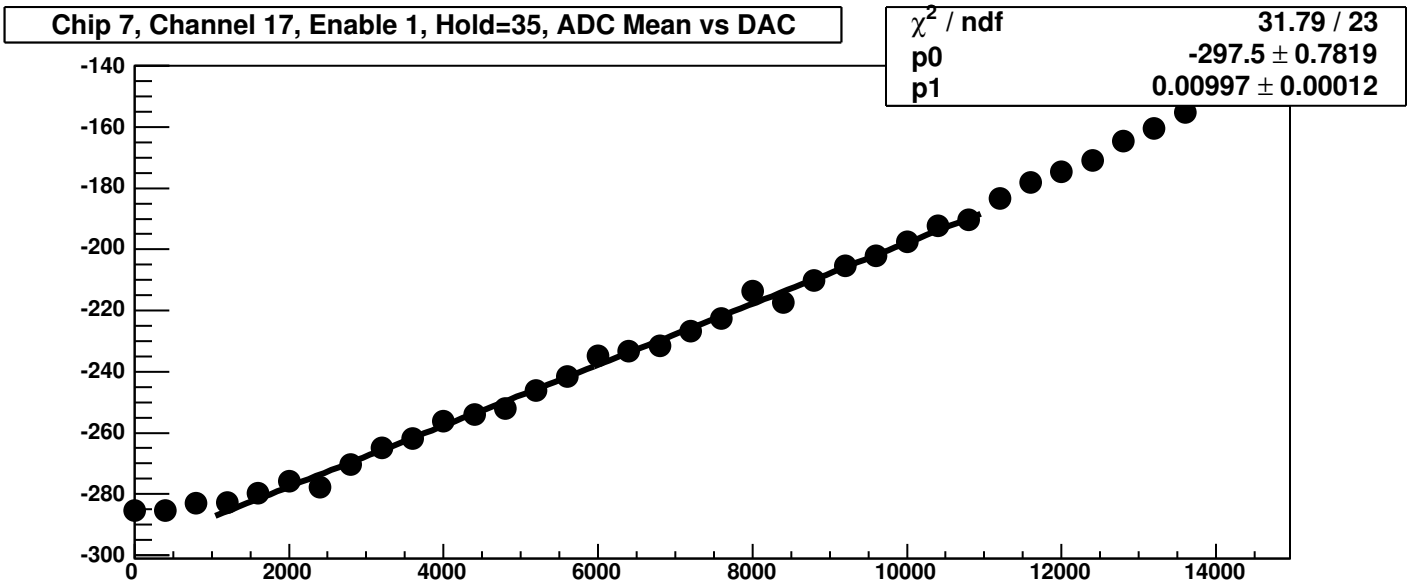


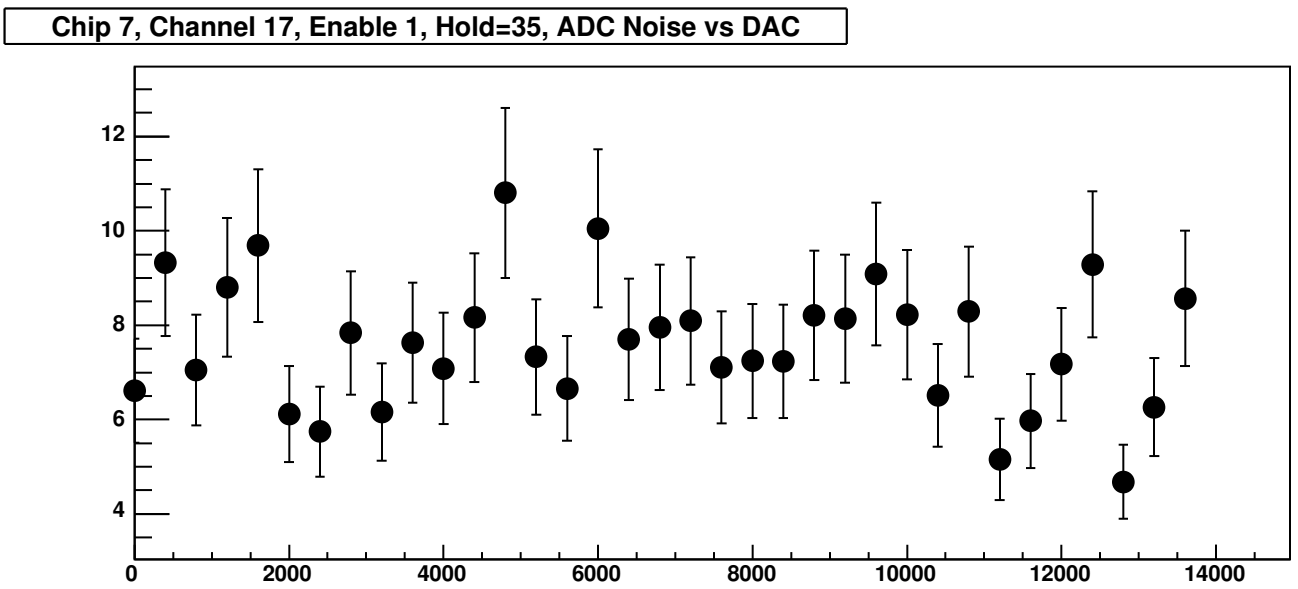
Chip 7, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC

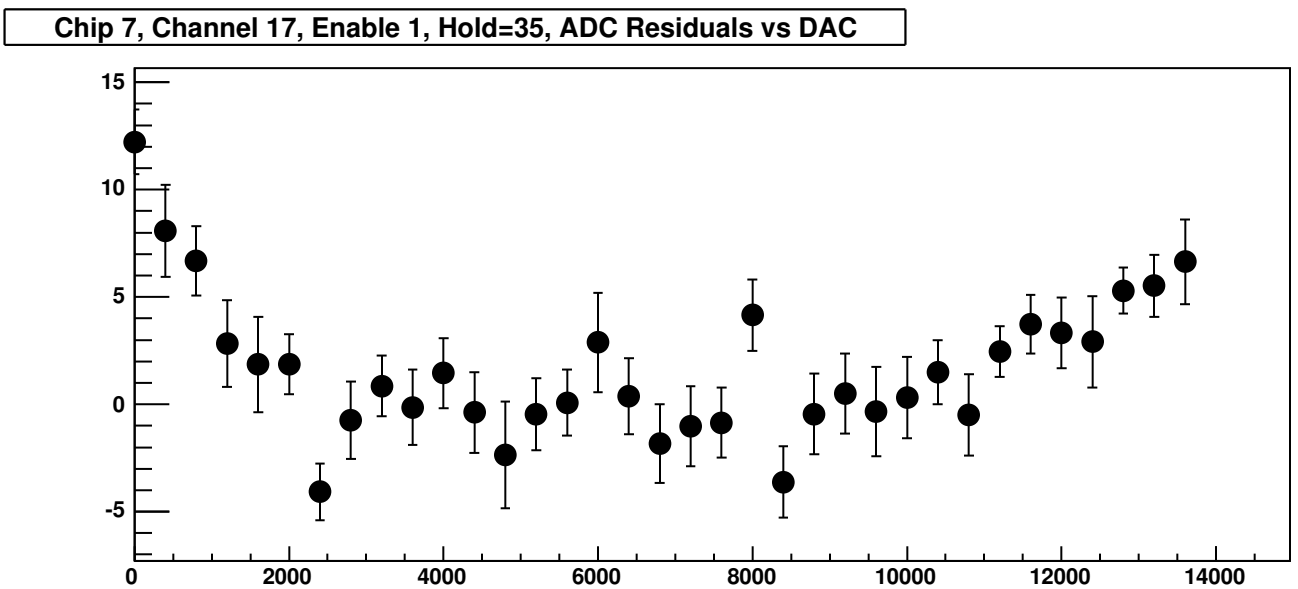


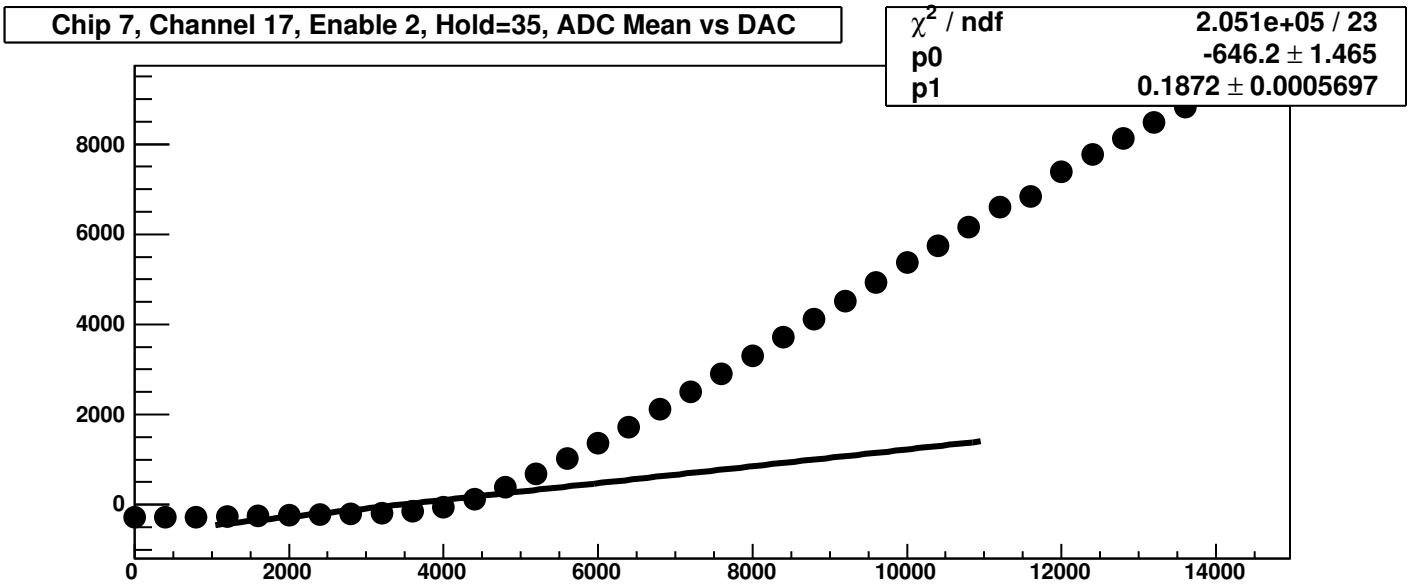
Chip 7, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC

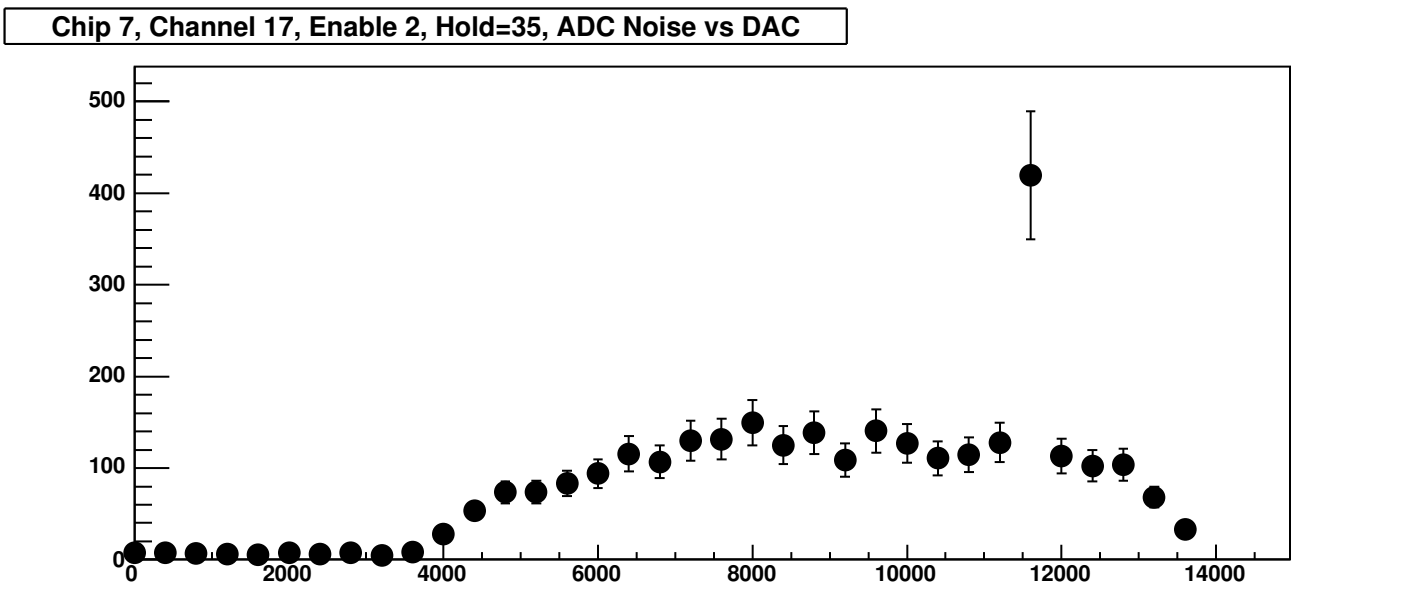


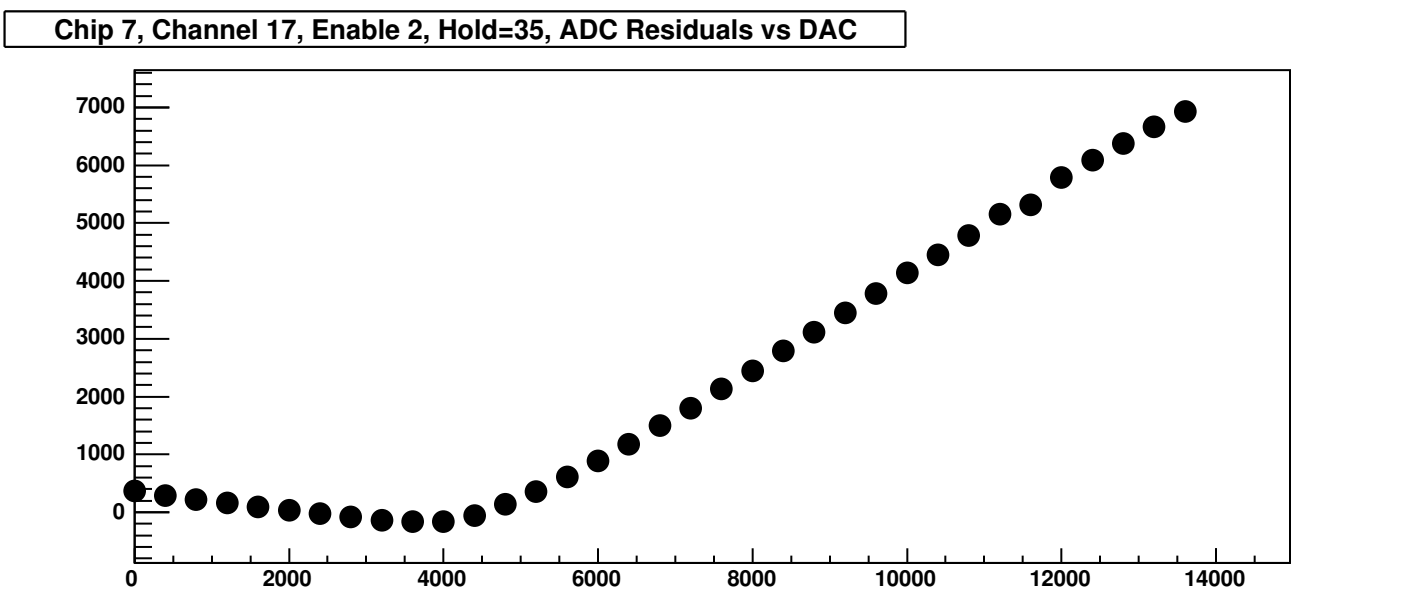

Chip 7, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.79 / 23
p0 -297.5 ± 0.7819
p1 0.00997 ± 0.00012
-140
-160
-180
-200
-220
-240
-260
-280
-300
0
2000
4000
6000
8000
10000
12000
14000

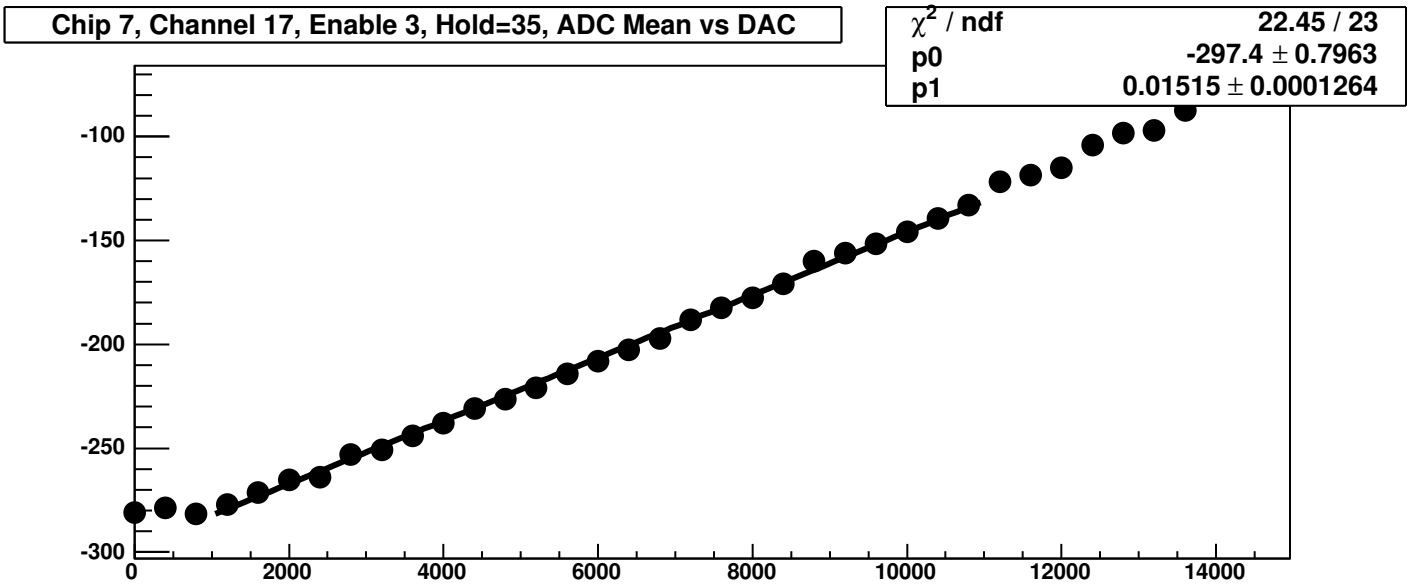

Chip 7, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

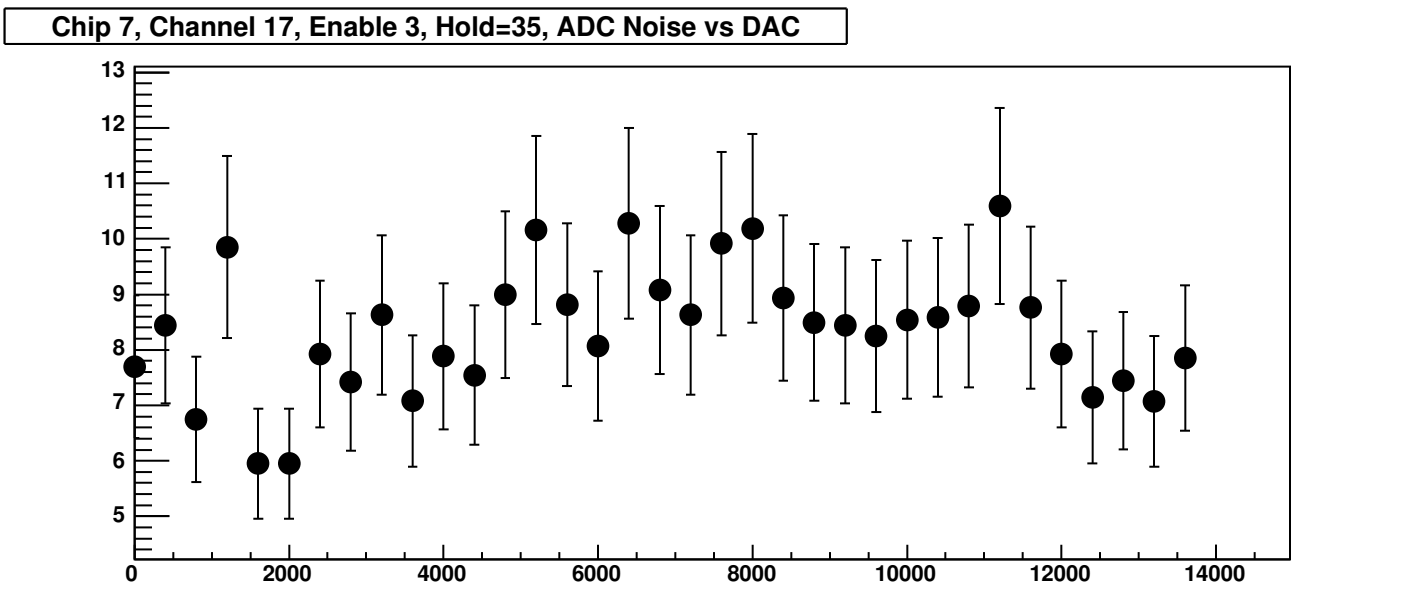

Chip 7, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

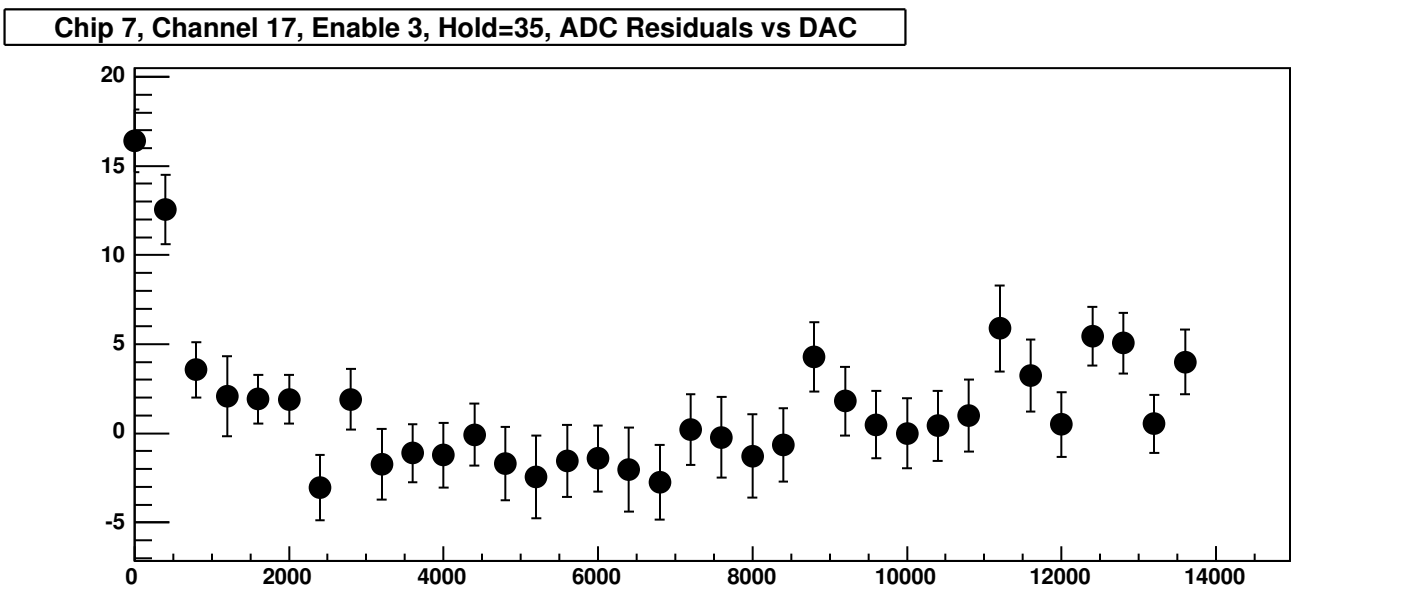

Chip 7, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 2.051e+05 / 23
p0 -646.2 ± 1.465
p1 0.1872 ± 0.0005697
8000
6000
4000
2000
0
0
2000
4000
6000
8000
10000
12000
14000

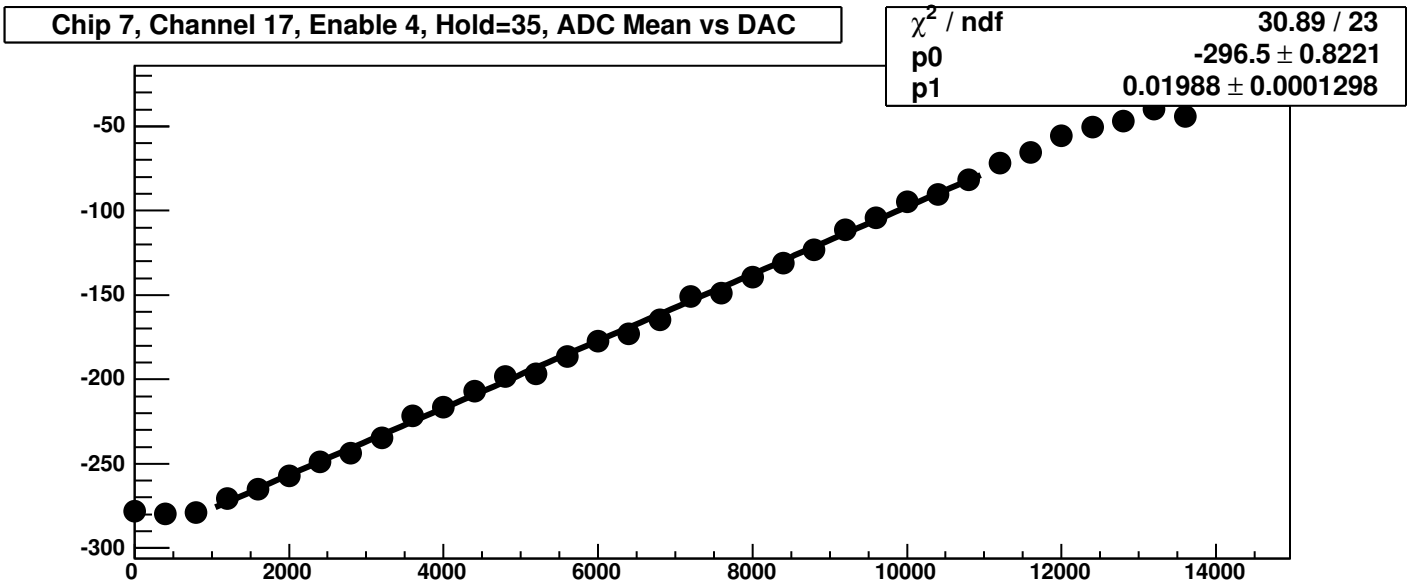

Chip 7, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

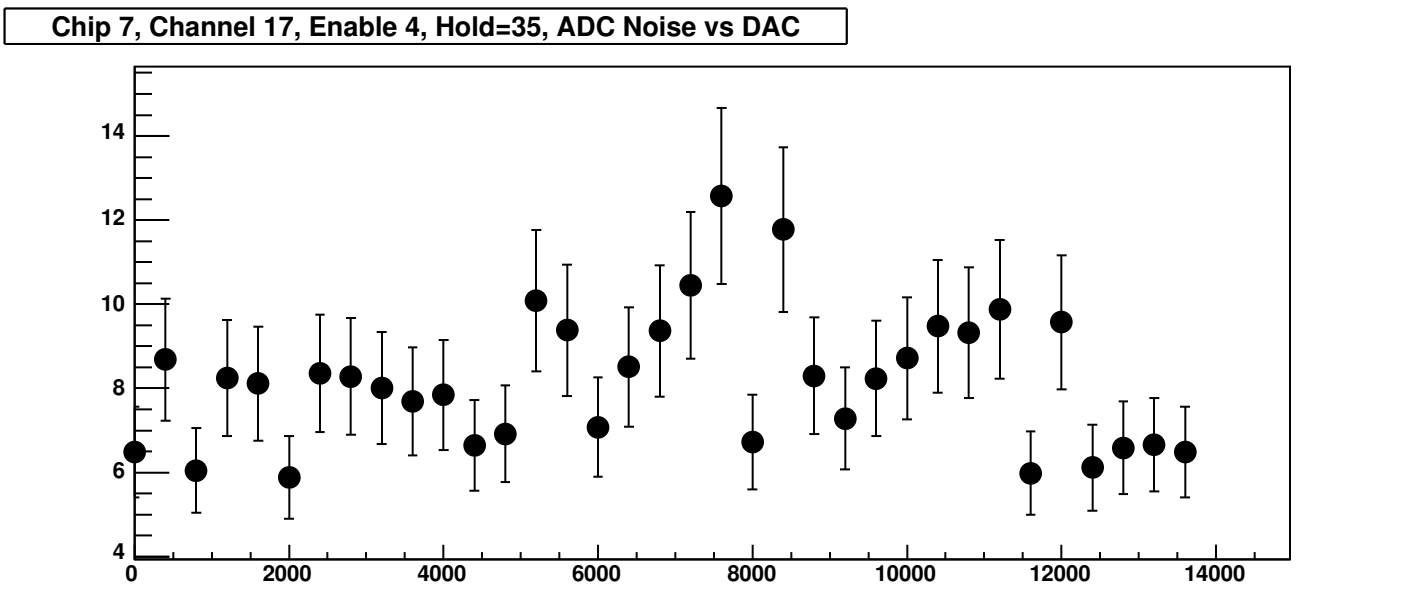

Chip 7, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
7000
6000
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

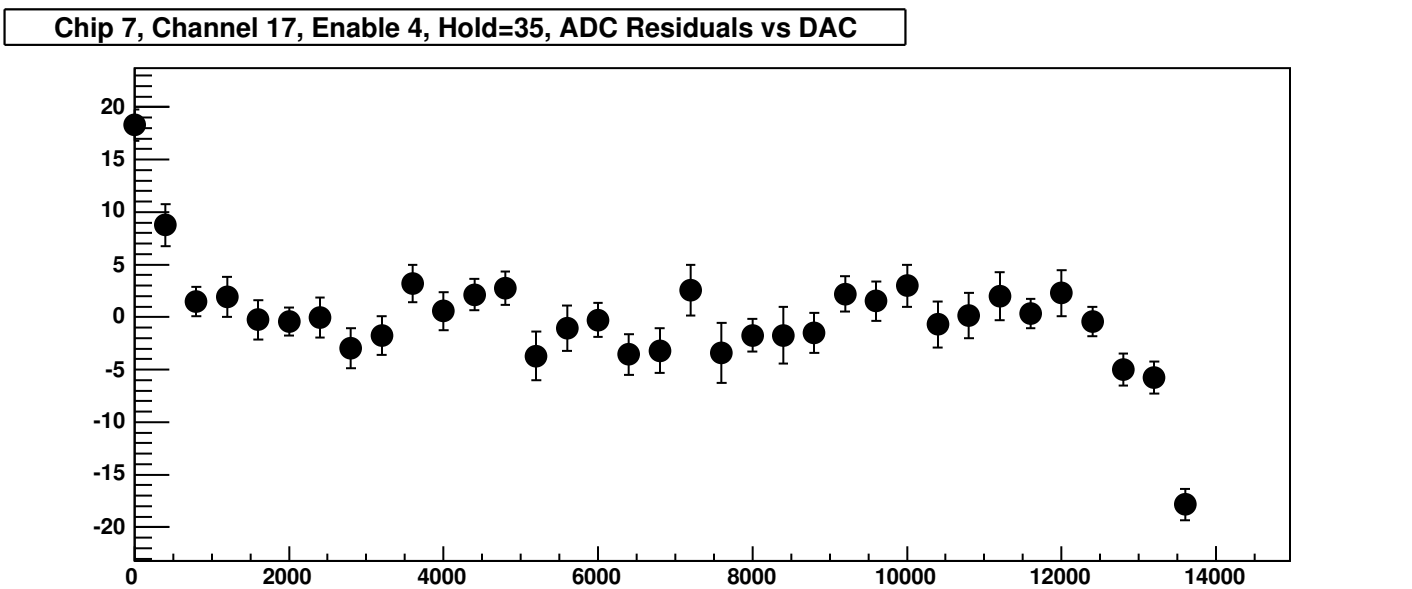

Chip 7, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 22.45 / 23
p0 -297.4 ± 0.7963
p1 0.01515 ± 0.0001264
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

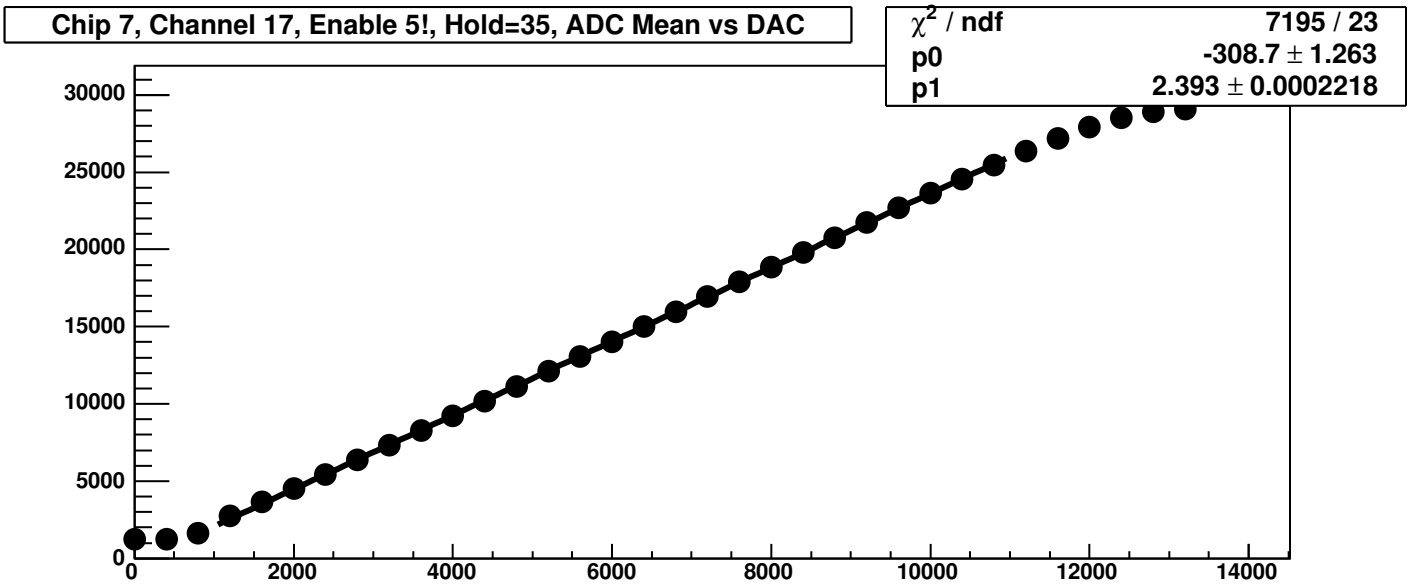

Chip 7, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

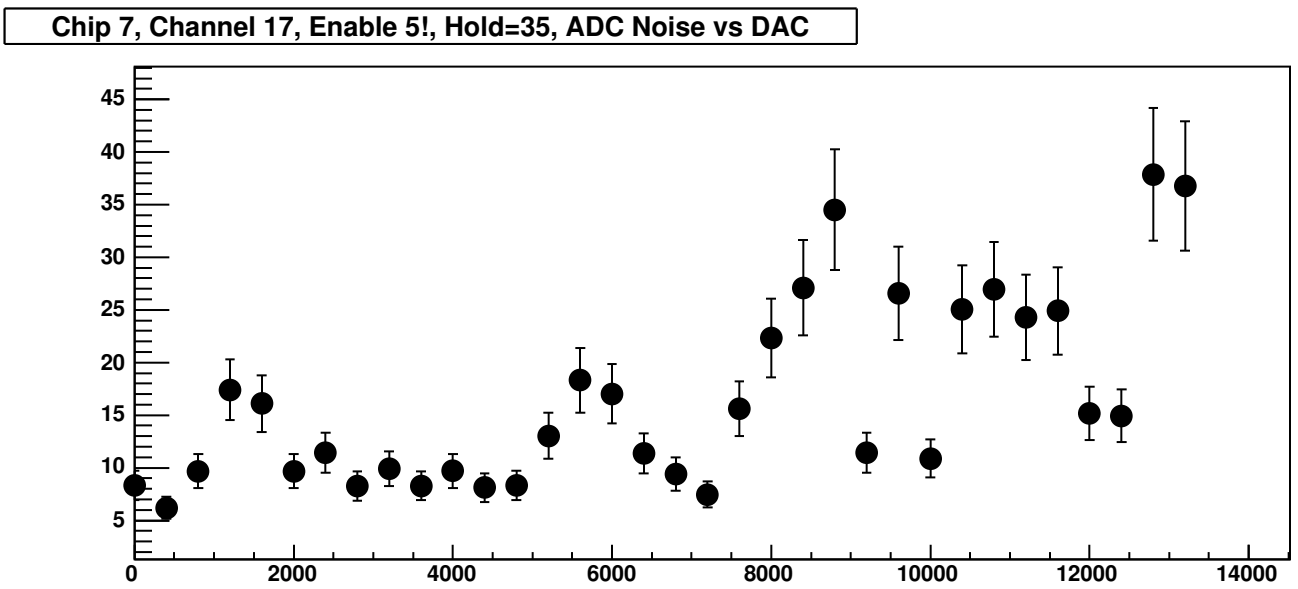

Chip 7, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

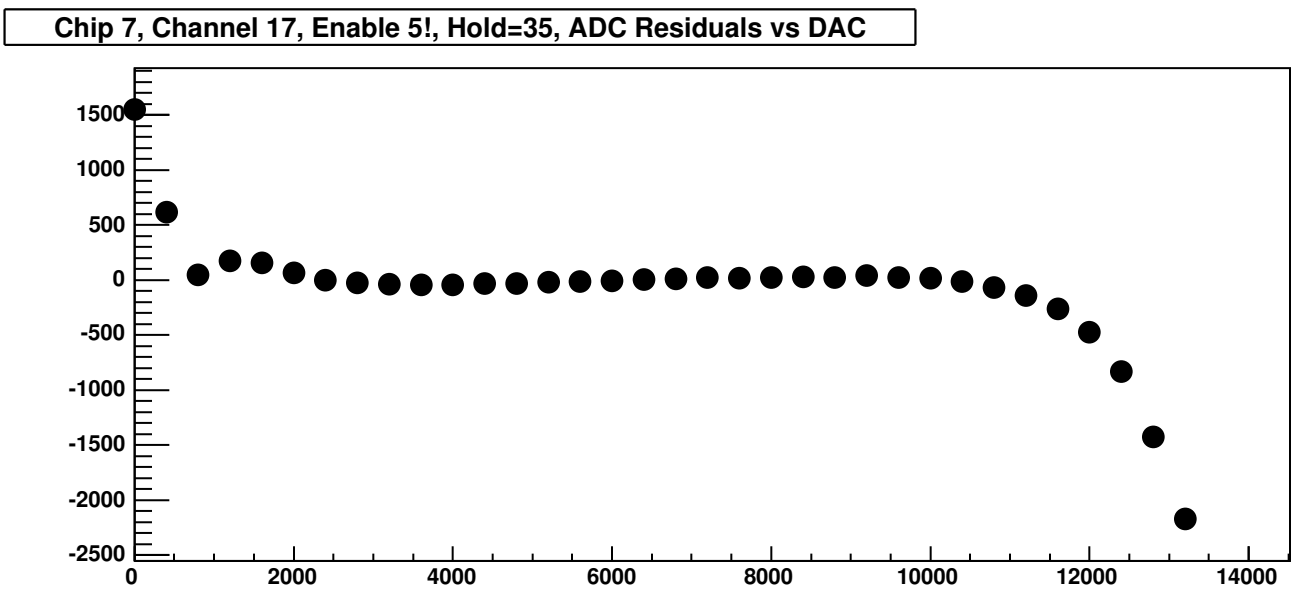

Chip 7, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.89 / 23
p0 -296.5 ± 0.8221
p1 0.01988 ± 0.0001298
-50
-100
-150
-200
-250
-300
0
2000
4000
6000
8000
10000
12000
14000

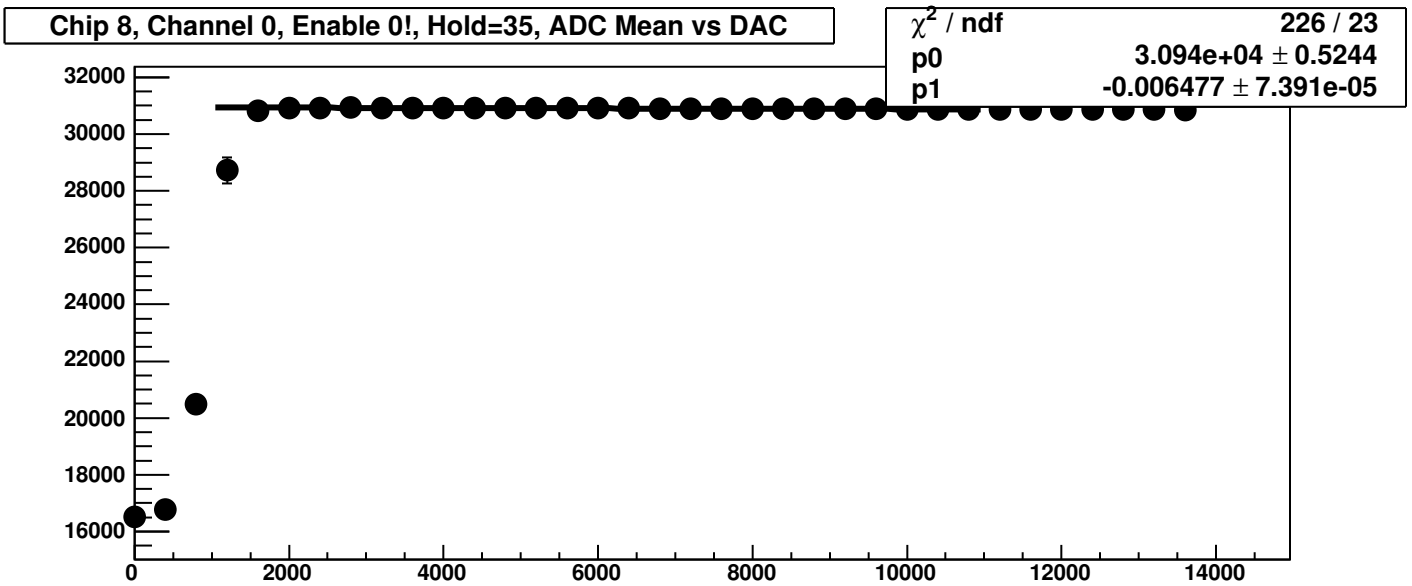

Chip 7, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

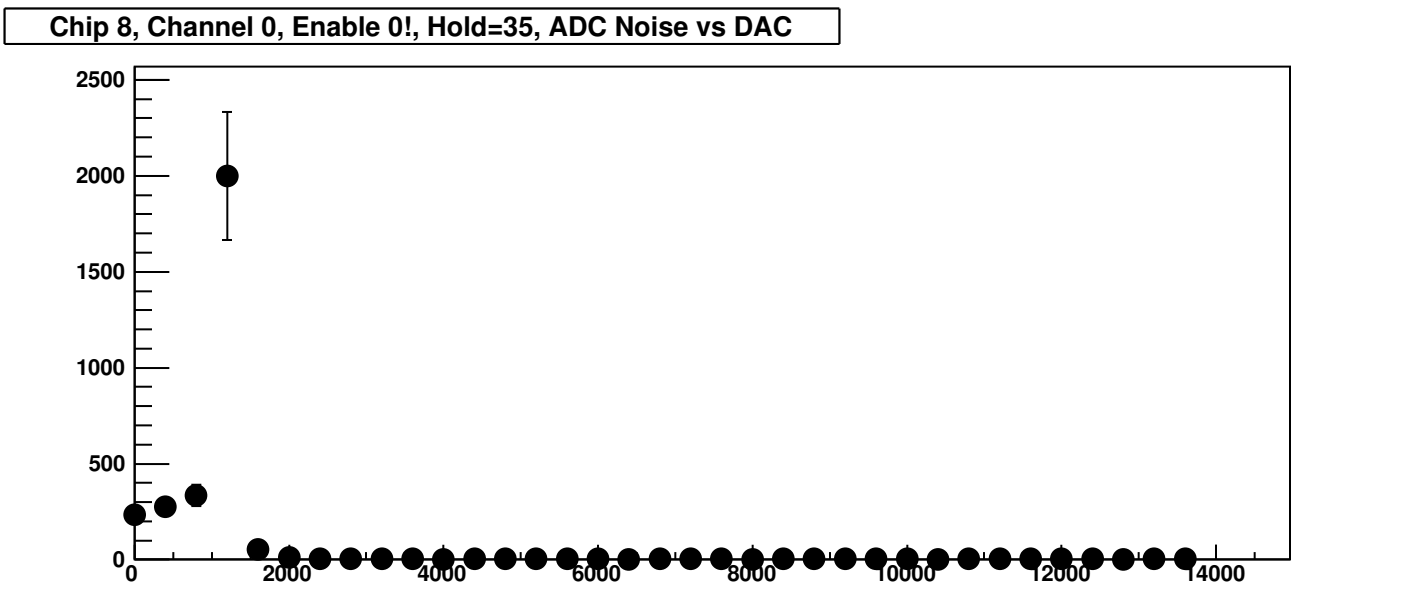

Chip 7, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

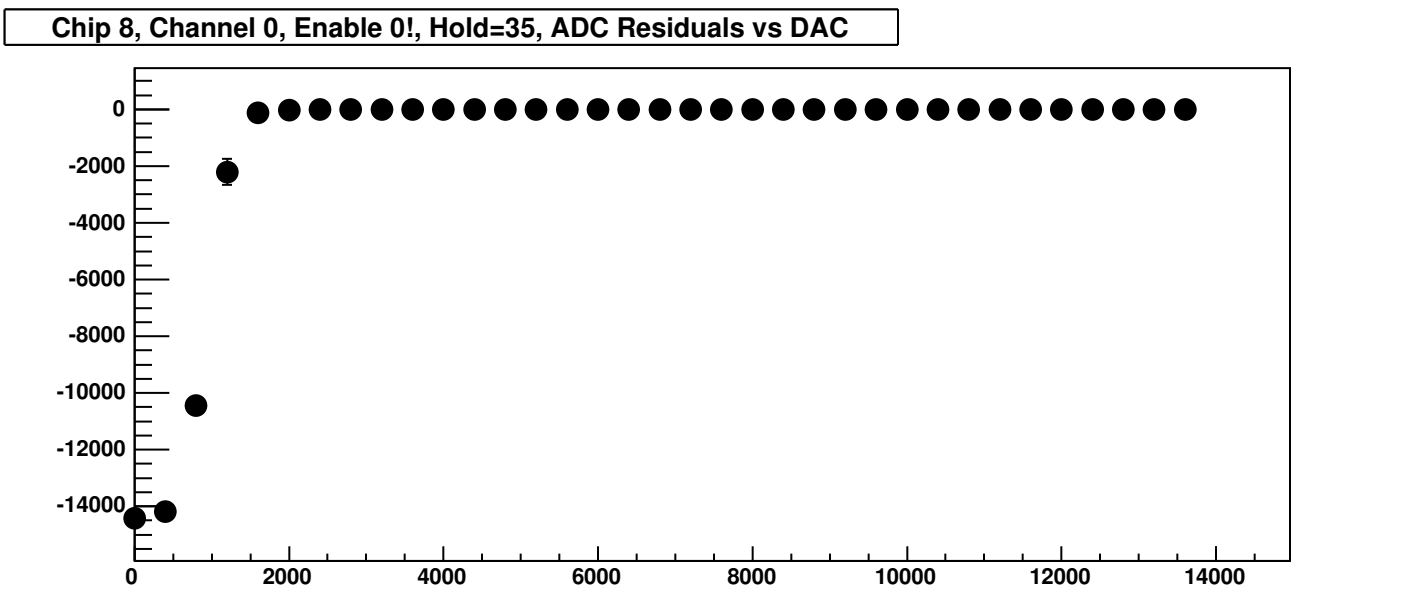

Chip 7, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 7195 / 23
p0 -308.7 ± 1.263
p1 2.393 ± 0.0002218
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

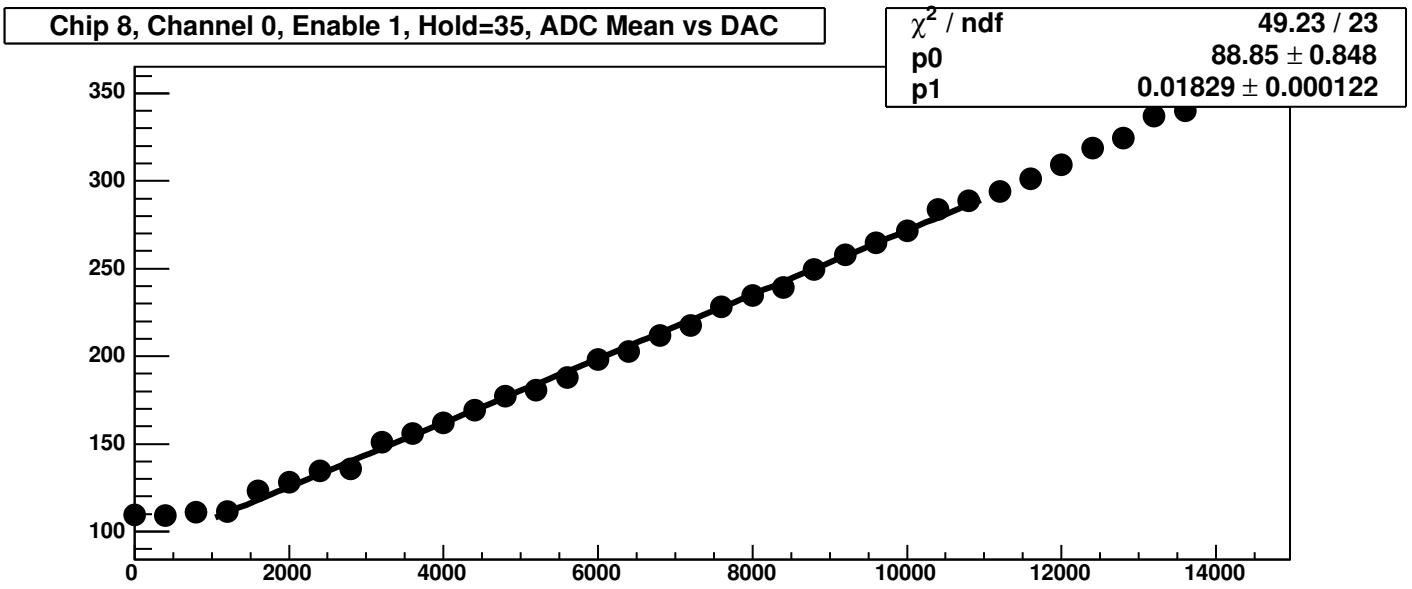

Chip 7, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

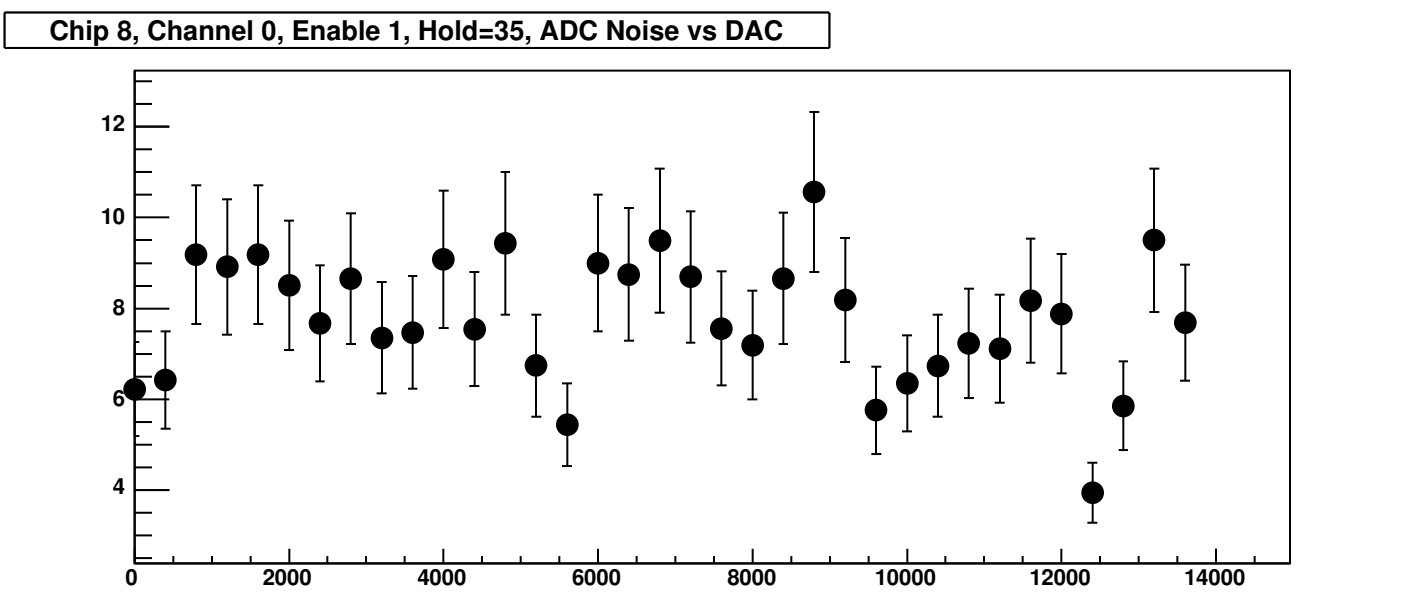

Chip 7, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

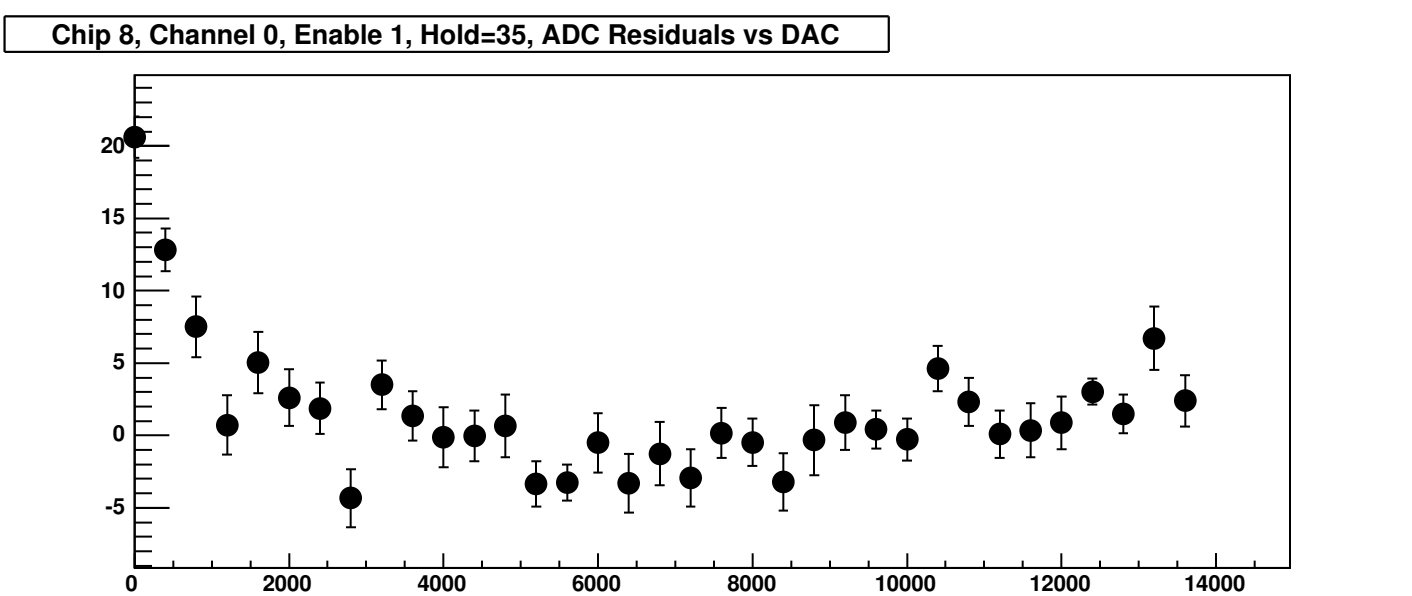

Chip 8, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 226 / 23
p0 3.094e+04 ± 0.5244
p1 -0.006477 ± 7.391e-05
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

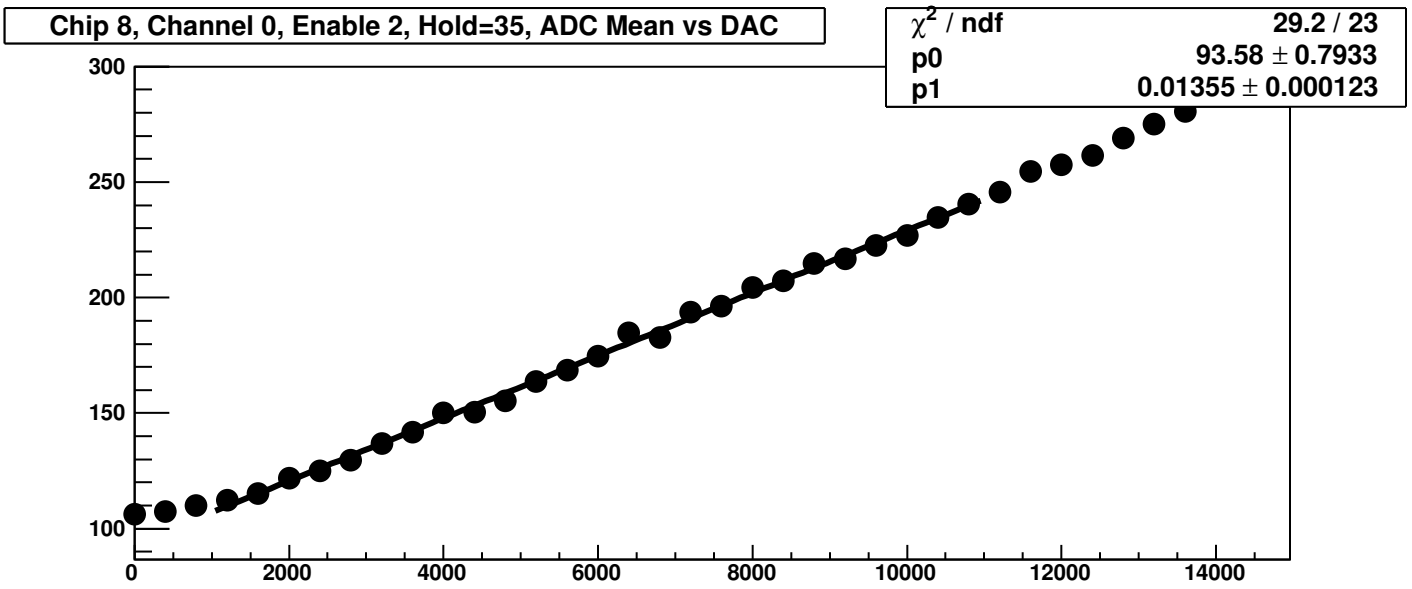

Chip 8, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

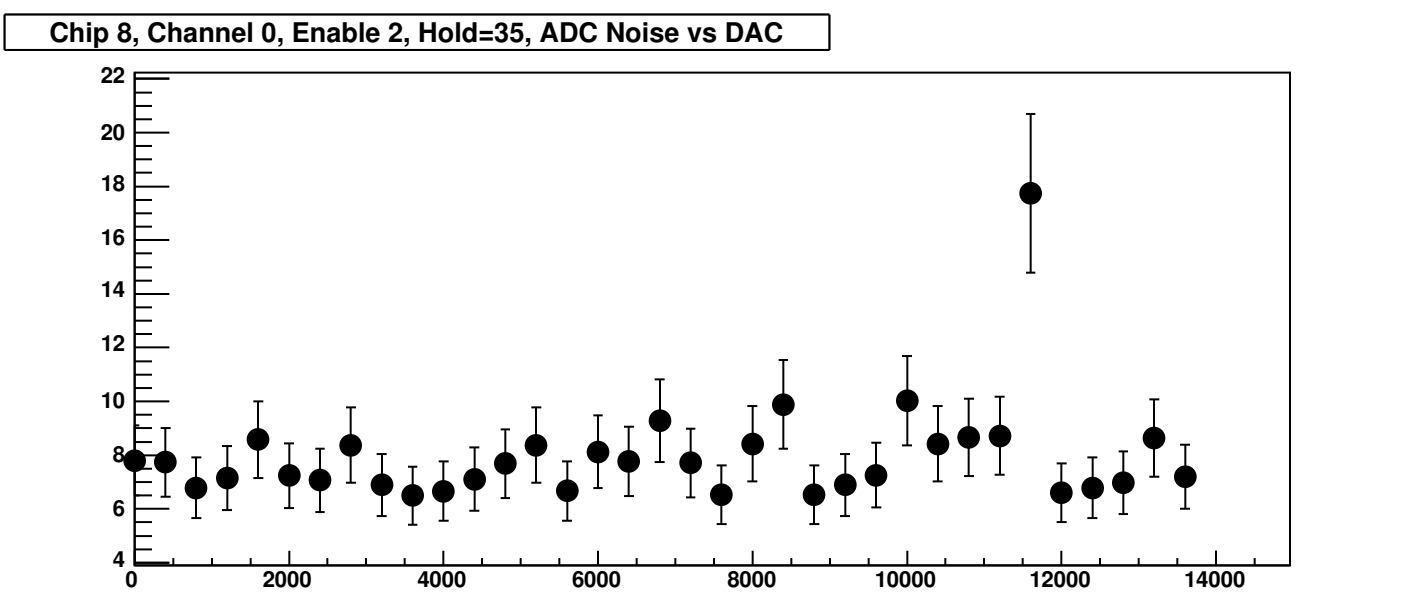

Chip 8, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

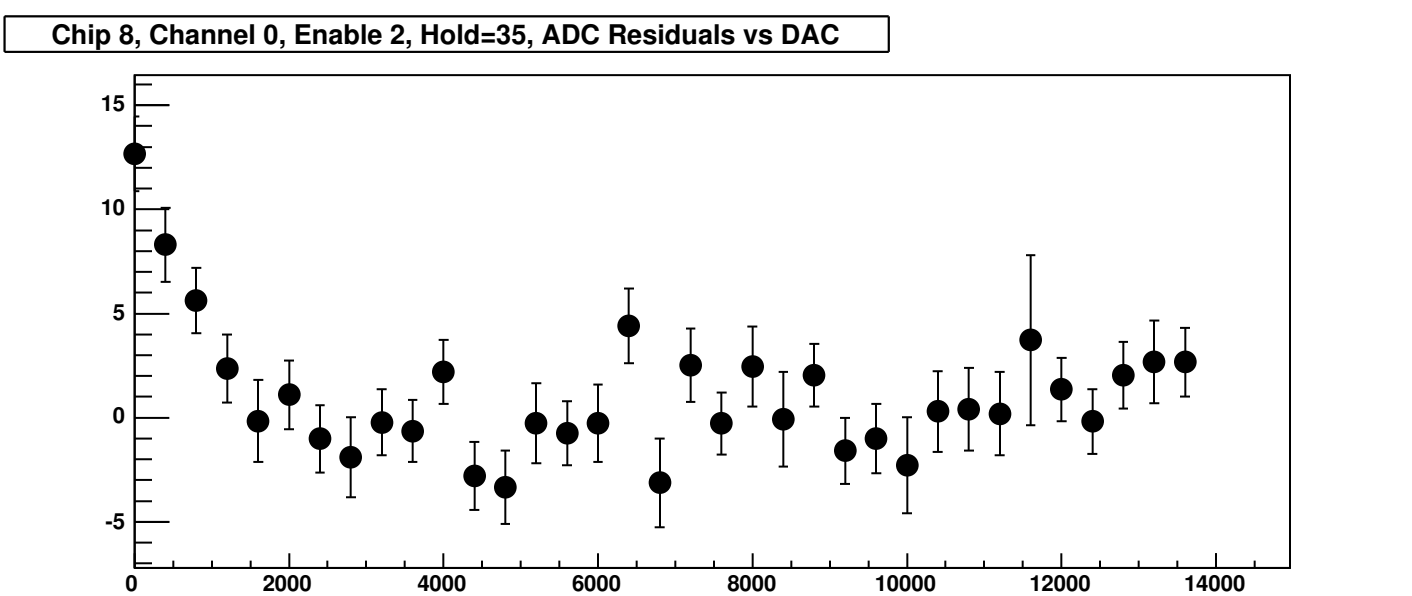

Chip 8, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 49.23 / 23
p0 88.85 ± 0.848
p1 0.01829 ± 0.000122
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

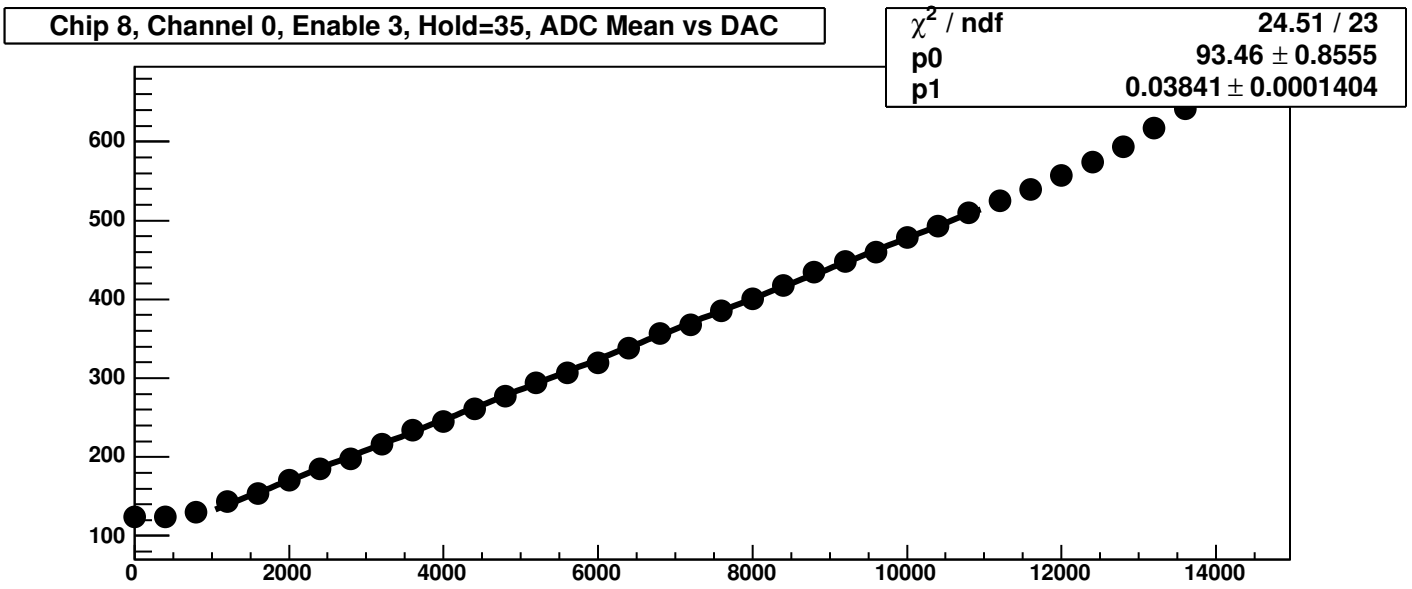

Chip 8, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

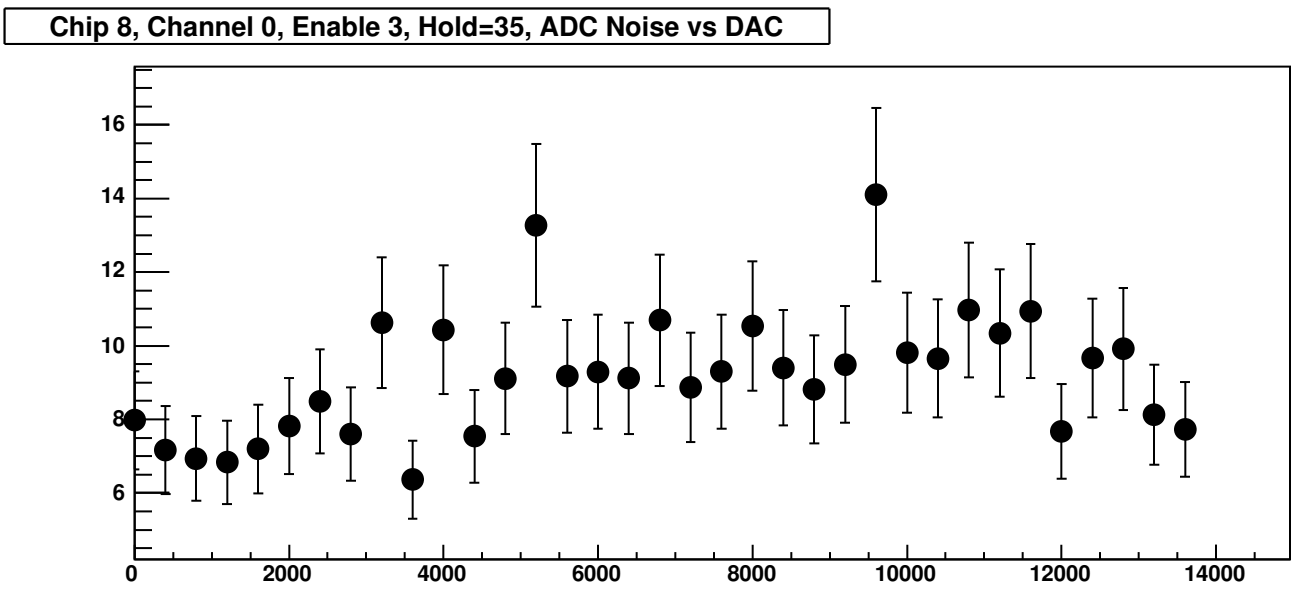

Chip 8, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

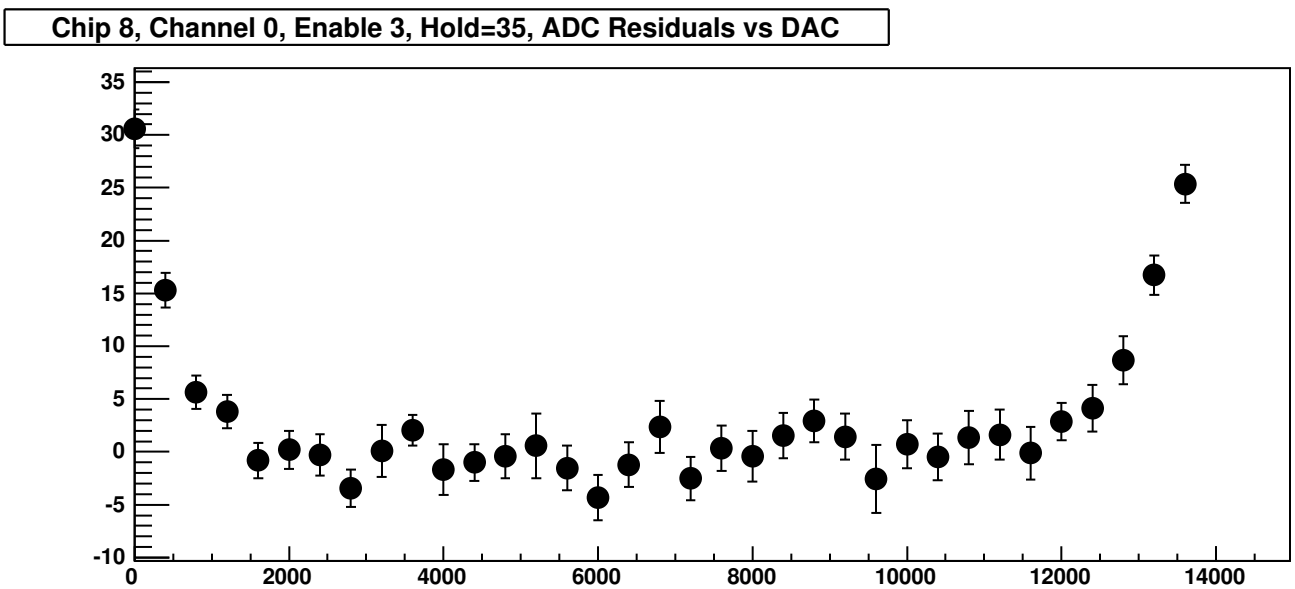

Chip 8, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 29.2 / 23
p0 93.58 ± 0.7933
p1 0.01355 ± 0.000123
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

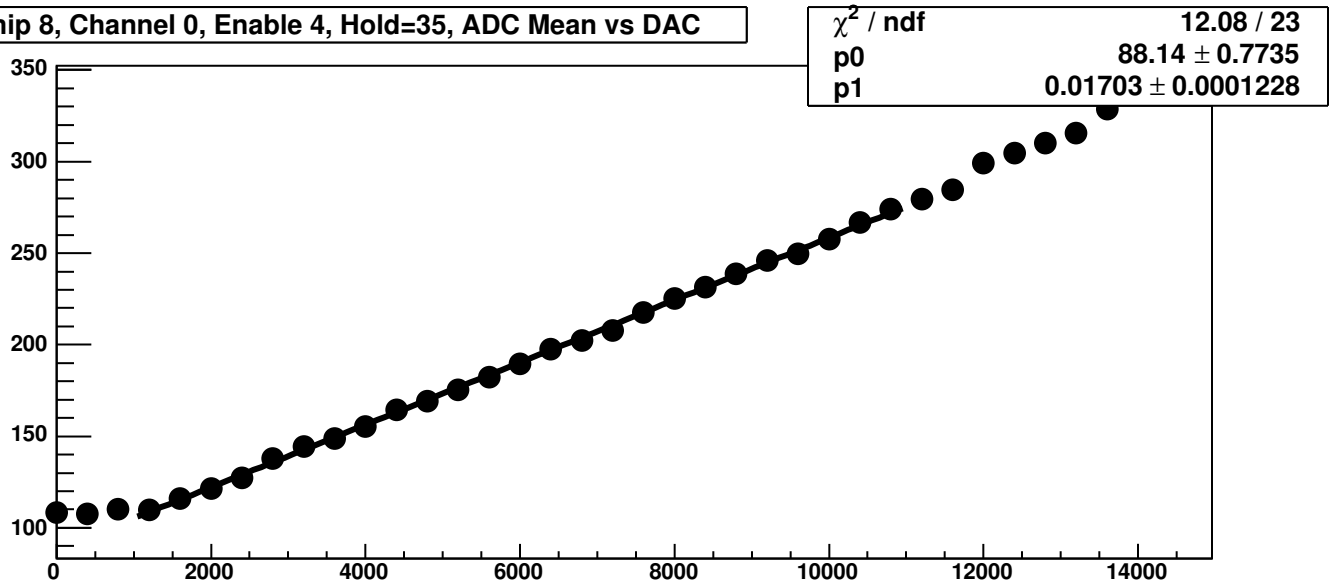

Chip 8, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.51 / 23
p0 93.46 ± 0.8555
p1 0.03841 ± 0.0001404
600
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000

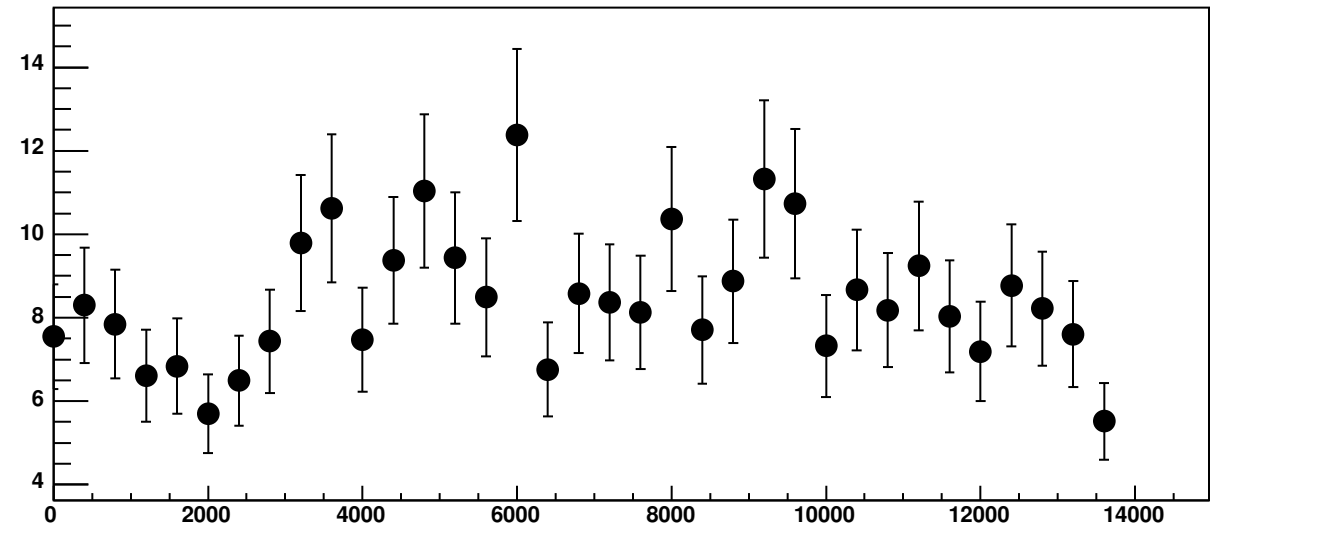

Chip 8, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

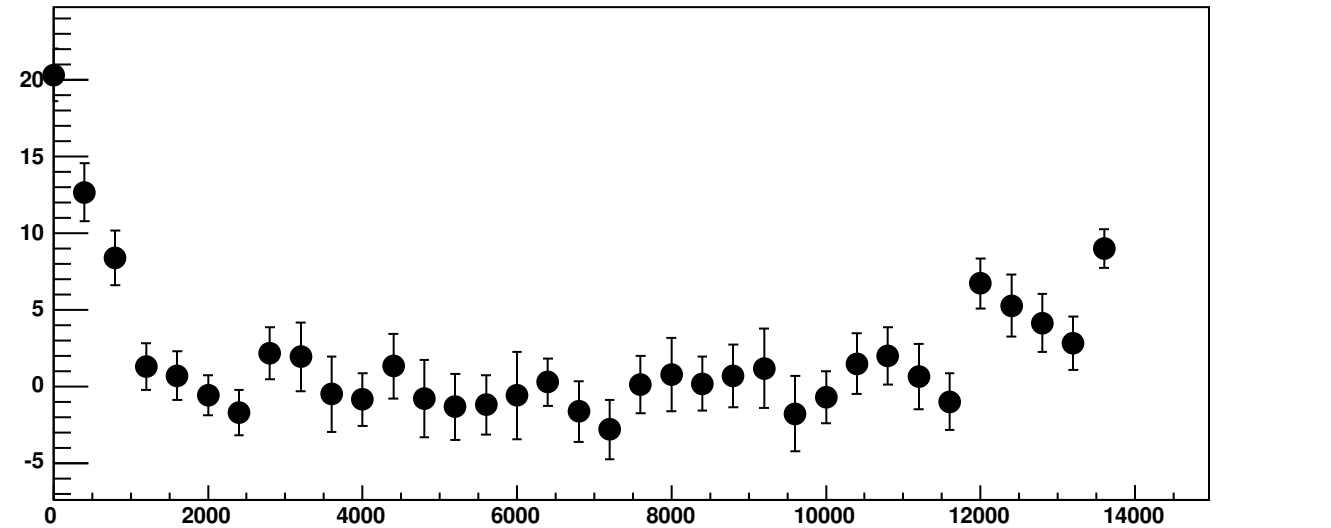
Chip 8, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

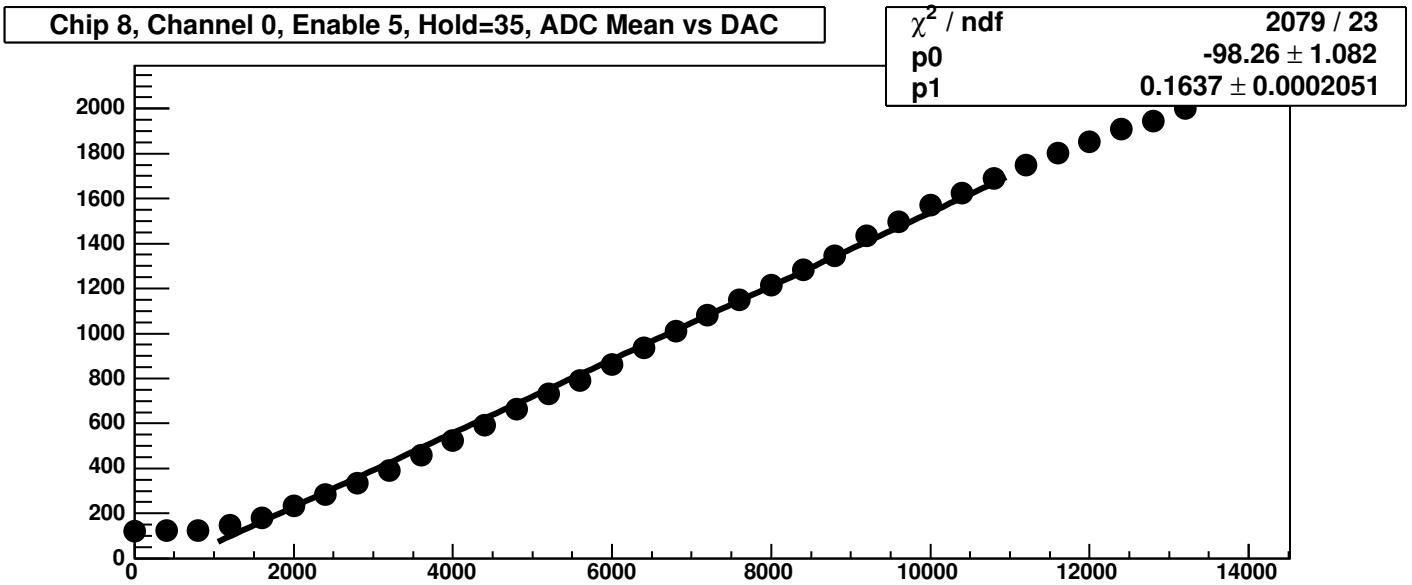


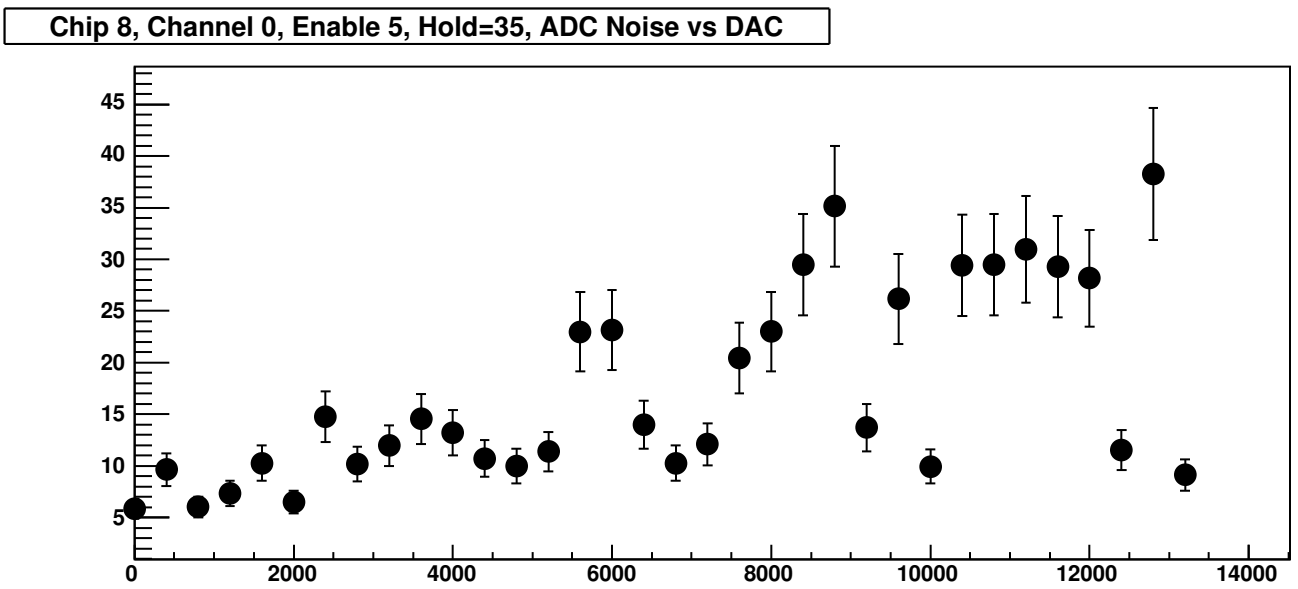
Chip 8, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

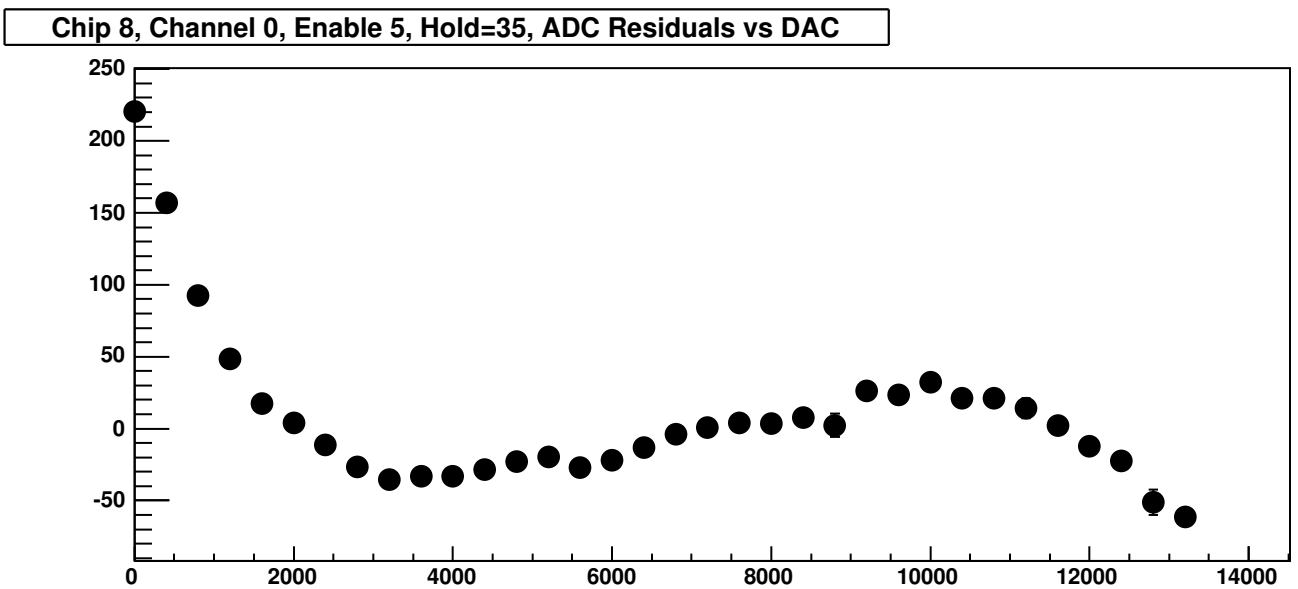


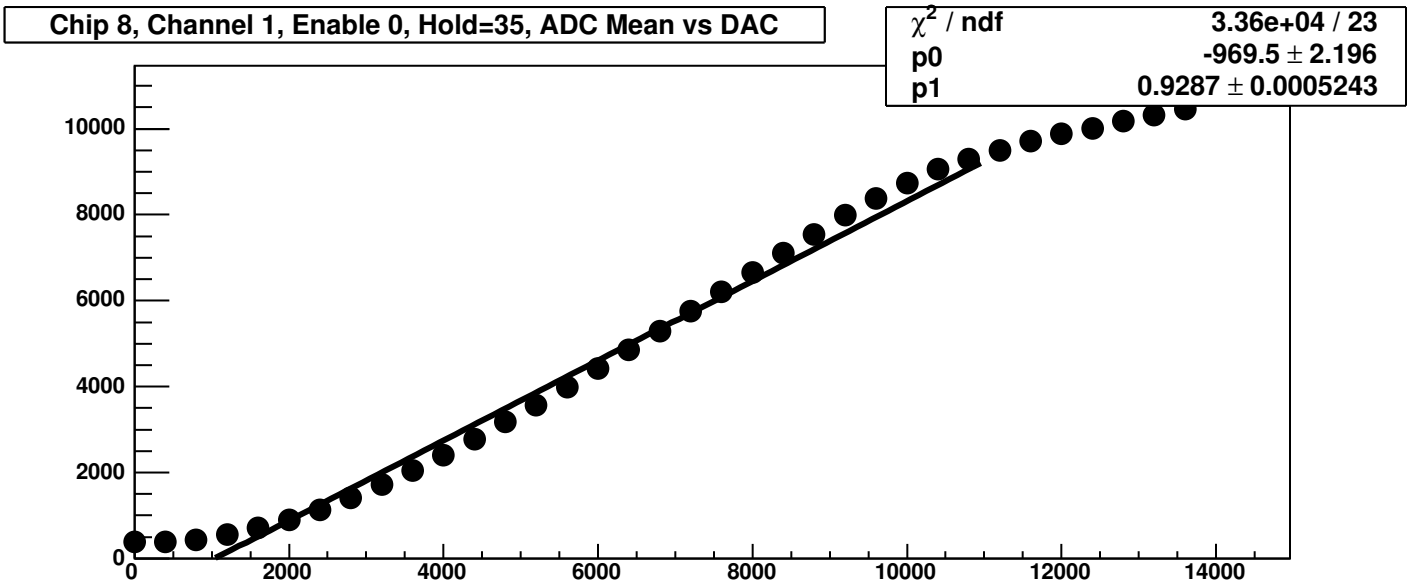
Chip 8, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

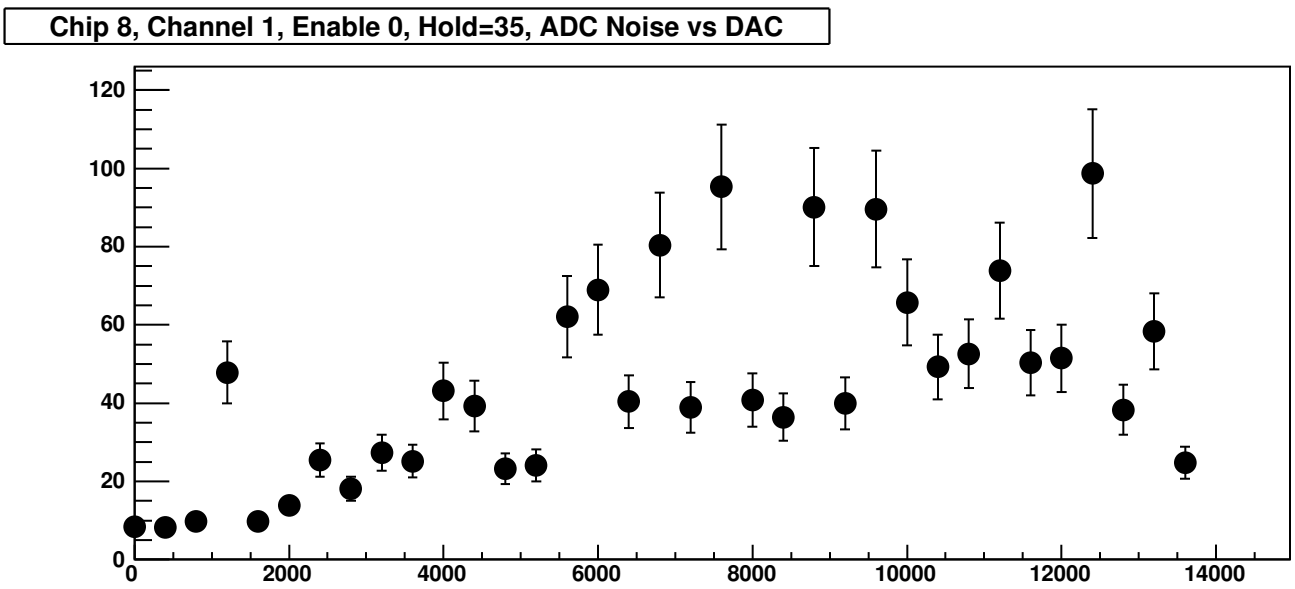


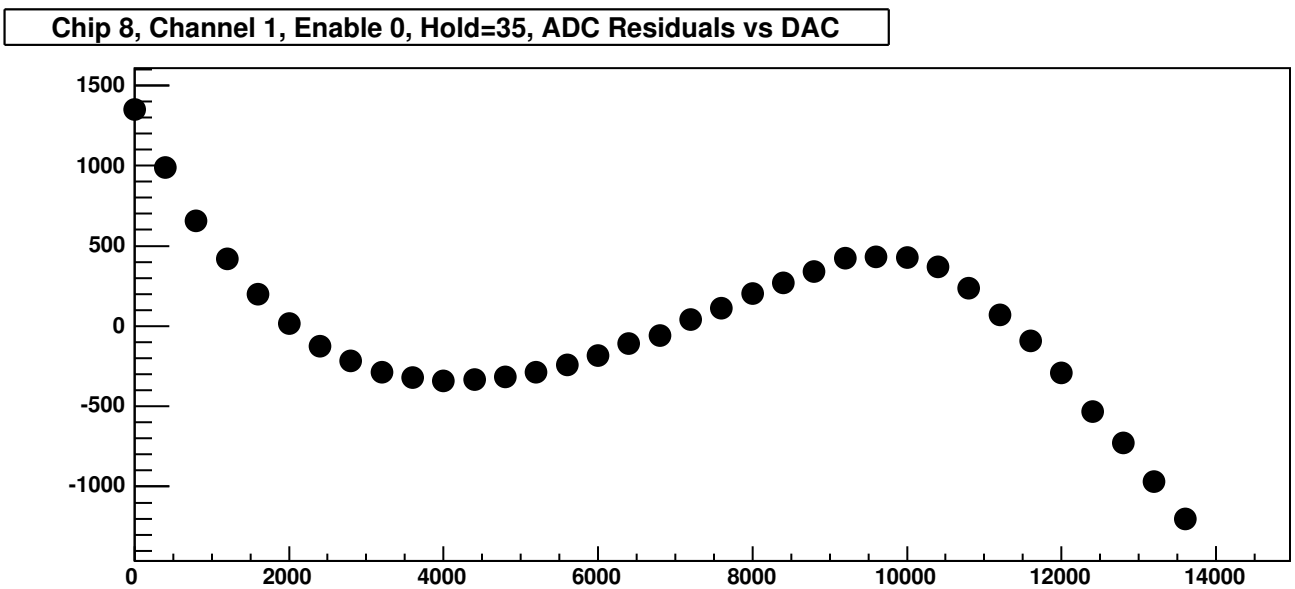

Chip 8, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2079 / 23
p0 -98.26 ± 1.082
p1 0.1637 ± 0.0002051
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

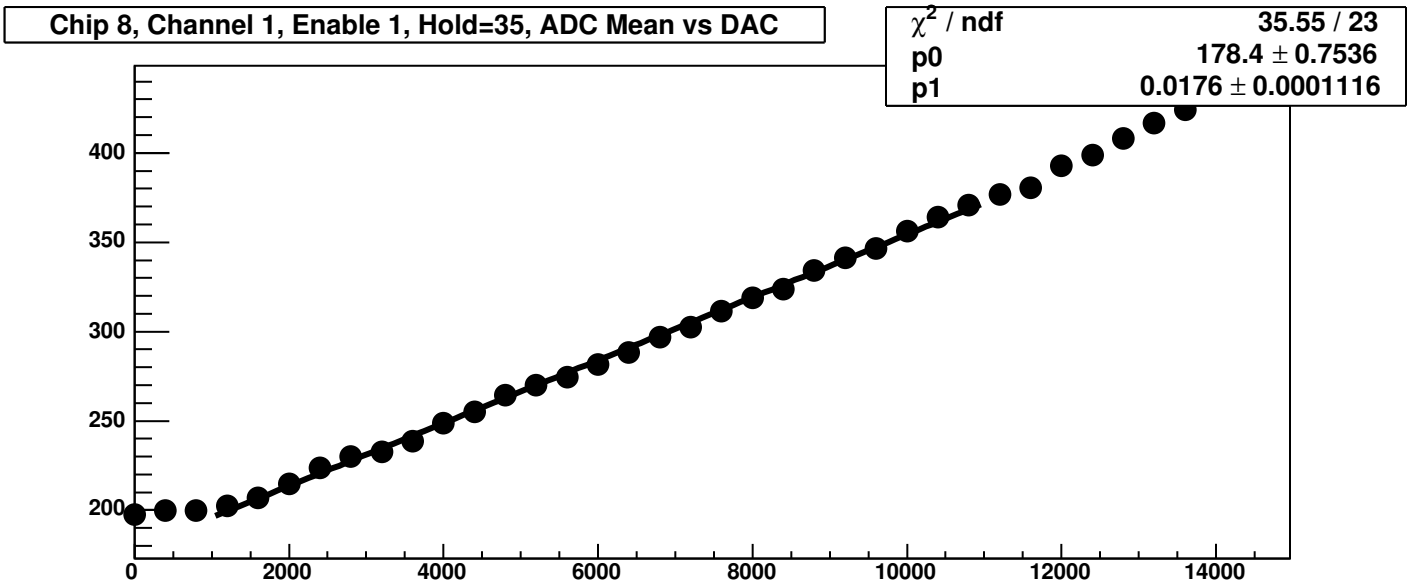

Chip 8, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

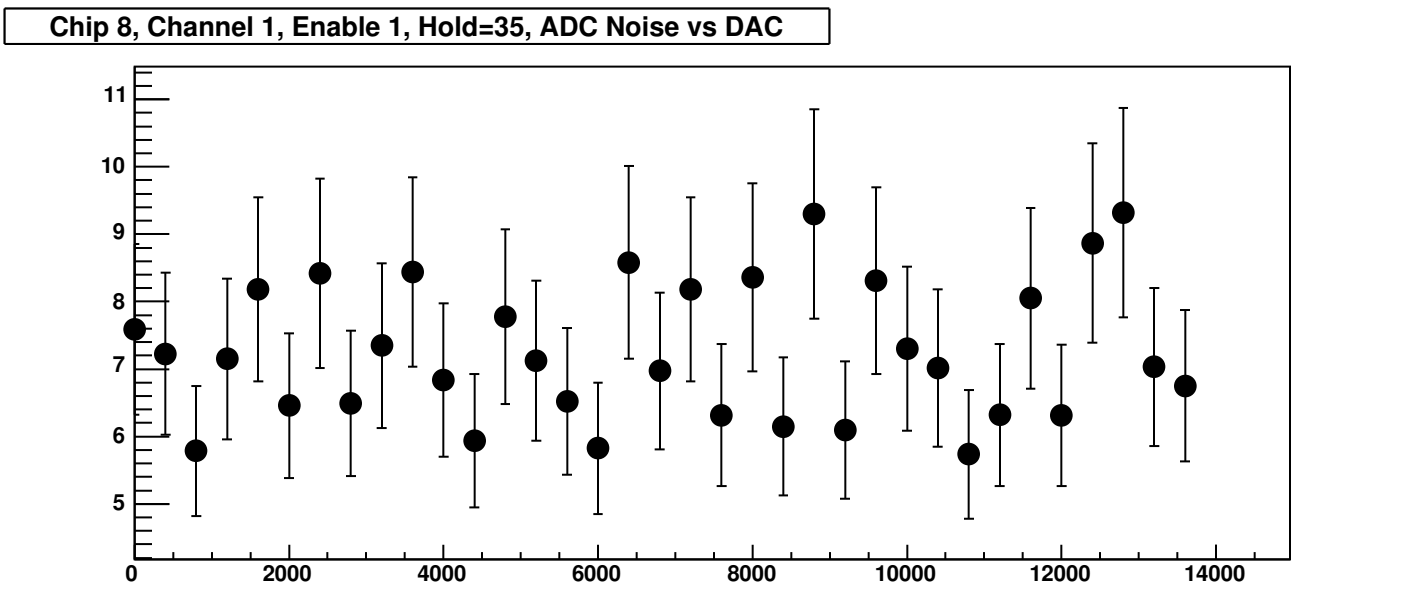

Chip 8, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

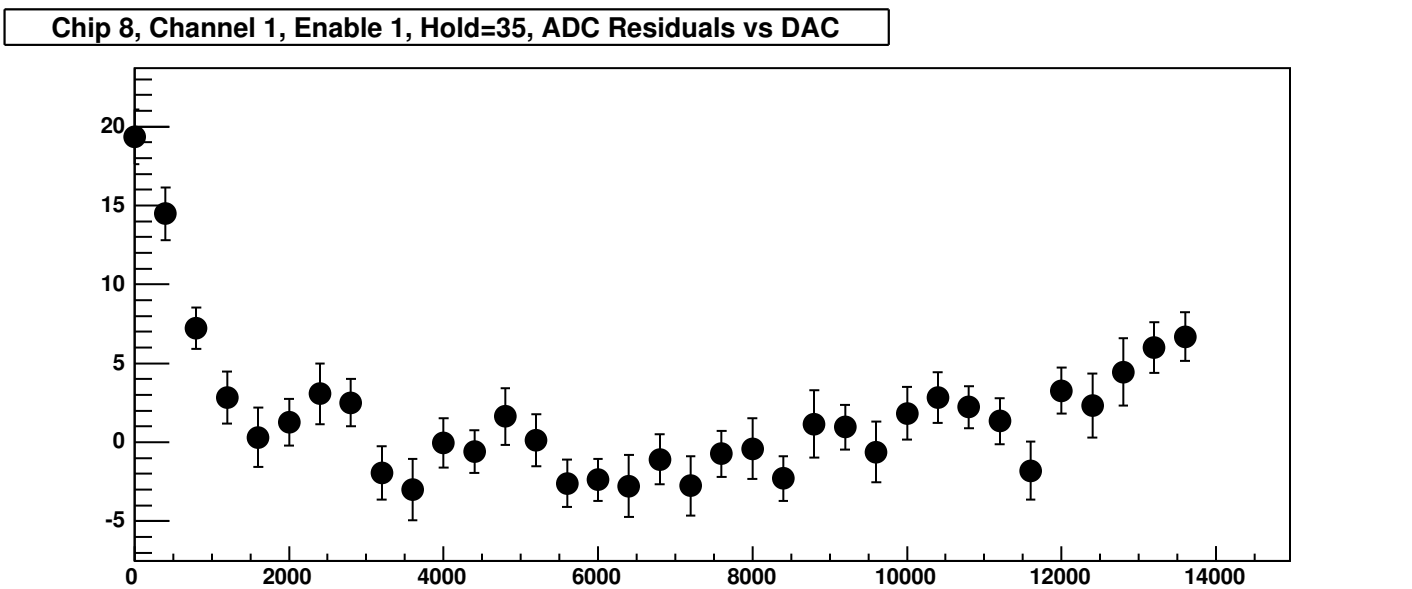

Chip 8, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.36e+04 / 23
p0 -969.5 ± 2.196
p1 0.9287 ± 0.0005243
0
2000
4000
6000
8000
10000
12000
14000

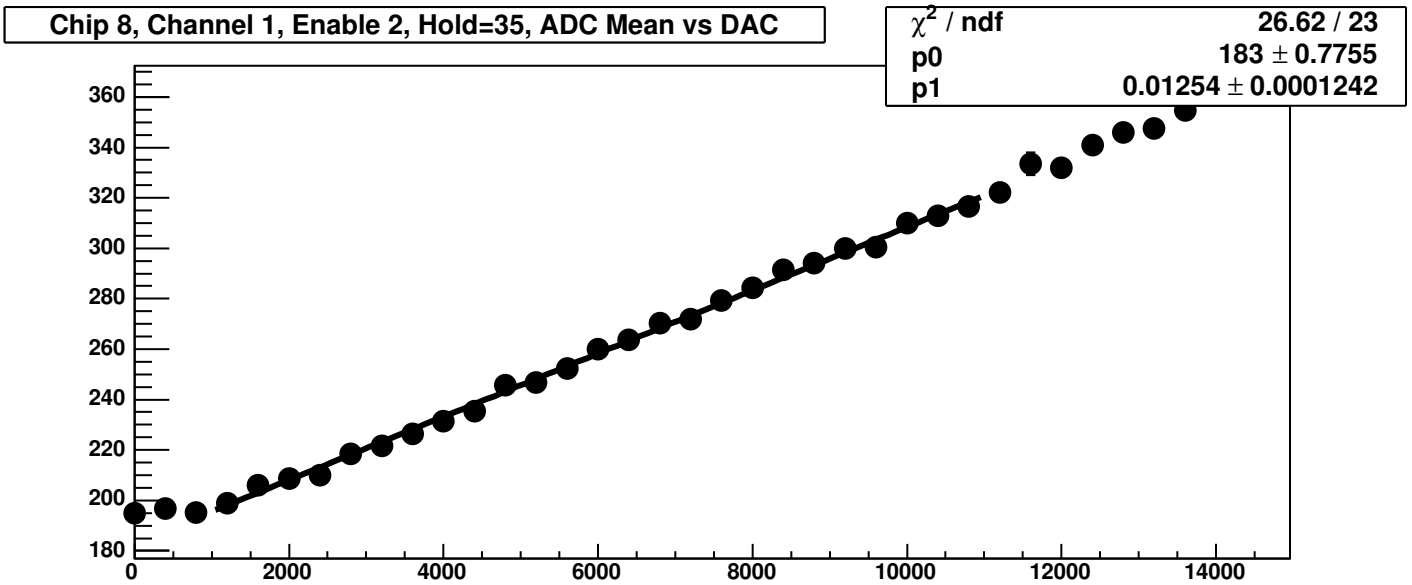

Chip 8, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
0
20
40
60
80
100
120
2000
4000
6000
8000
10000
12000
14000

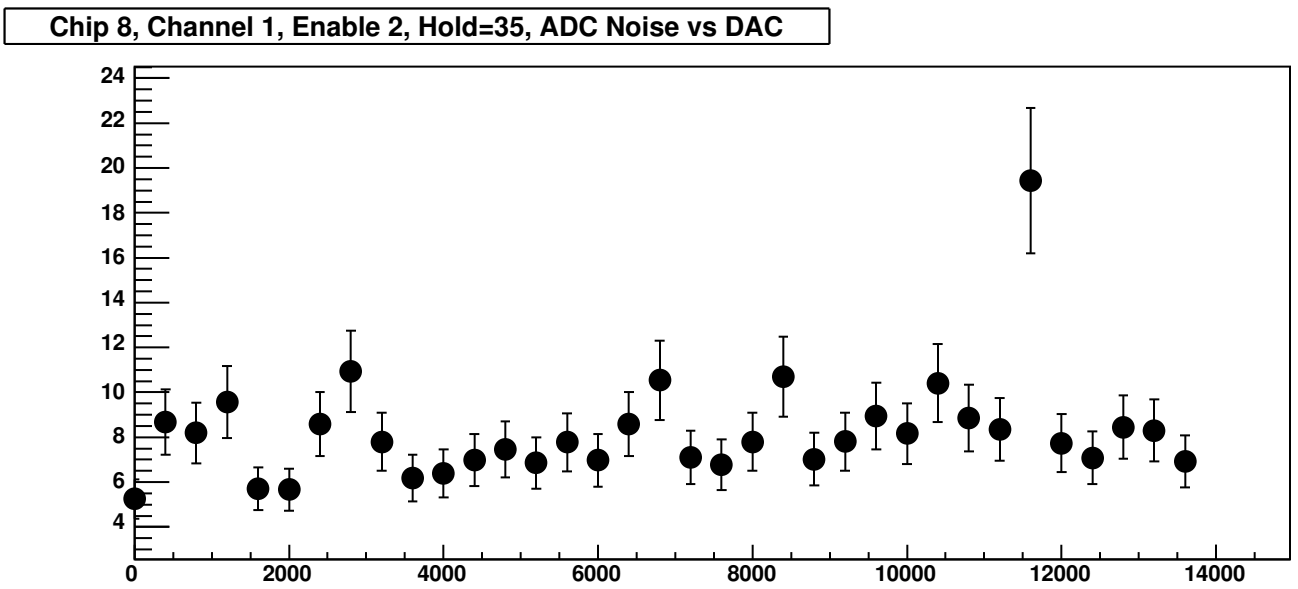

Chip 8, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
2000
4000
6000
8000
10000
12000
14000

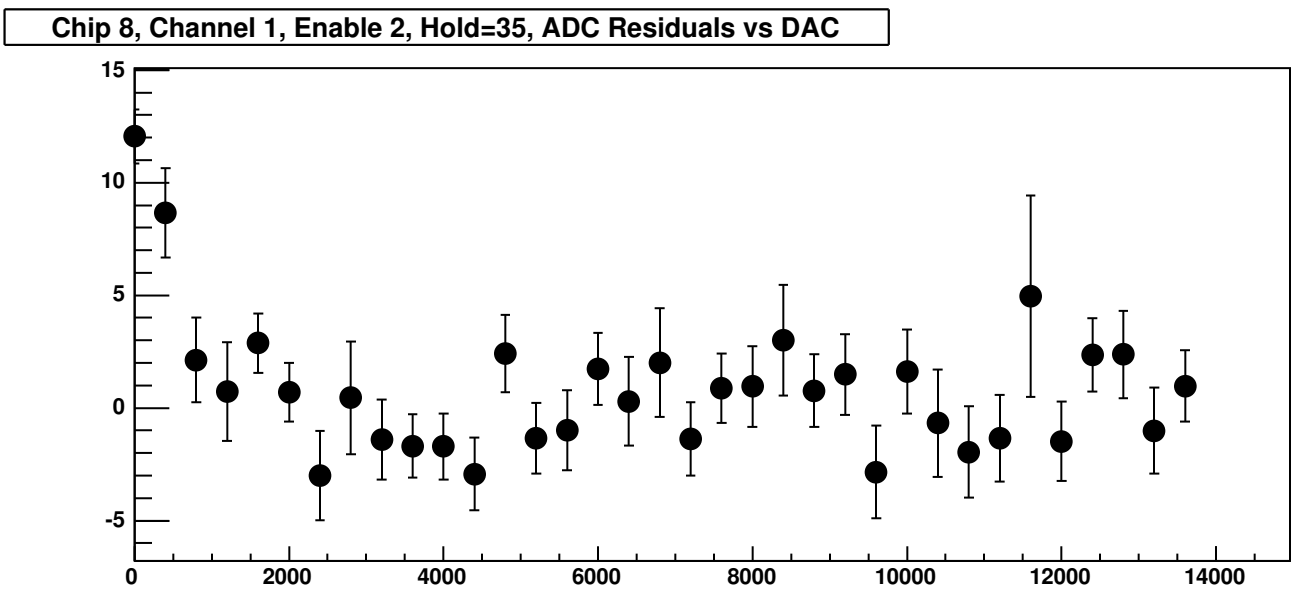

Chip 8, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 35.55 / 23
p0 178.4 ± 0.7536
p1 0.0176 ± 0.0001116
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

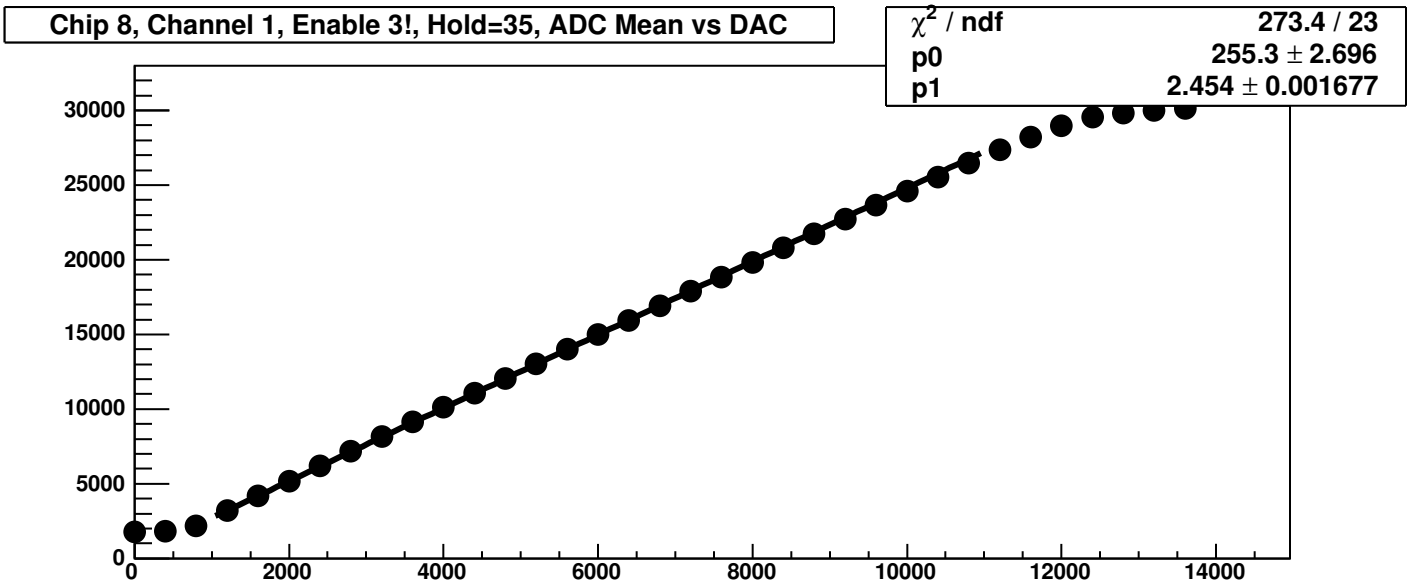

Chip 8, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

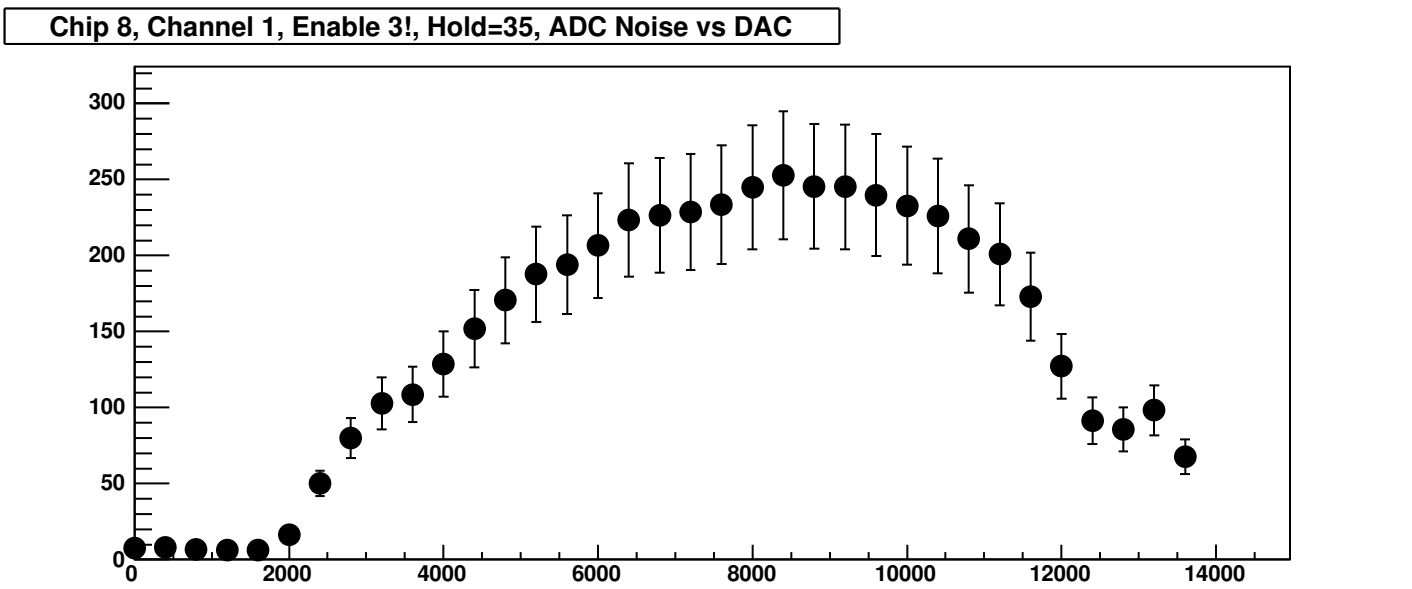

Chip 8, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

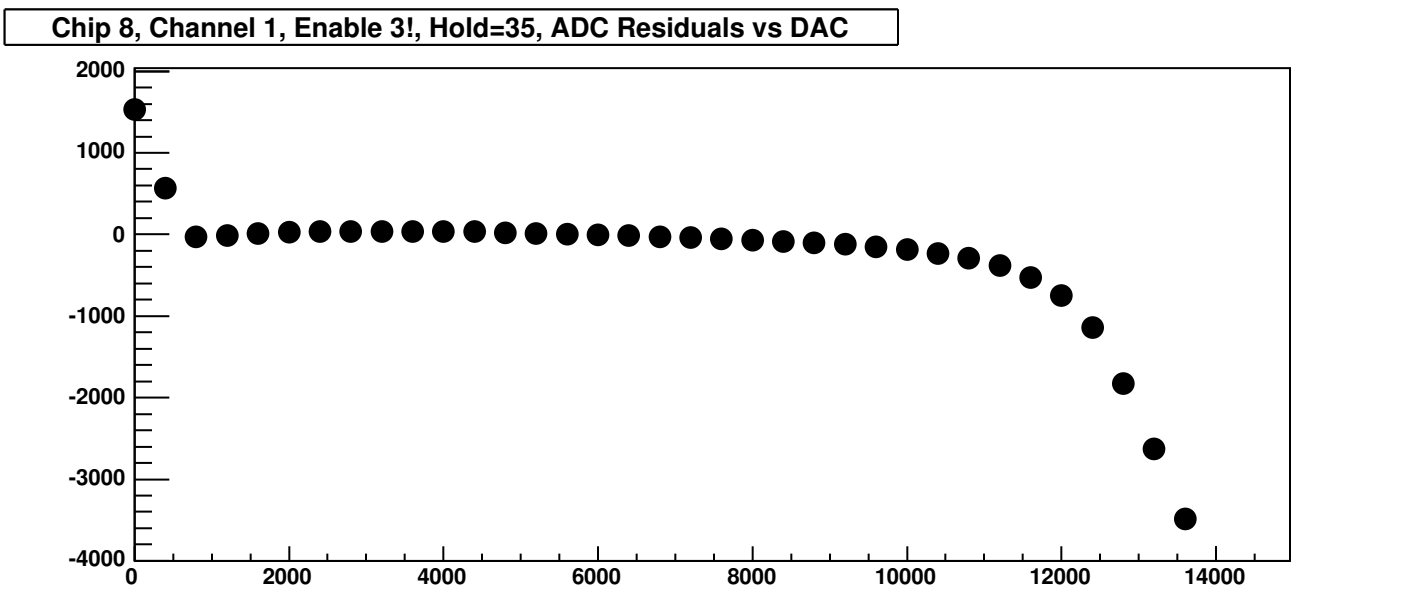

Chip 8, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 26.62 / 23
p0 183 ± 0.7755
p1 0.01254 ± 0.0001242
360
340
320
300
280
260
240
220
200
180
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
24
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

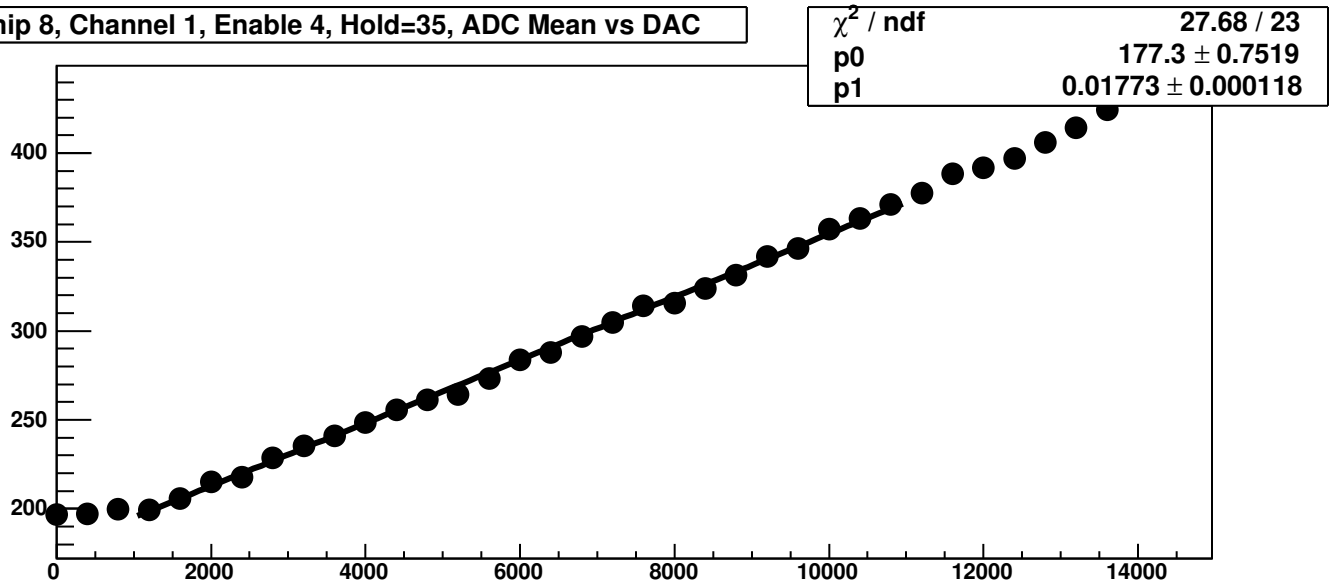

Chip 8, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 273.4 / 23
p0 255.3 ± 2.696
p1 2.454 ± 0.001677
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

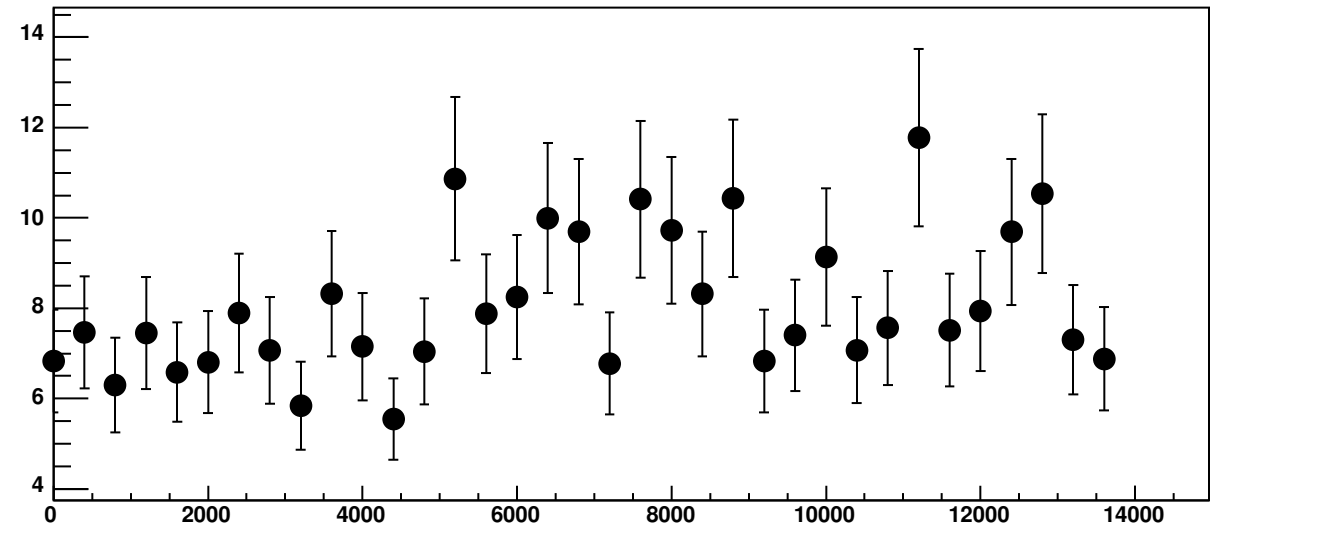

Chip 8, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

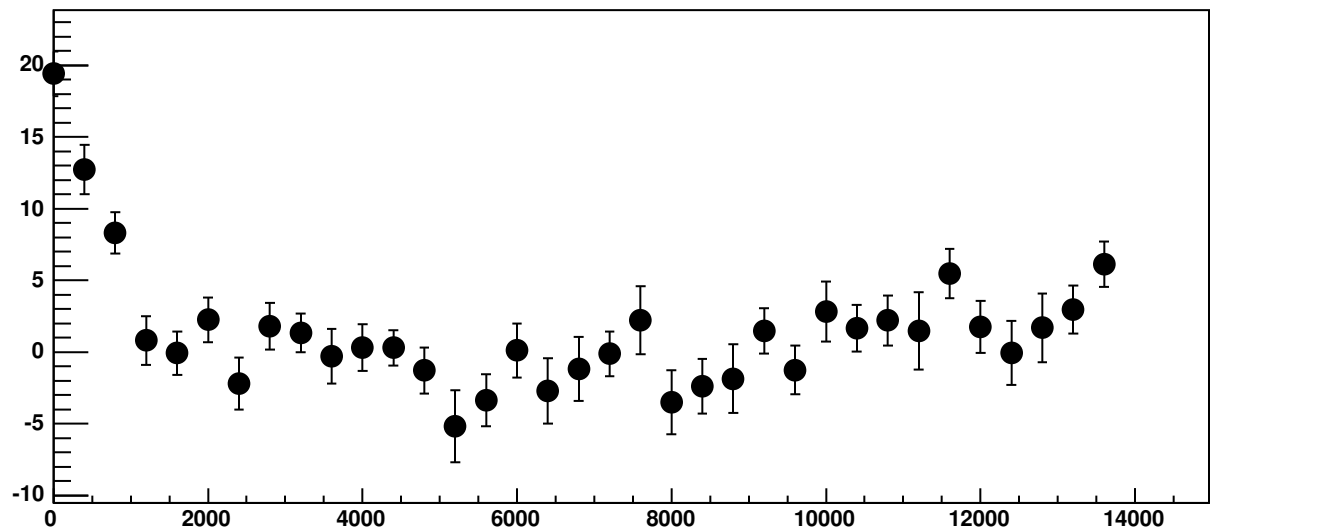
Chip 8, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

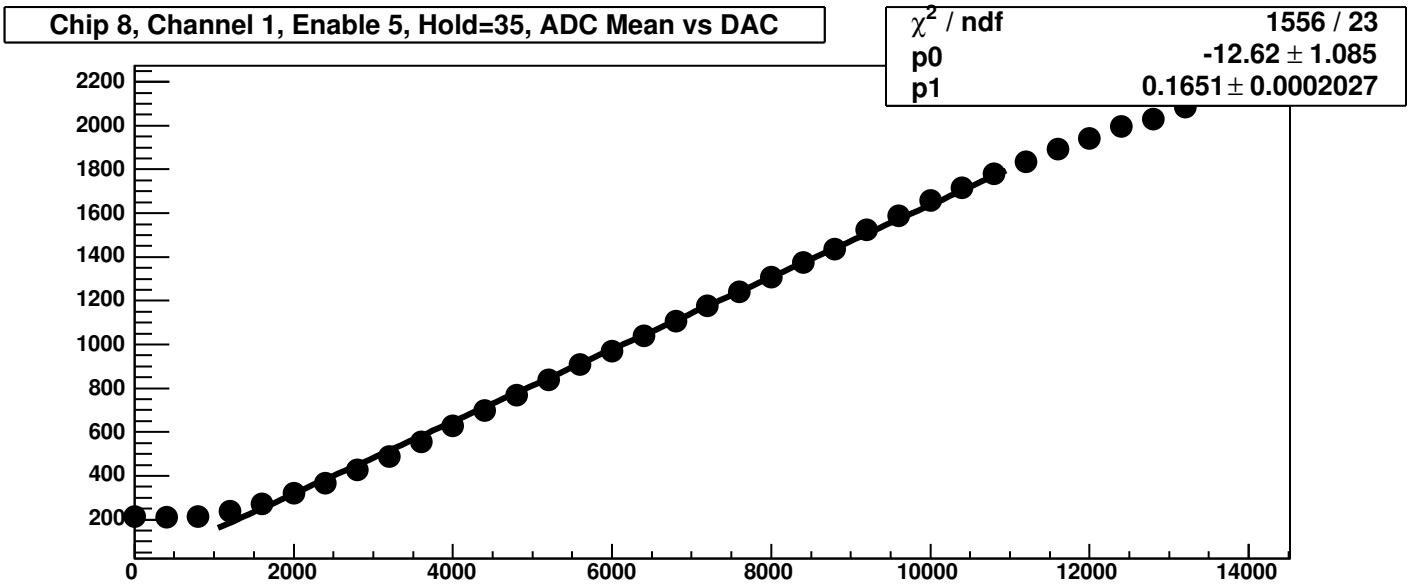


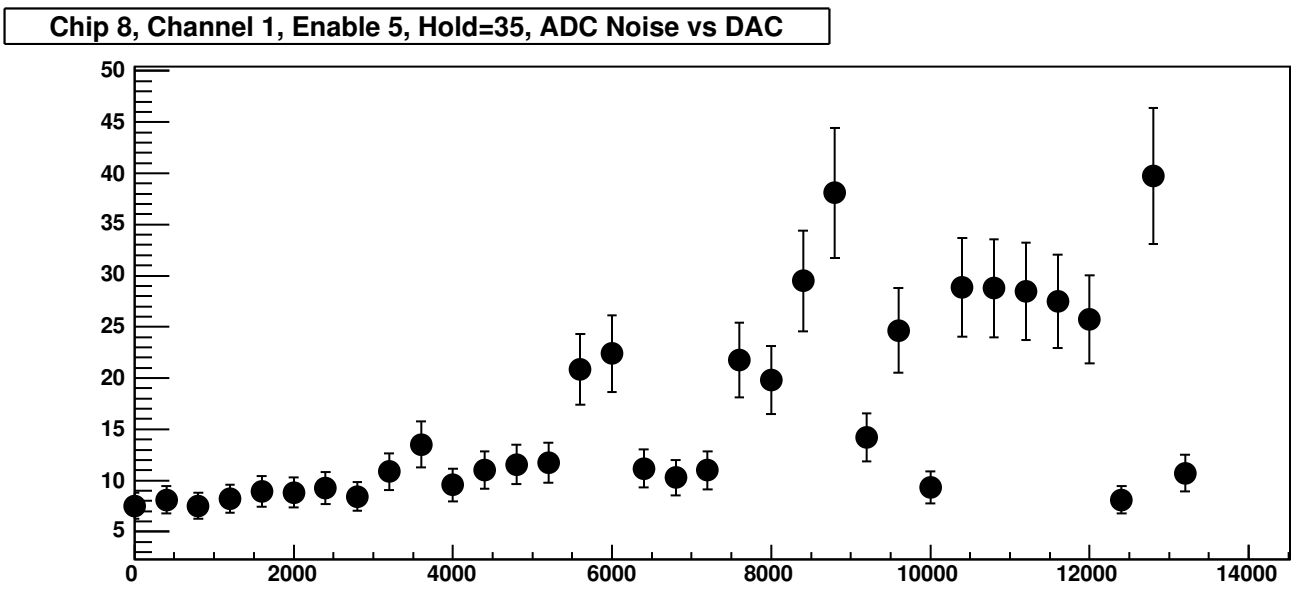
Chip 8, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

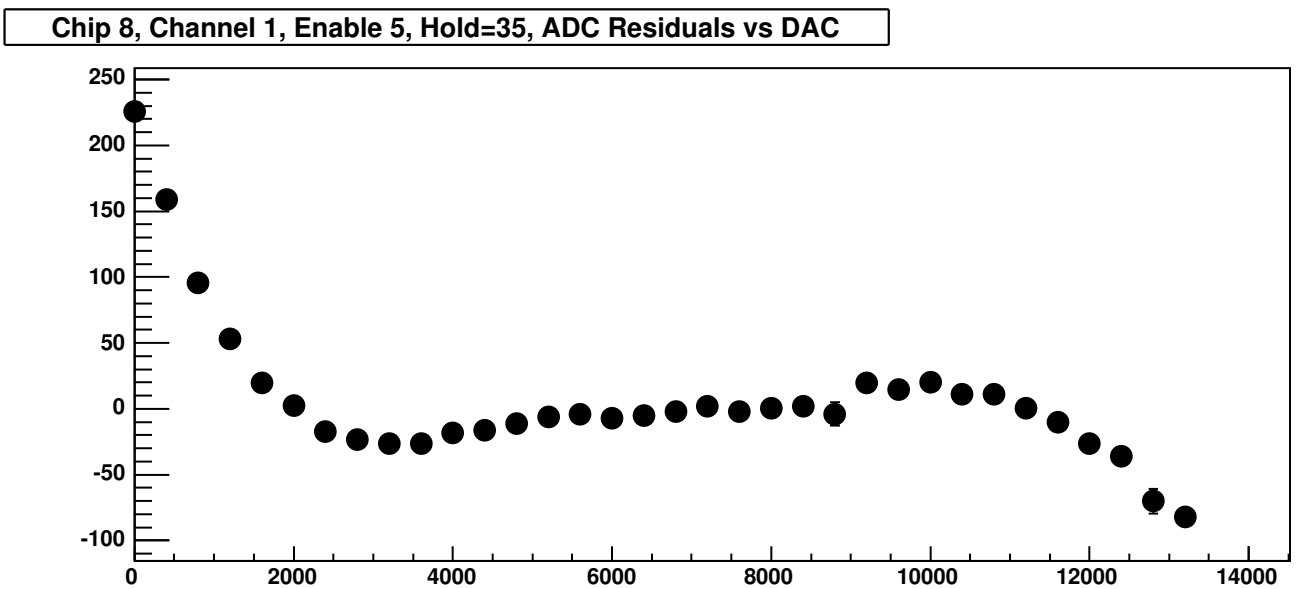


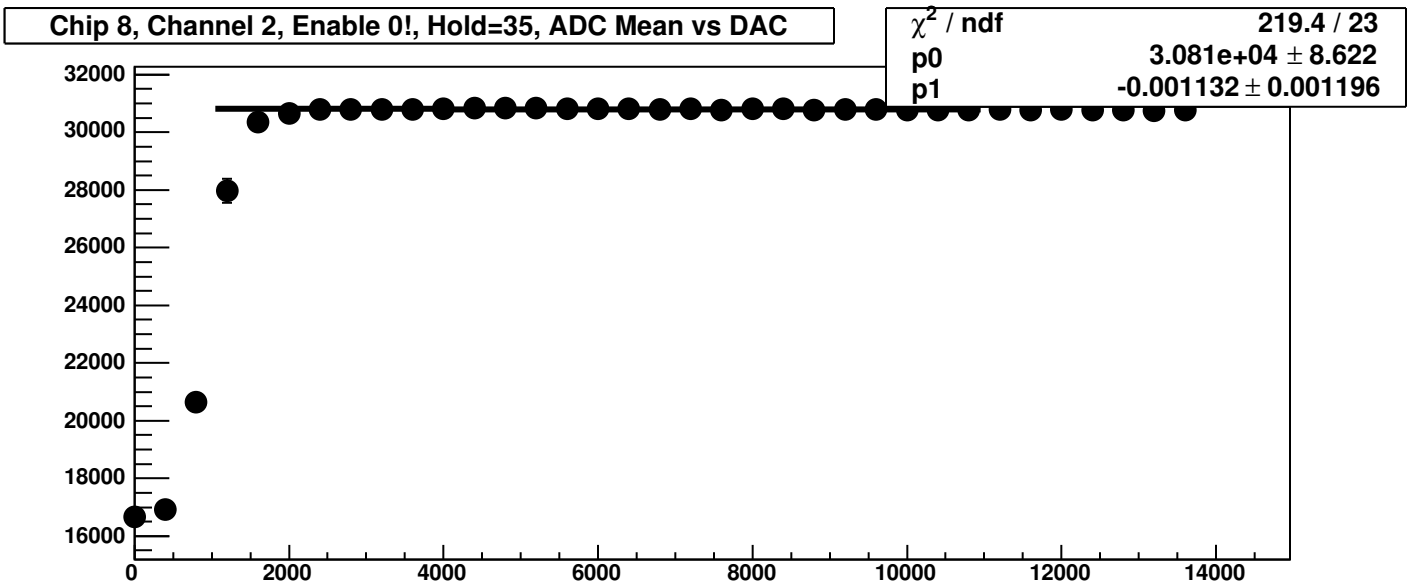
Chip 8, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

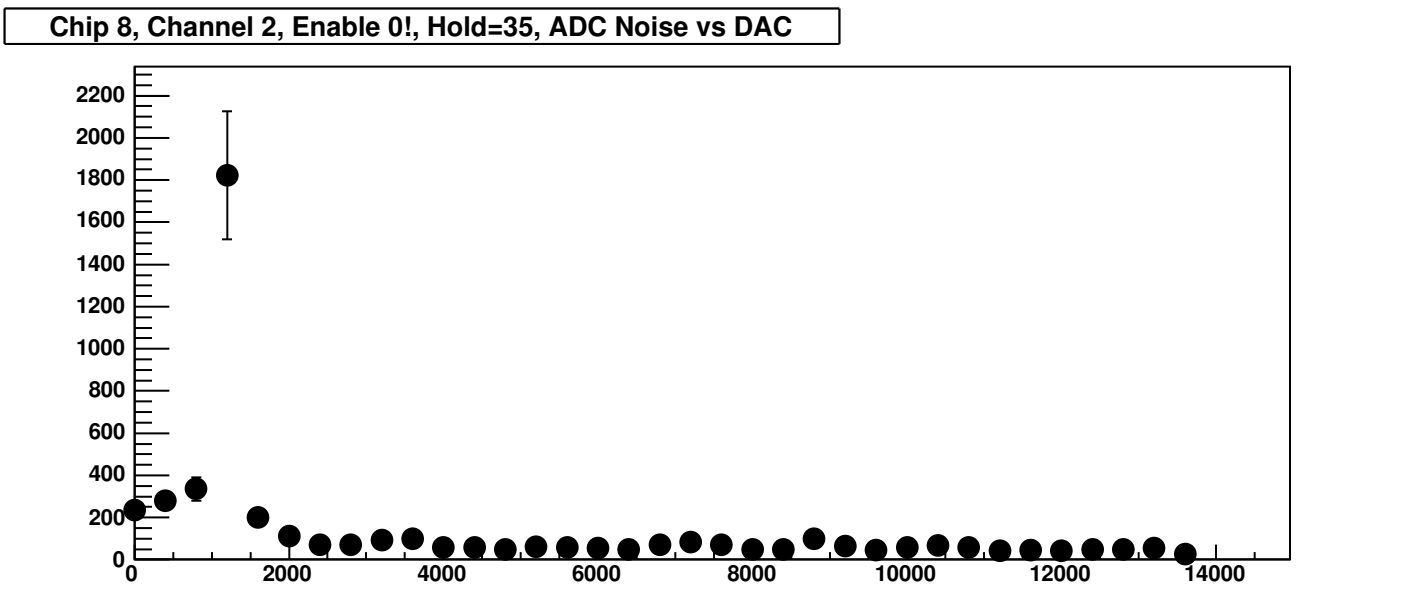


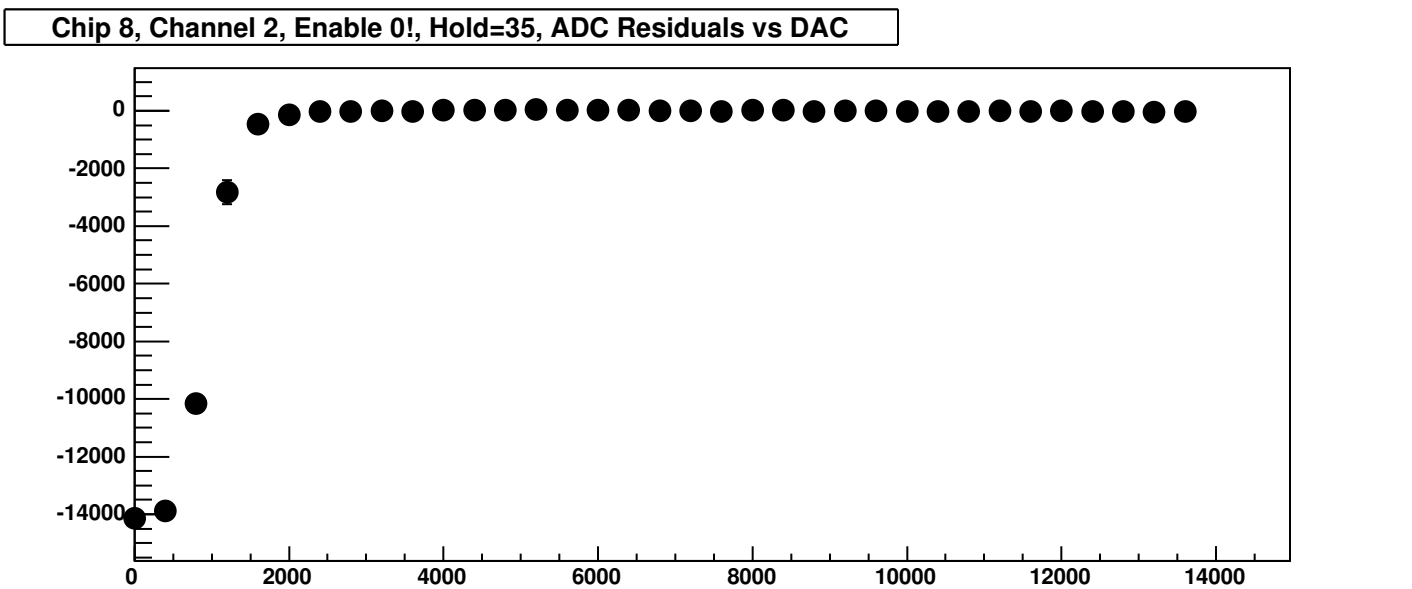

Chip 8, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1556 / 23
p0 -12.62 ± 1.085
p1 0.1651 ± 0.0002027
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

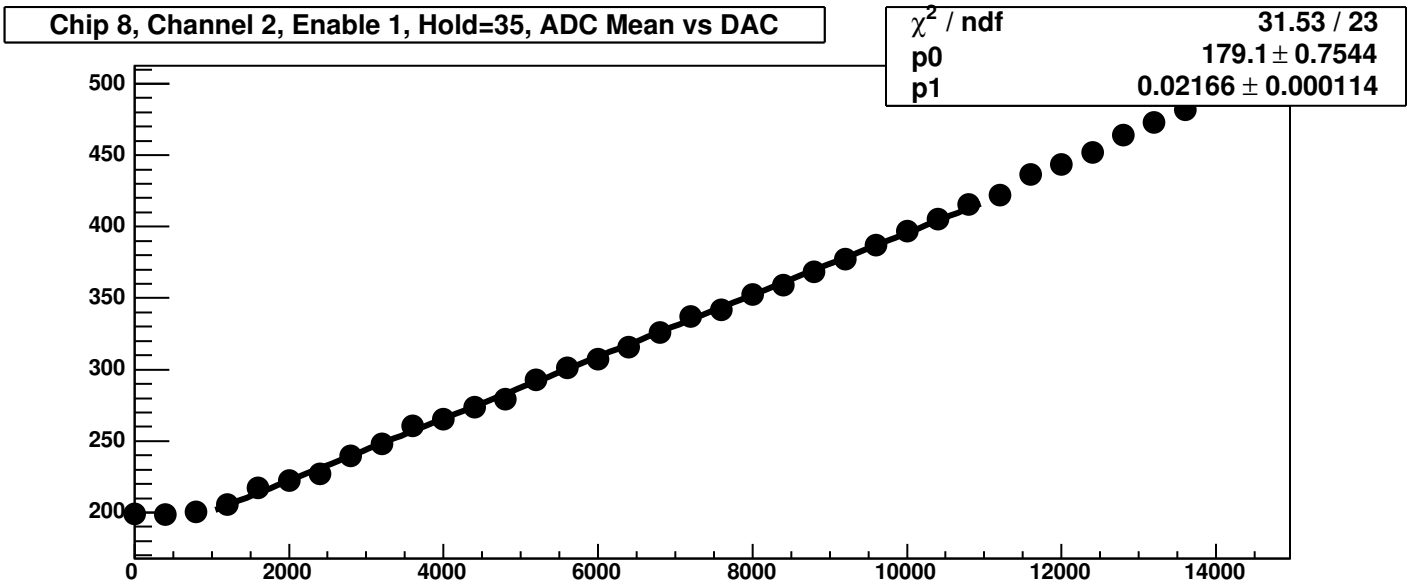

Chip 8, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

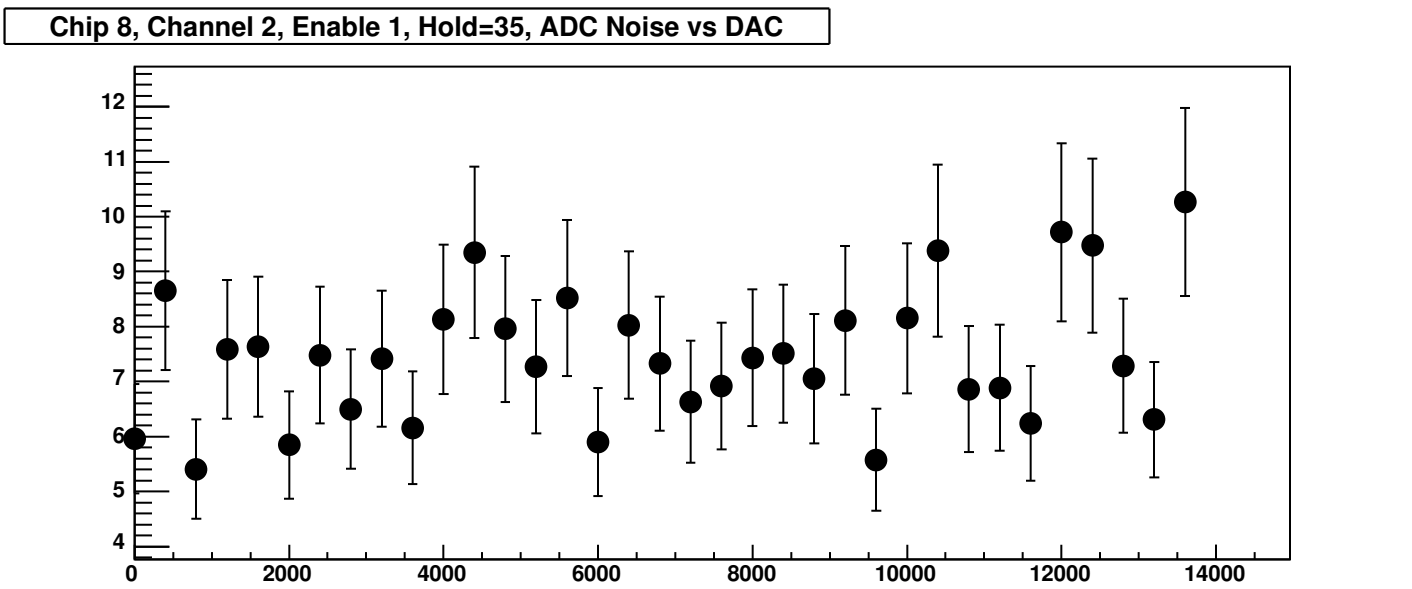

Chip 8, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

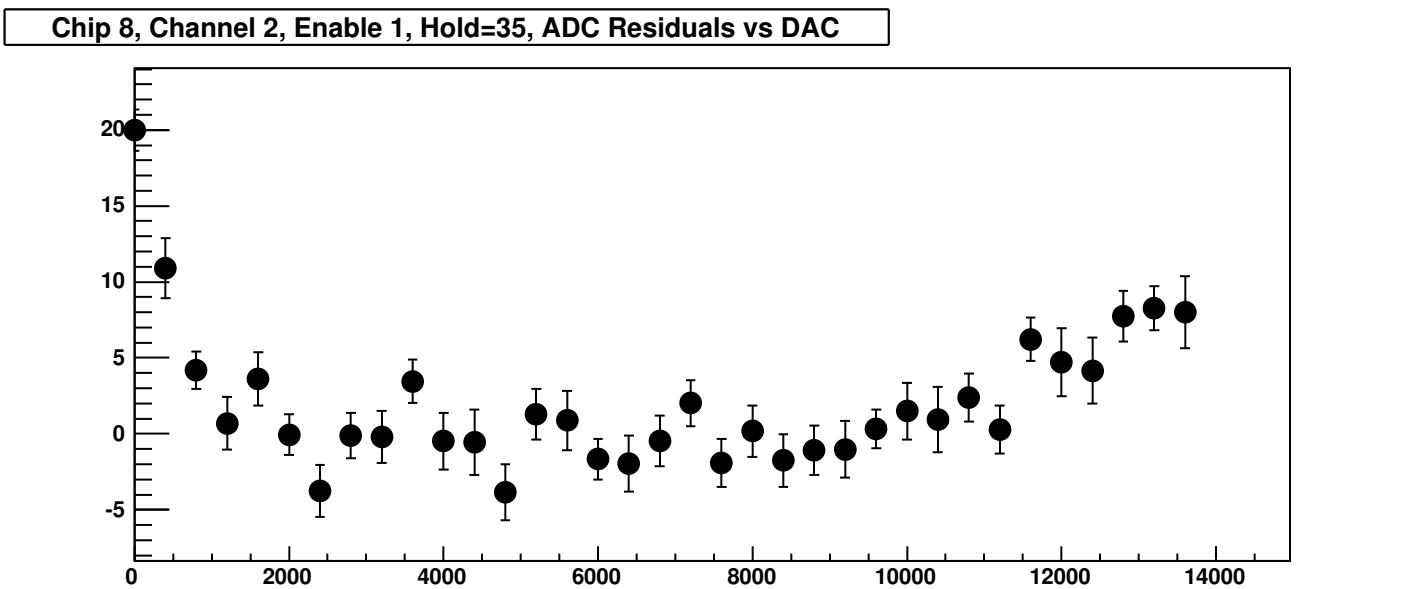

Chip 8, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 219.4 / 23
p0 3.081e+04 ± 8.622
p1 -0.001132 ± 0.001196
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

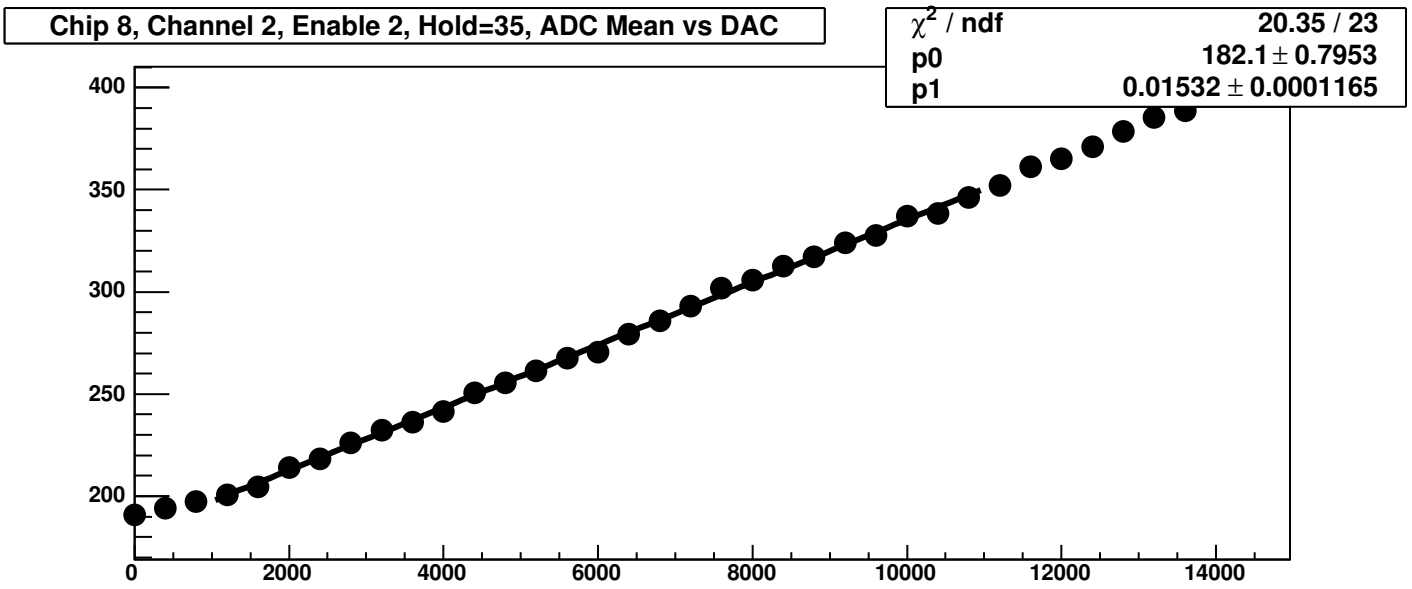

Chip 8, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

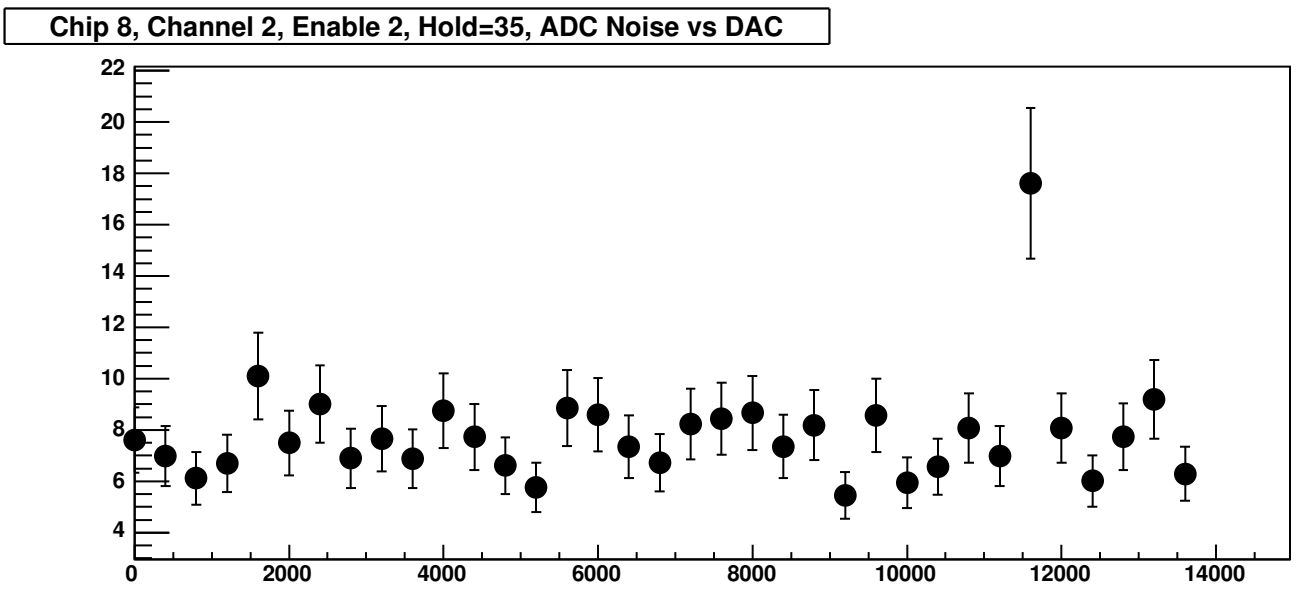

Chip 8, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

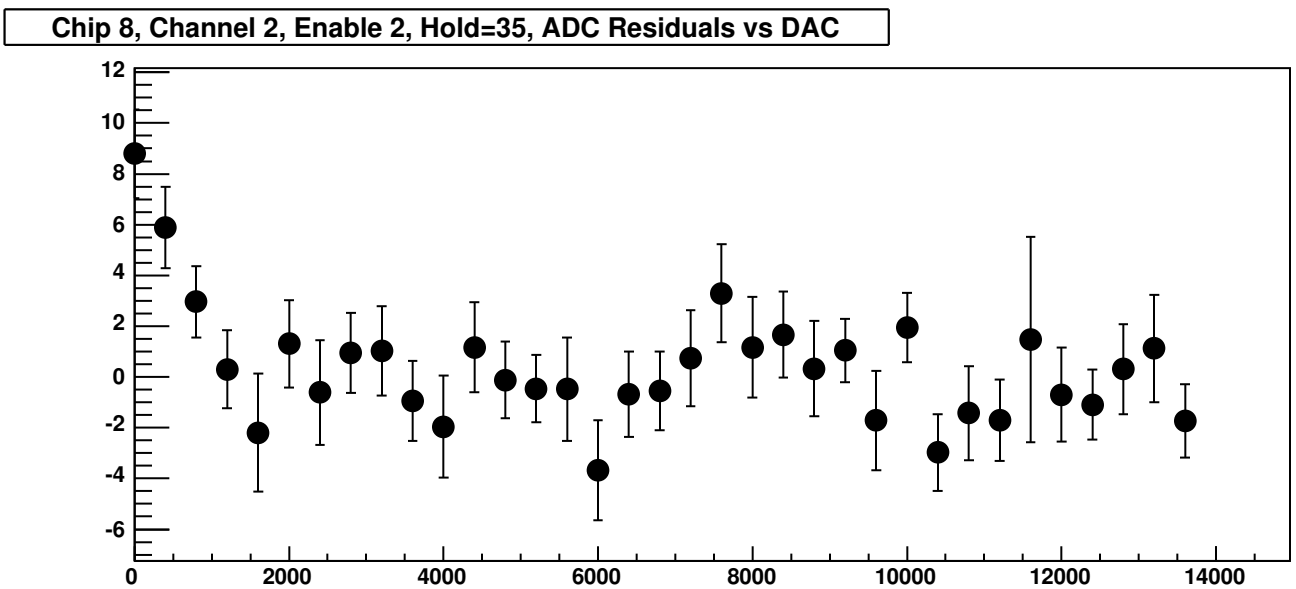

Chip 8, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.53 / 23
p0 179.1 ± 0.7544
p1 0.02166 ± 0.000114
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

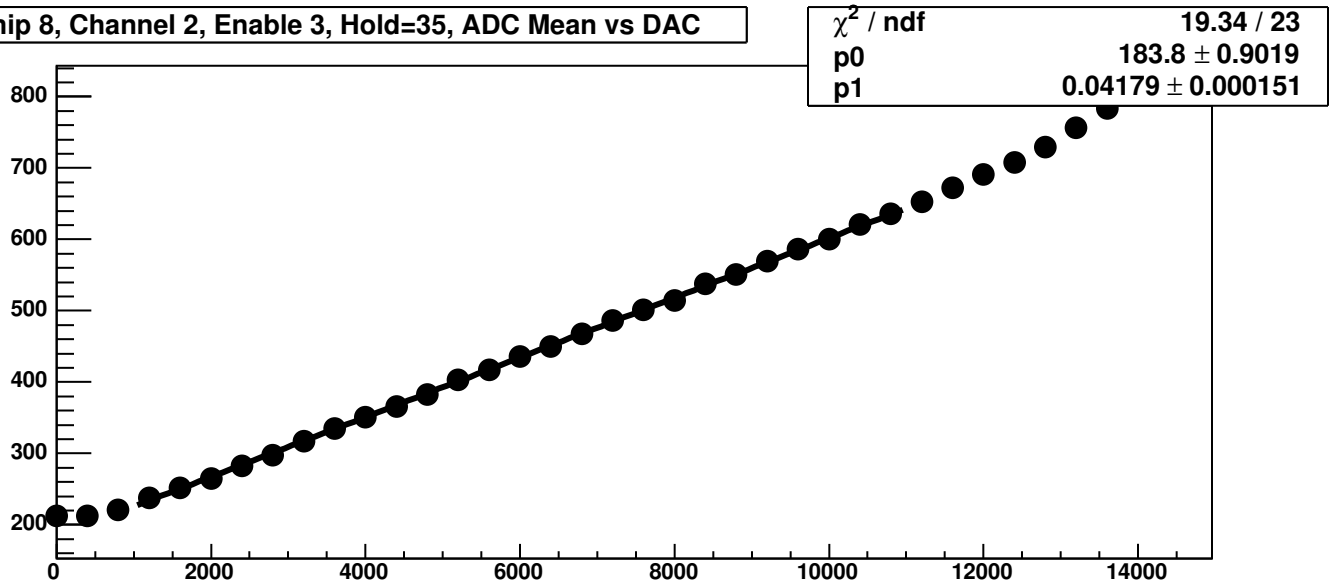

Chip 8, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 20.35 / 23
p0 182.1 ± 0.7953
p1 0.01532 ± 0.0001165
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

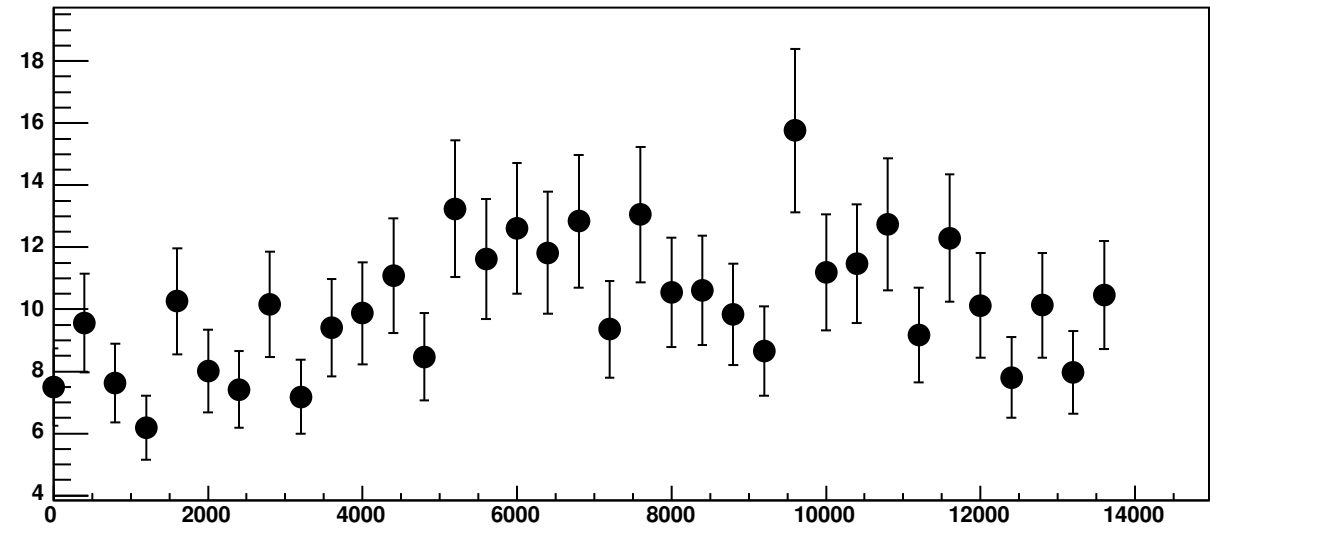

Chip 8, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000

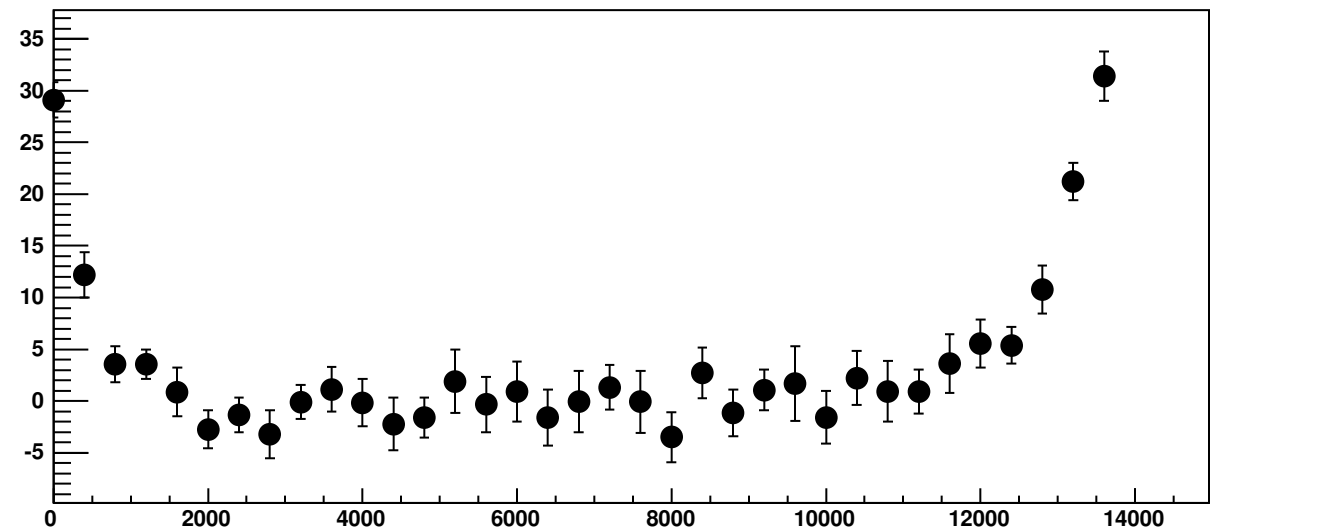
Chip 8, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC



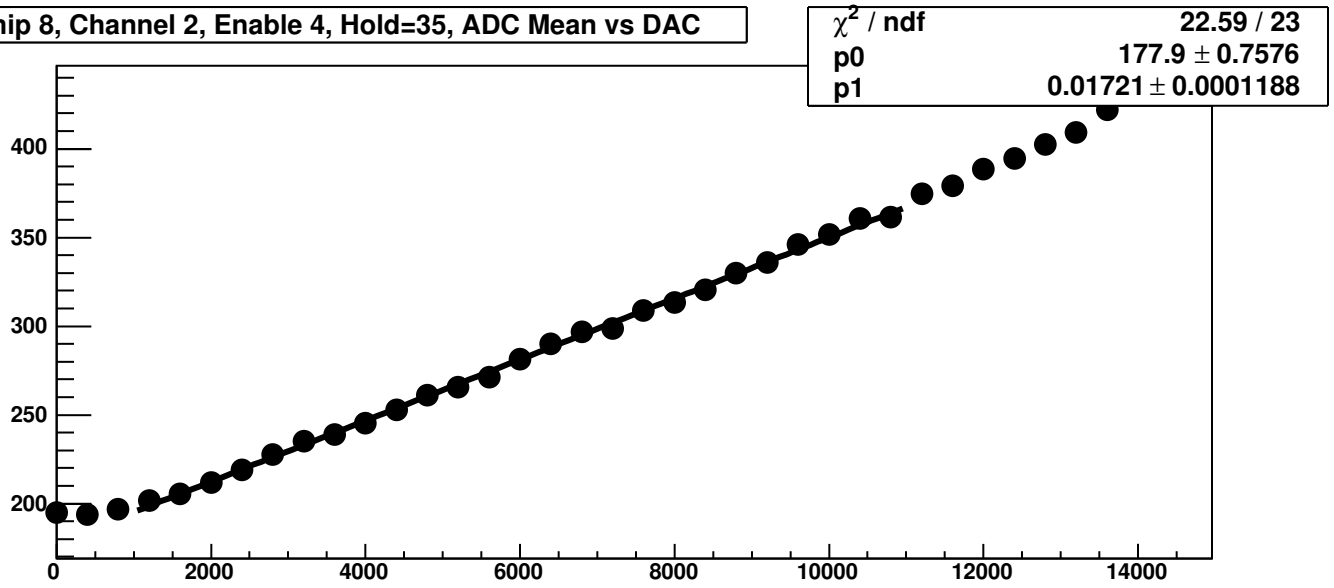
Chip 8, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC



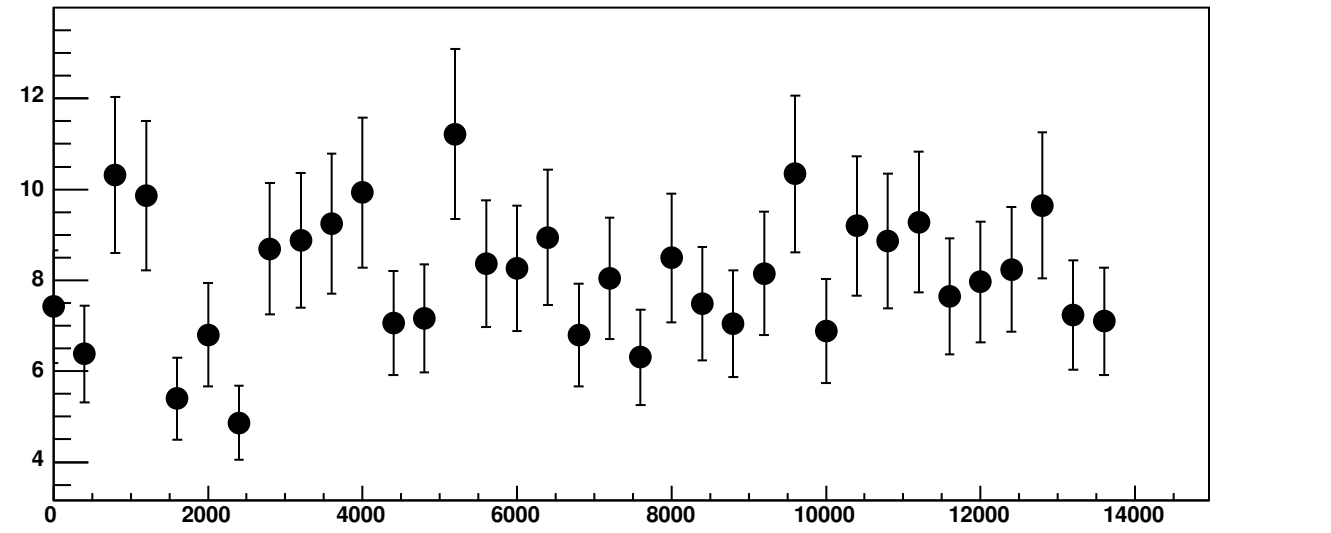
Chip 8, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC



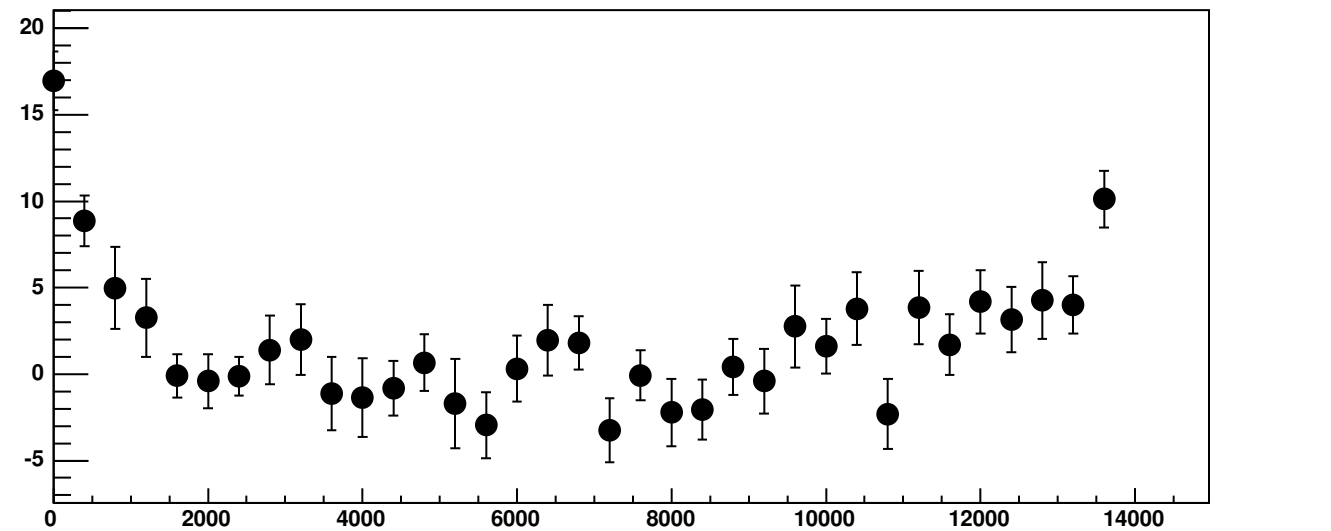
Chip 8, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

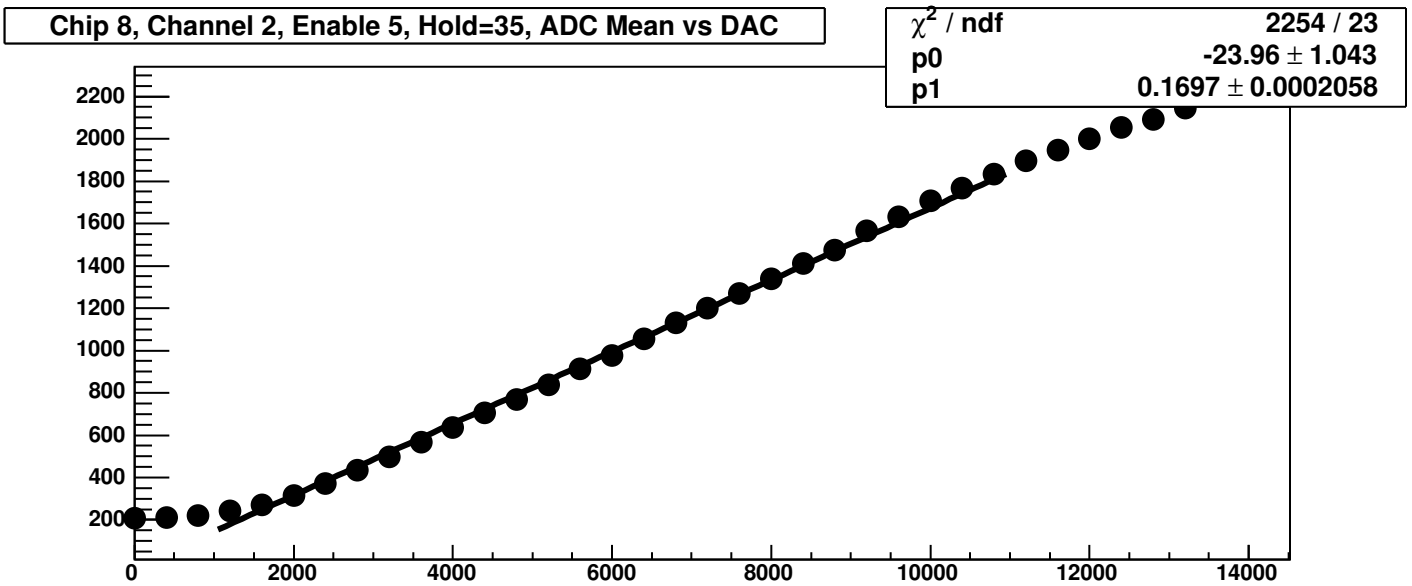


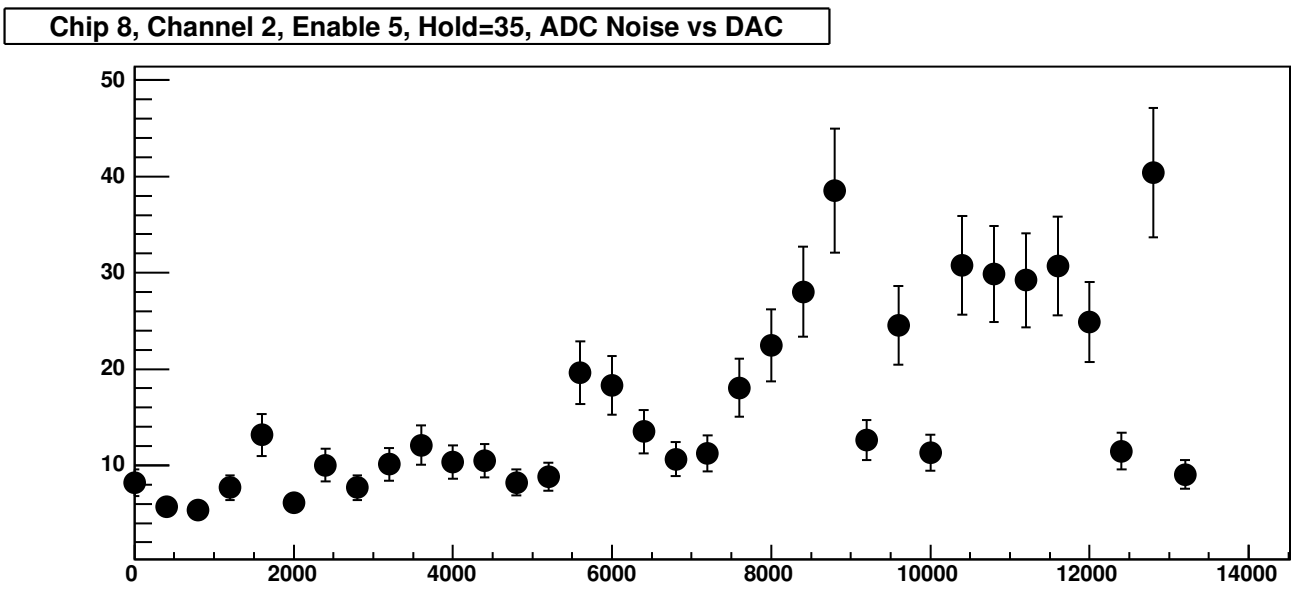
Chip 8, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

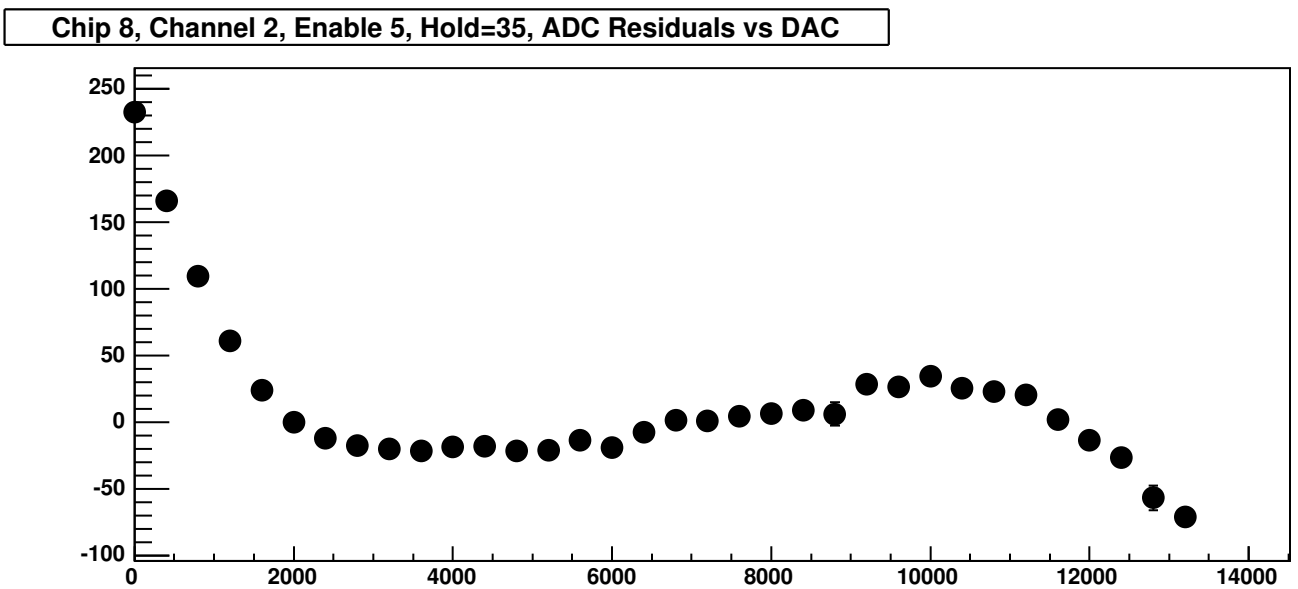


Chip 8, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

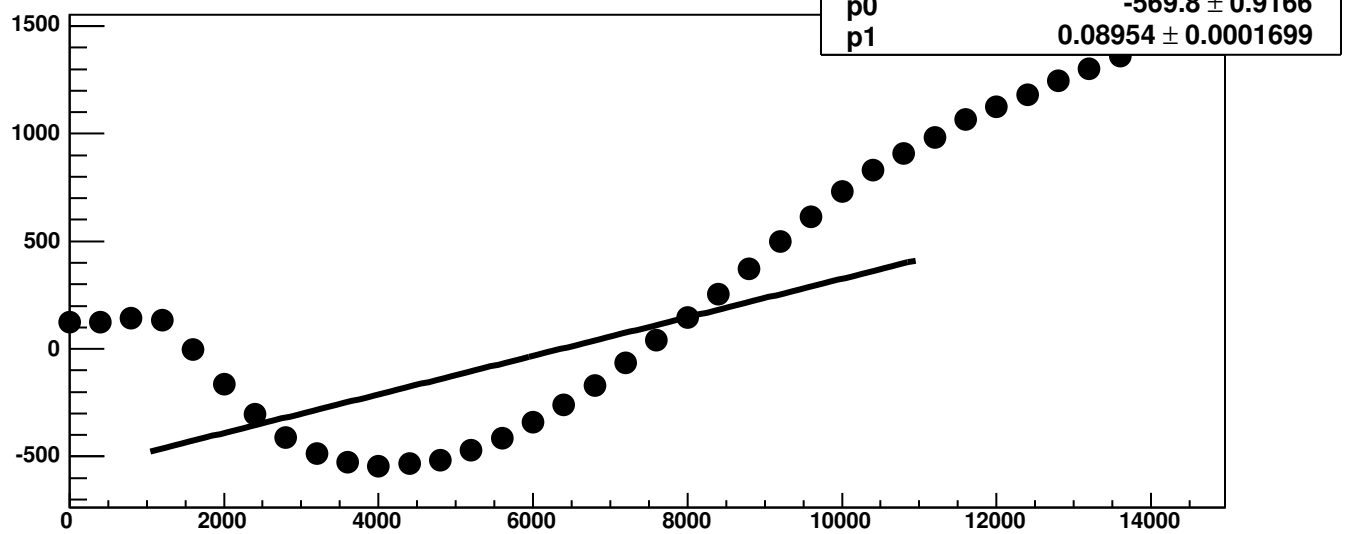



Chip 8, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2254 / 23
p0 -23.96 ± 1.043
p1 0.1697 ± 0.0002058
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

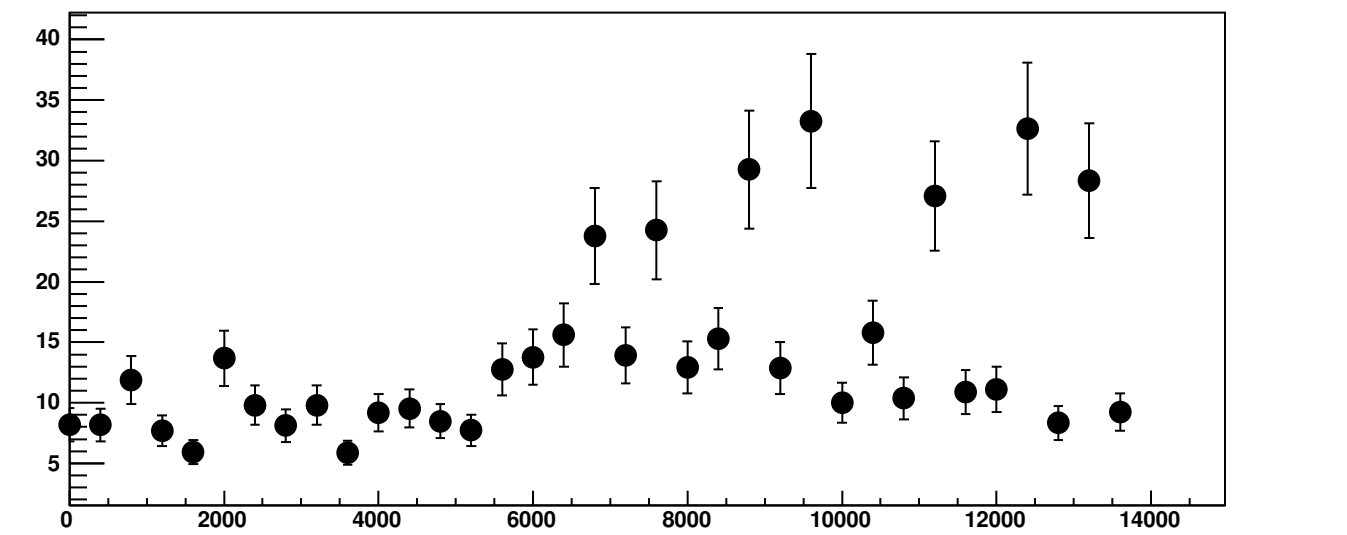

Chip 8, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

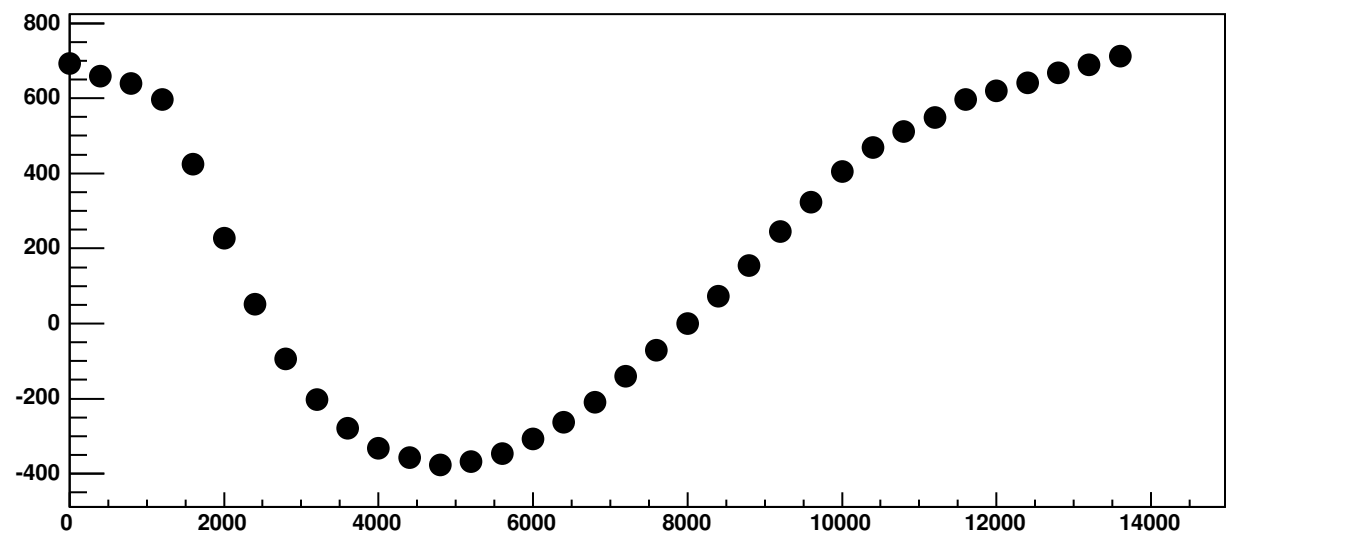
Chip 8, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC

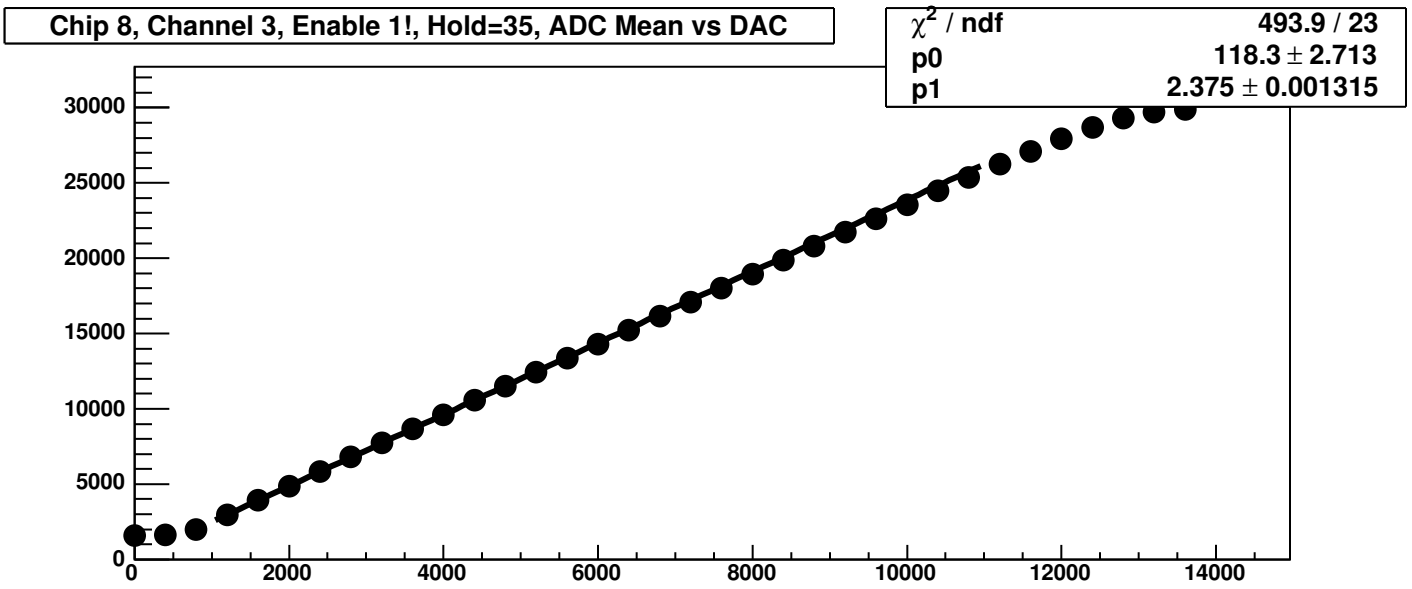


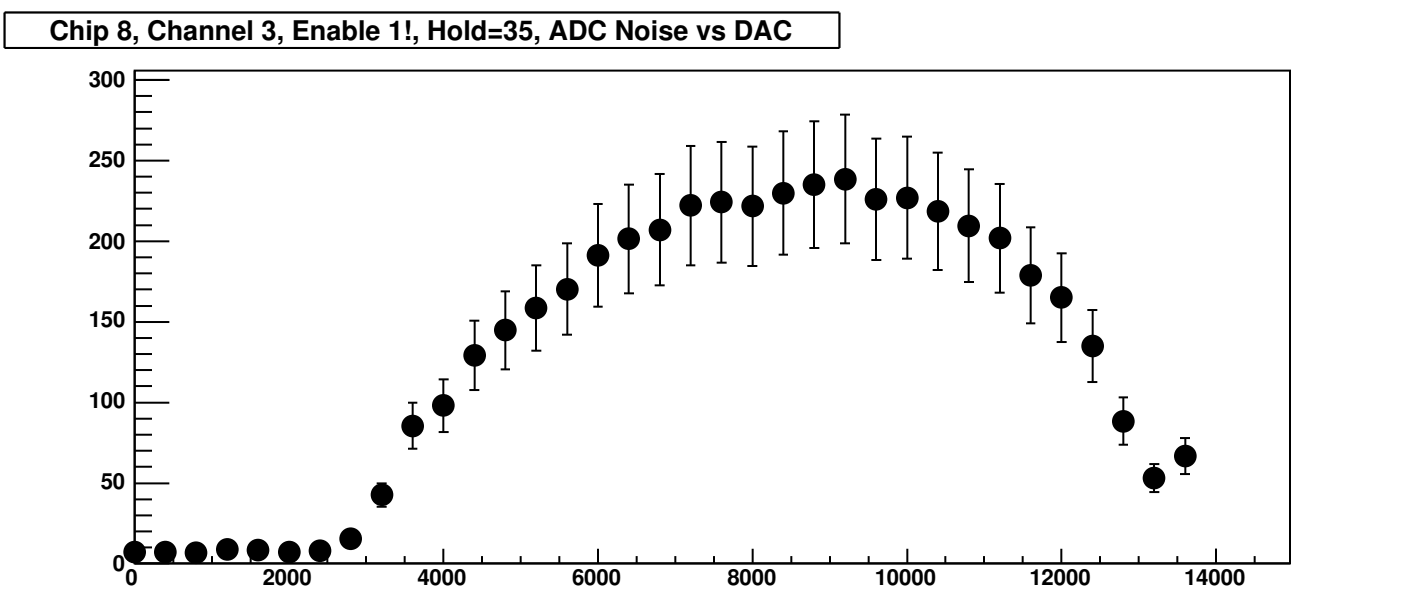
Chip 8, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC

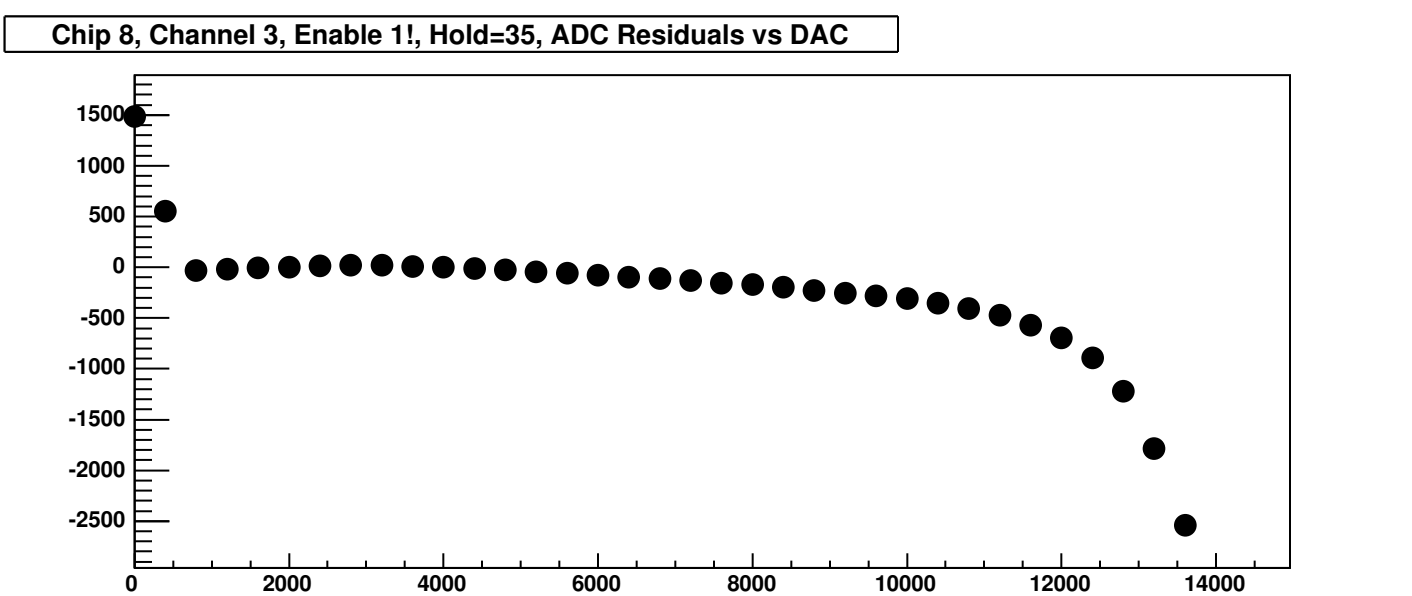


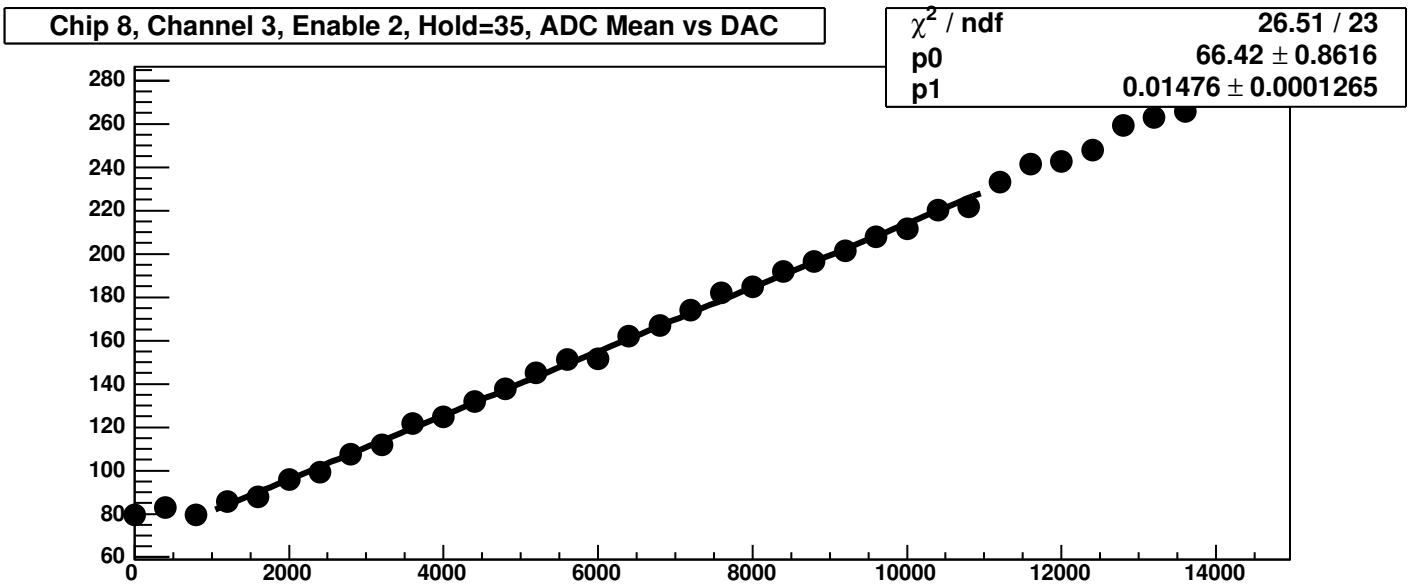
Chip 8, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC

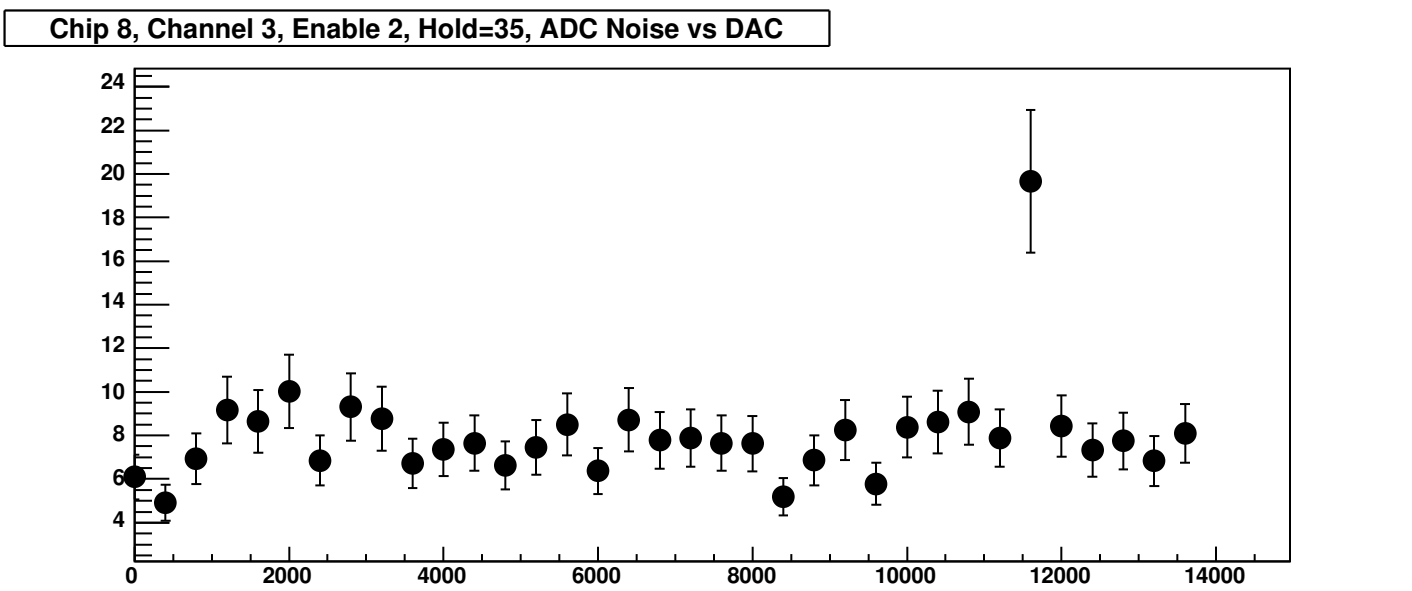


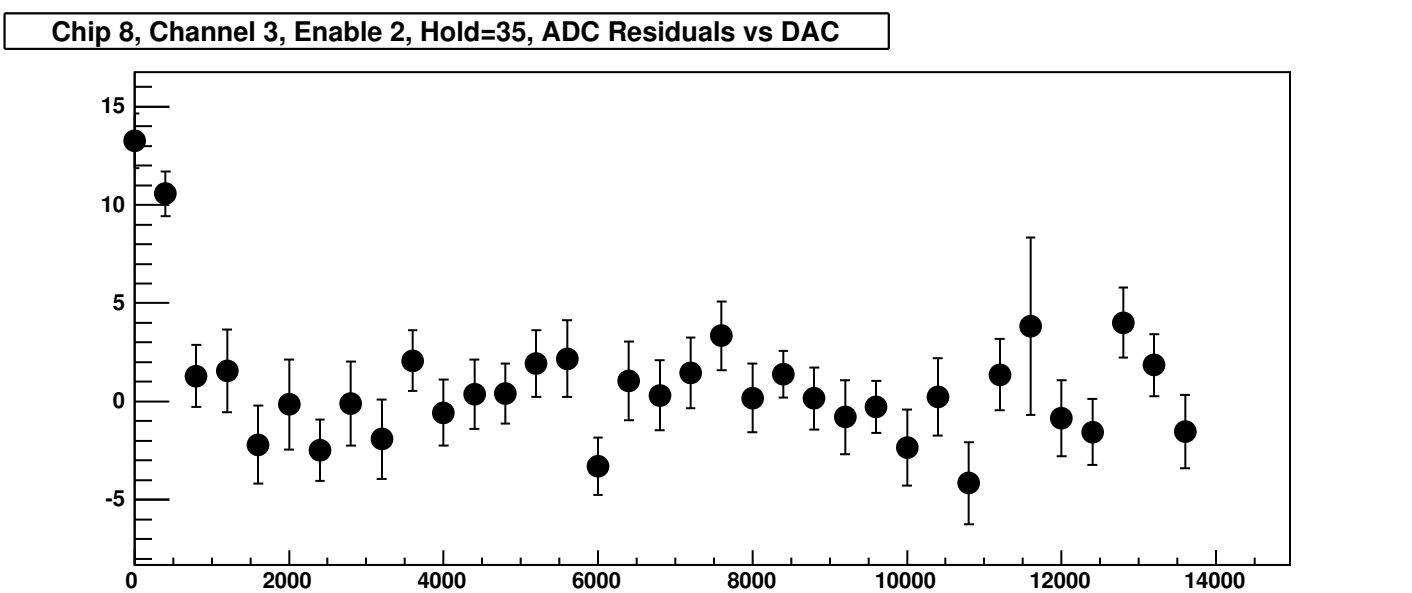

Chip 8, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 493.9 / 23
p0 118.3 ± 2.713
p1 2.375 ± 0.001315
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

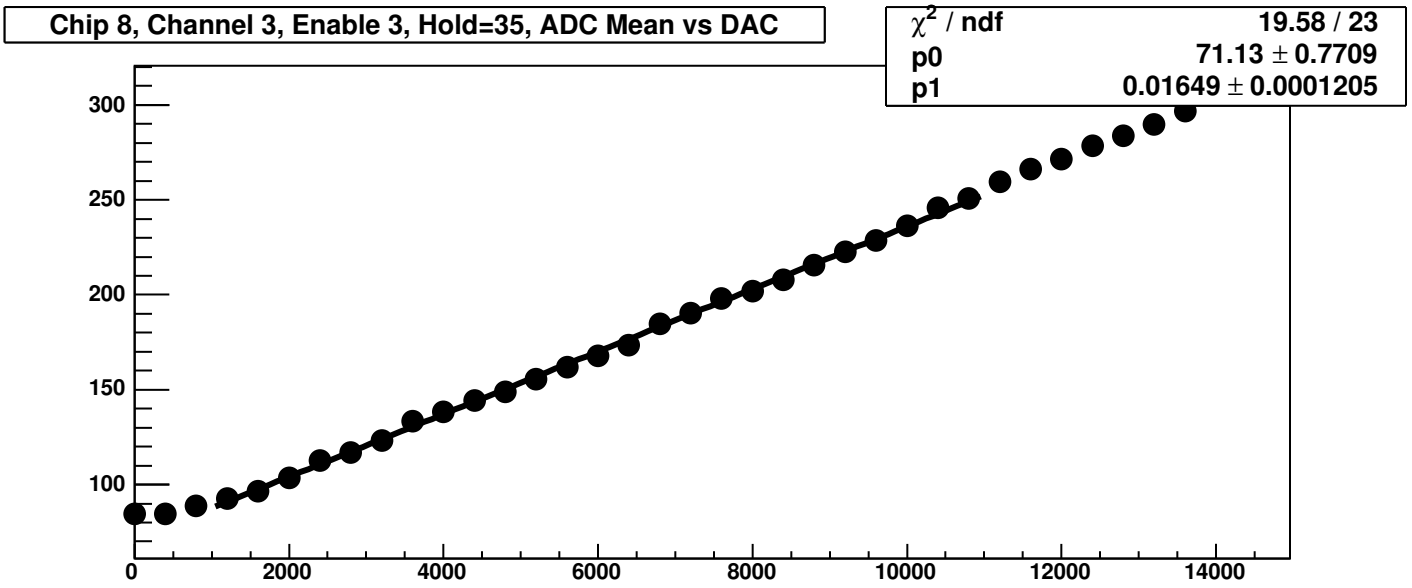

Chip 8, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

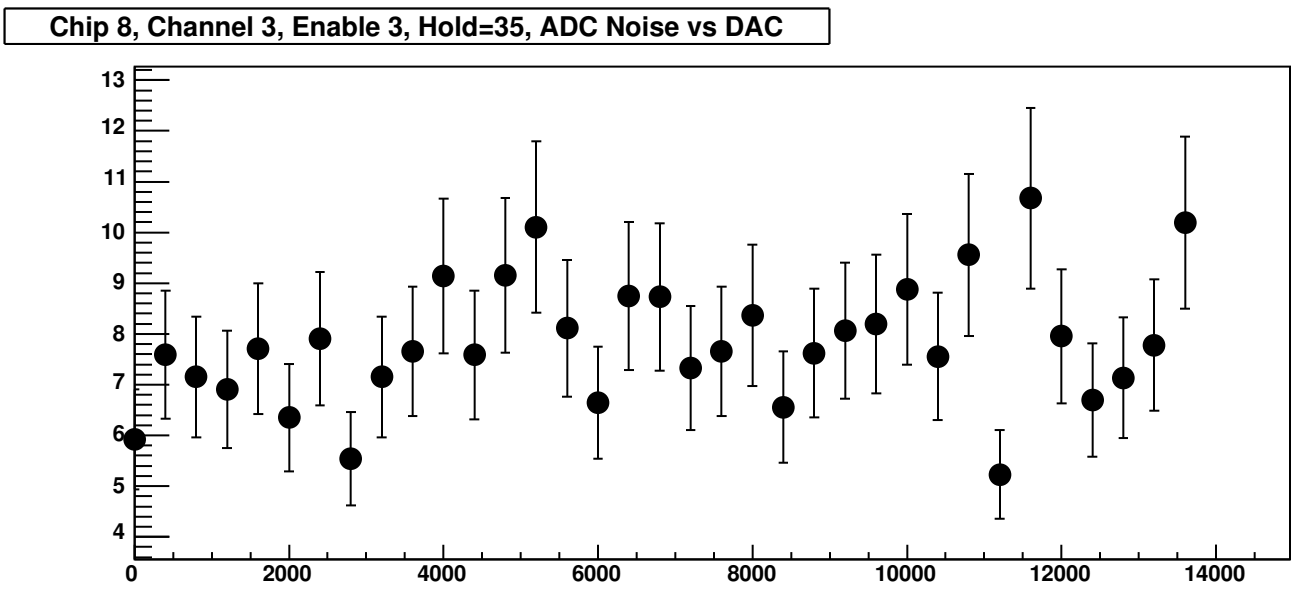

Chip 8, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

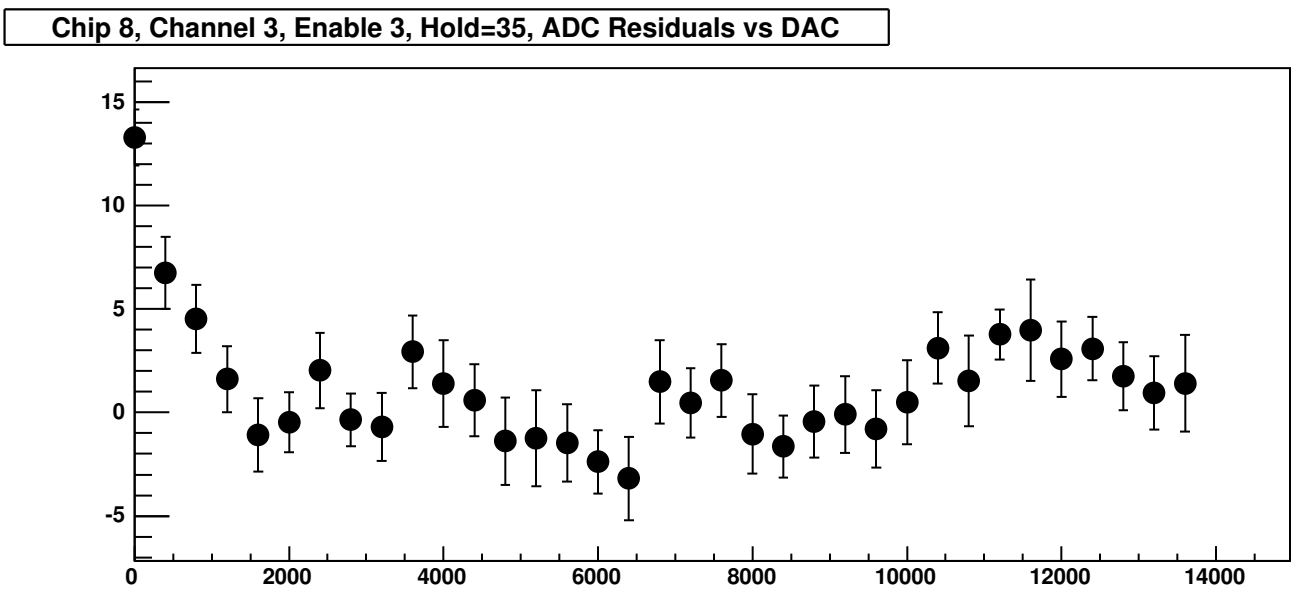

Chip 8, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 26.51 / 23
p0 66.42 ± 0.8616
p1 0.01476 ± 0.0001265
280
260
240
220
200
180
160
140
120
100
80
60
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
24
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

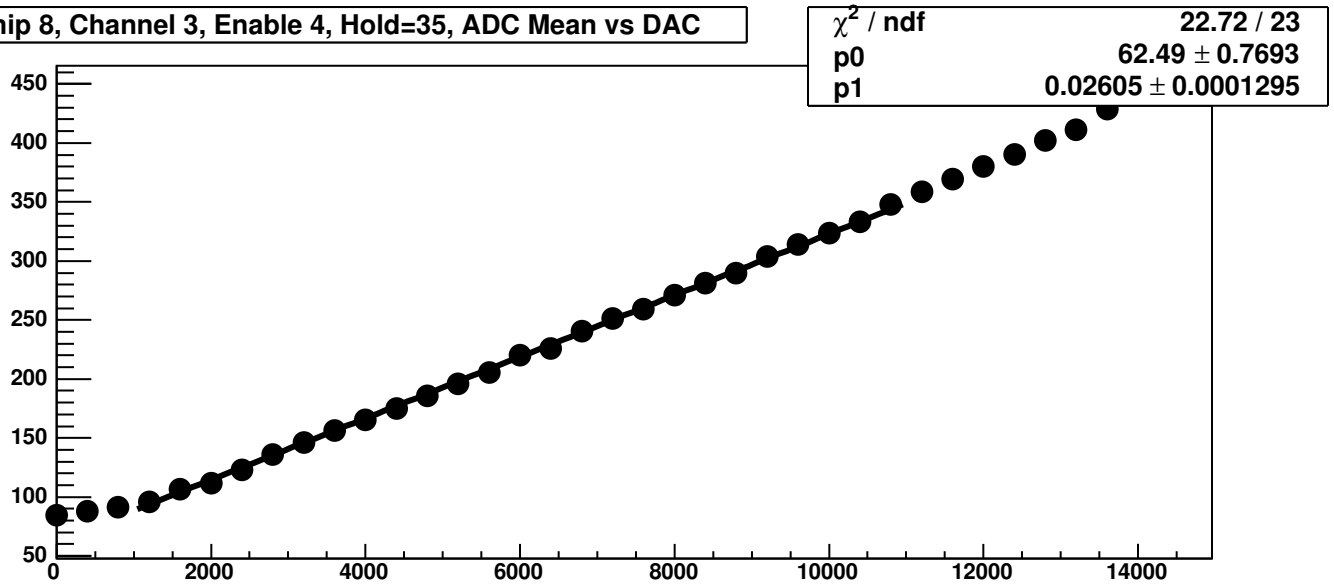

Chip 8, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 19.58 / 23
p0 71.13 ± 0.7709
p1 0.01649 ± 0.0001205
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

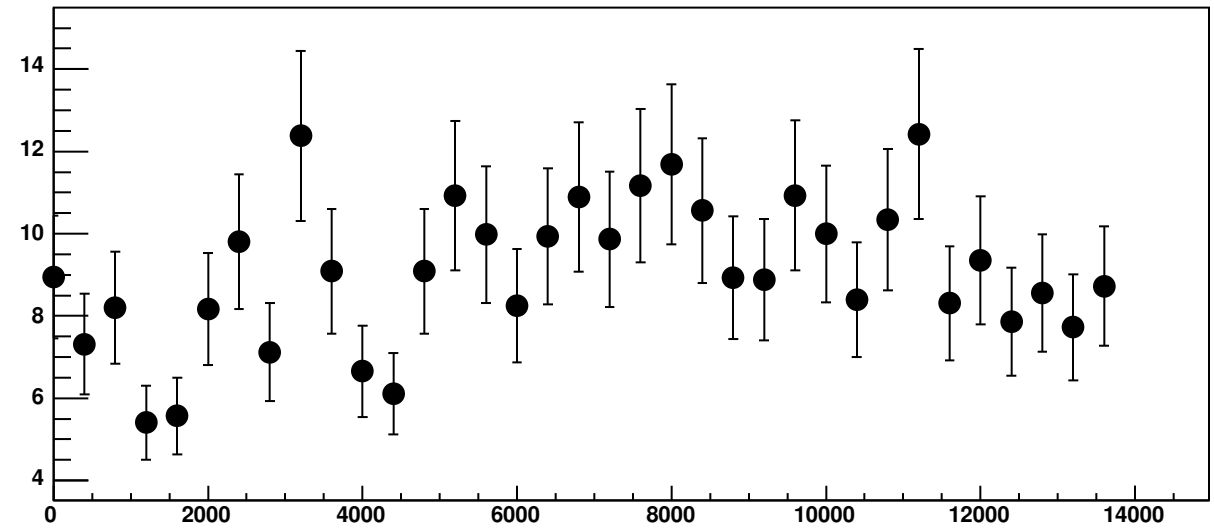

Chip 8, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

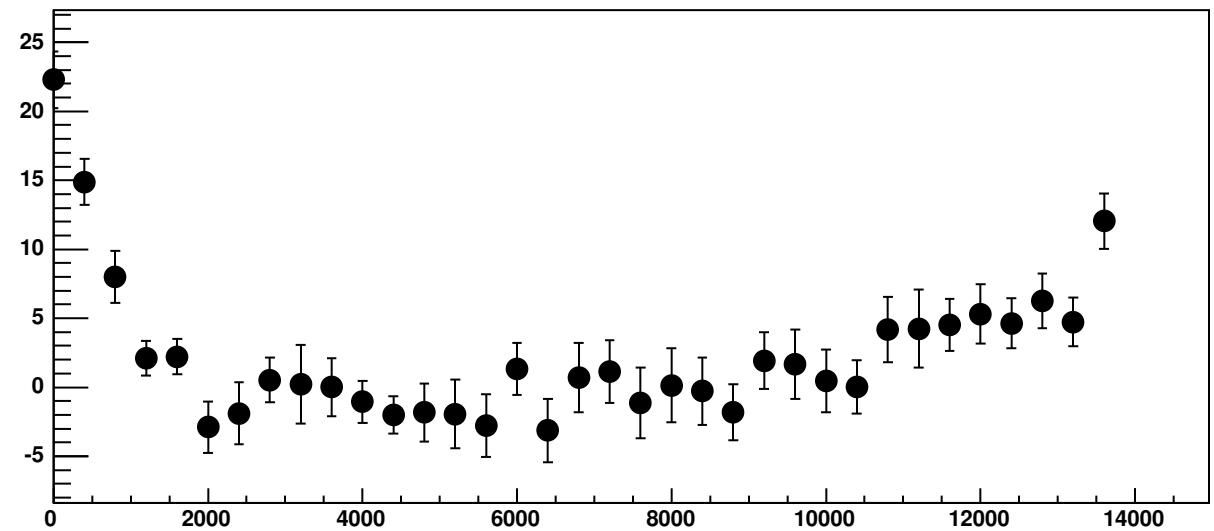
Chip 8, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

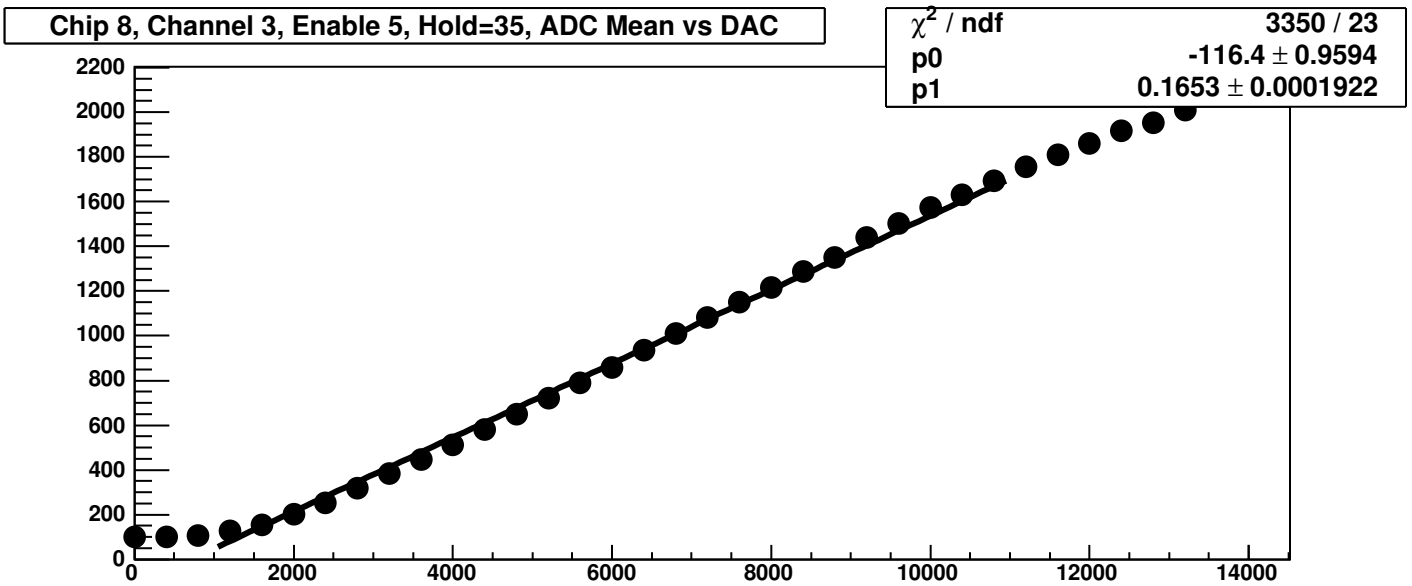


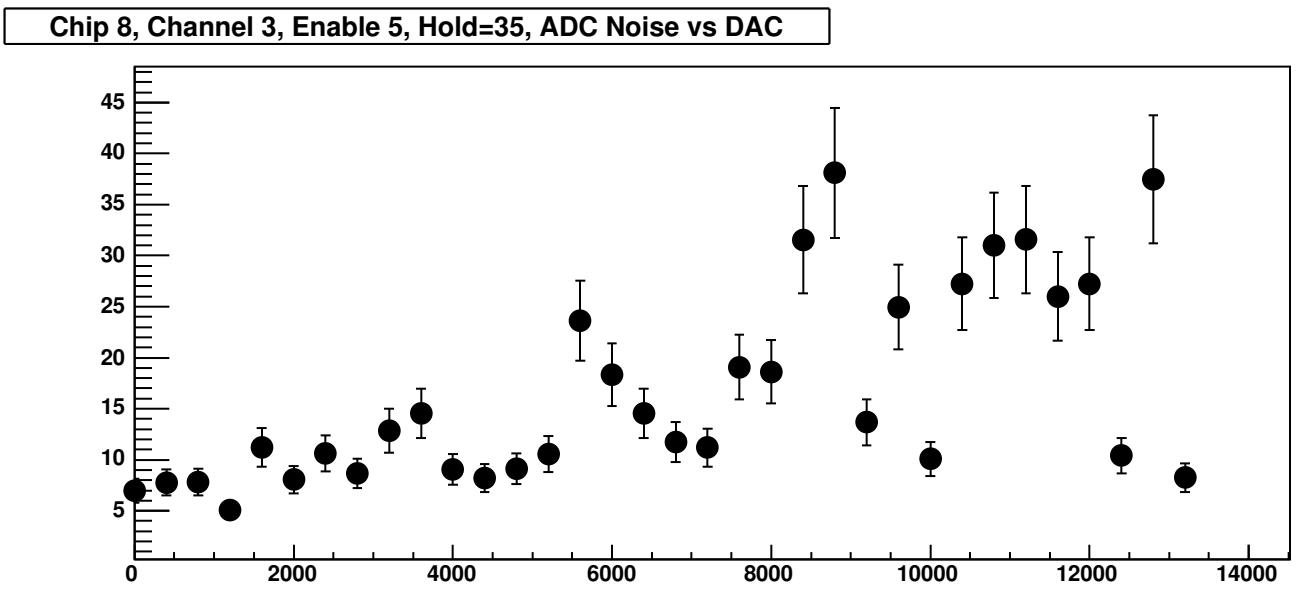
Chip 8, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC

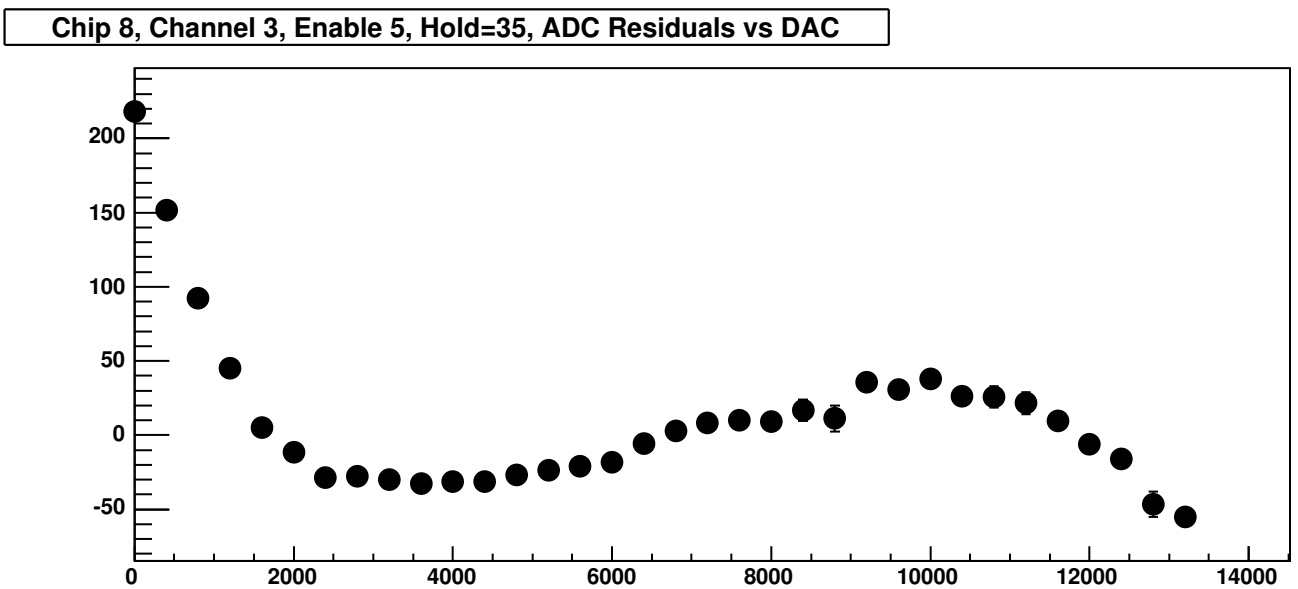


Chip 8, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC



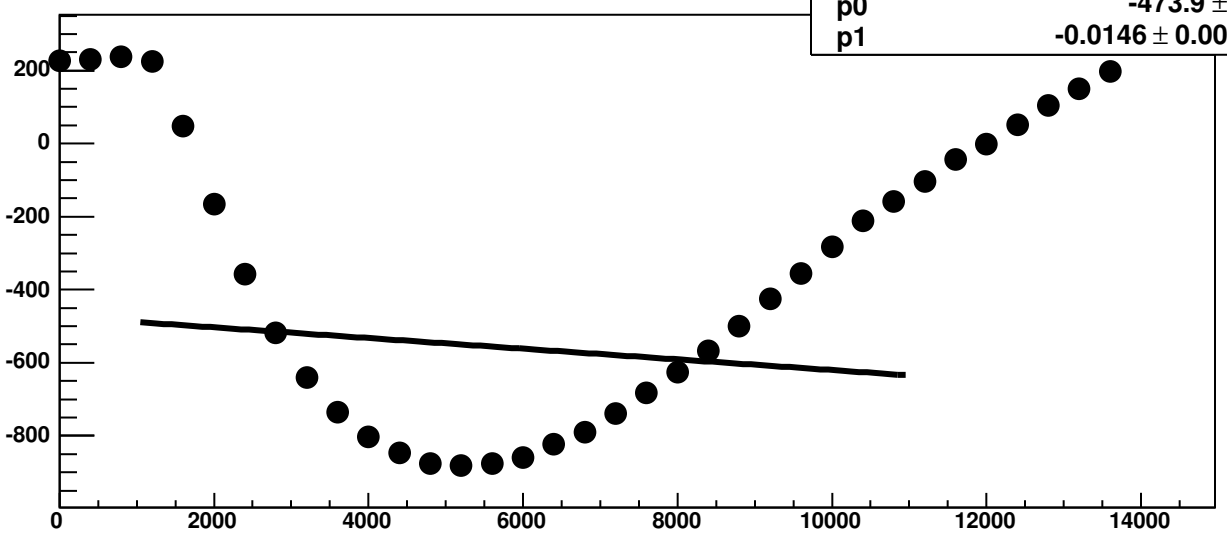

Chip 8, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3350 / 23
p0 -116.4 ± 0.9594
p1 0.1653 ± 0.0001922
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

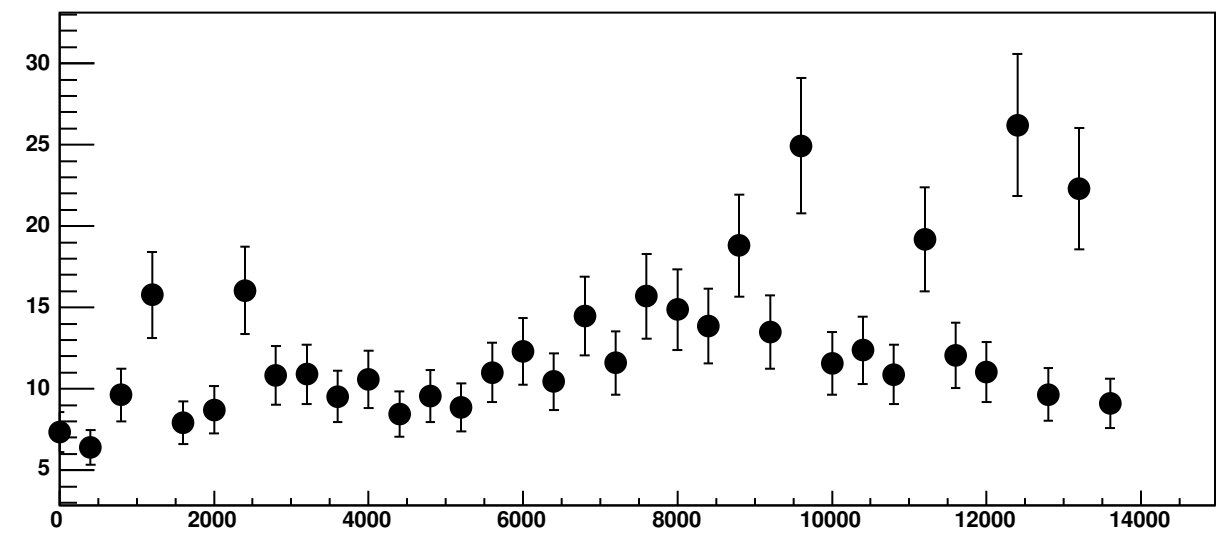

Chip 8, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

Chip 8, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

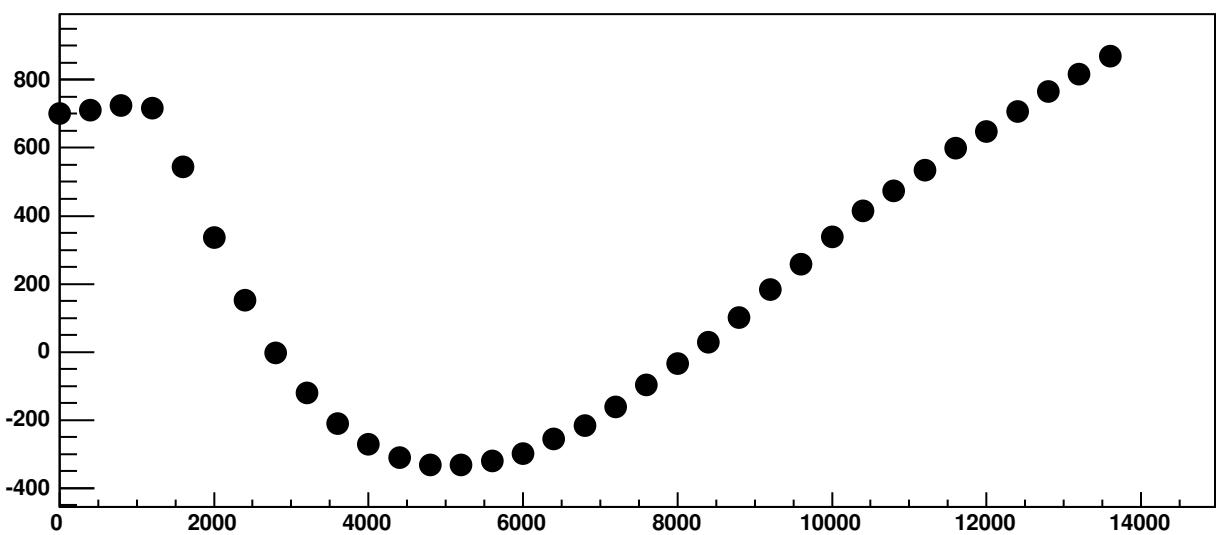
χ^2 / ndf	3.858e+05 / 23
p0	-473.9 ± 1.127
p1	-0.0146 ± 0.0001867

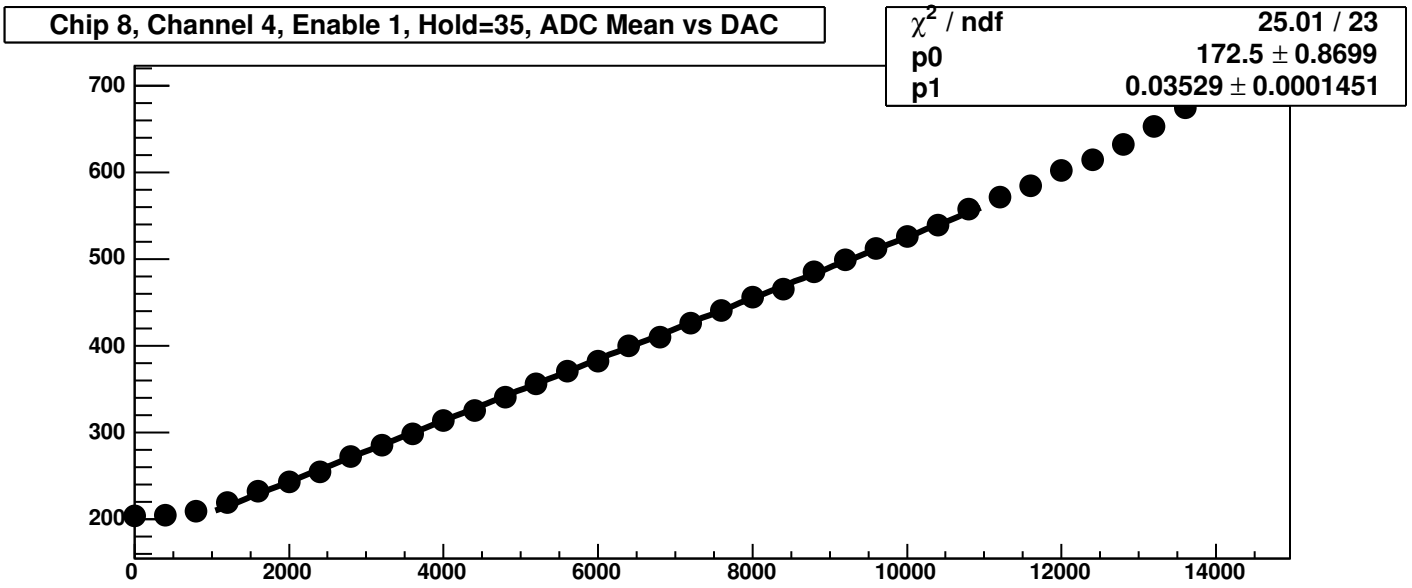


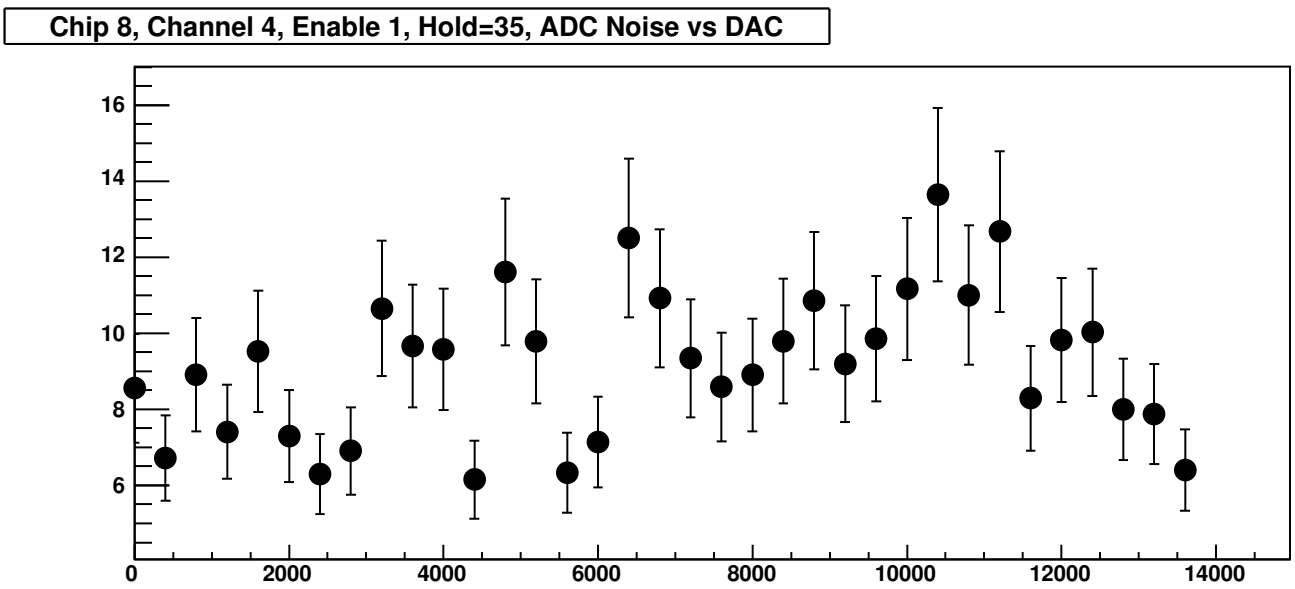
Chip 8, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC

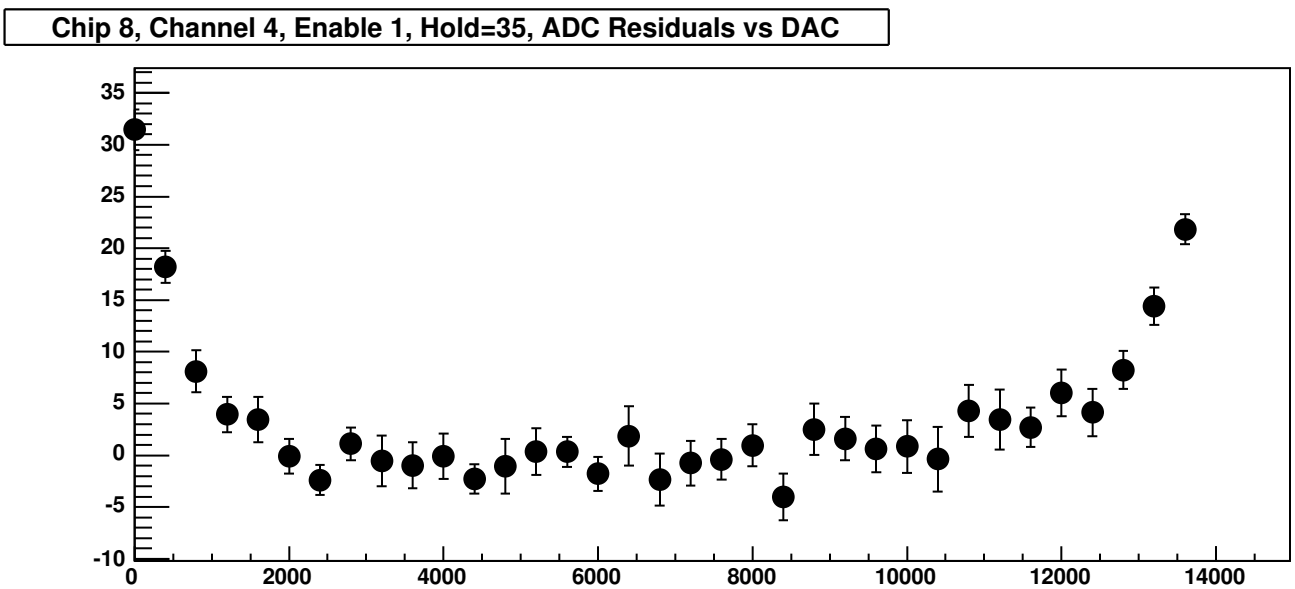


Chip 8, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

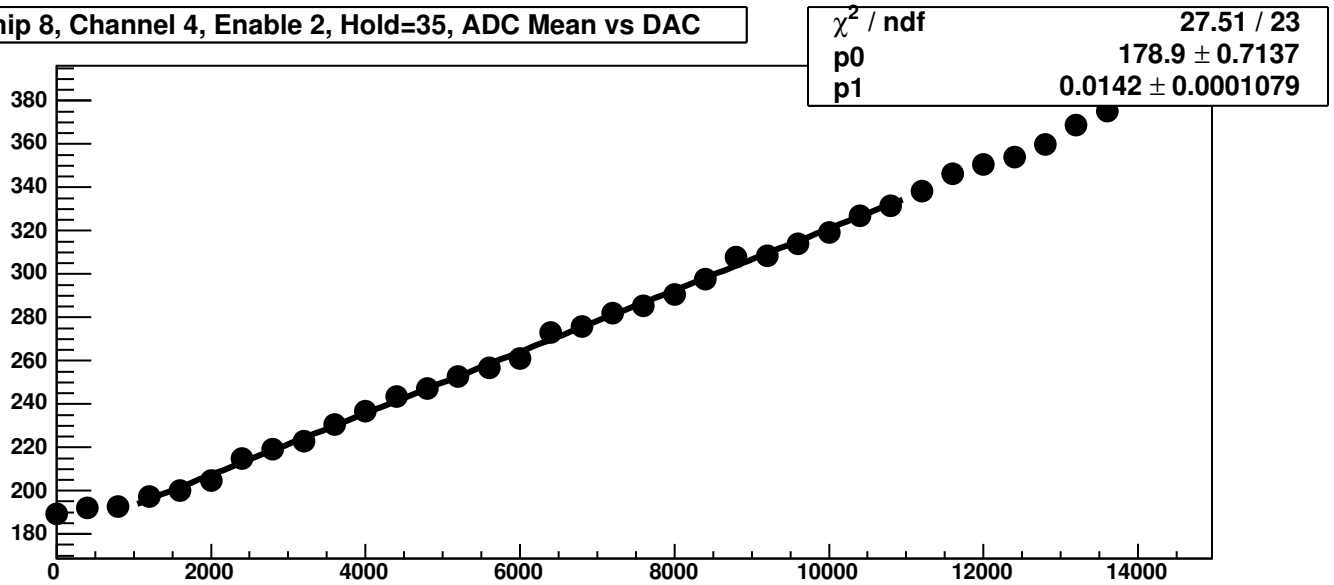



Chip 8, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 25.01 / 23
p0 172.5 ± 0.8699
p1 0.03529 ± 0.0001451
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000

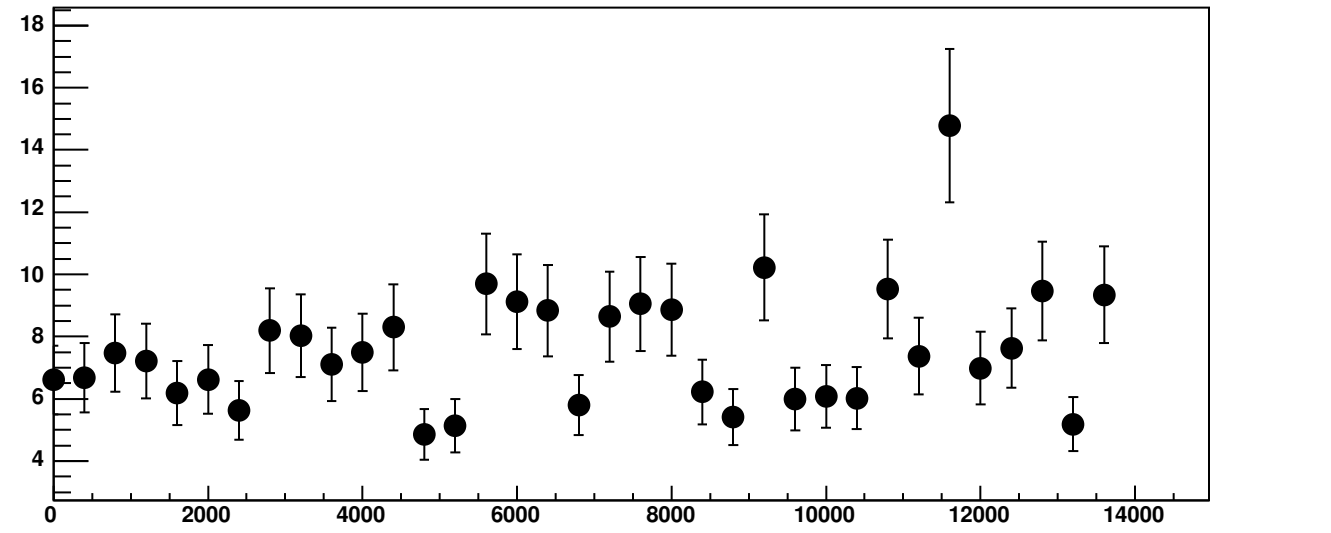

Chip 8, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

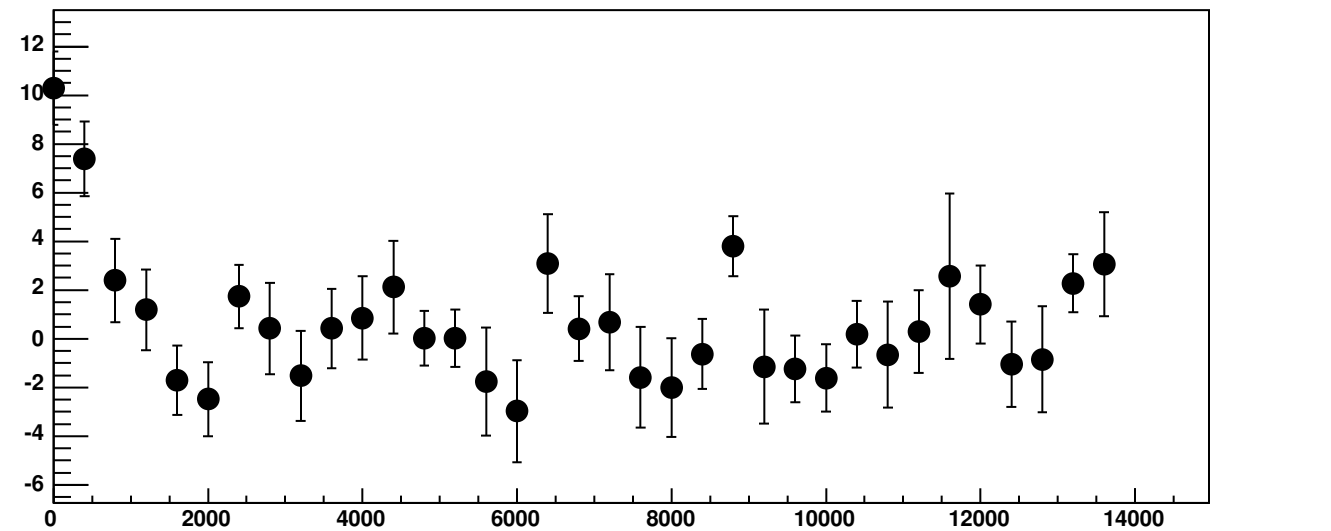
Chip 8, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



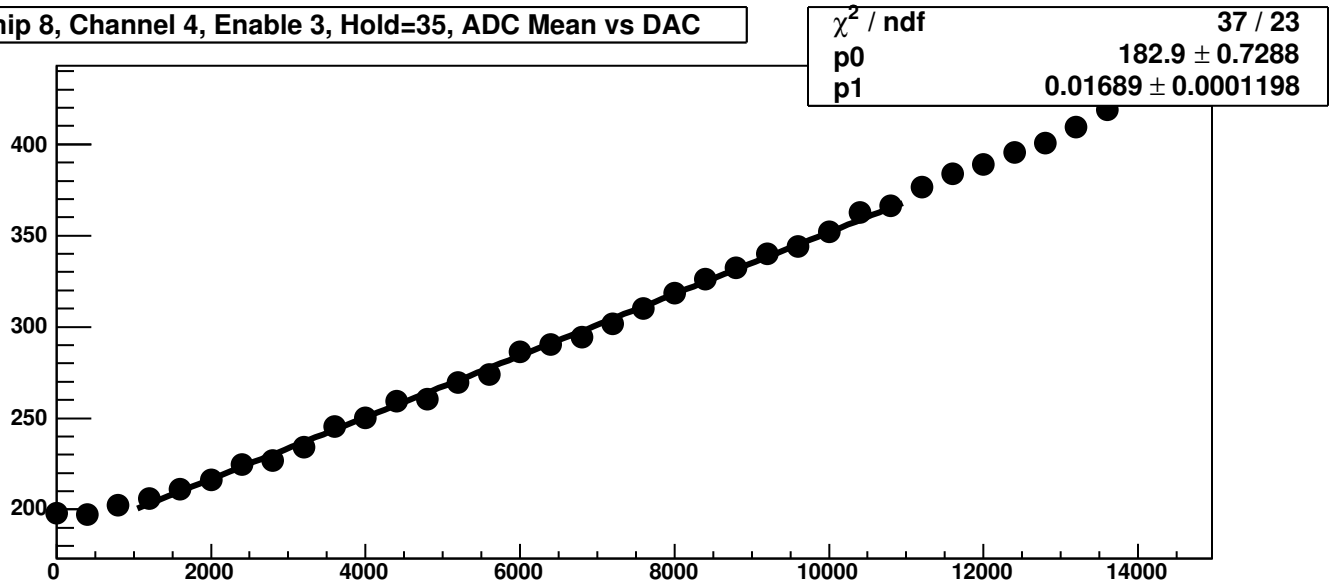
Chip 8, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



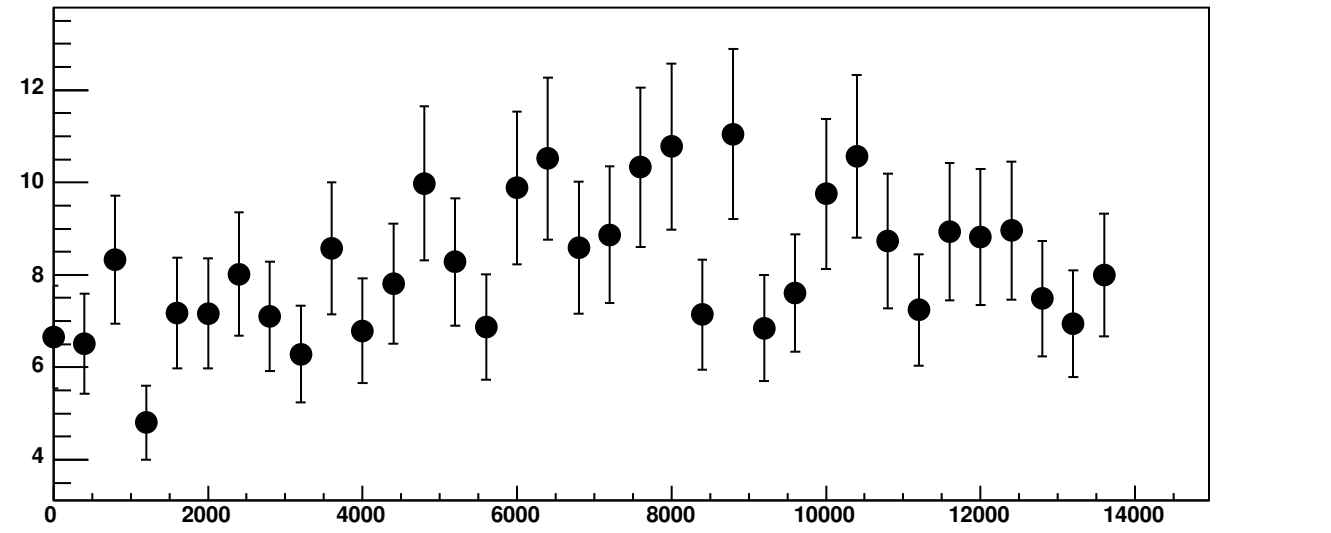
Chip 8, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



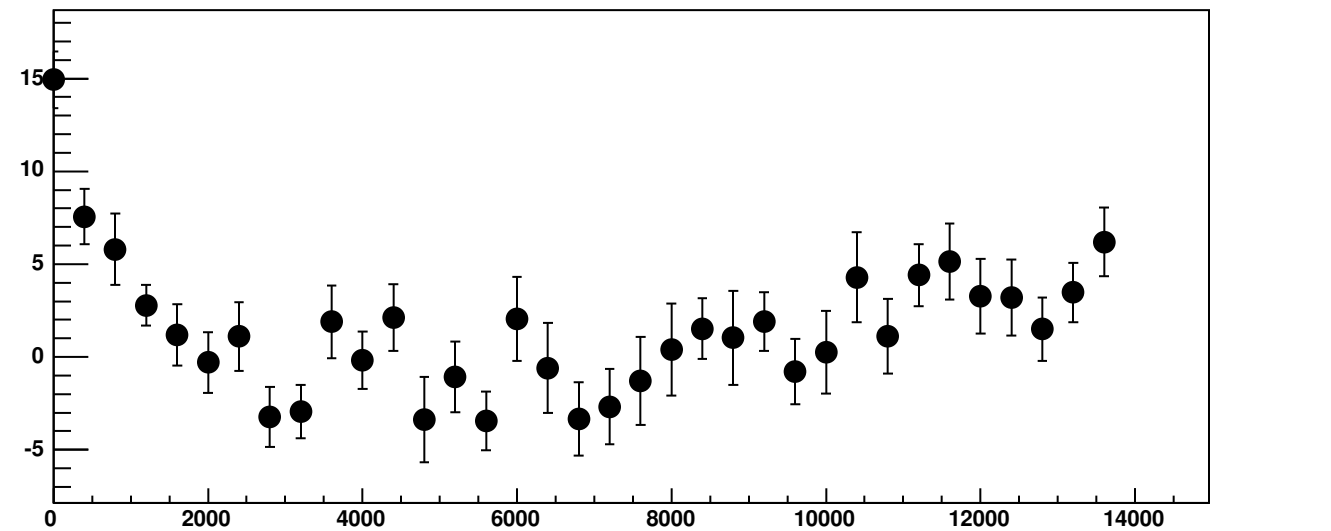
Chip 8, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

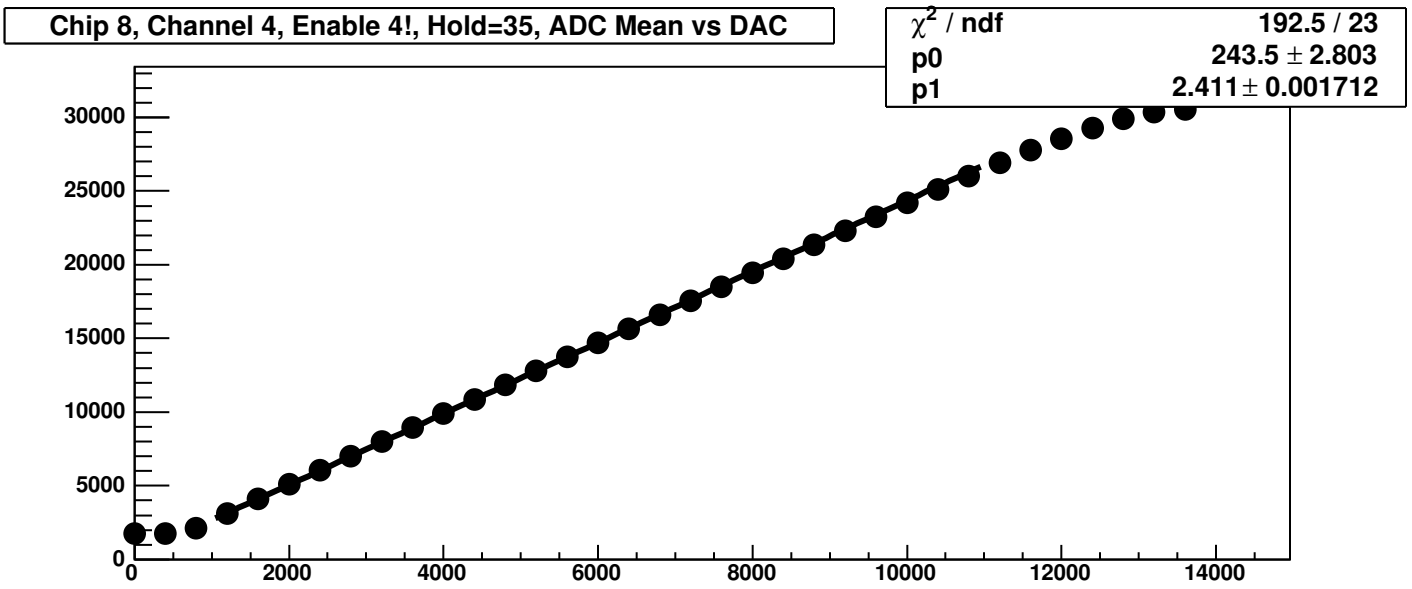


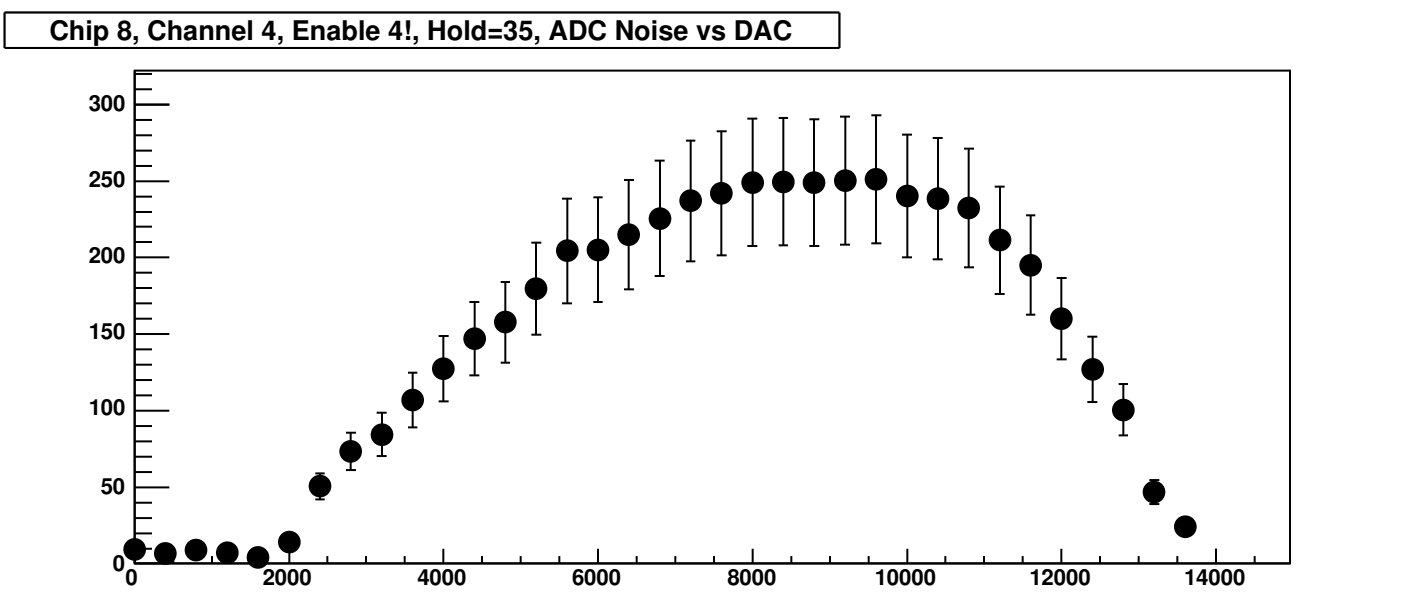
Chip 8, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC

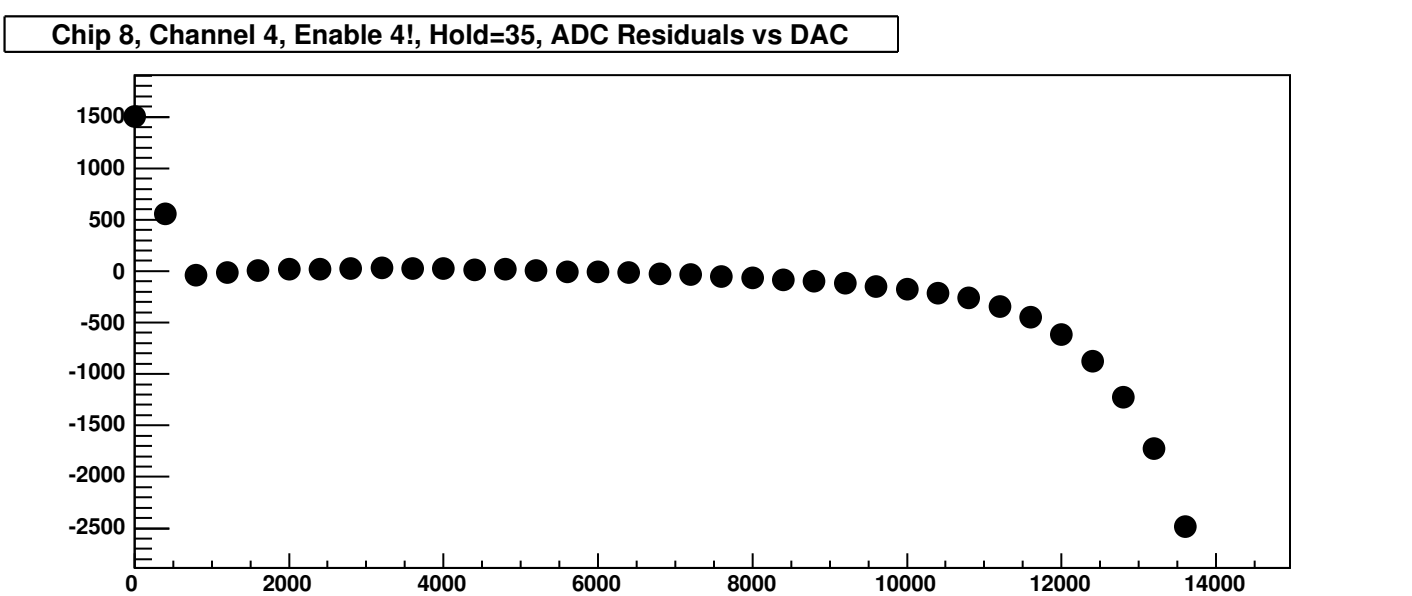


Chip 8, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

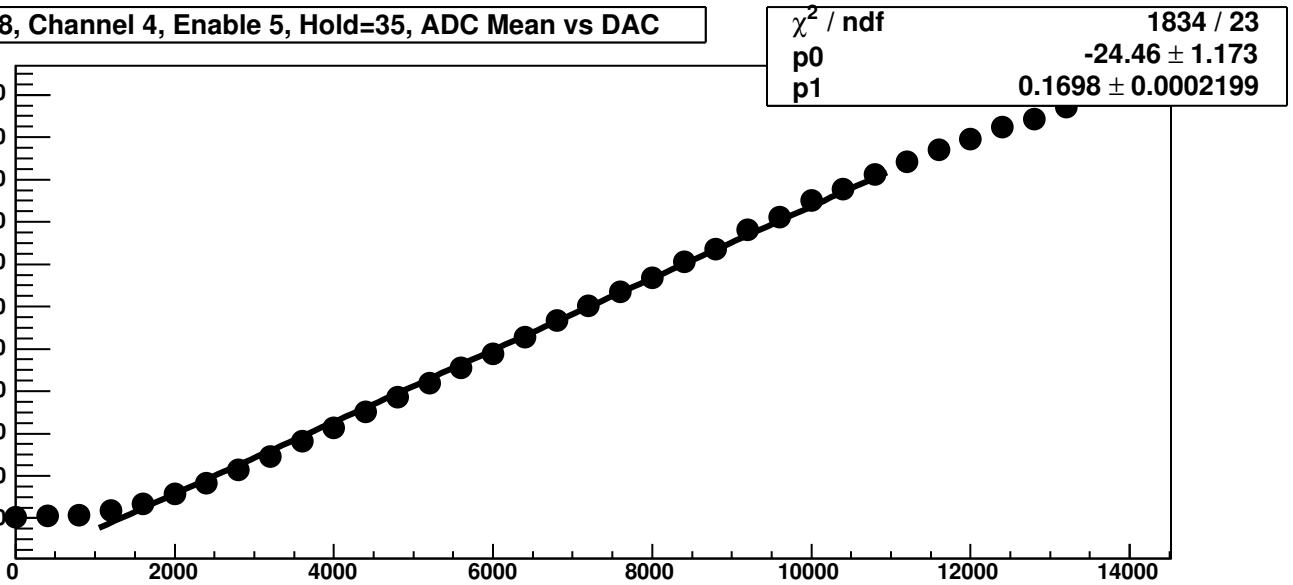



Chip 8, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 192.5 / 23
p0 243.5 ± 2.803
p1 2.411 ± 0.001712
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

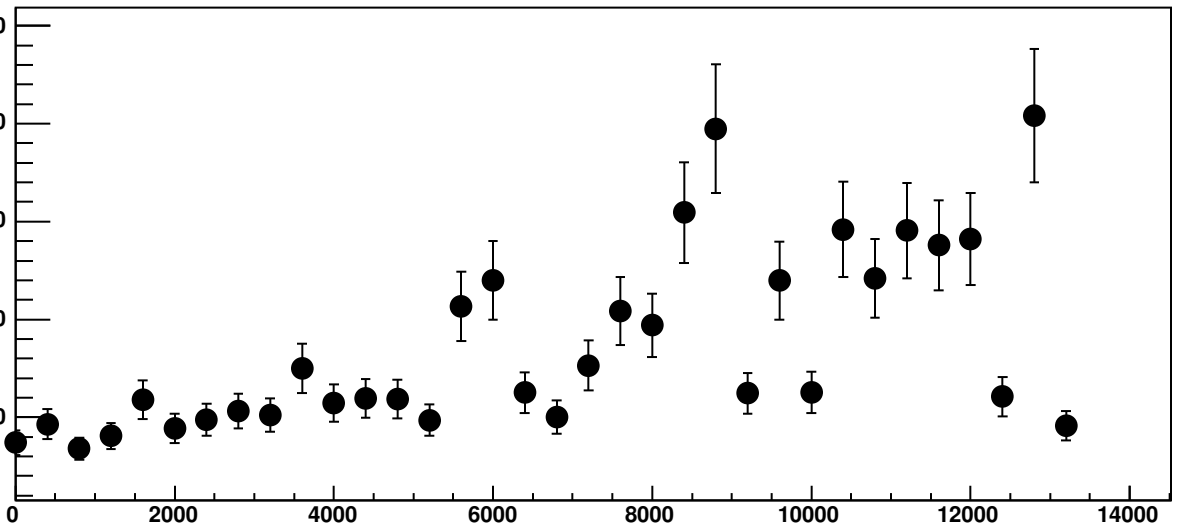

Chip 8, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

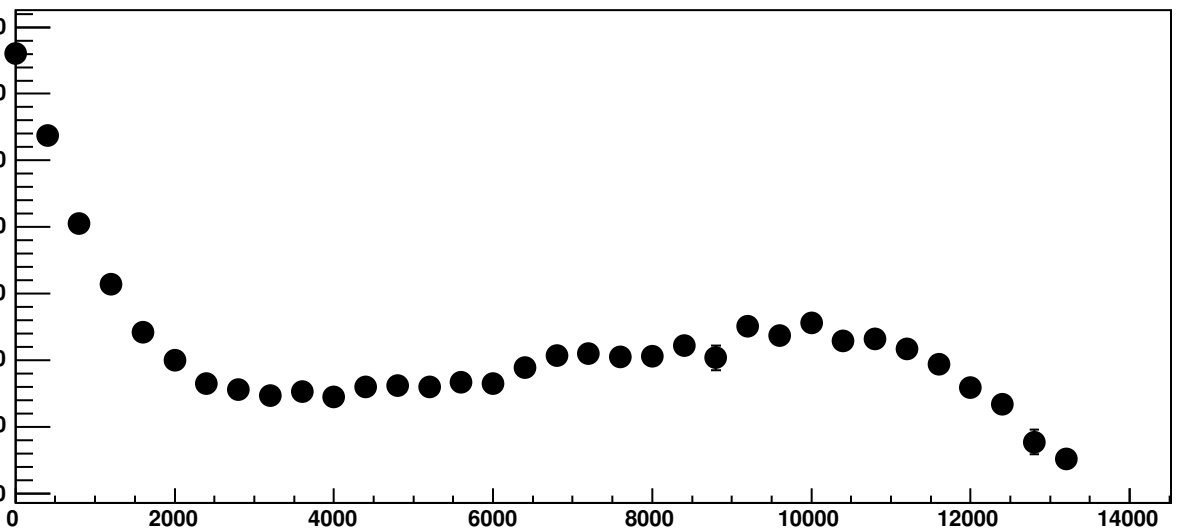
Chip 8, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

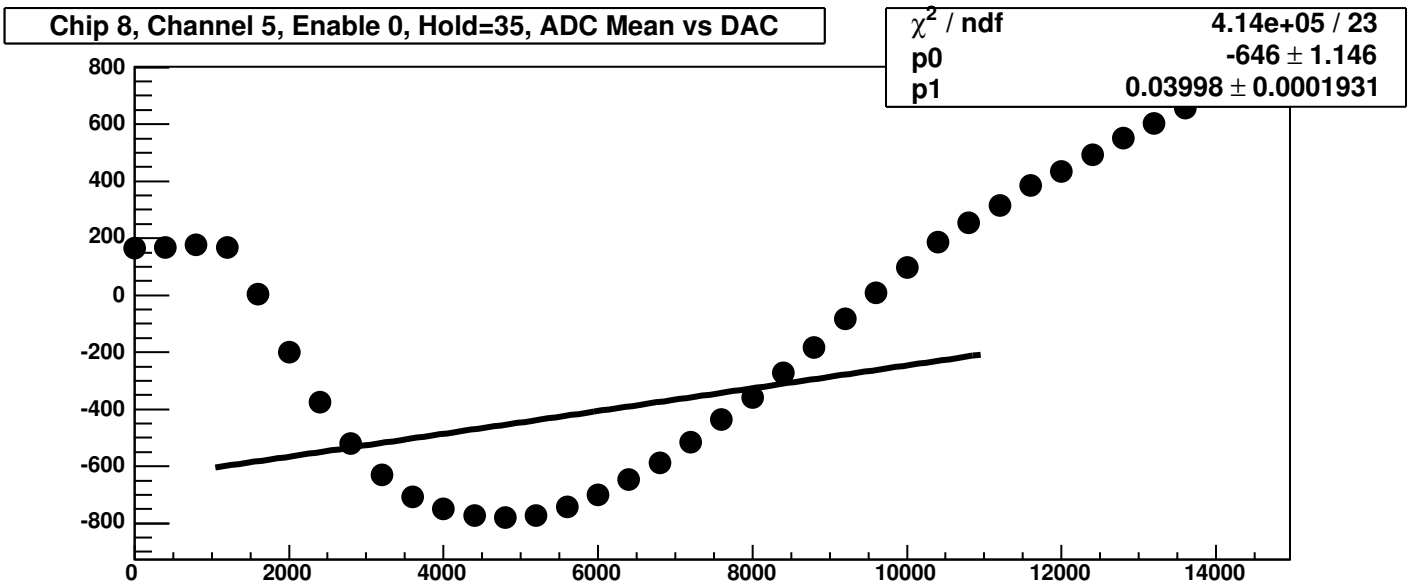


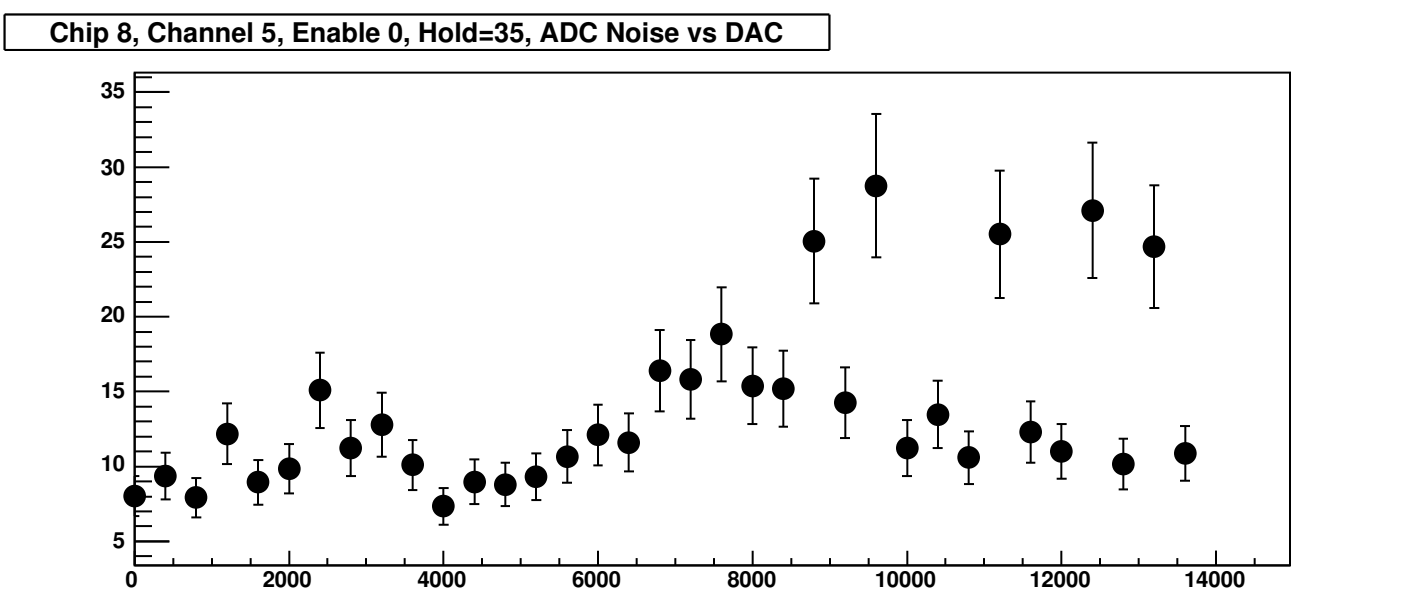
Chip 8, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC

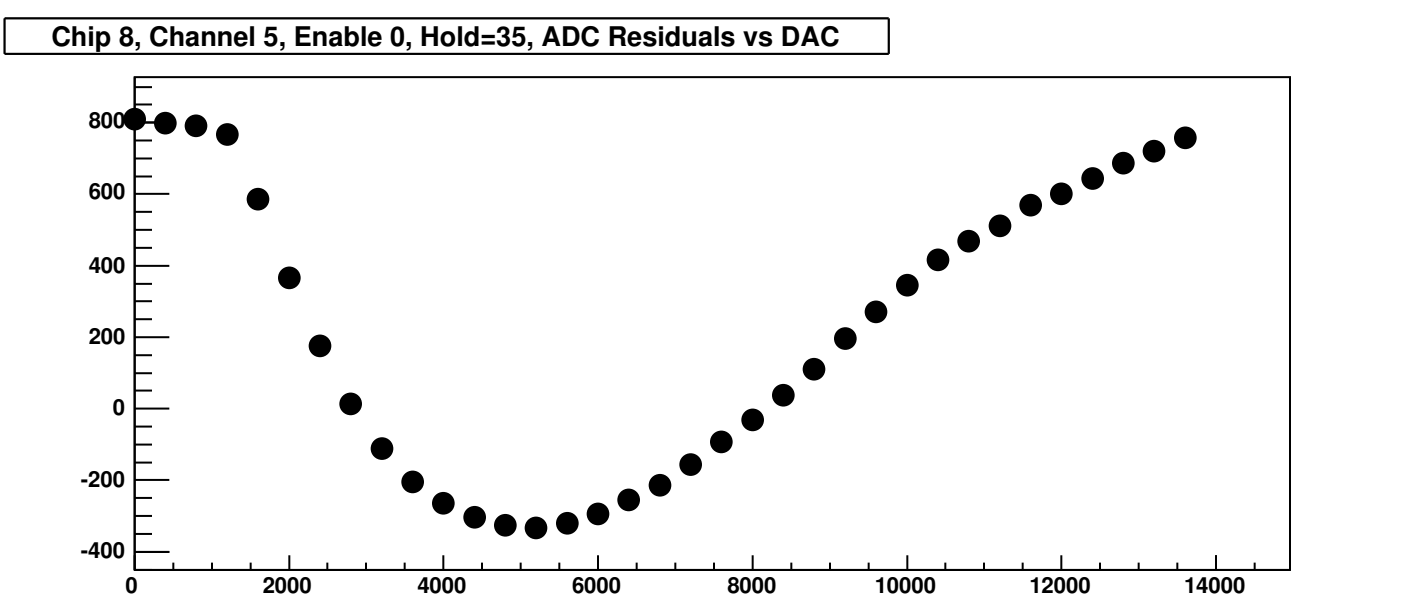


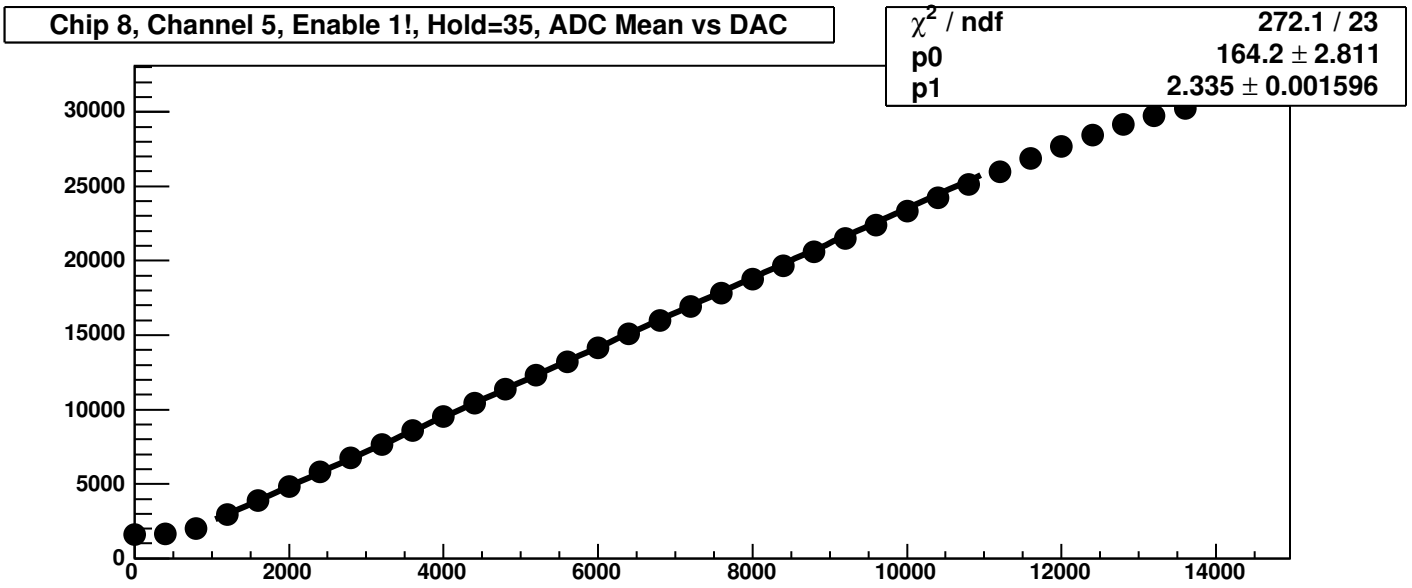
Chip 8, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

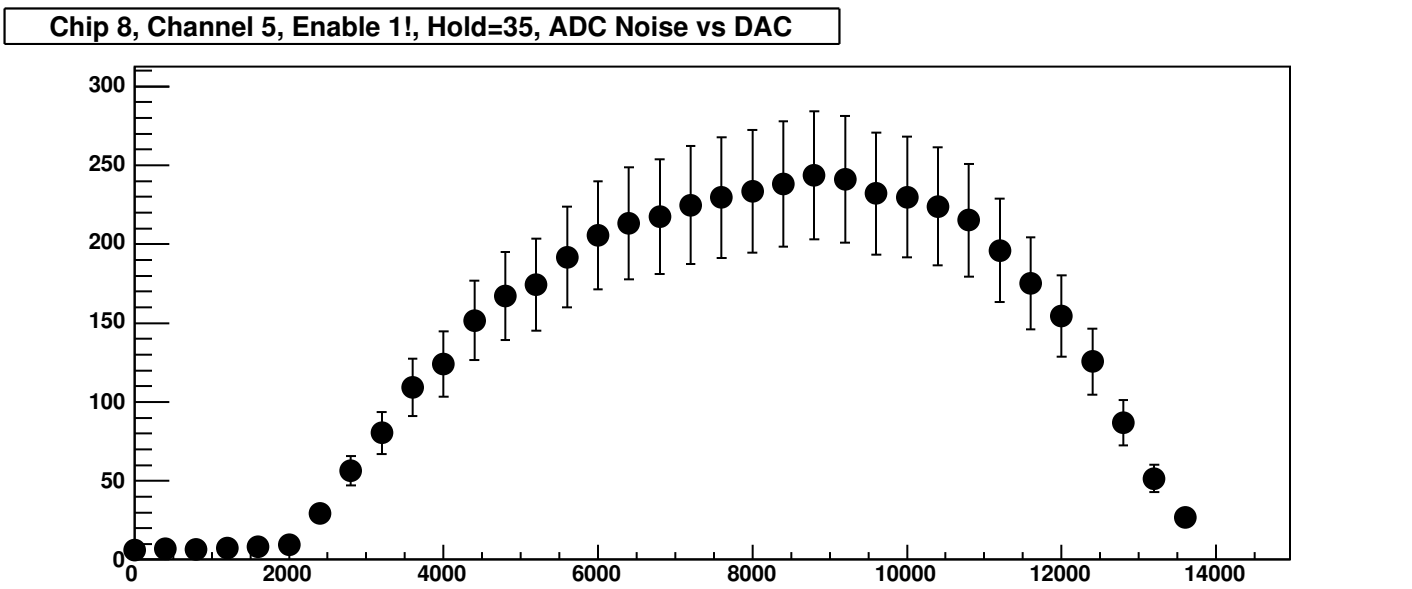


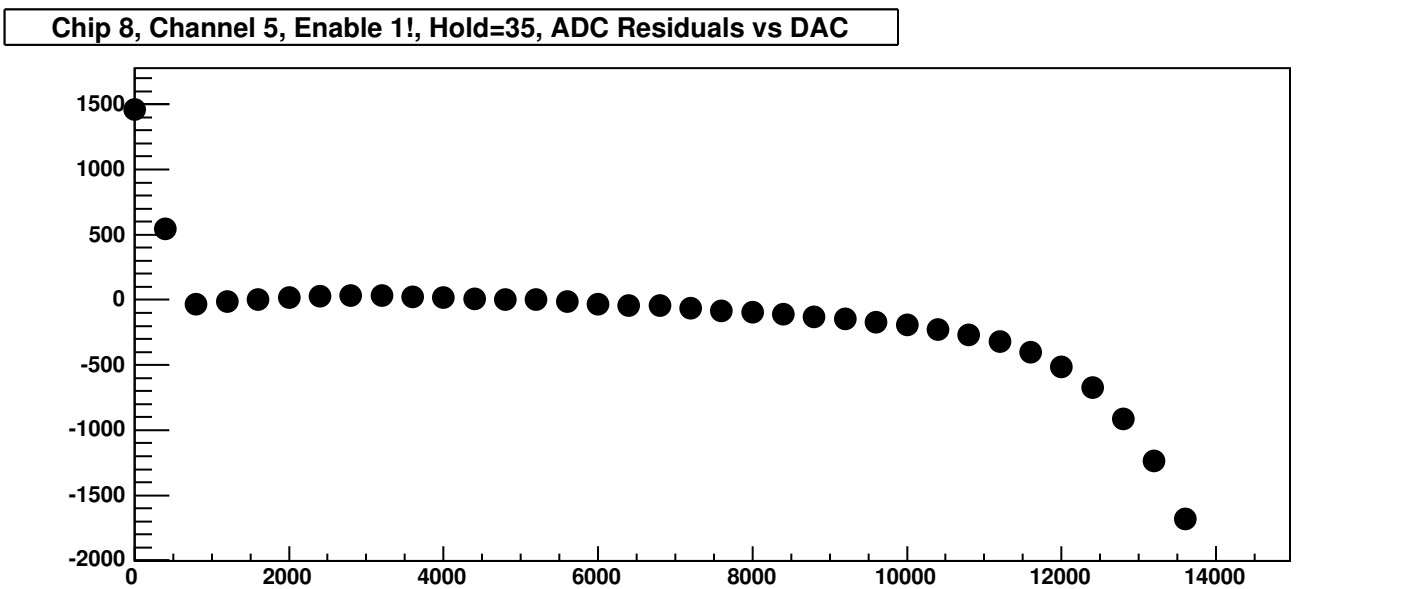

Chip 8, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.14e+05 / 23
p0 -646 ± 1.146
p1 0.03998 ± 0.0001931
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

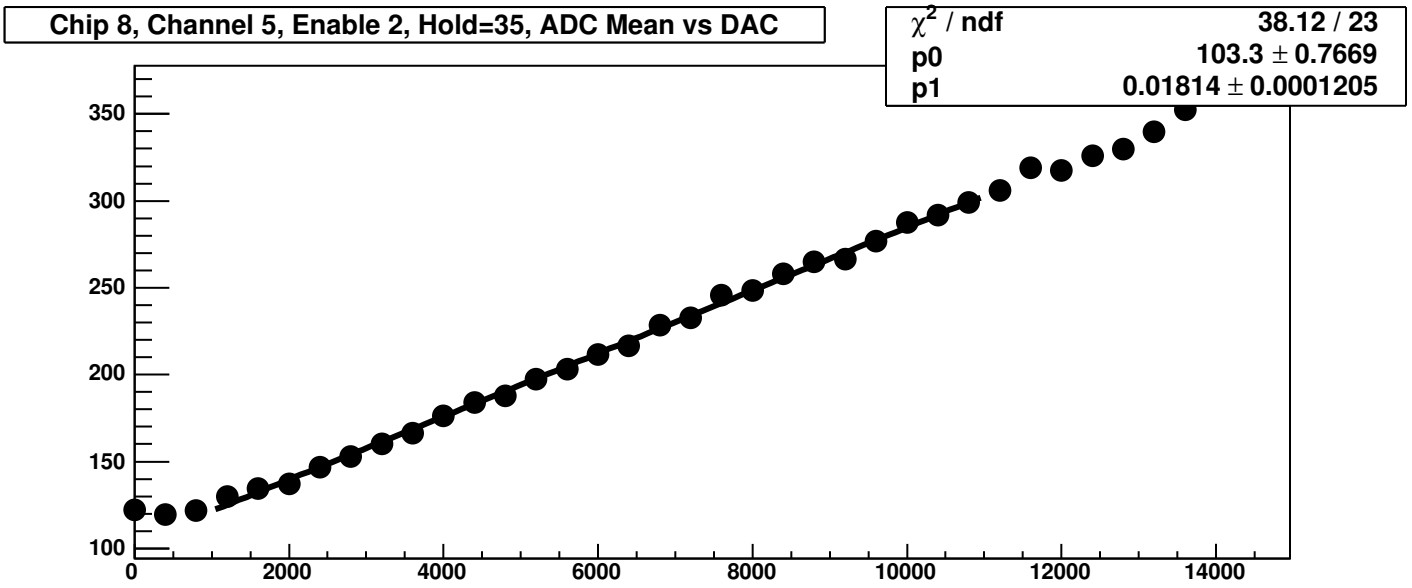

Chip 8, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

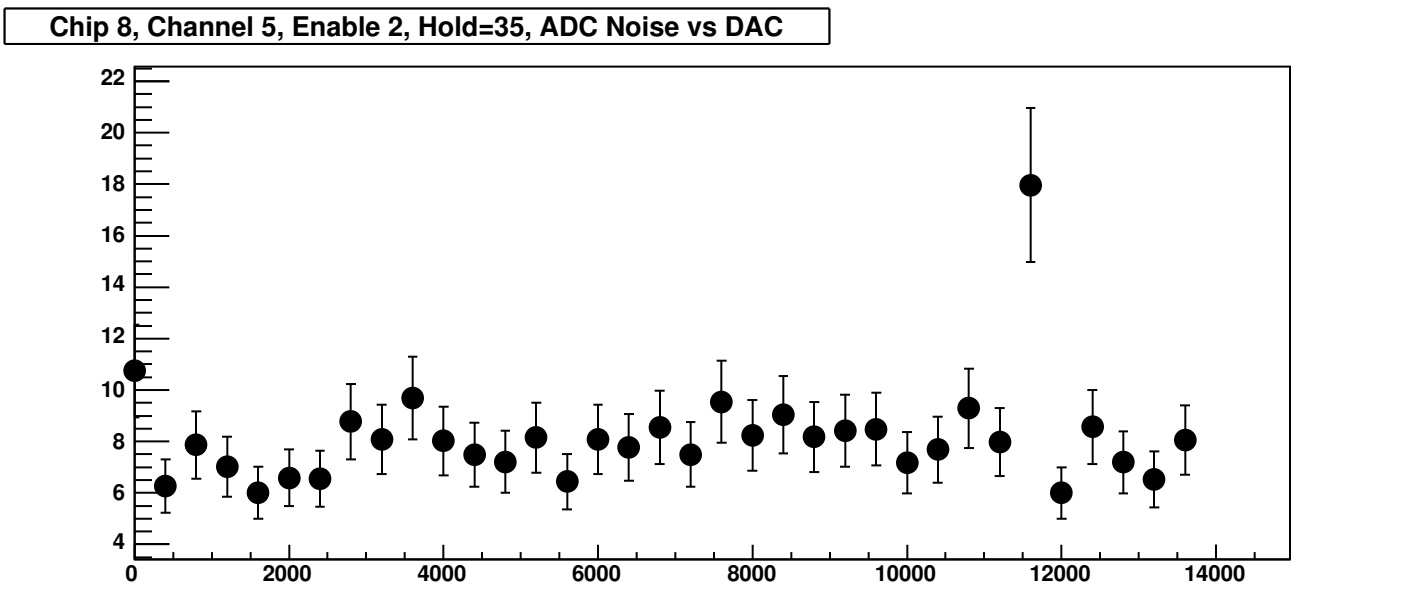

Chip 8, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

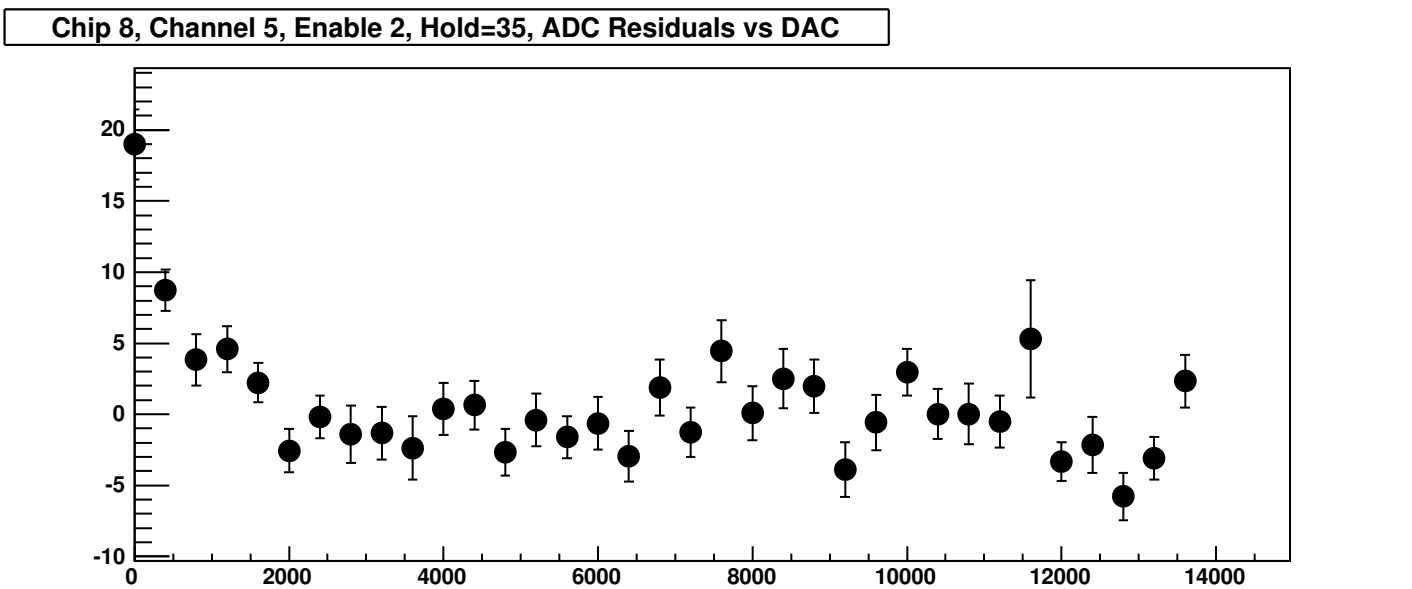

Chip 8, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 272.1 / 23
p0 164.2 ± 2.811
p1 2.335 ± 0.001596
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

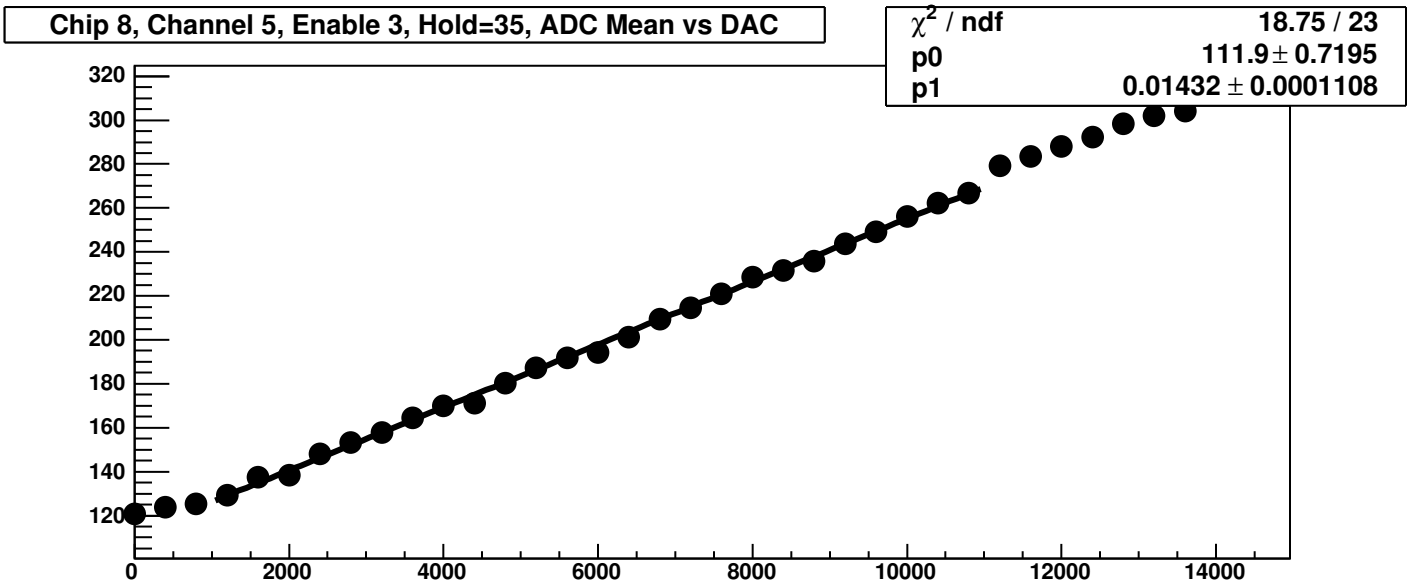

Chip 8, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

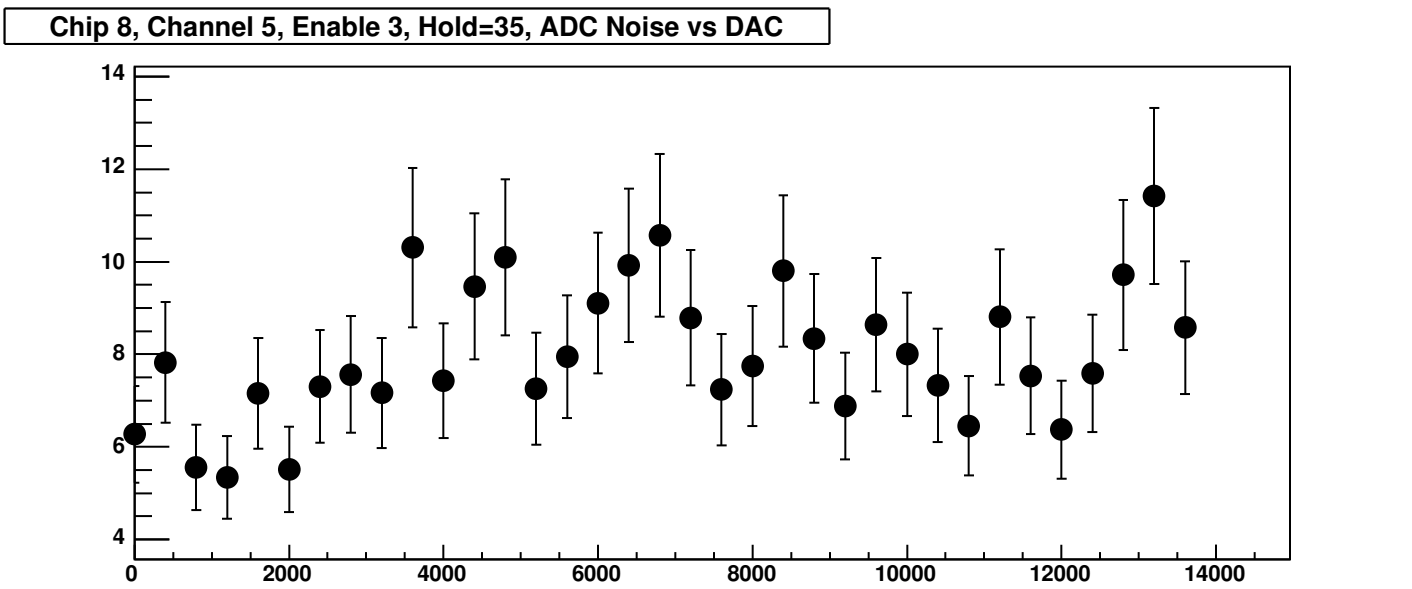

Chip 8, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

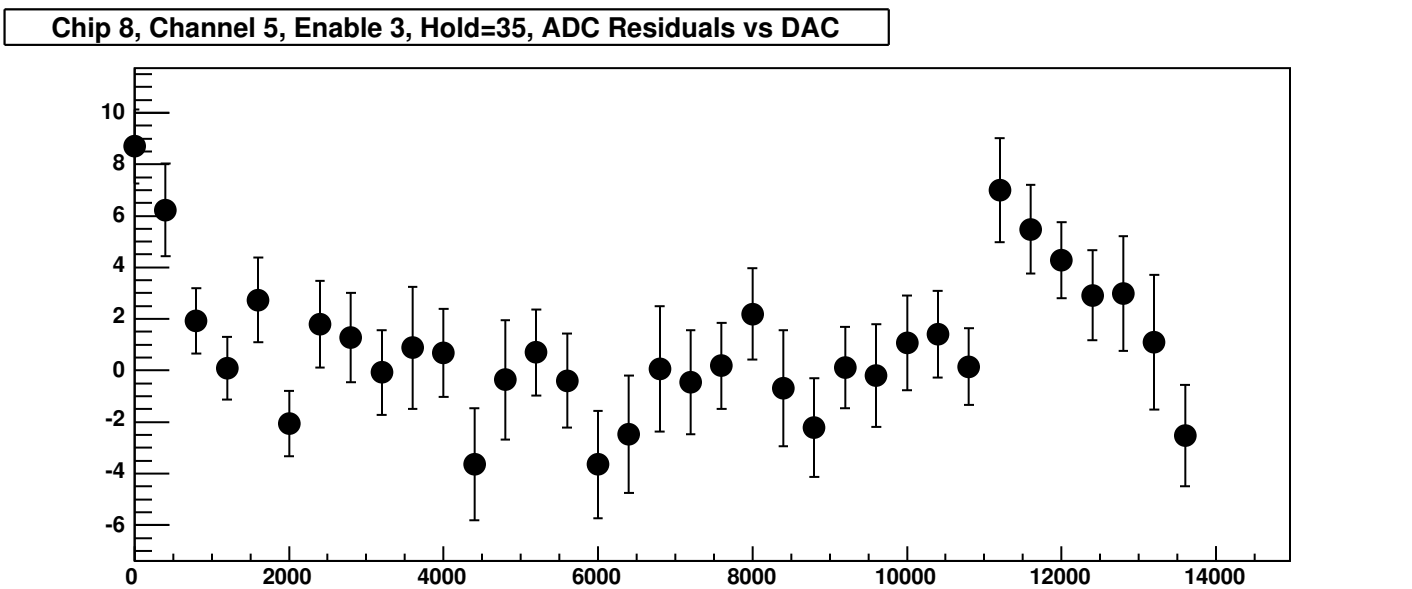

Chip 8, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 38.12 / 23
p0 103.3 ± 0.7669
p1 0.01814 ± 0.0001205
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

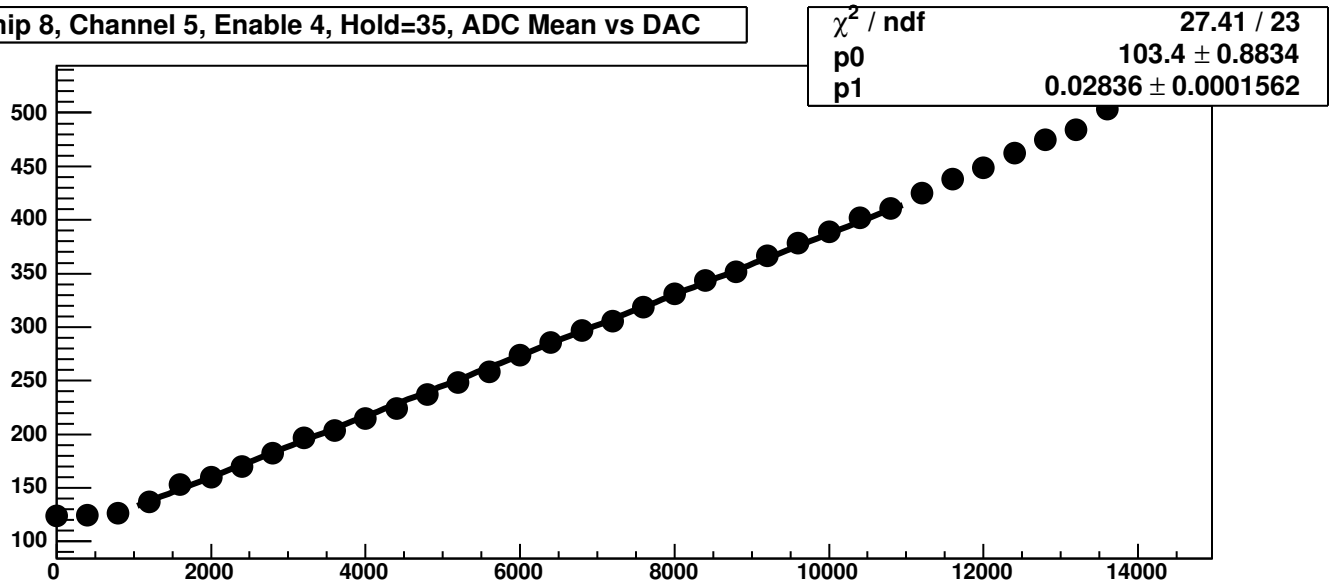

Chip 8, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 18.75 / 23
p0 111.9 ± 0.7195
p1 0.01432 ± 0.0001108
320
300
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

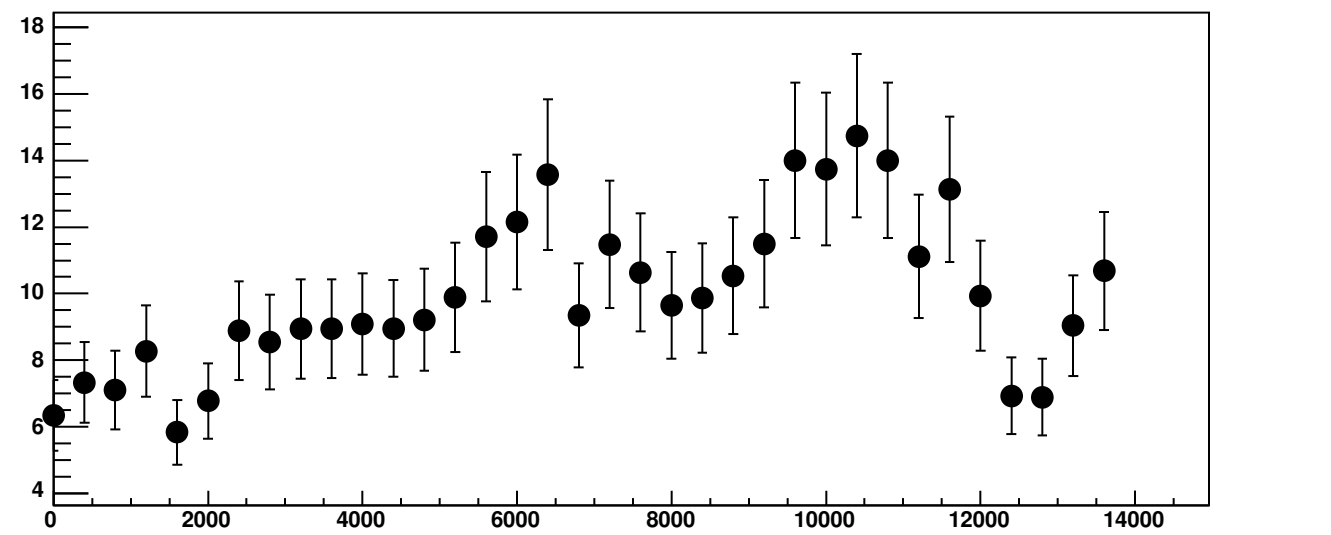

Chip 8, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000

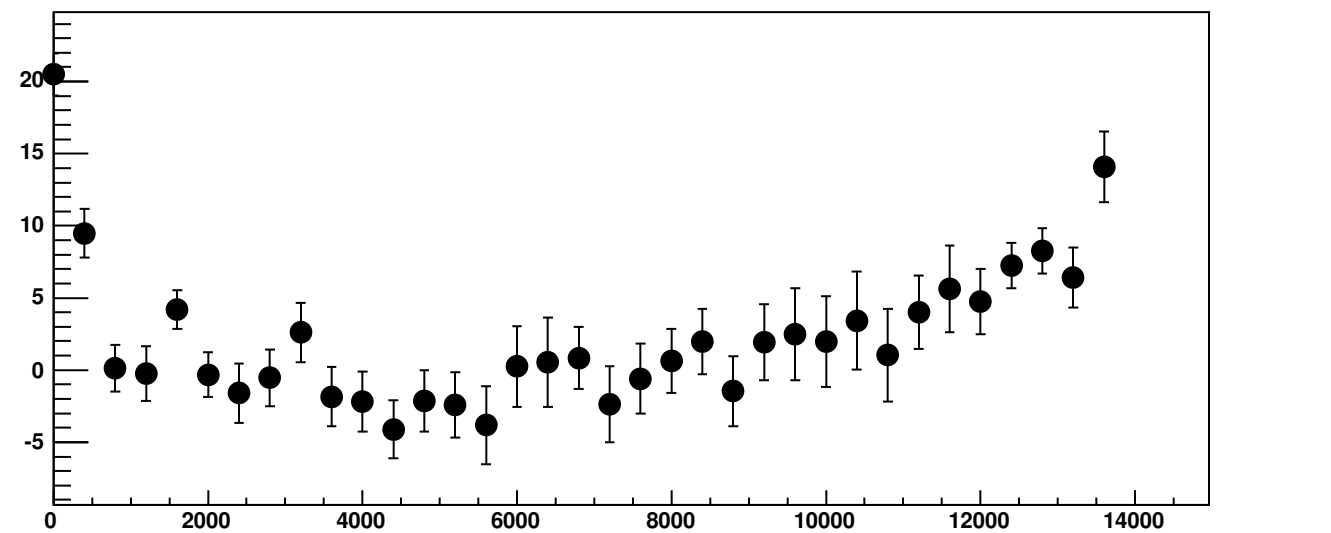
Chip 8, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC

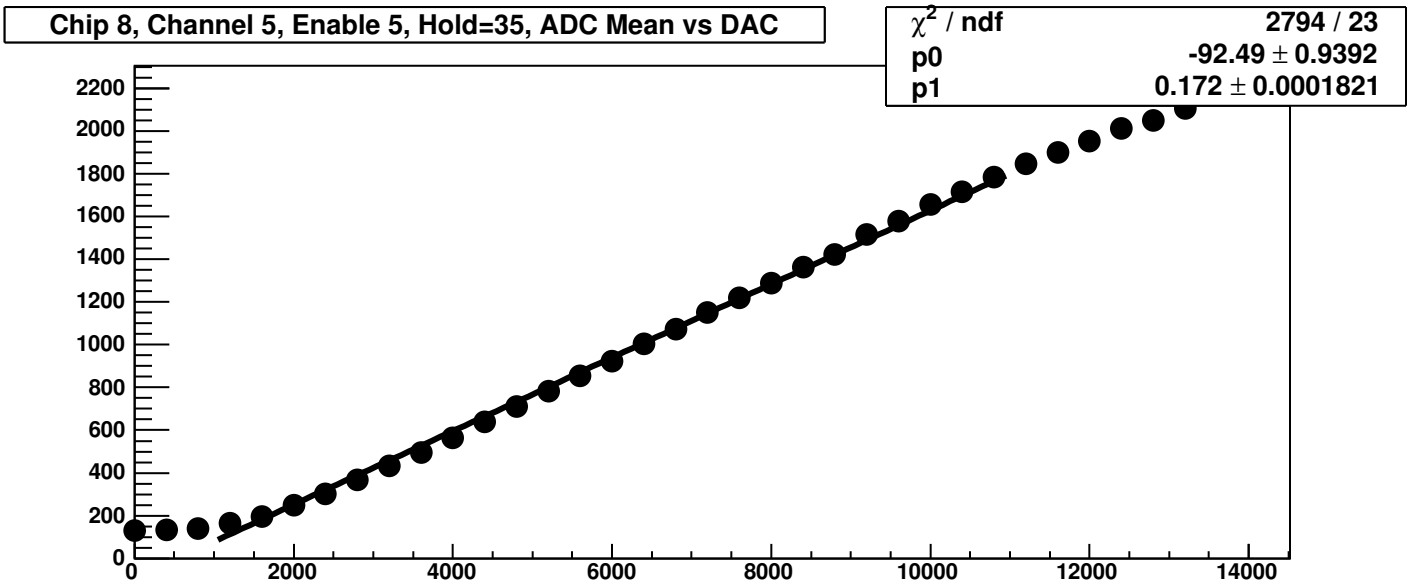


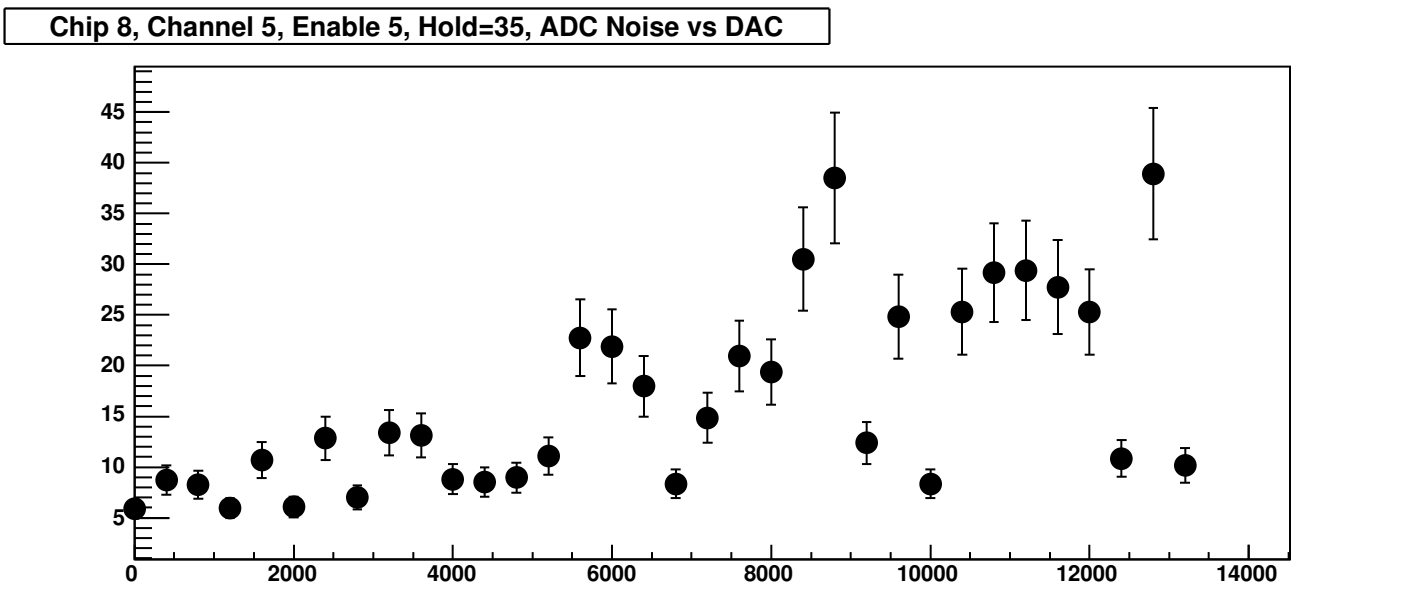
Chip 8, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC

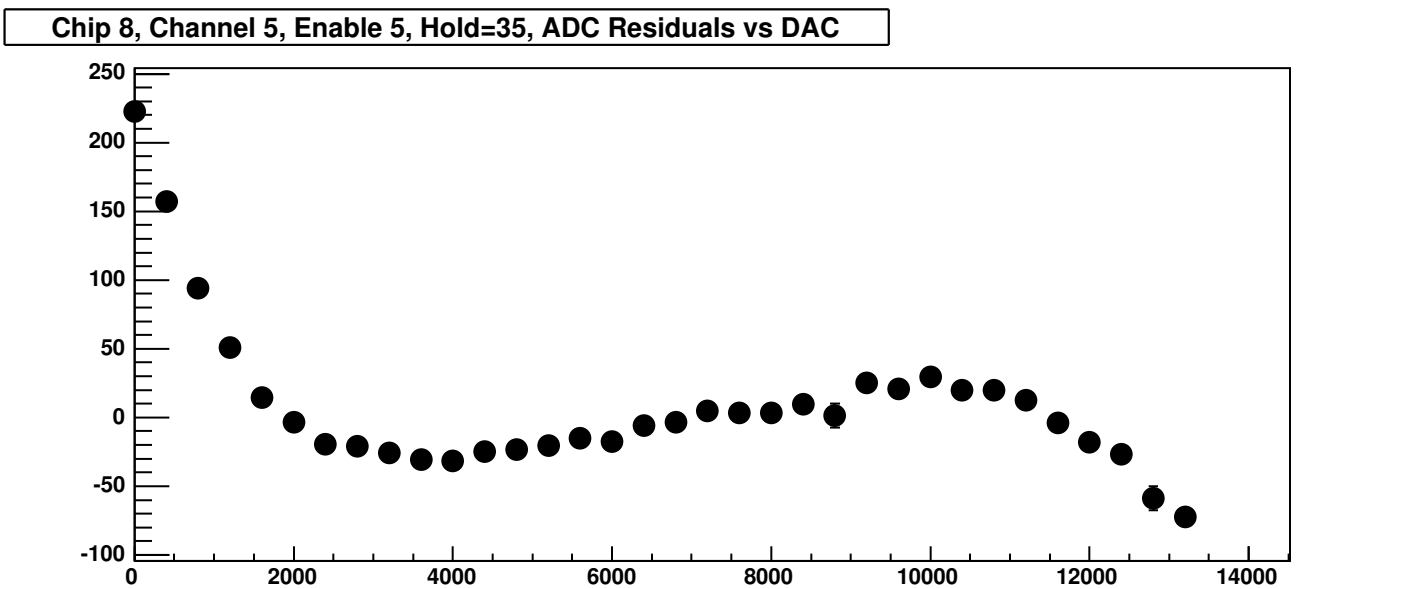


Chip 8, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC

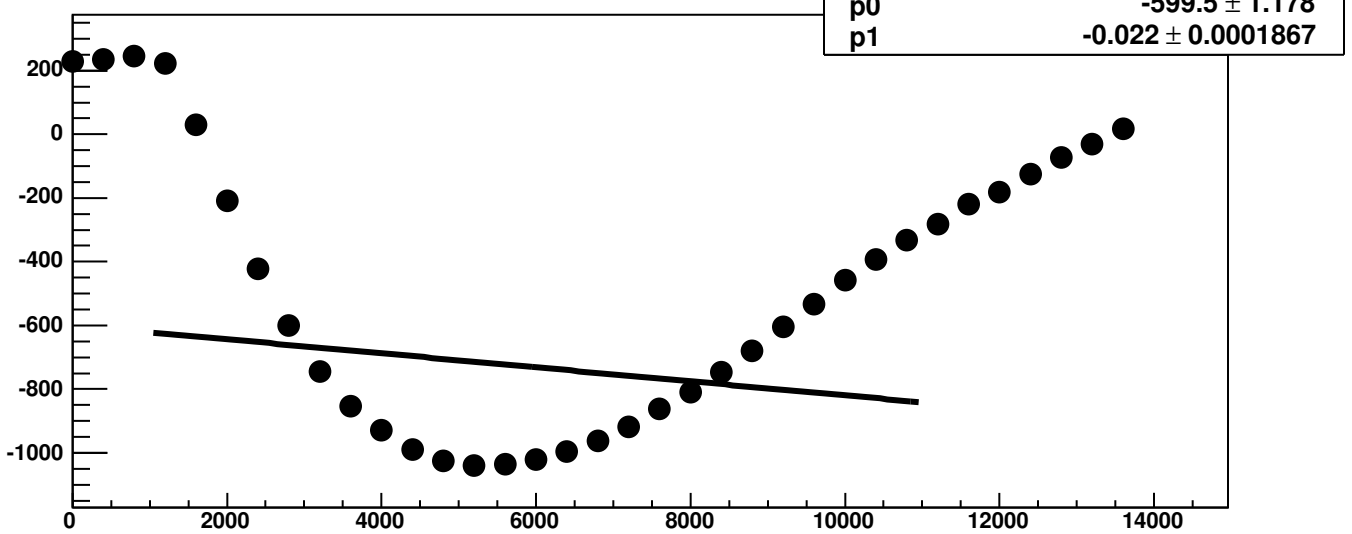



Chip 8, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2794 / 23
p0 -92.49 ± 0.9392
p1 0.172 ± 0.0001821
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

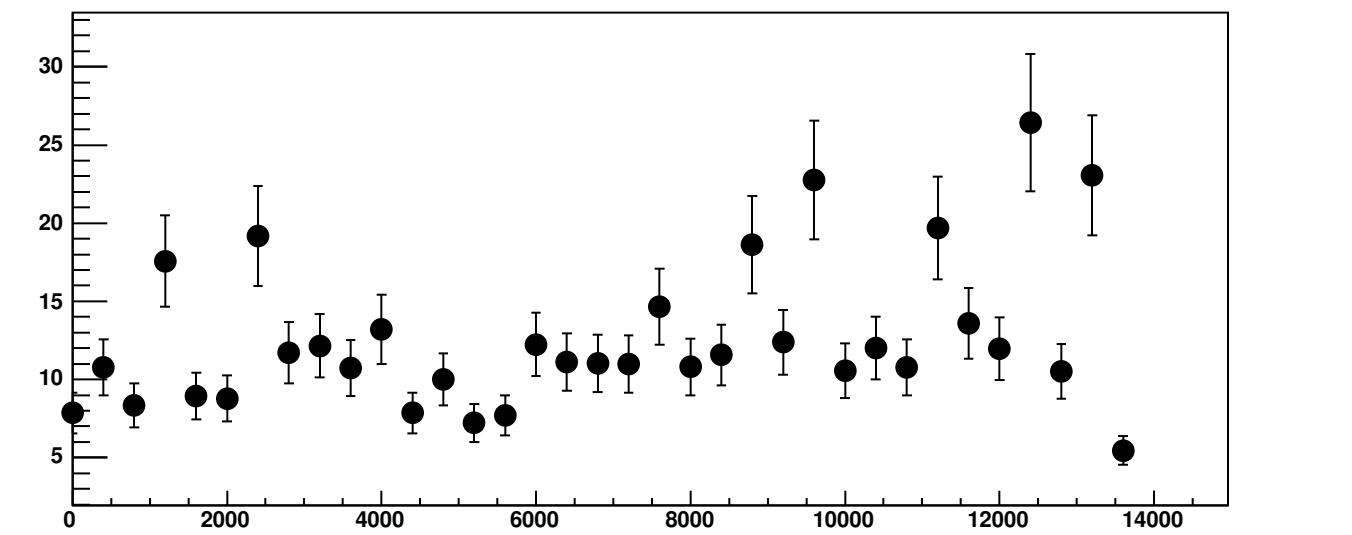

Chip 8, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

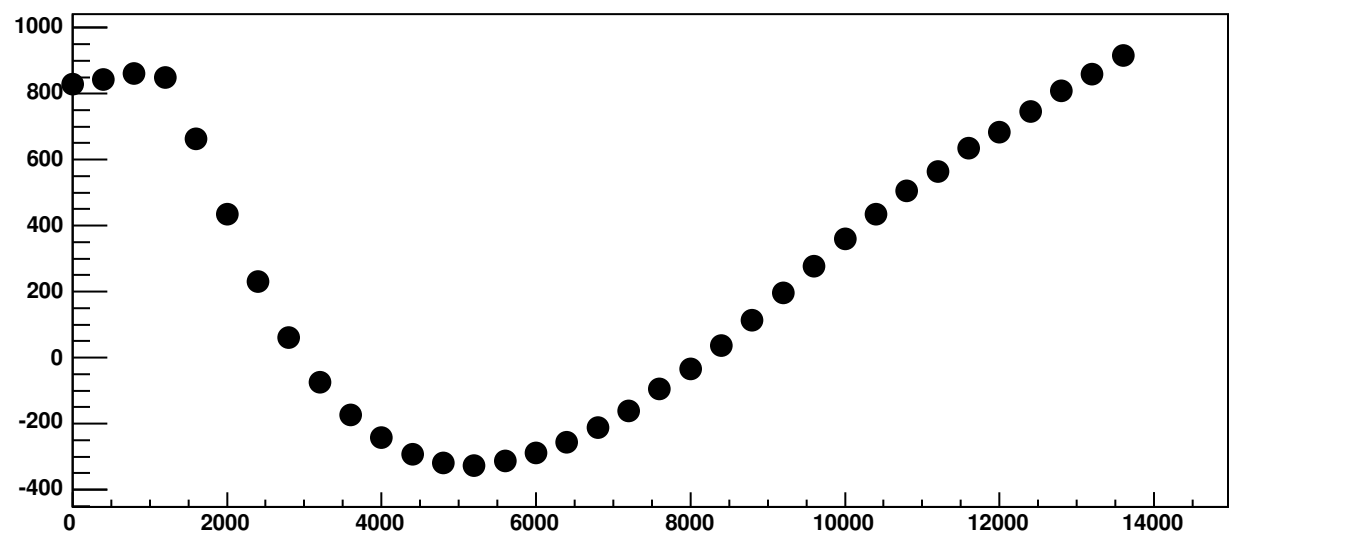
Chip 8, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC

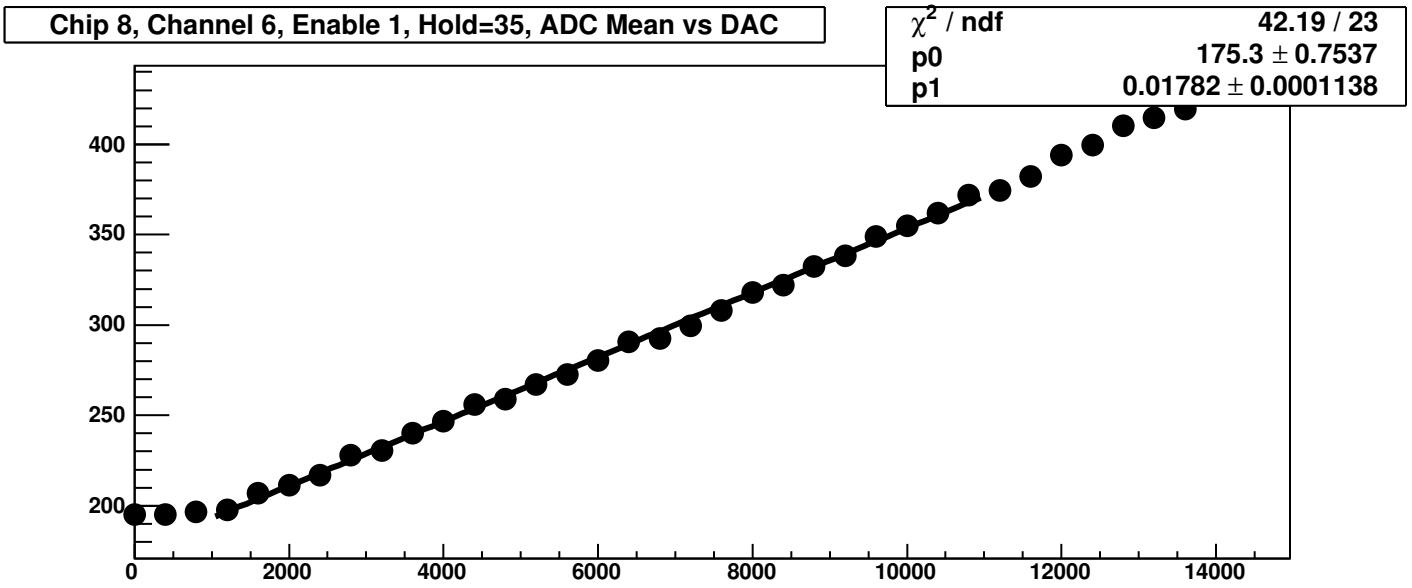


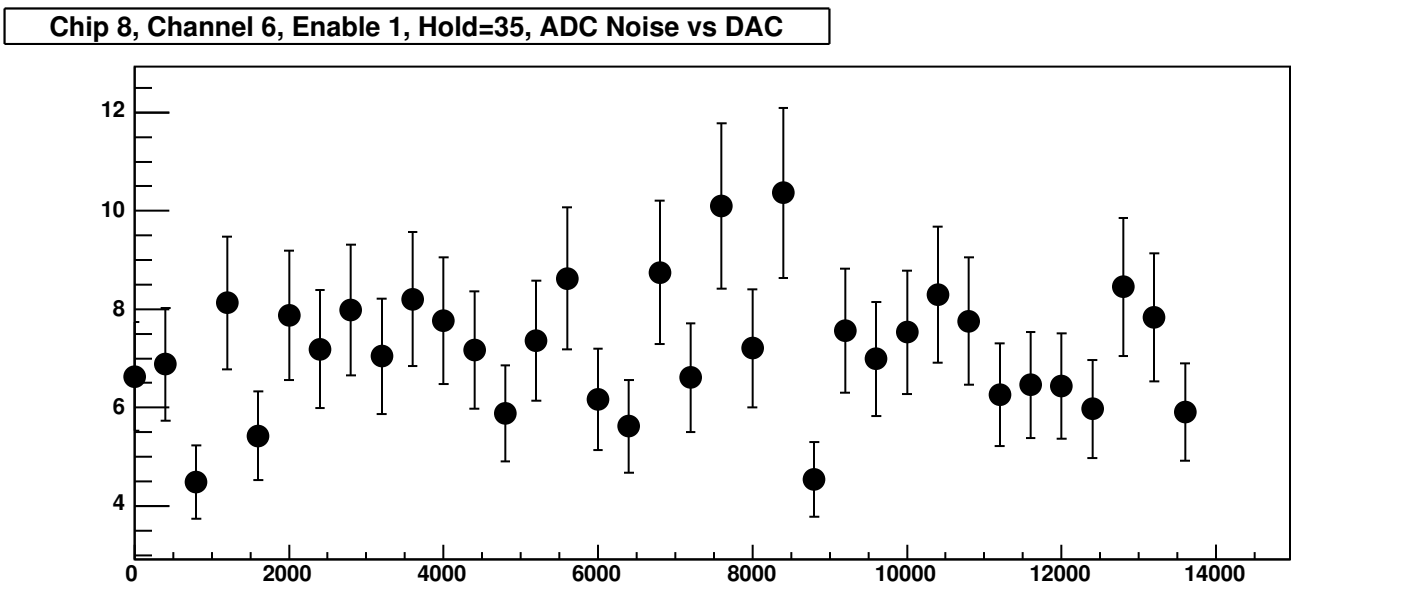
Chip 8, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC

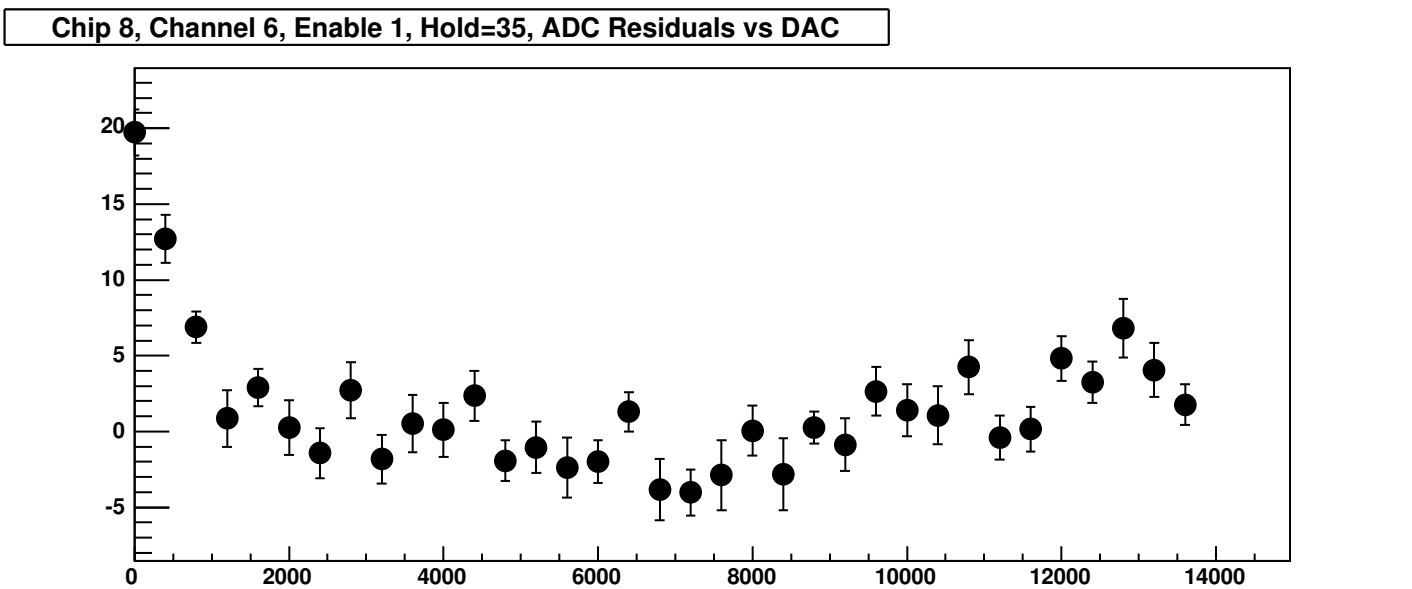


Chip 8, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC

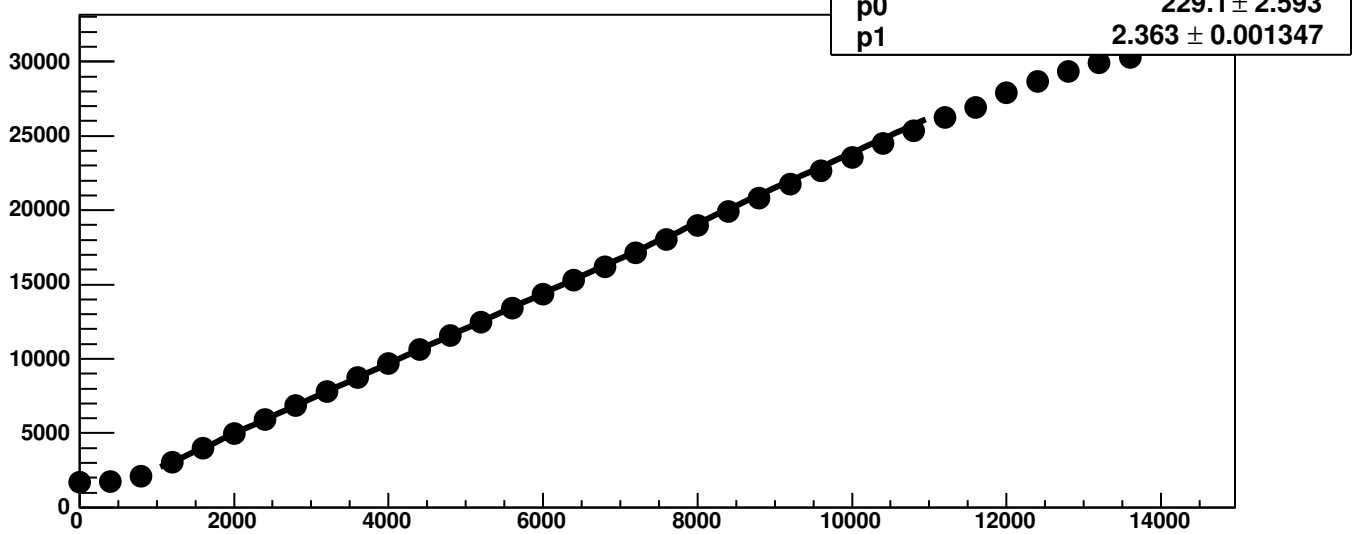



Chip 8, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 42.19 / 23
p0 175.3 ± 0.7537
p1 0.01782 ± 0.0001138
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

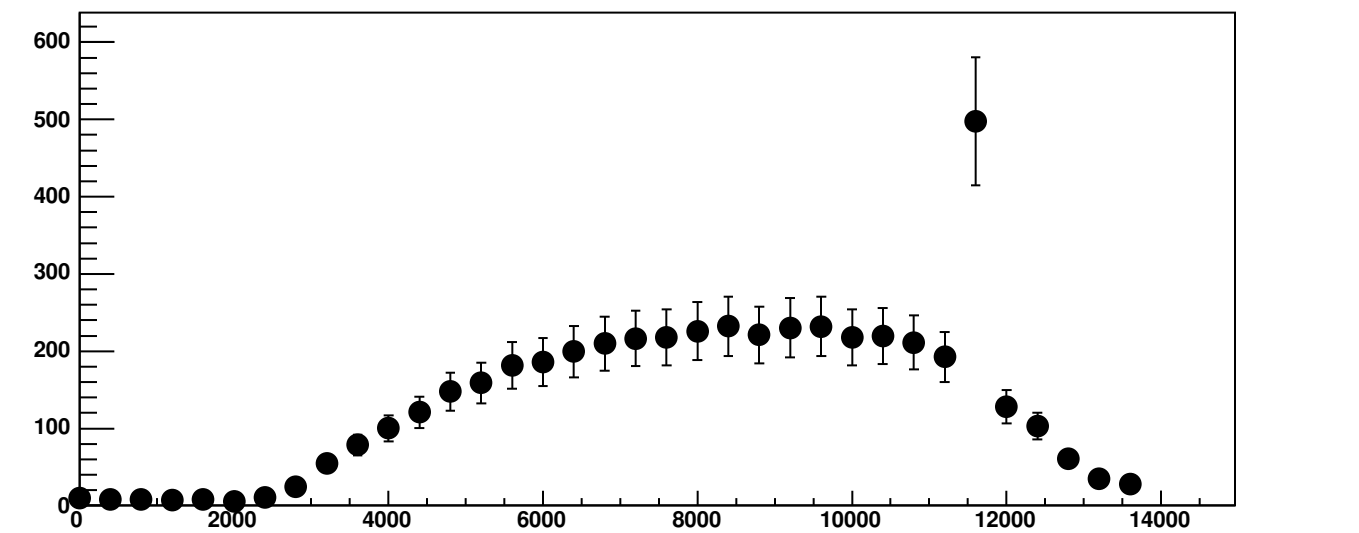

Chip 8, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

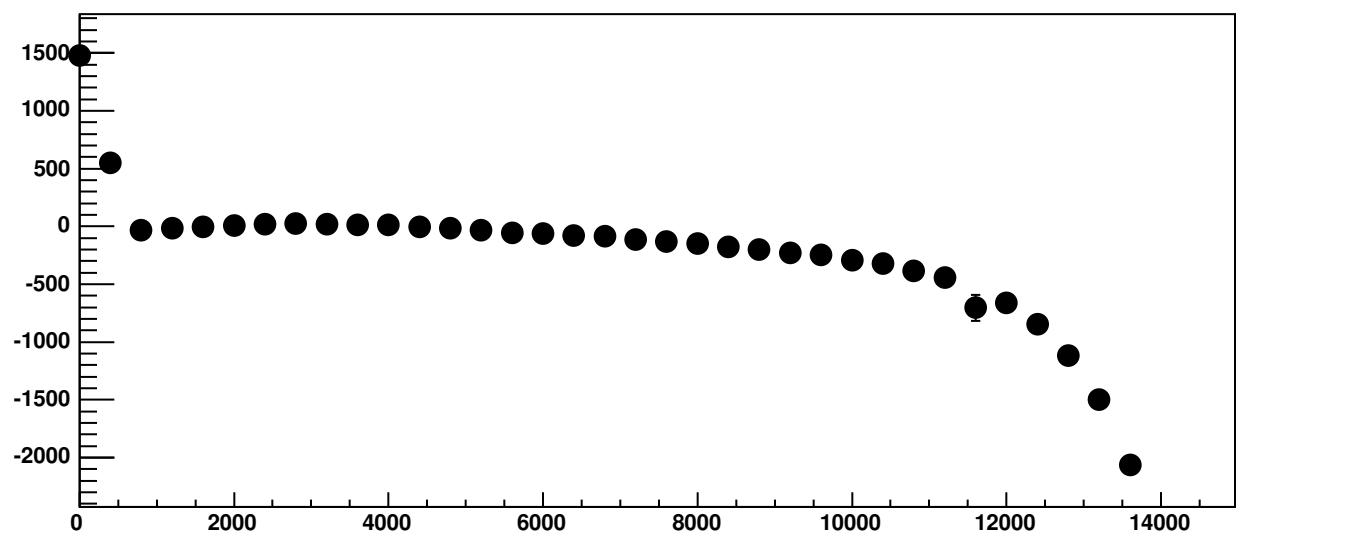
Chip 8, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC



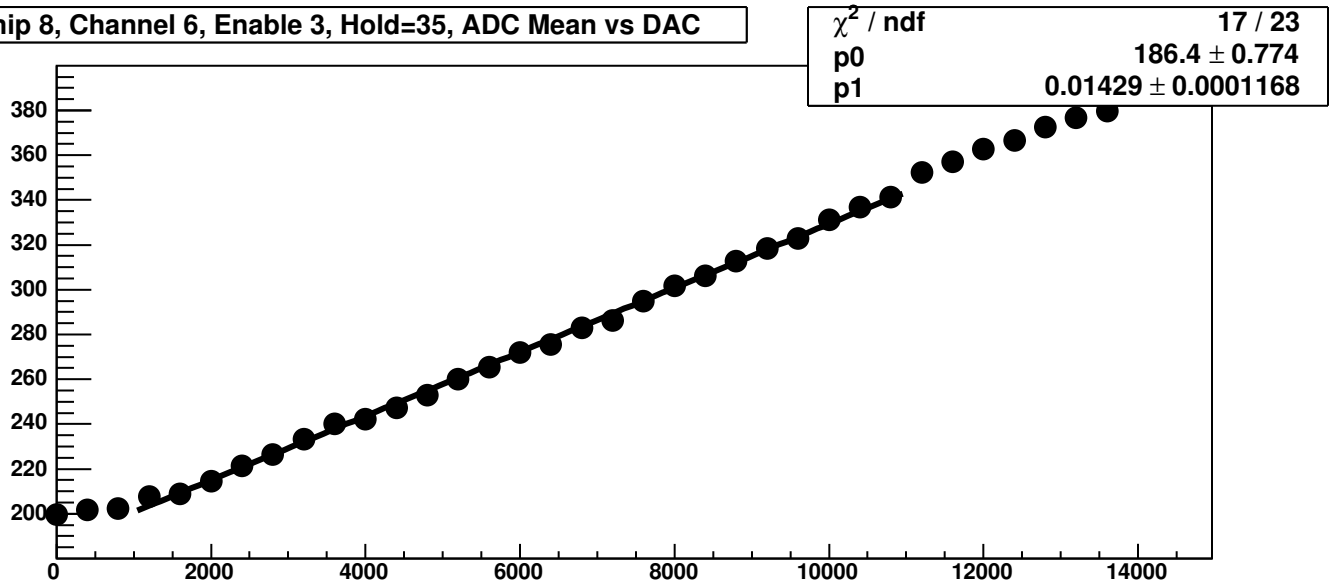
Chip 8, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC



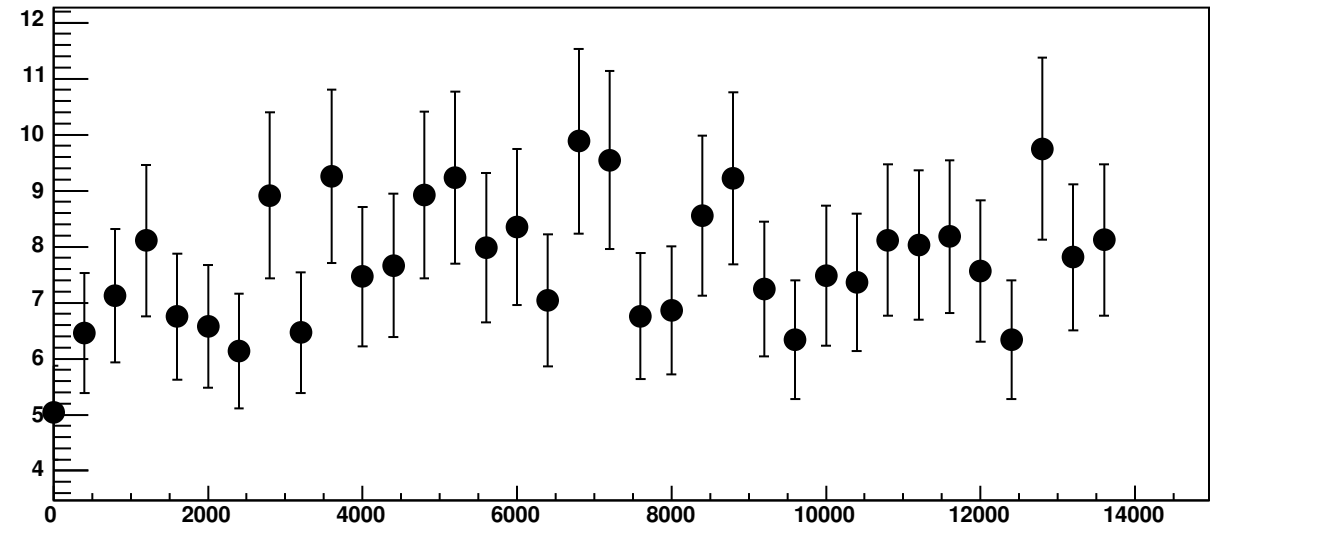
Chip 8, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC



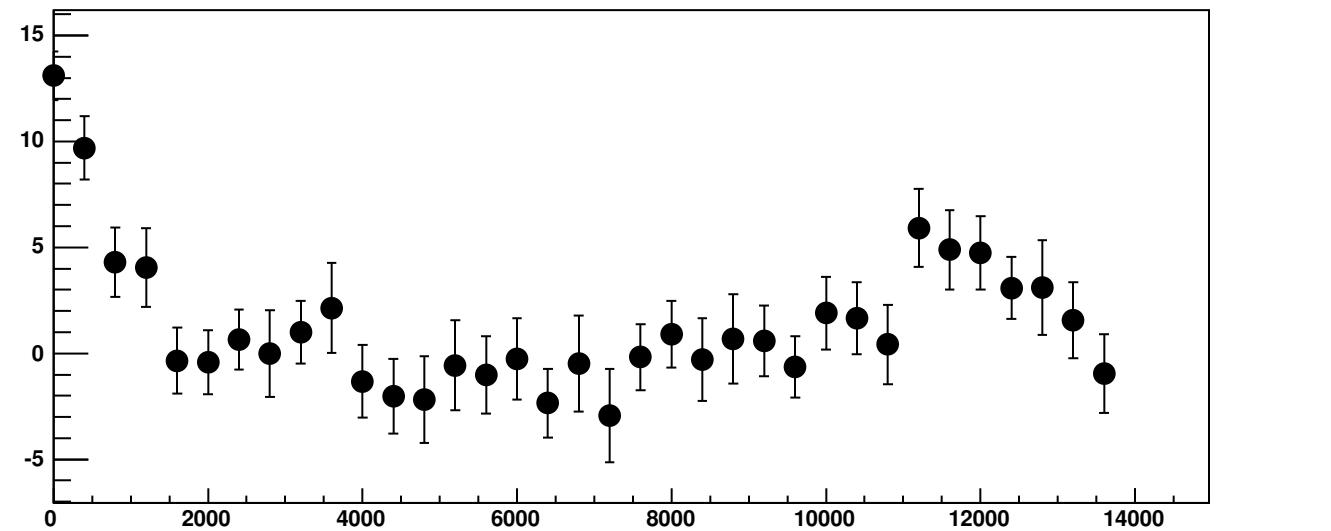
Chip 8, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC

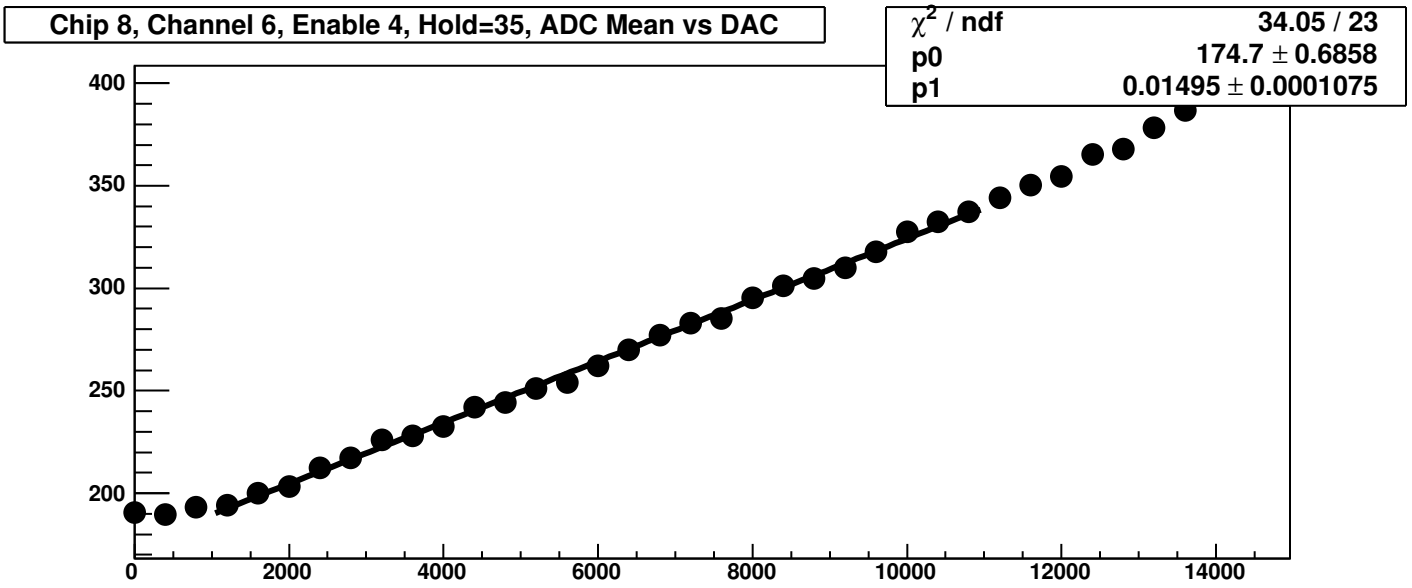


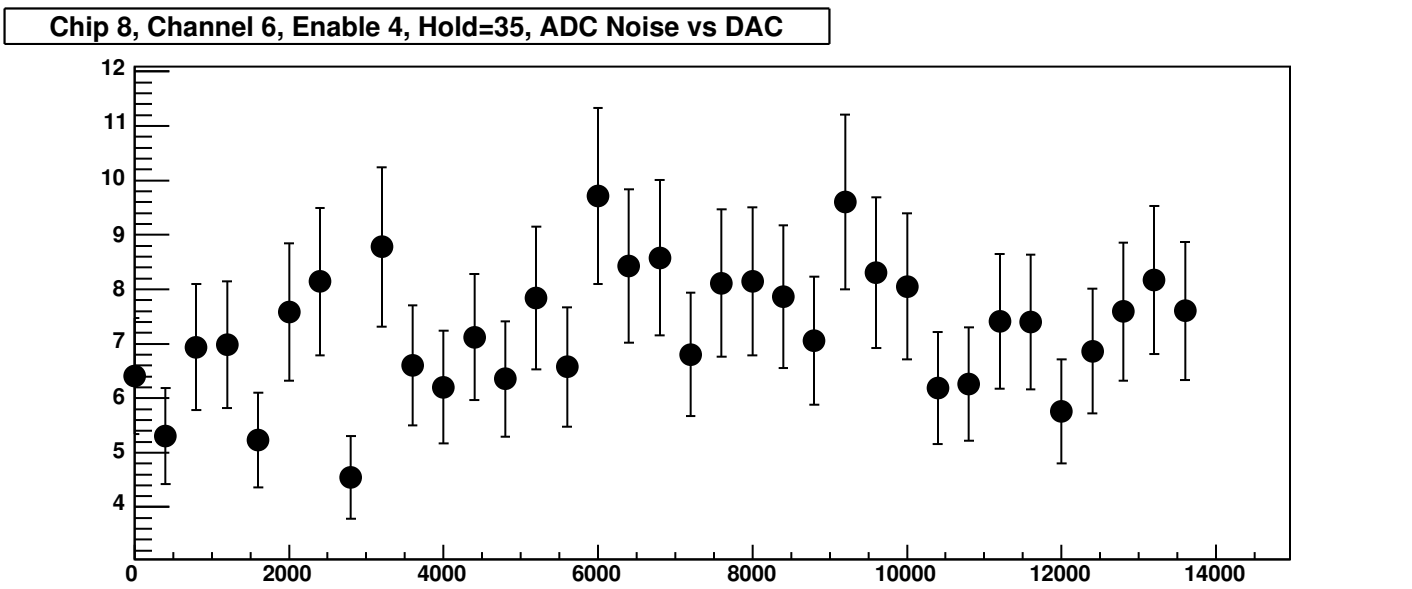
Chip 8, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC

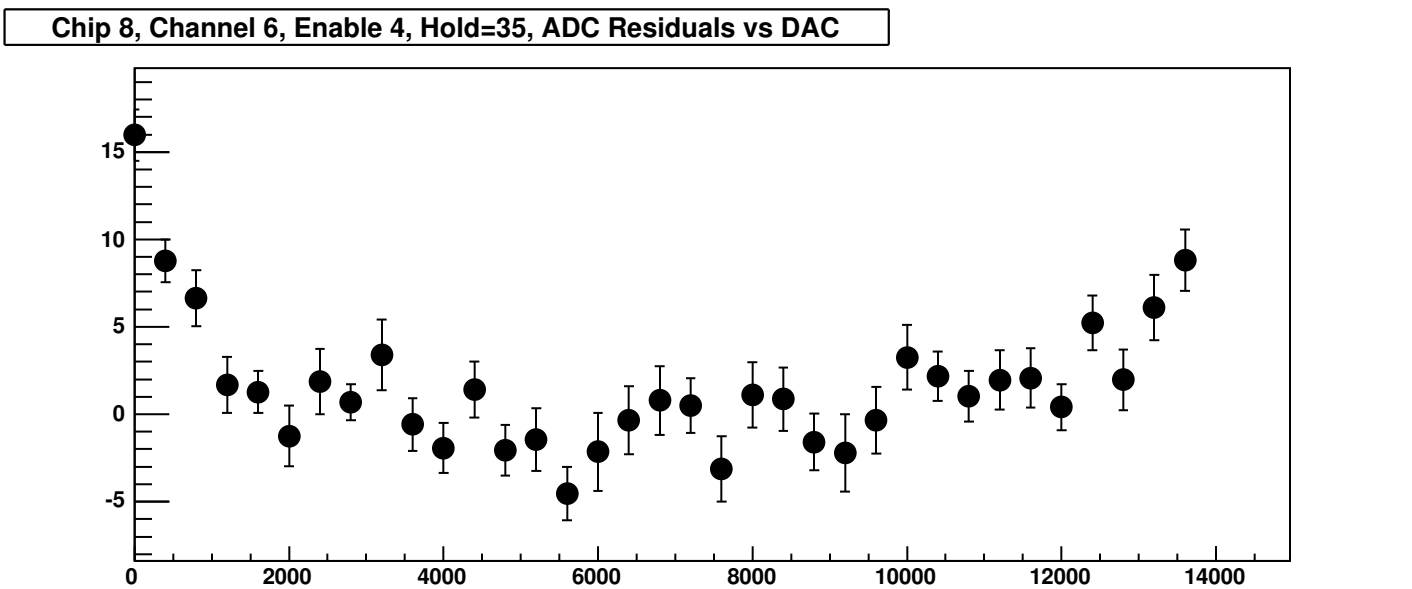


Chip 8, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC

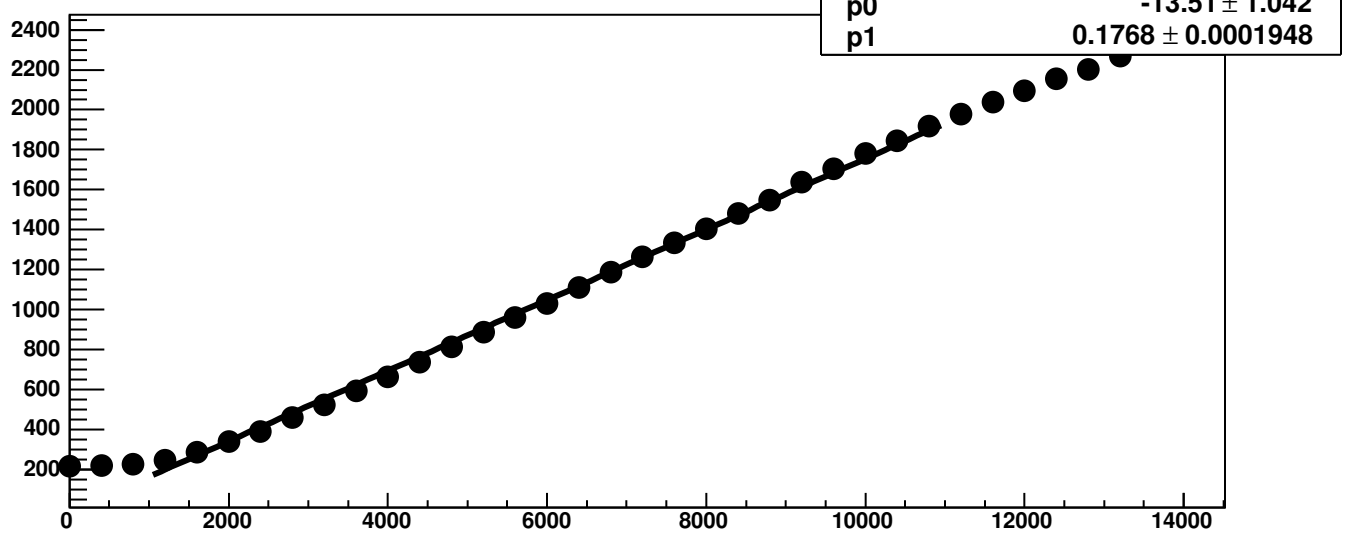



Chip 8, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 34.05 / 23
p0 174.7 ± 0.6858
p1 0.01495 ± 0.0001075
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

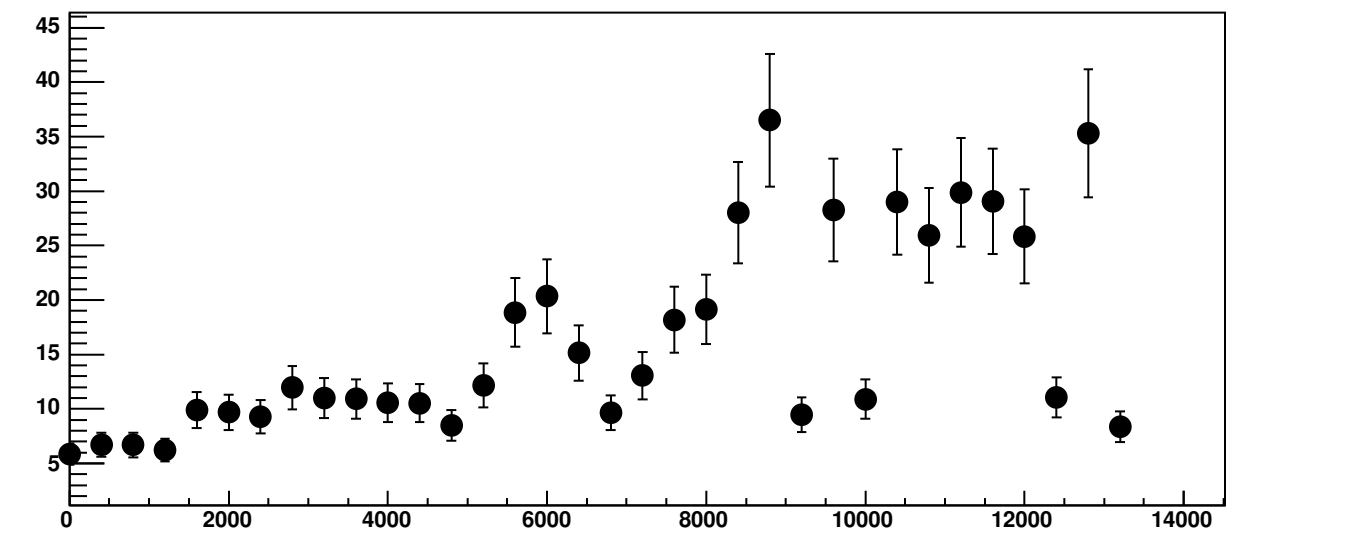

Chip 8, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

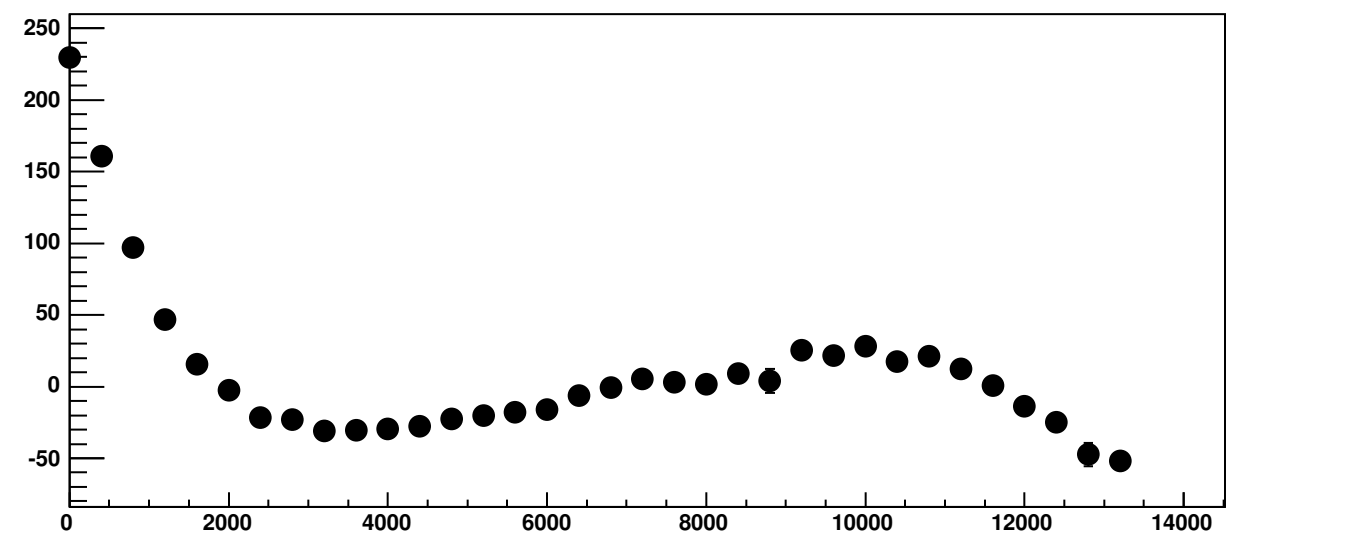
Chip 8, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC



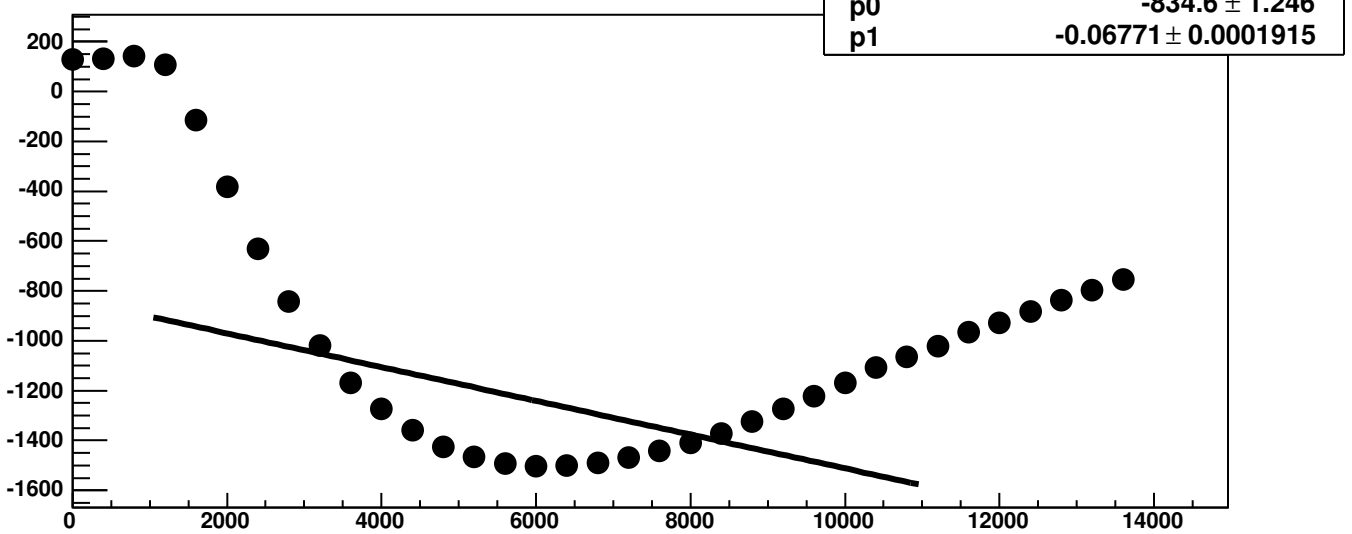
Chip 8, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



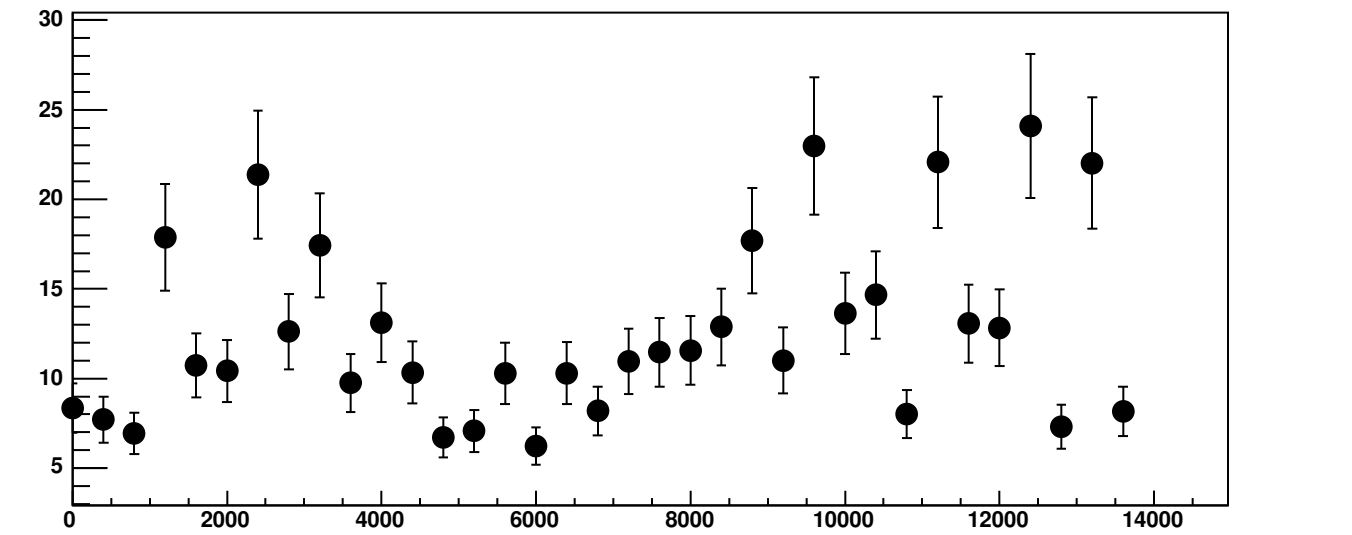
Chip 8, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC



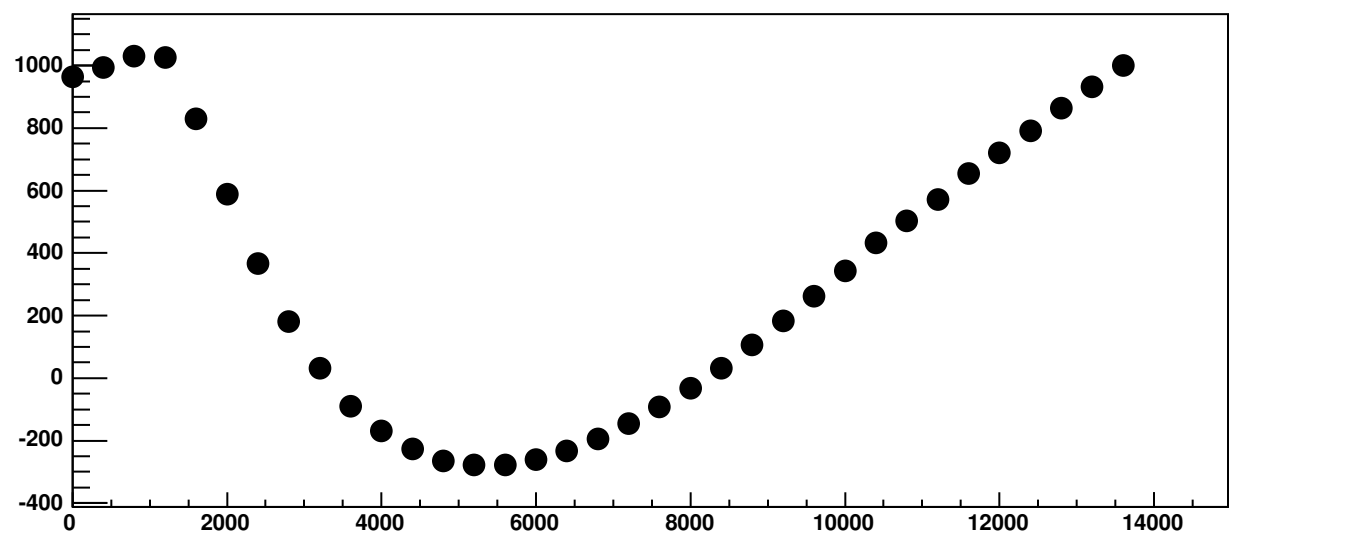
Chip 8, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

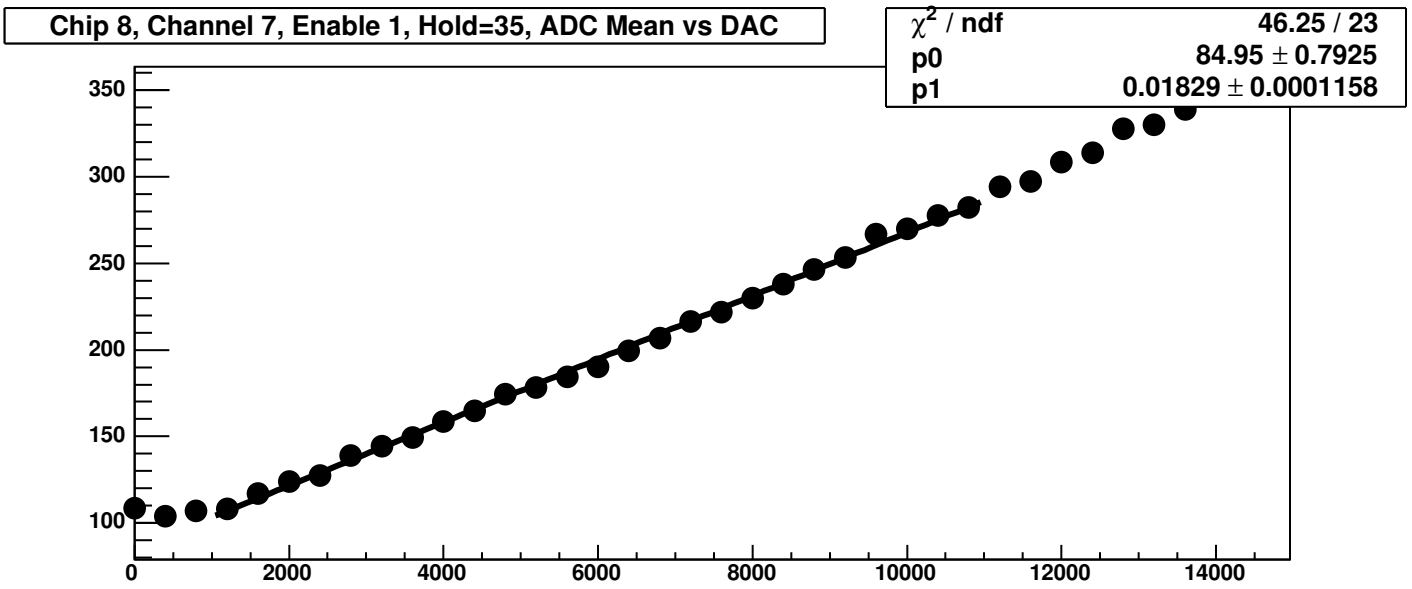


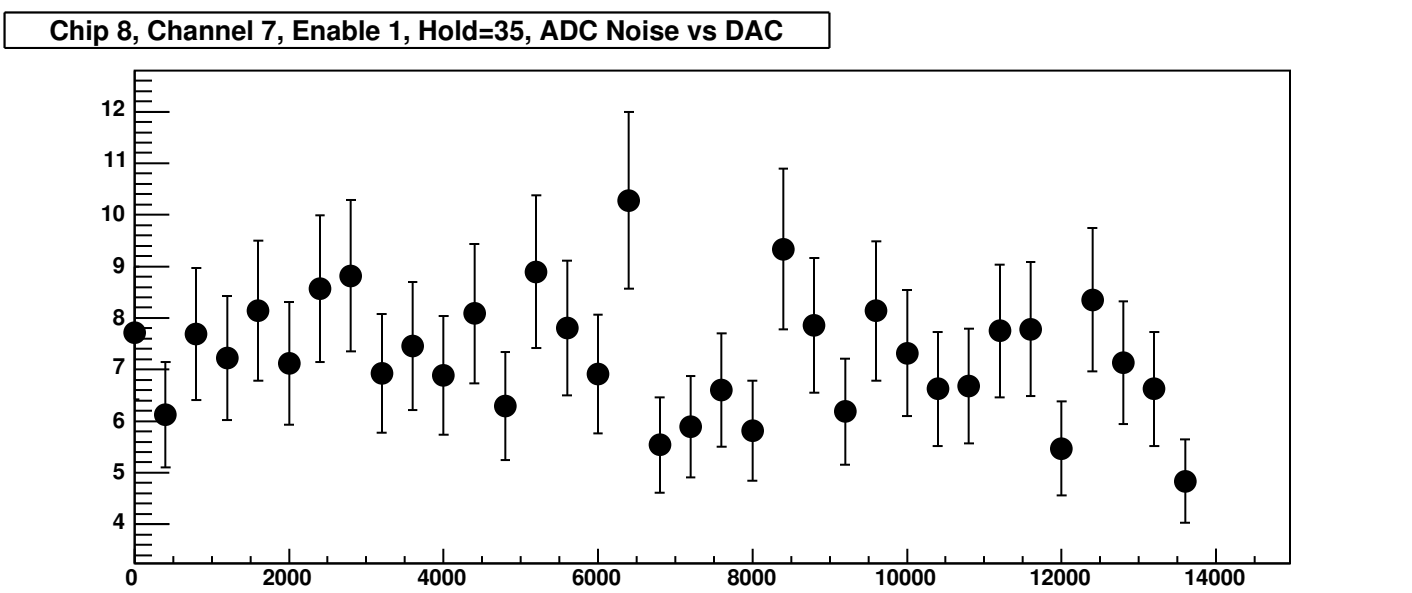
Chip 8, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC

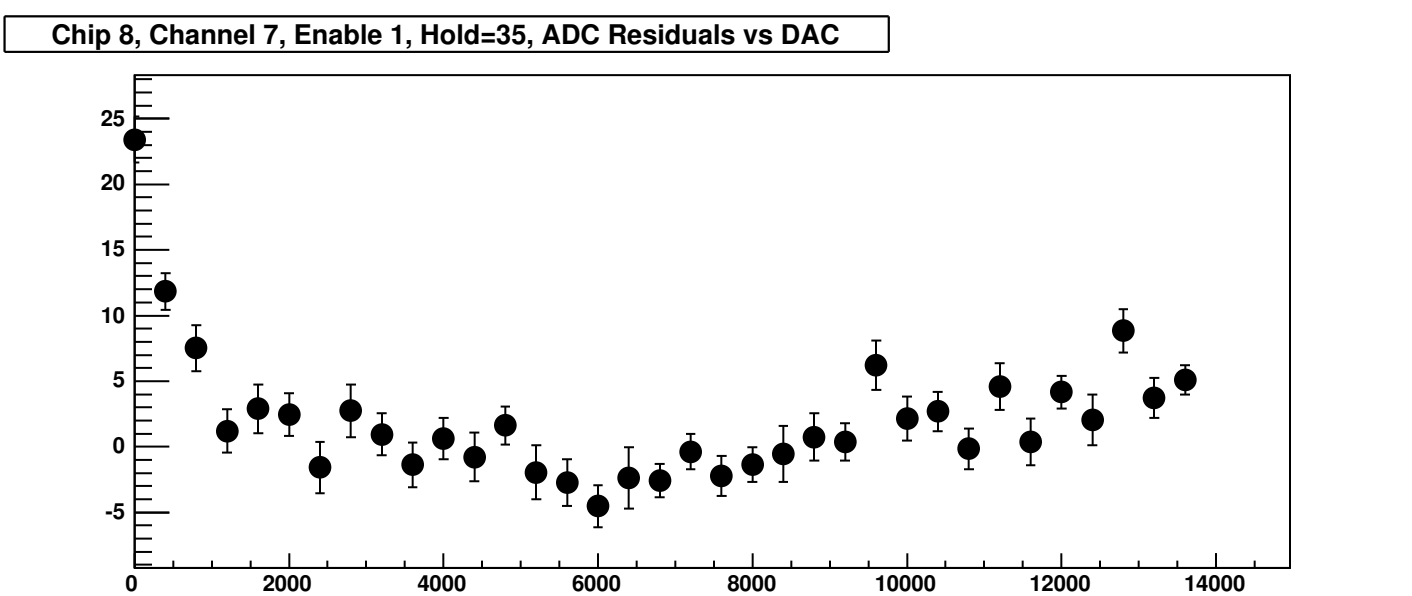


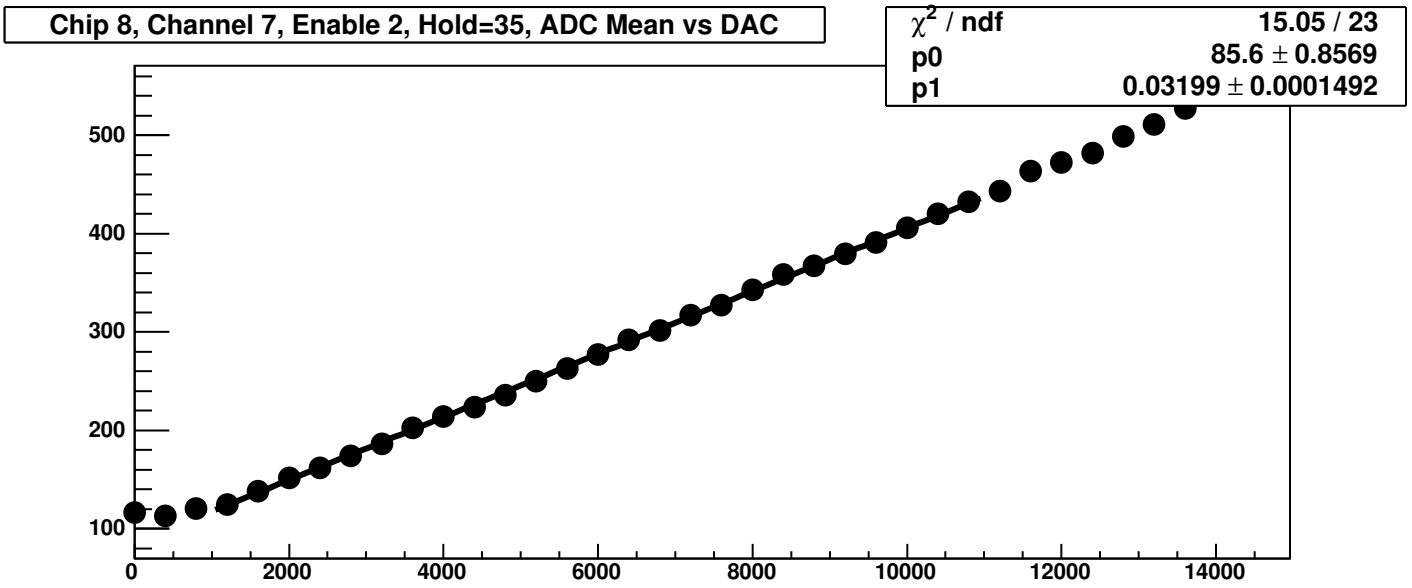
Chip 8, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

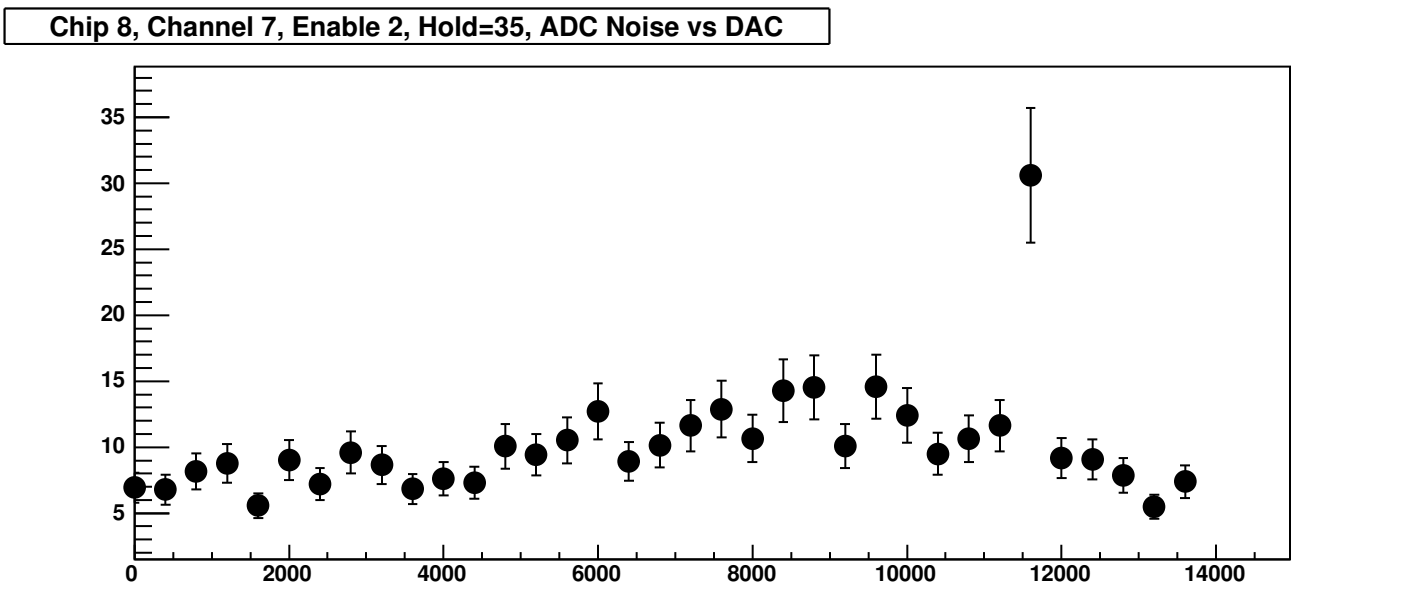


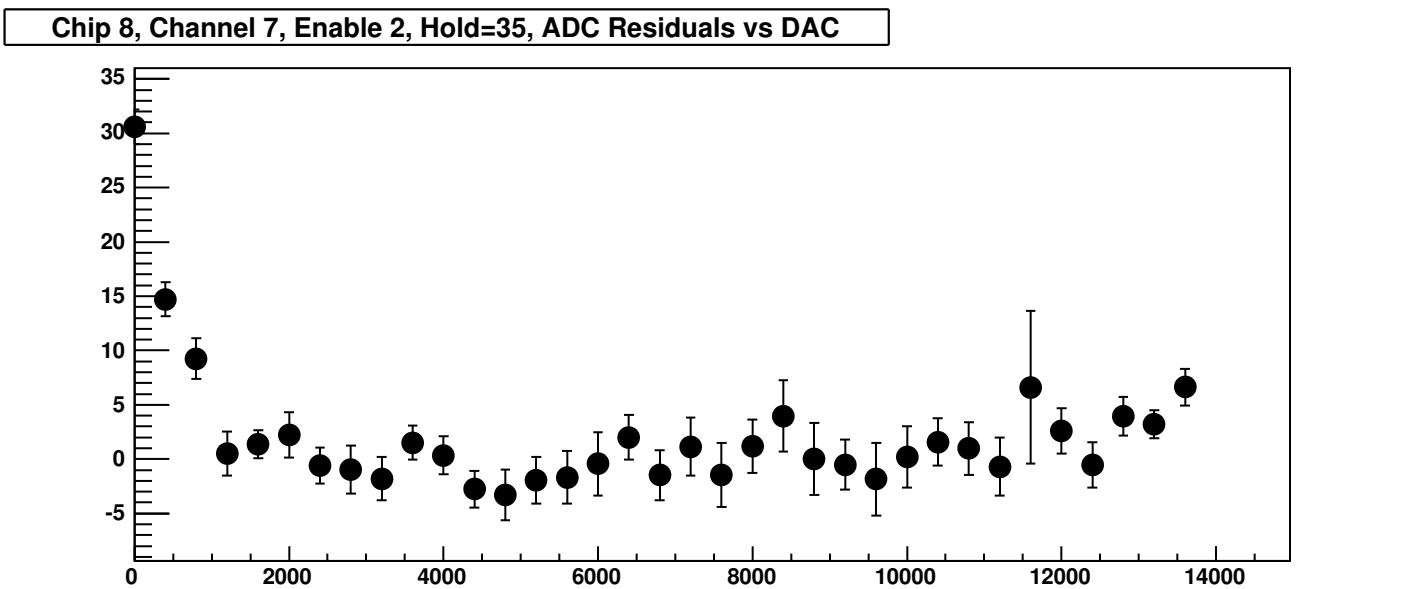

Chip 8, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 46.25 / 23
p0 84.95 ± 0.7925
p1 0.01829 ± 0.0001158
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

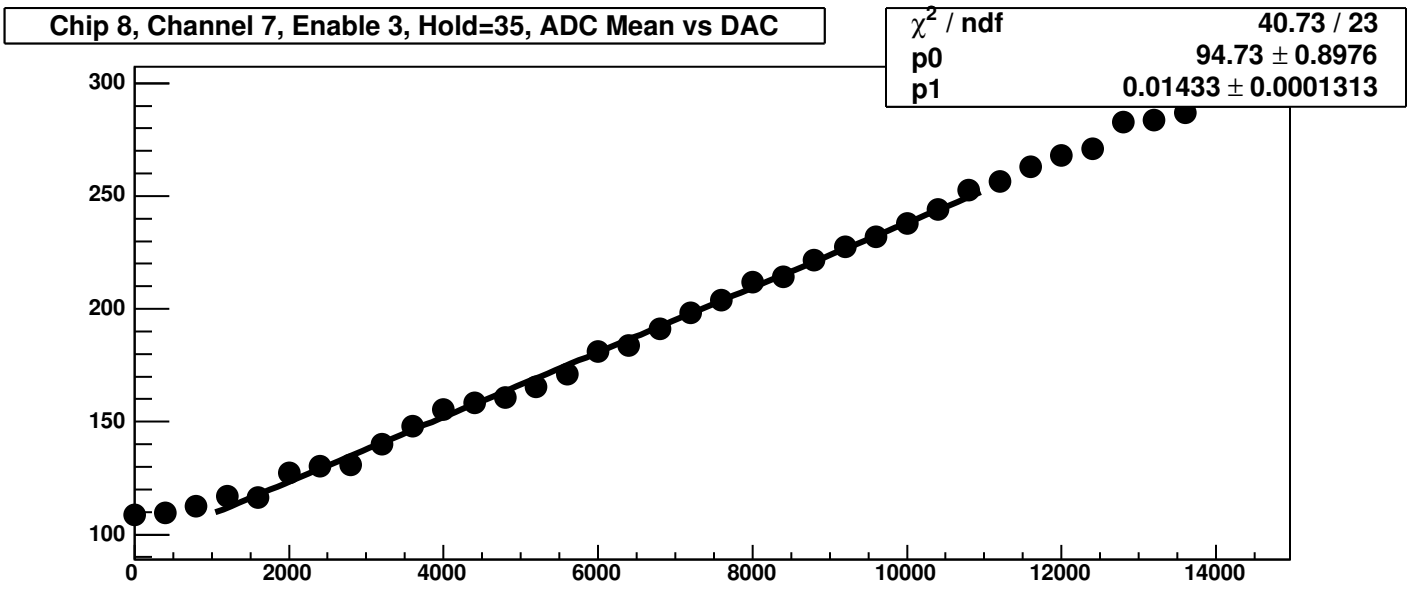

Chip 8, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

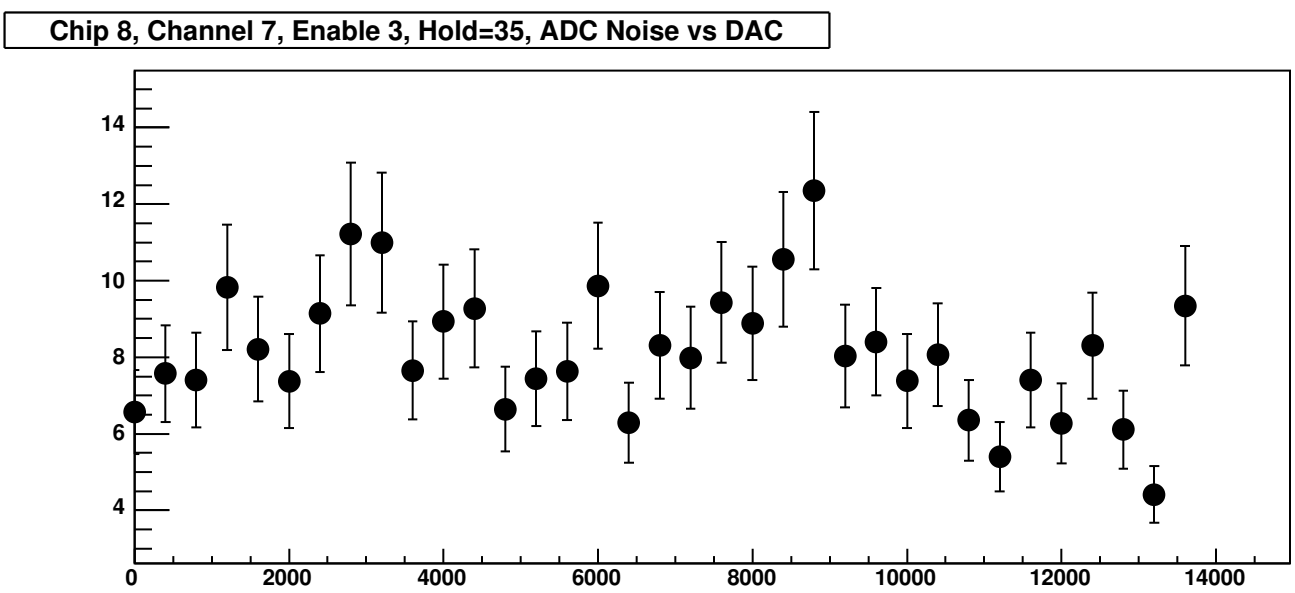

Chip 8, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

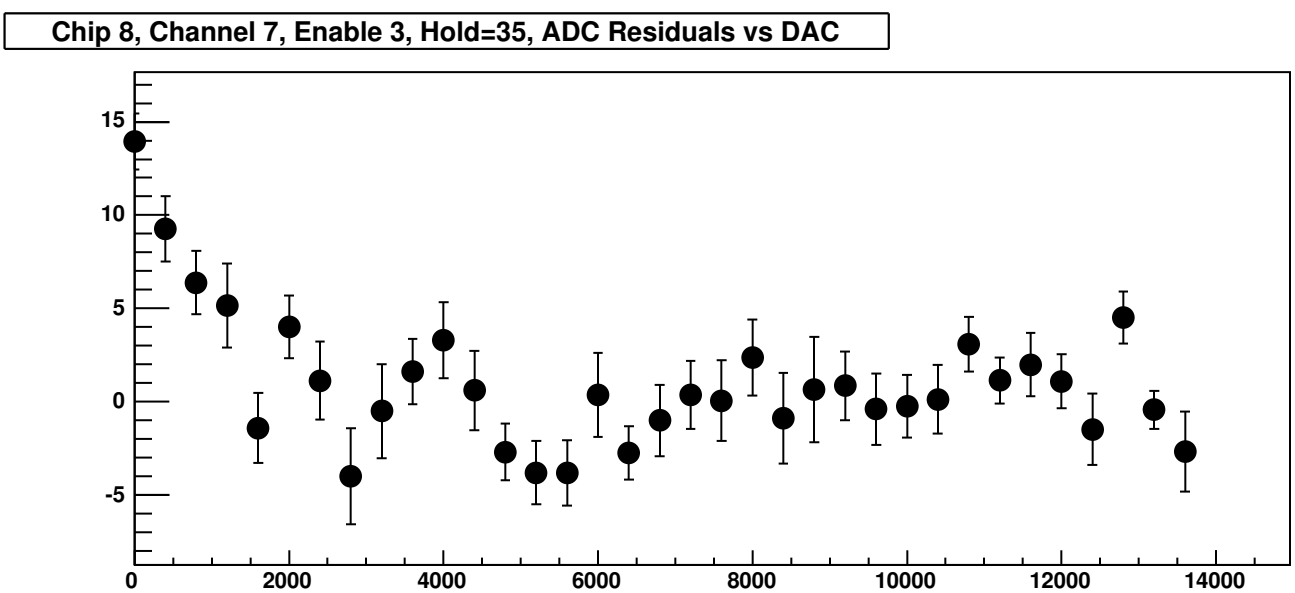

Chip 8, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 15.05 / 23
p0 85.6 ± 0.8569
p1 0.03199 ± 0.0001492
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

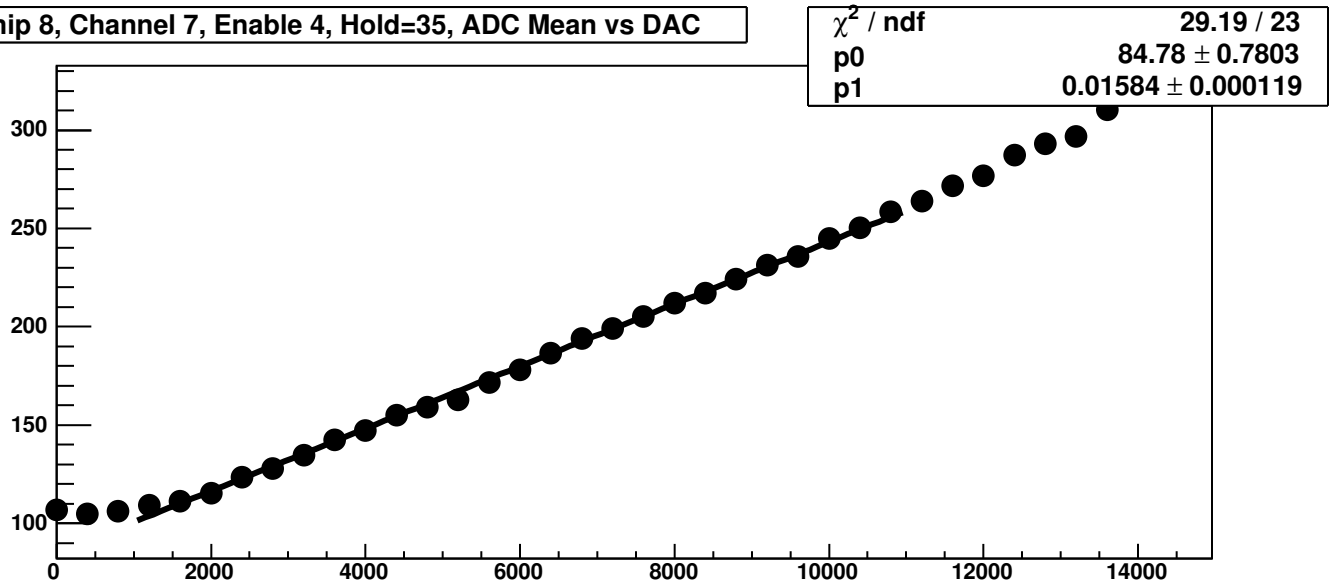

Chip 8, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 40.73 / 23
p0 94.73 ± 0.8976
p1 0.01433 ± 0.0001313
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

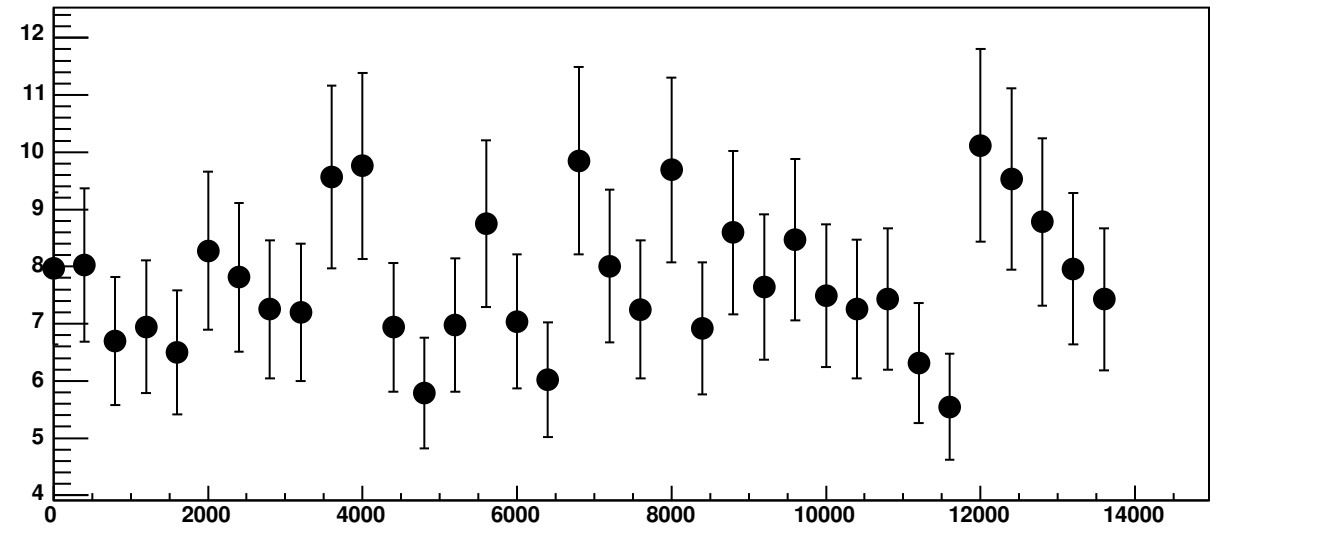

Chip 8, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

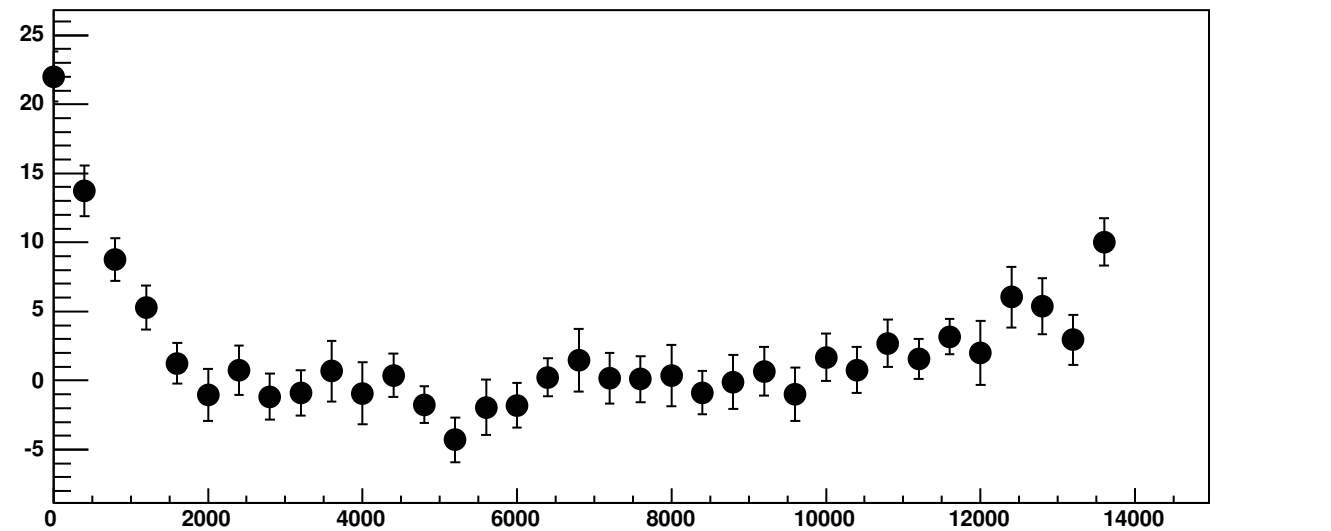
Chip 8, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

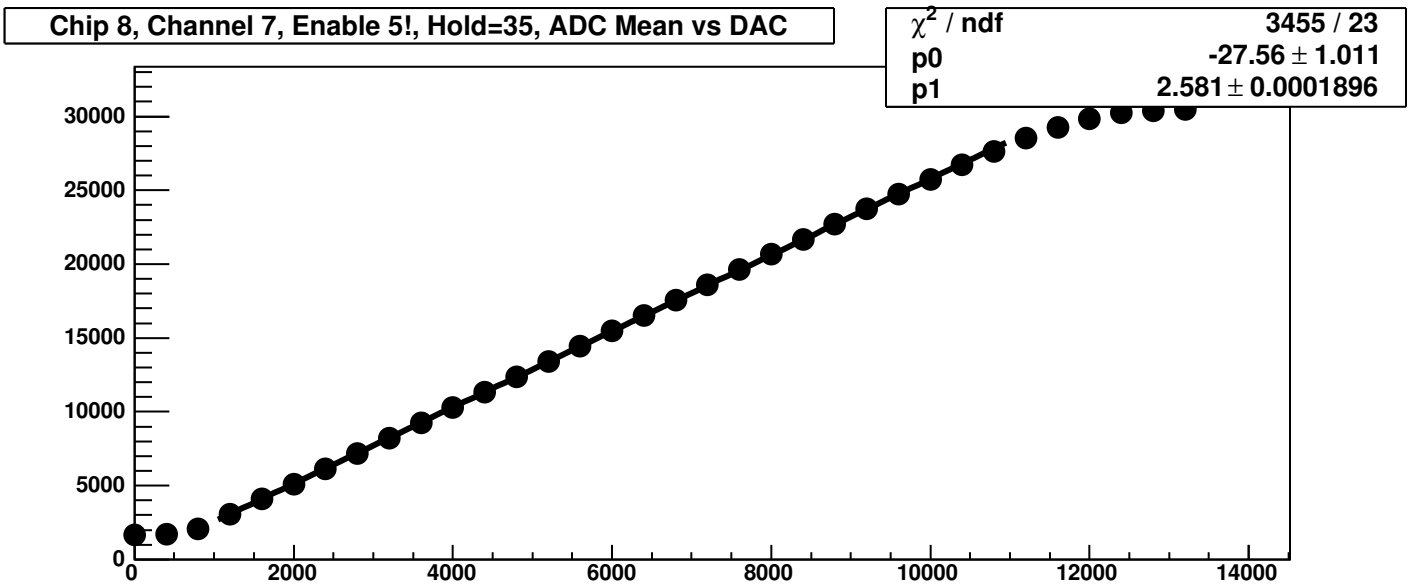


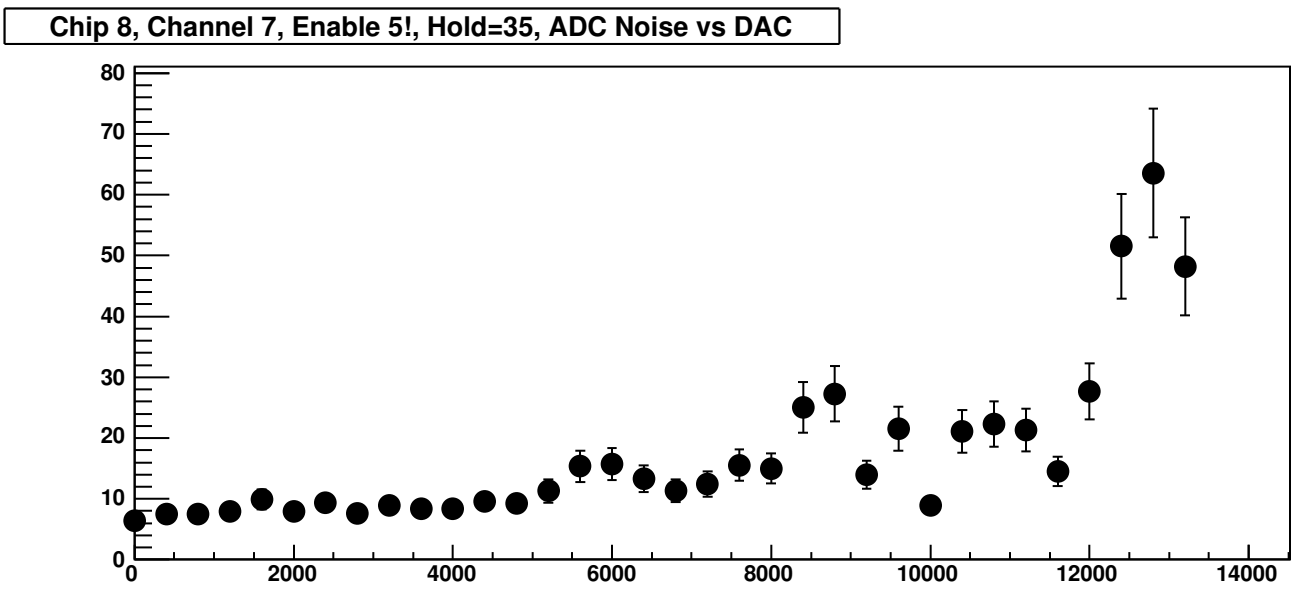
Chip 8, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

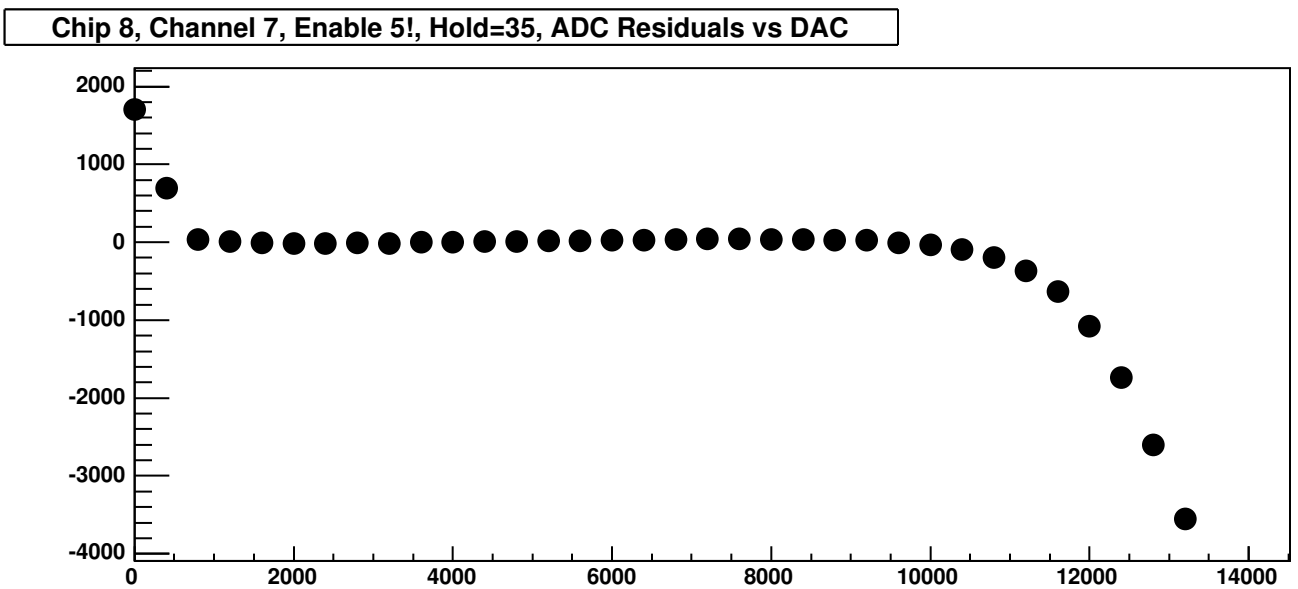


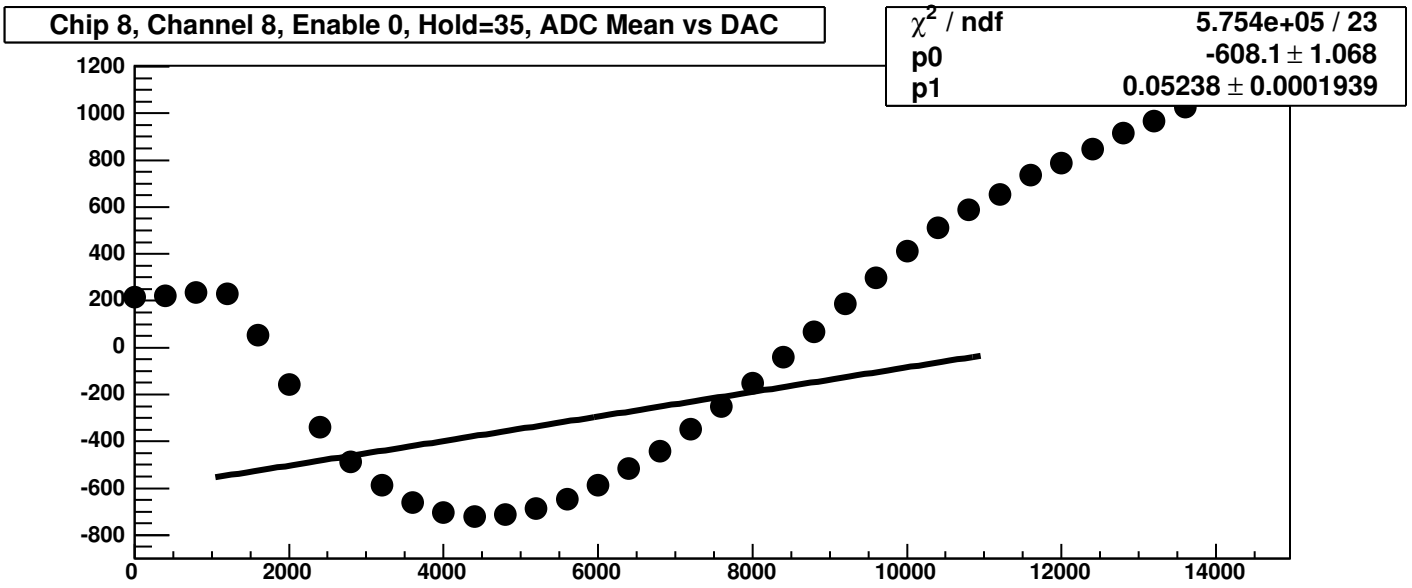
Chip 8, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

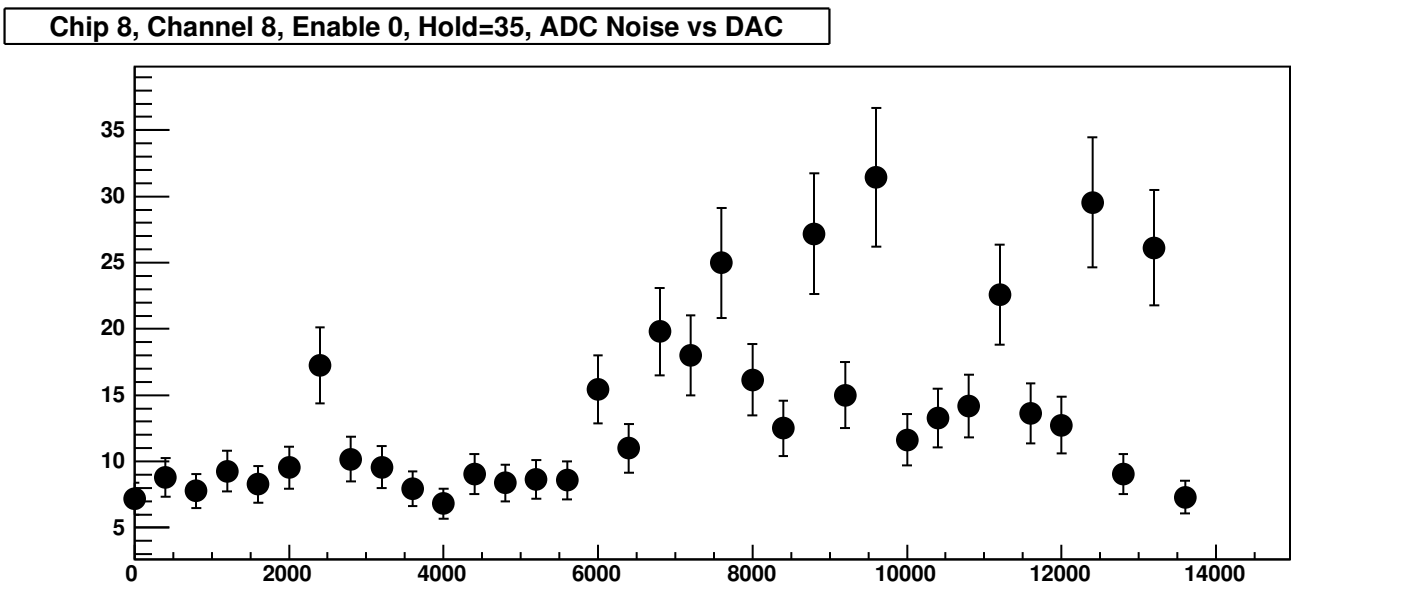


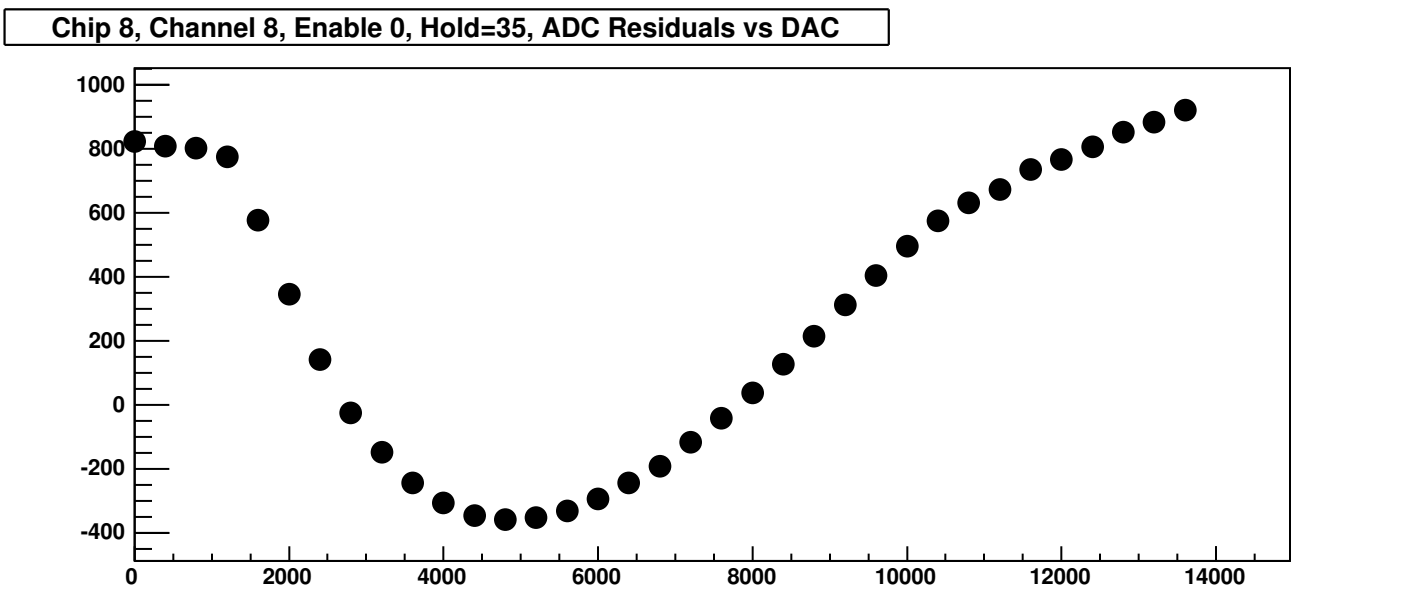

Chip 8, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3455 / 23
p0 -27.56 ± 1.011
p1 2.581 ± 0.0001896
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

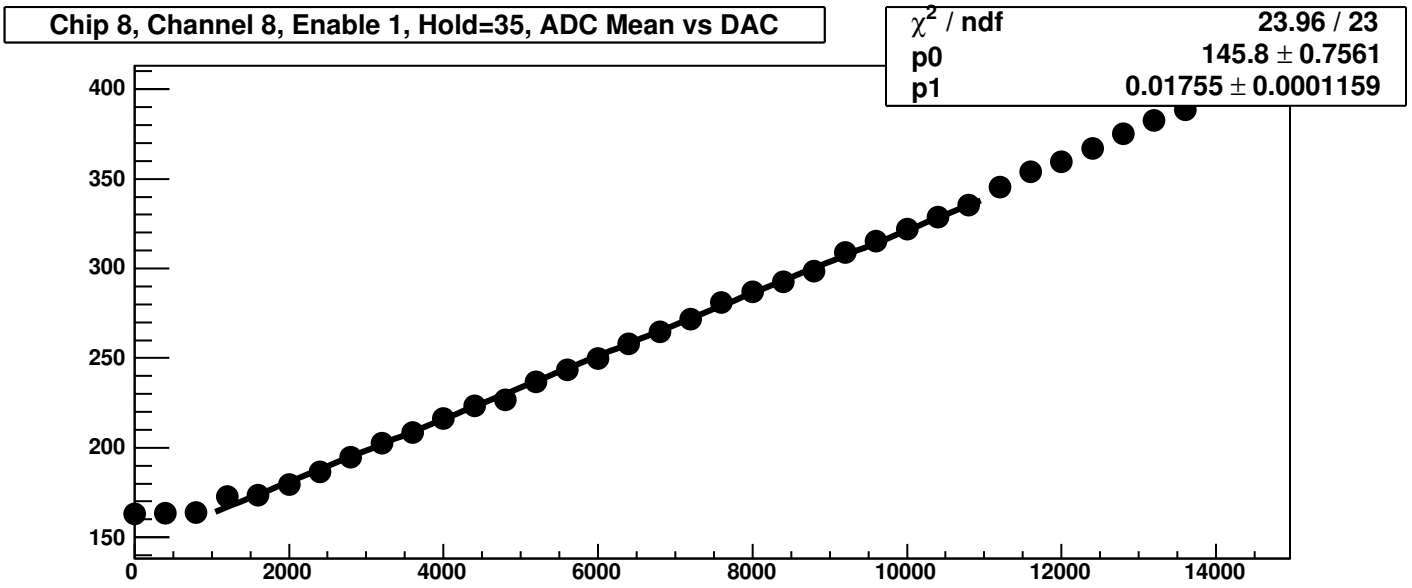

Chip 8, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

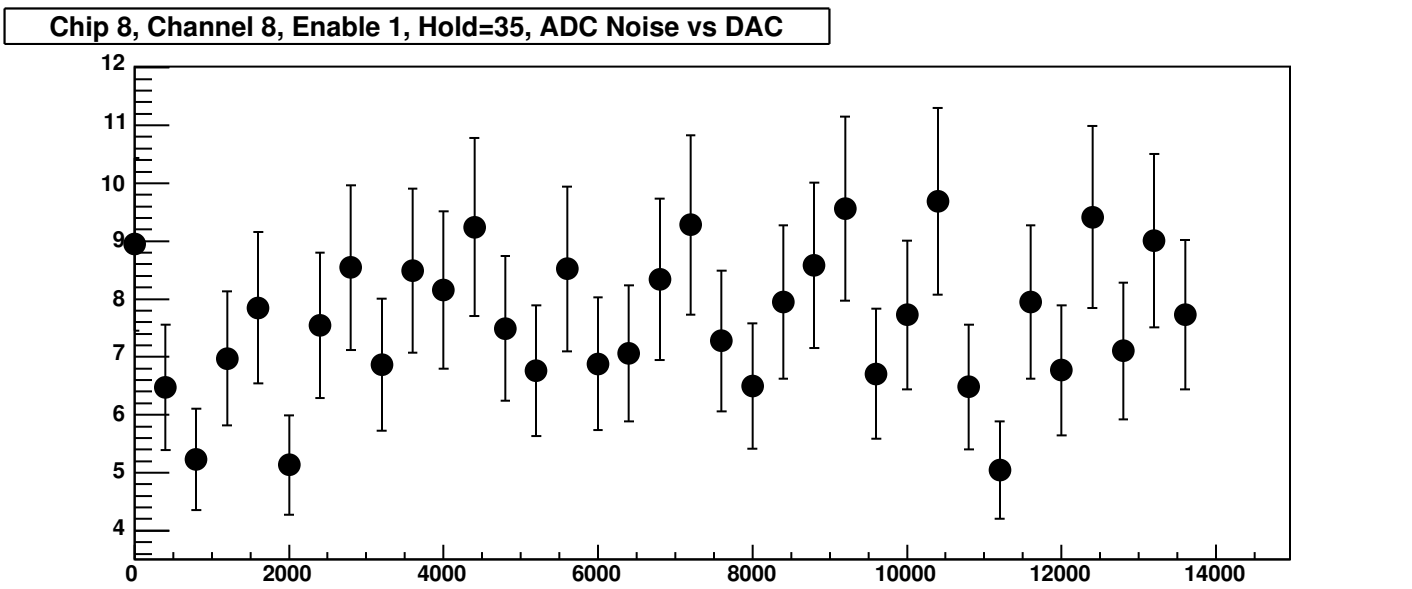

Chip 8, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

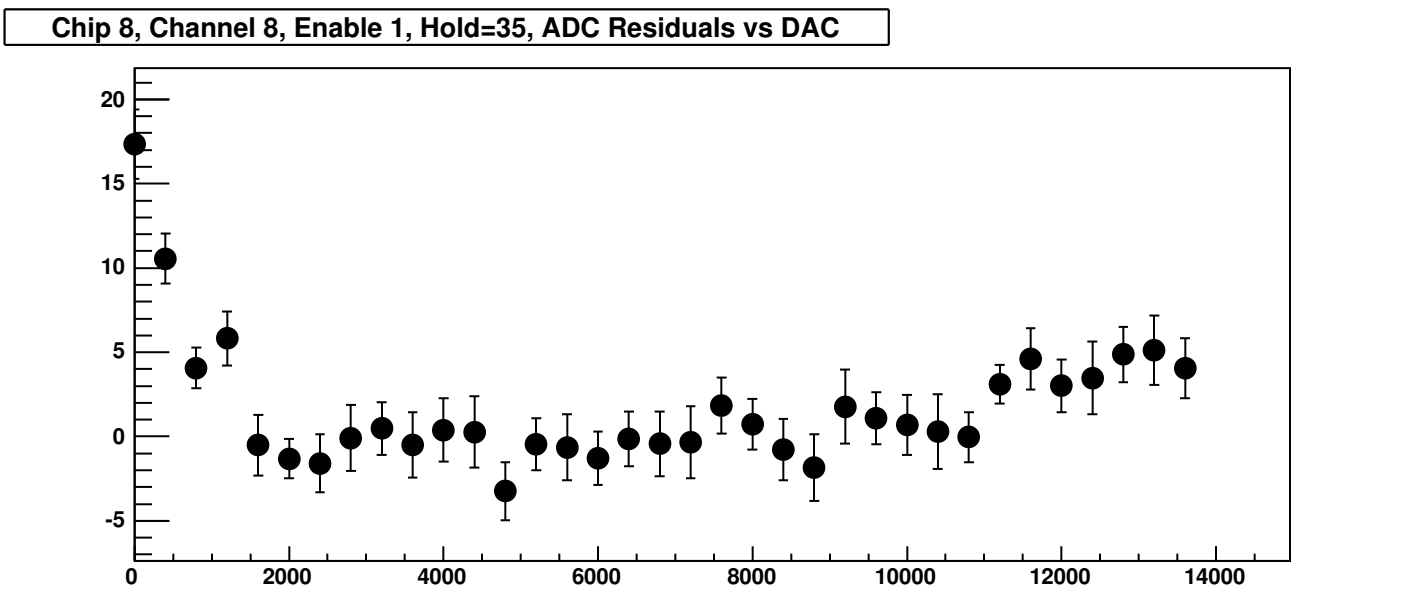

Chip 8, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.754e+05 / 23
p0 -608.1 ± 1.068
p1 0.05238 ± 0.0001939
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

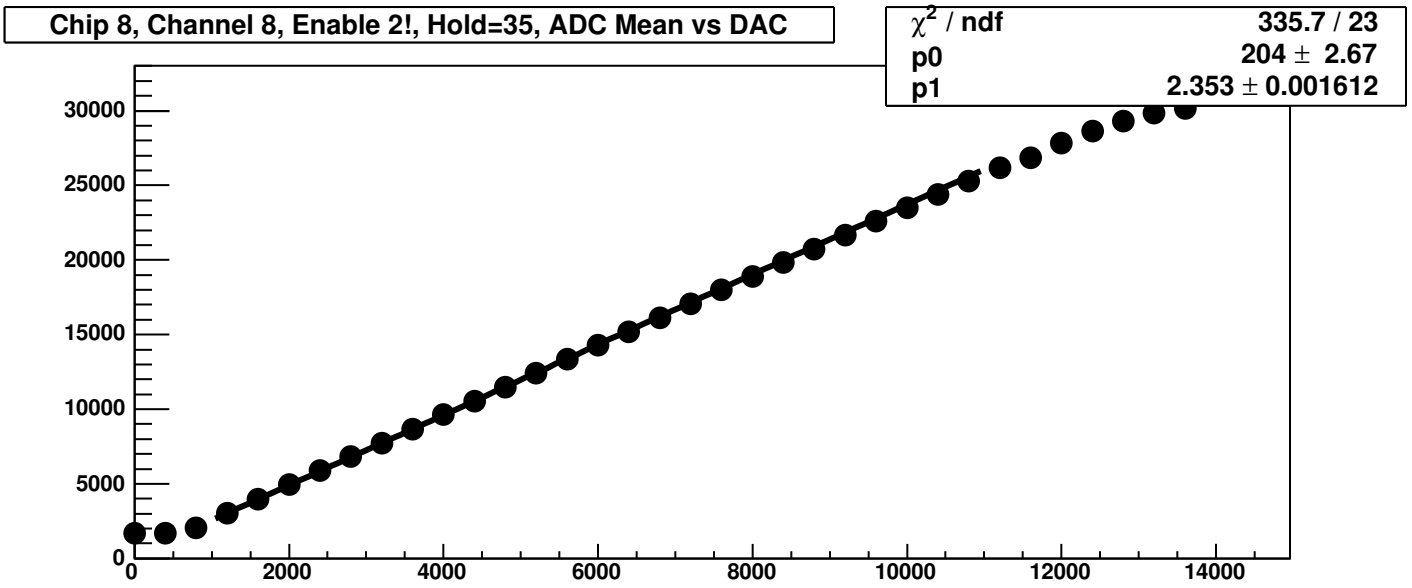

Chip 8, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

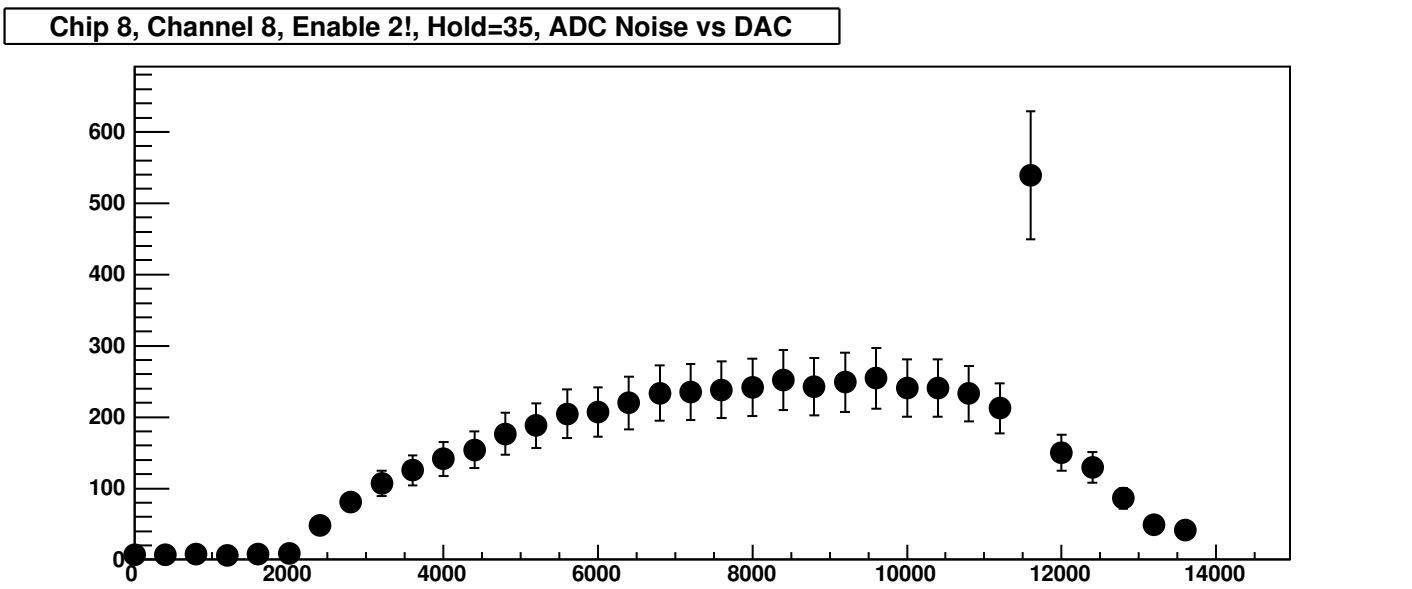

Chip 8, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

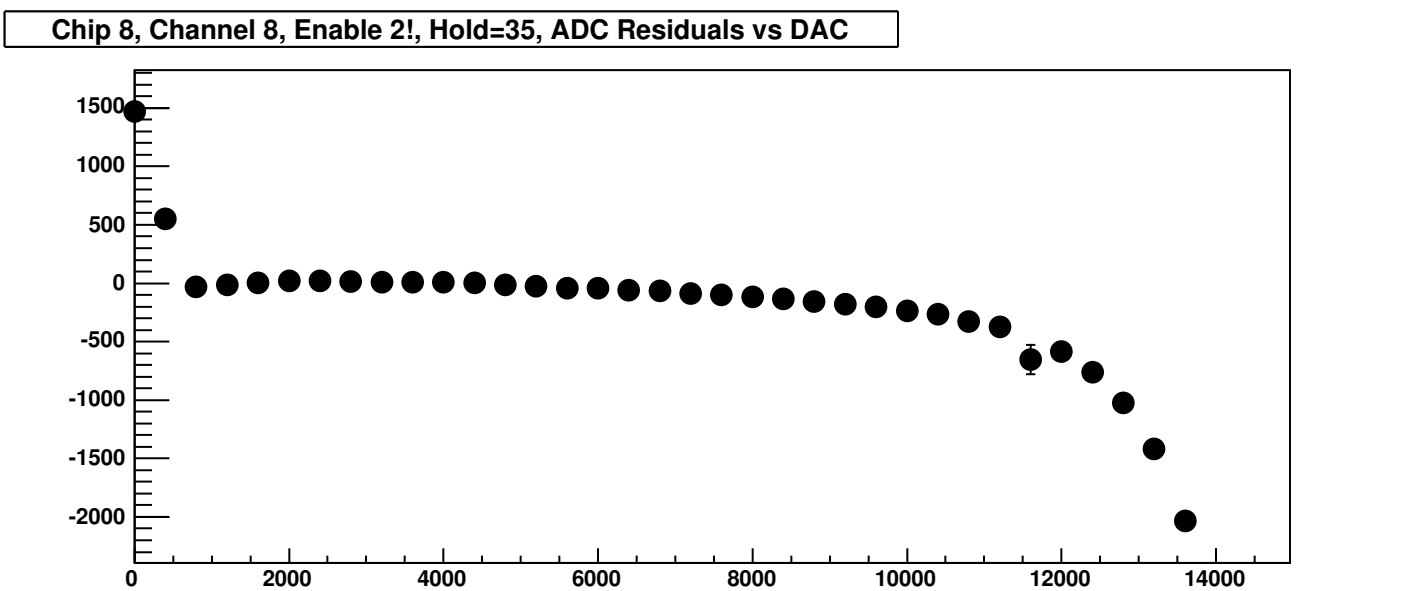

Chip 8, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 23.96 / 23
p0 145.8 ± 0.7561
p1 0.01755 ± 0.0001159
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

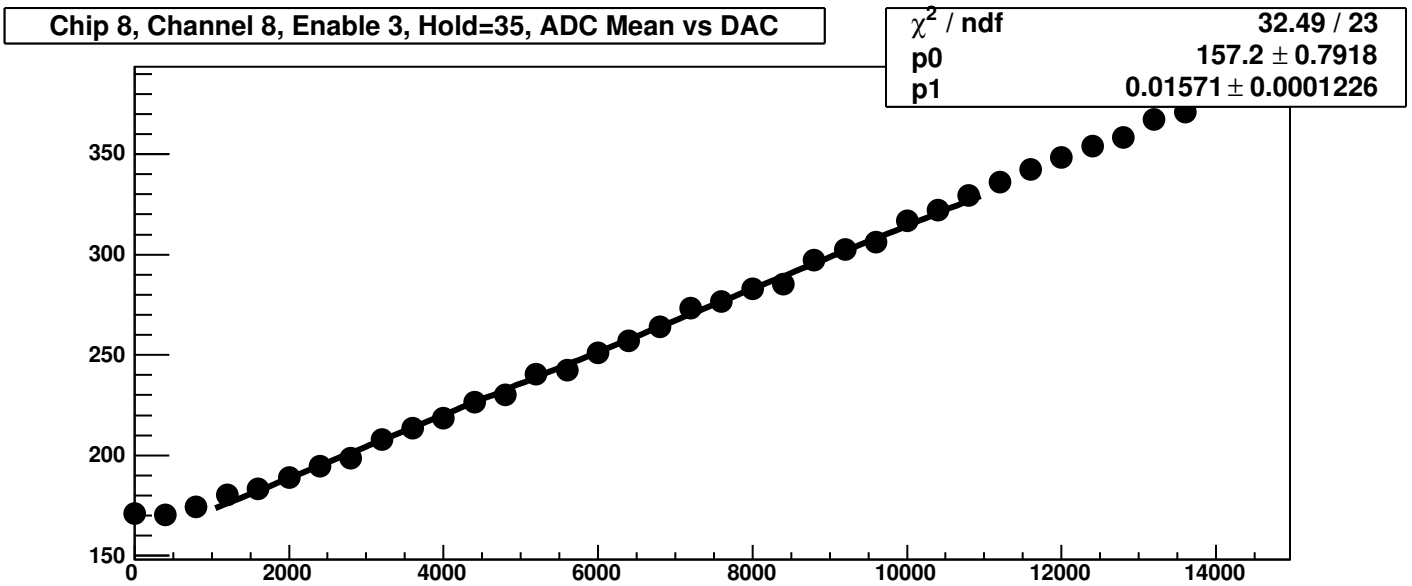

Chip 8, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

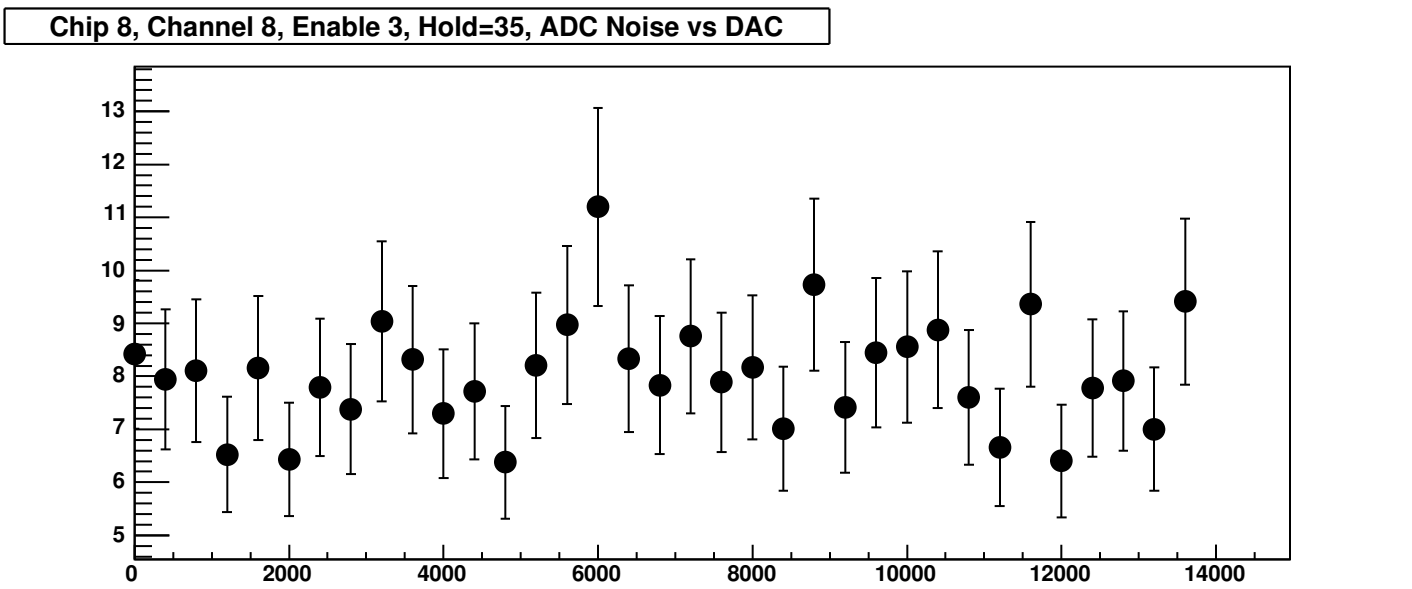

Chip 8, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

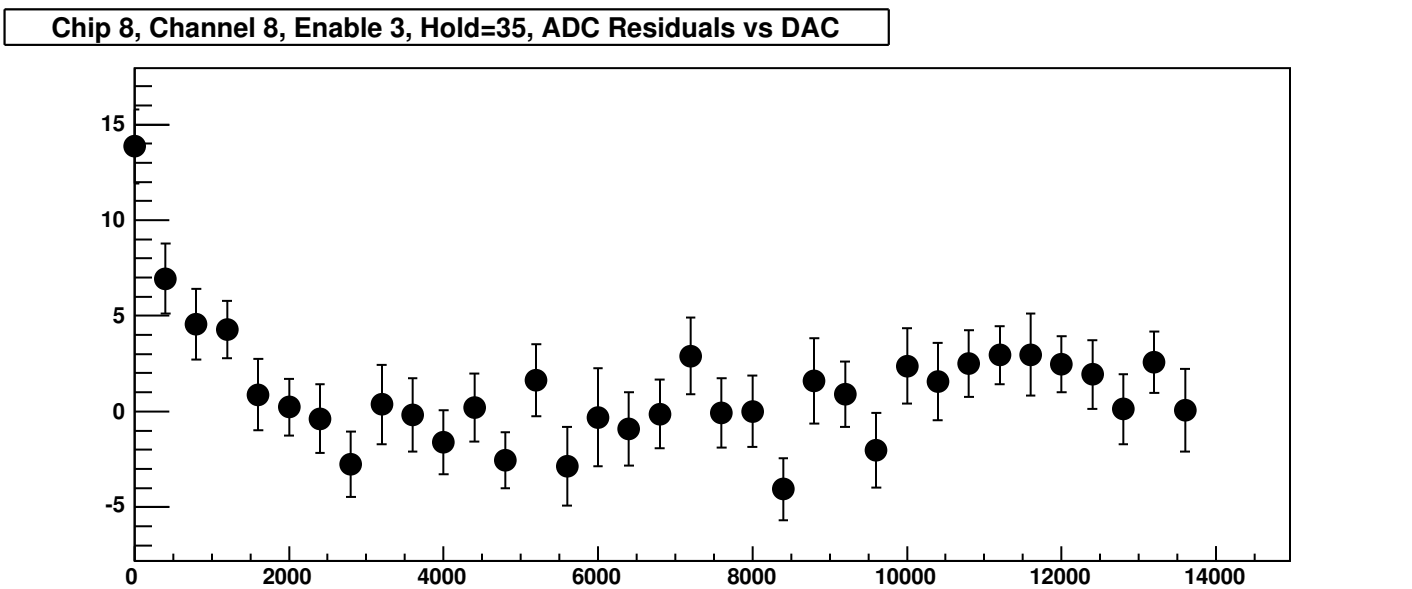

Chip 8, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 335.7 / 23
p0 204 ± 2.67
p1 2.353 ± 0.001612
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

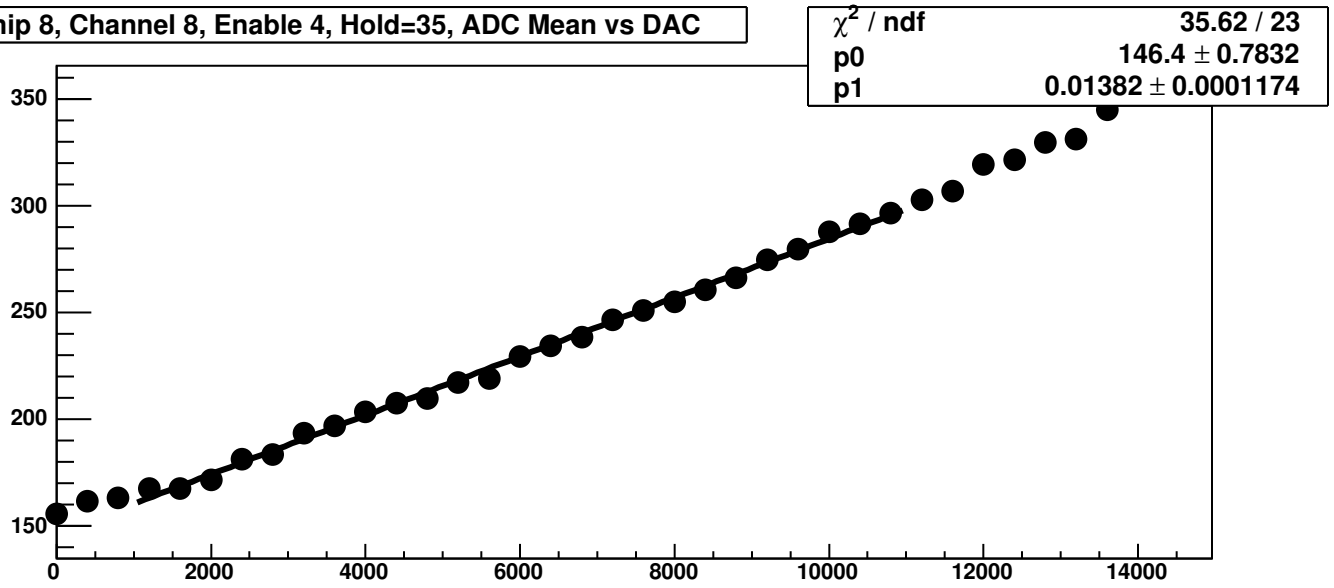

Chip 8, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 32.49 / 23
p0 157.2 ± 0.7918
p1 0.01571 ± 0.0001226
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

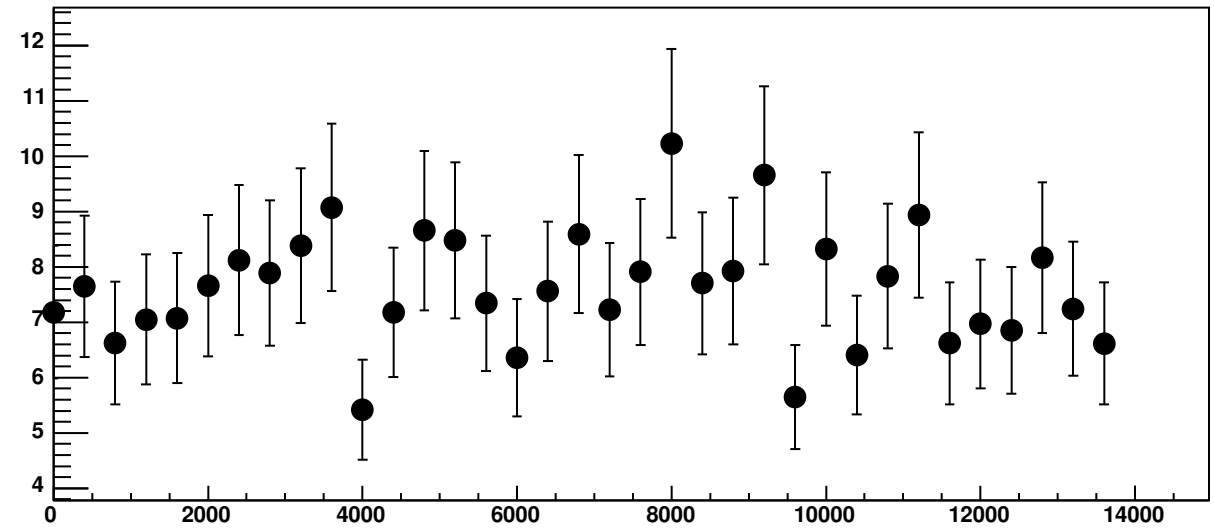

Chip 8, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

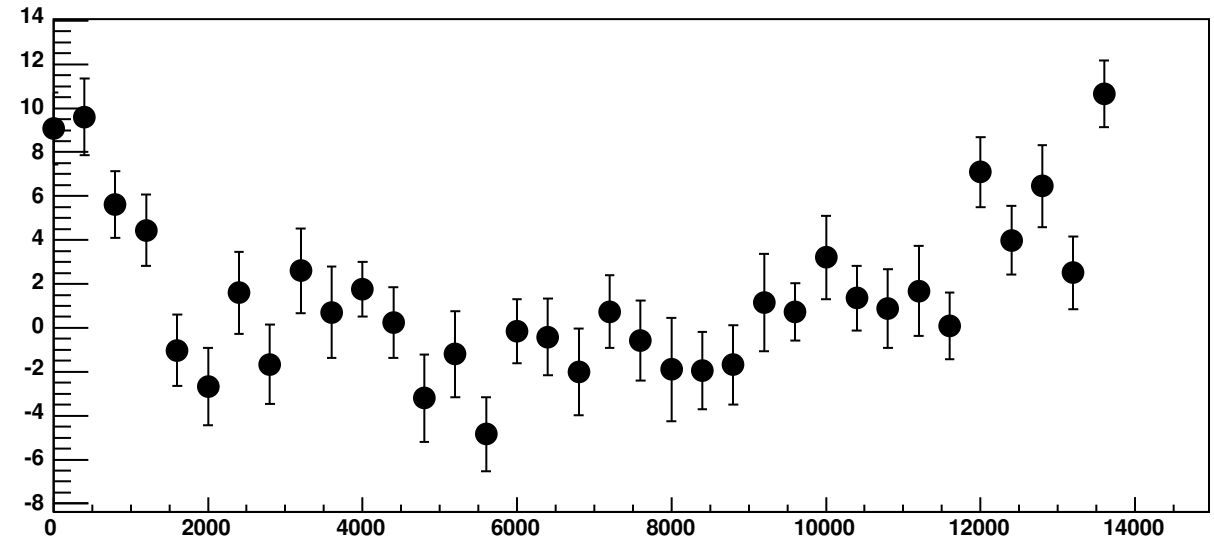
Chip 8, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC

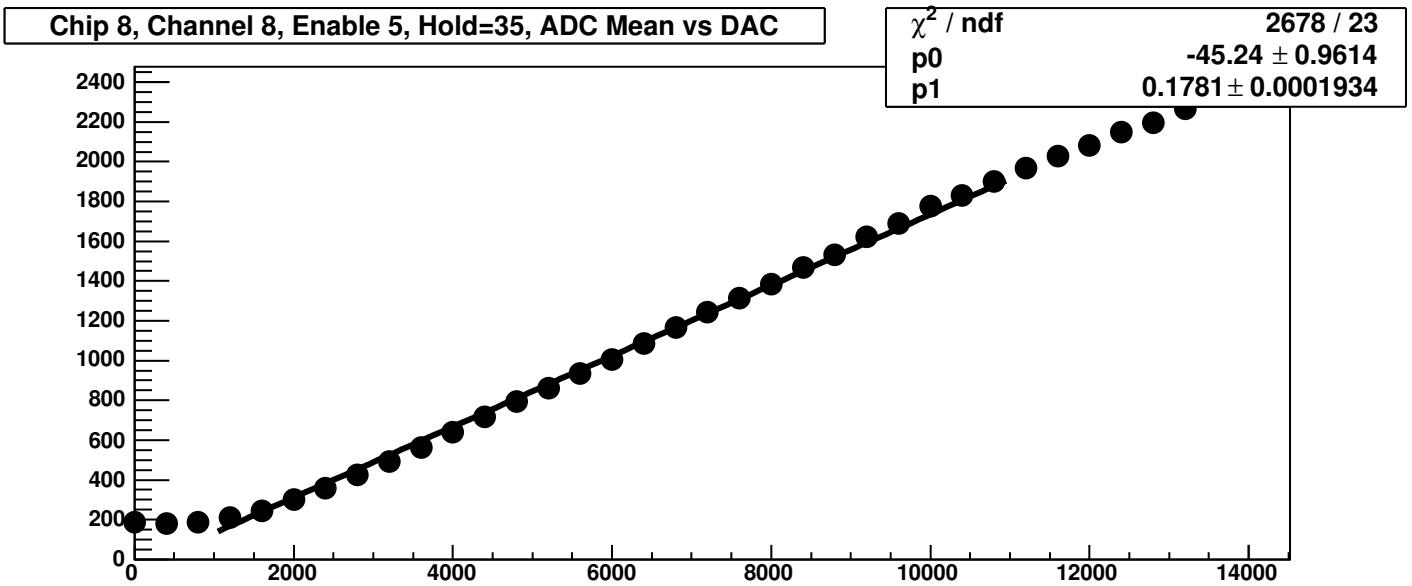


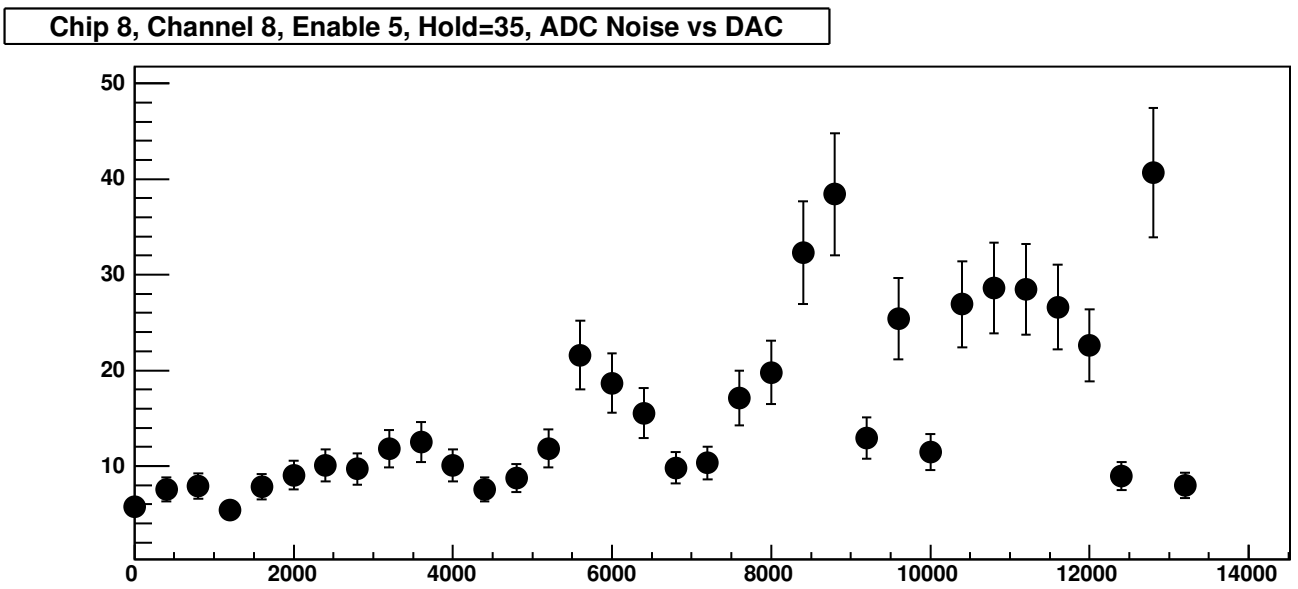
Chip 8, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC

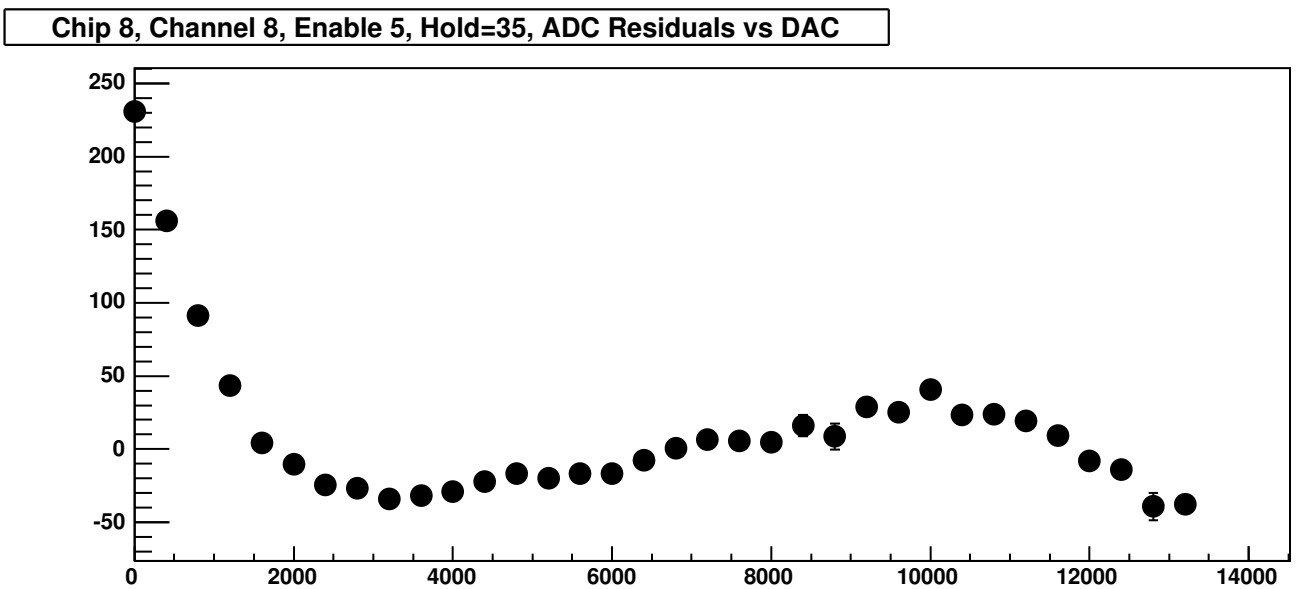


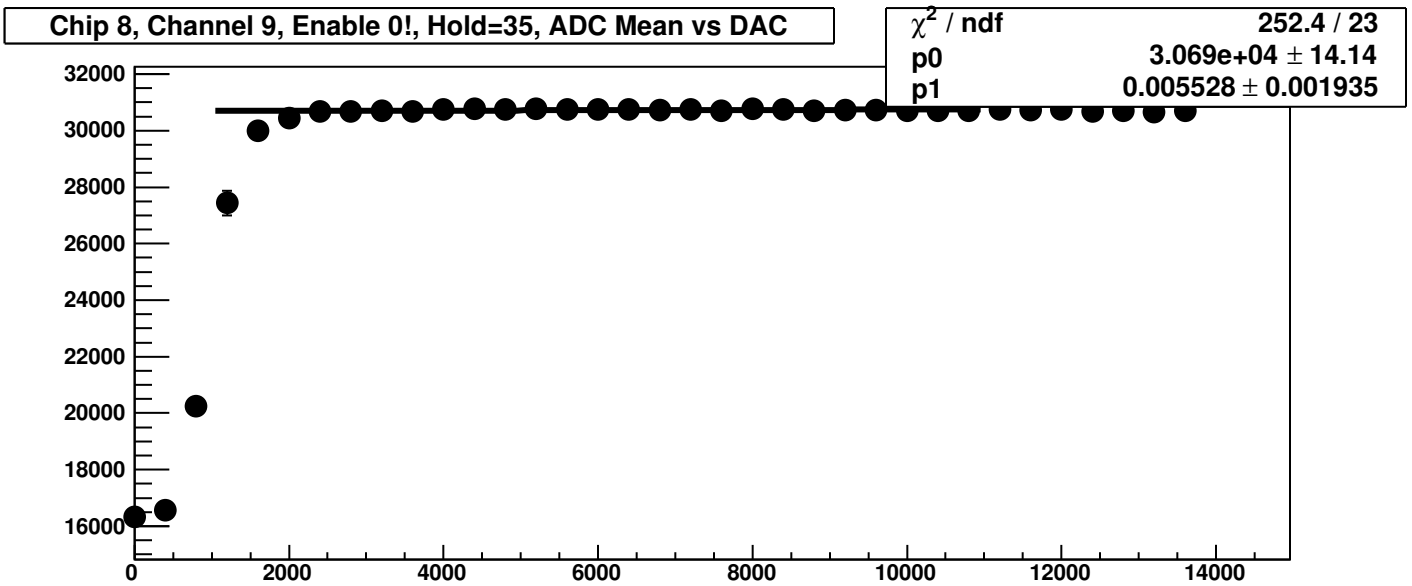
Chip 8, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC

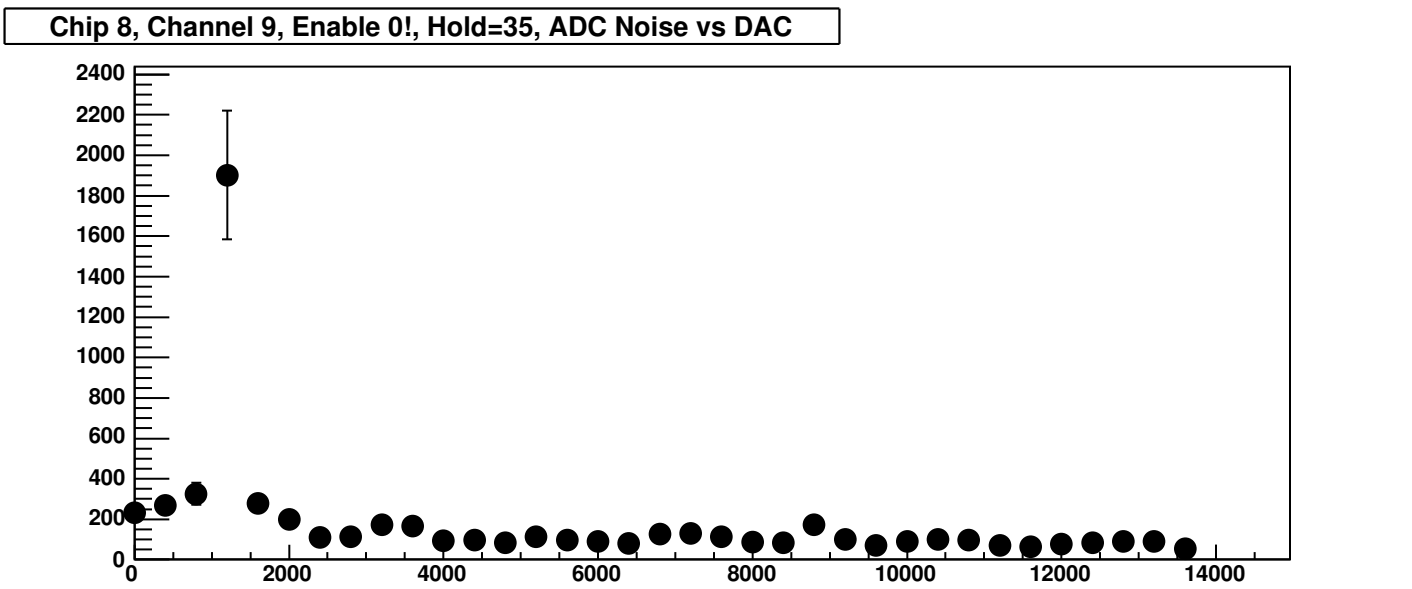


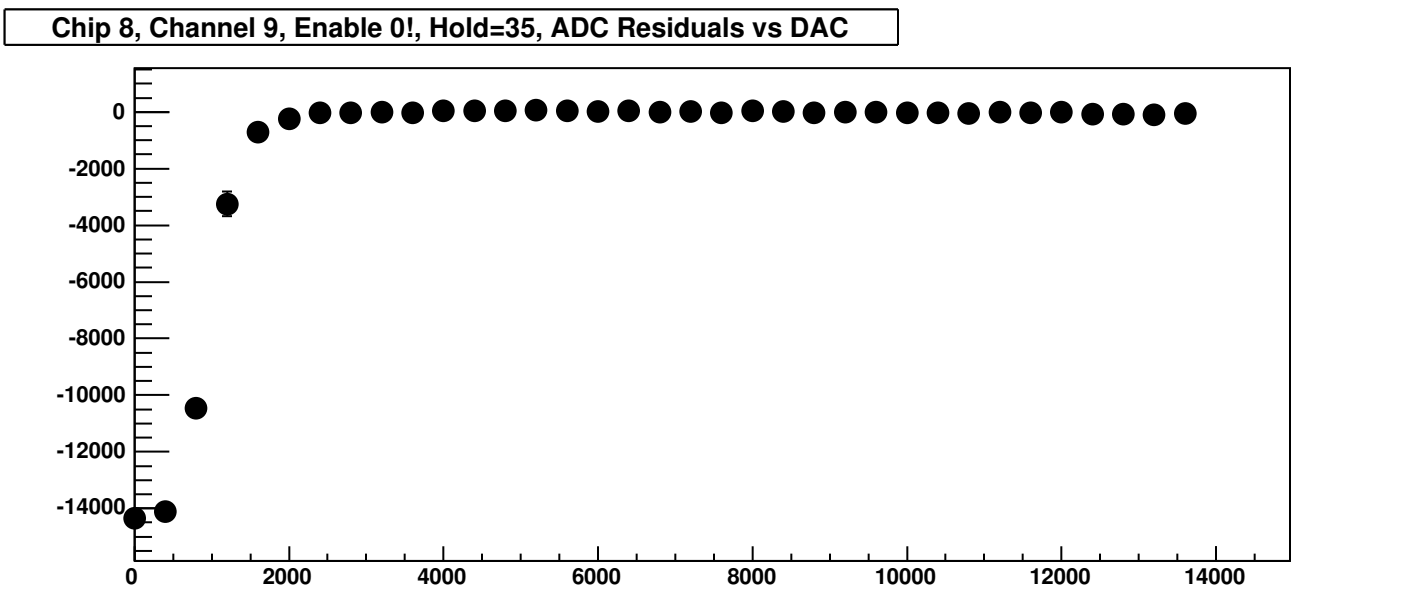

Chip 8, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2678 / 23
p0 -45.24 ± 0.9614
p1 0.1781 ± 0.0001934
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

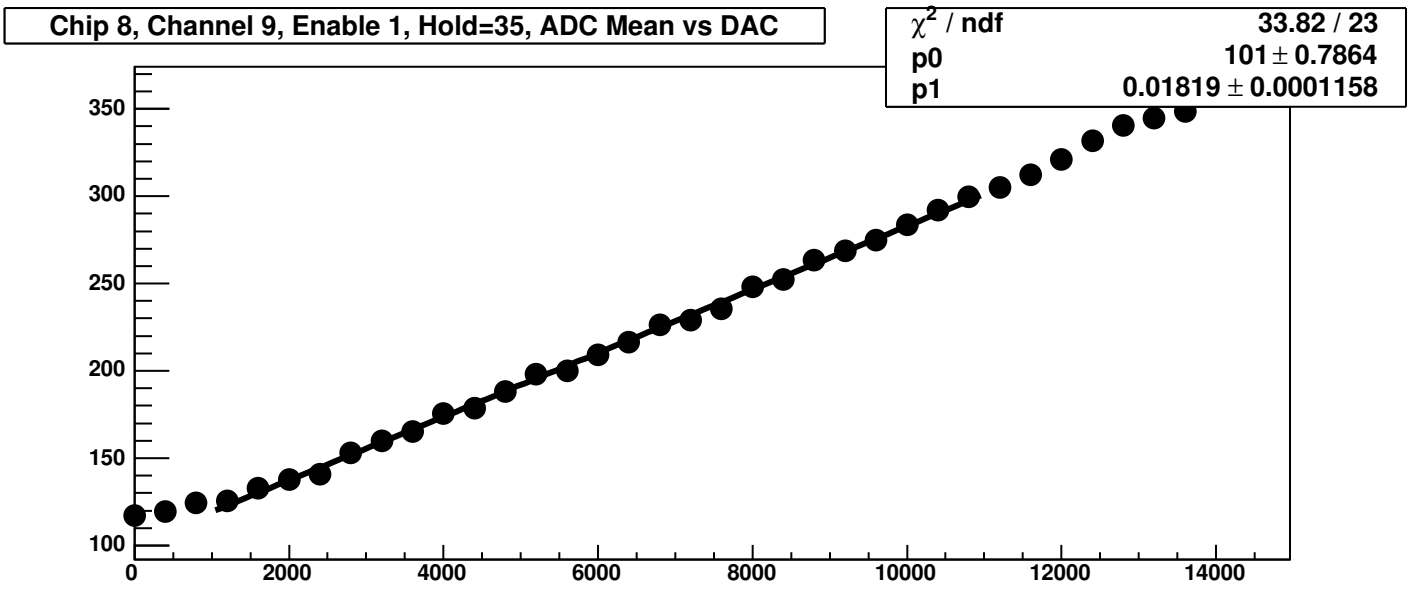

Chip 8, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

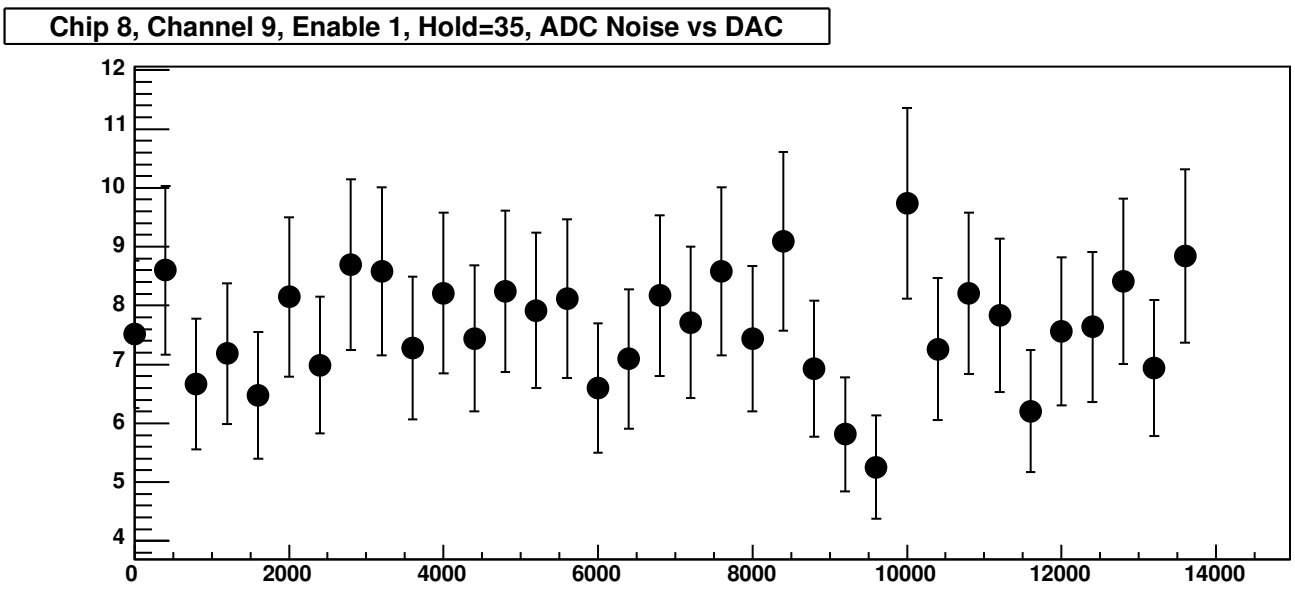

Chip 8, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

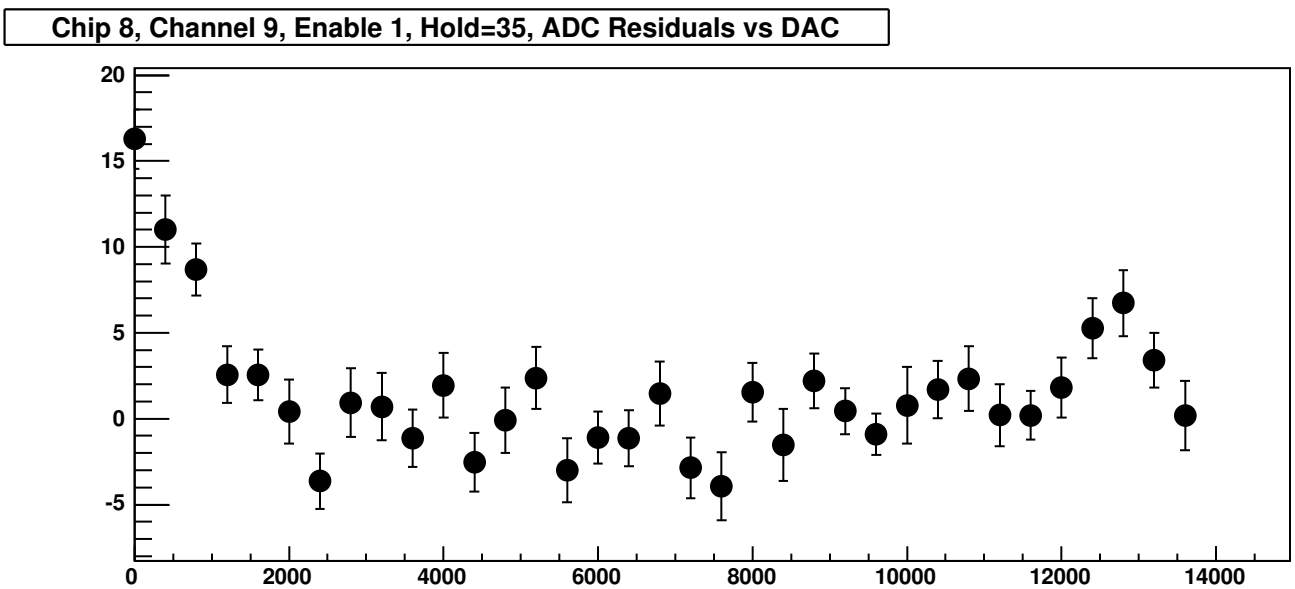

Chip 8, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 252.4 / 23
p0 3.069e+04 ± 14.14
p1 0.005528 ± 0.001935
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

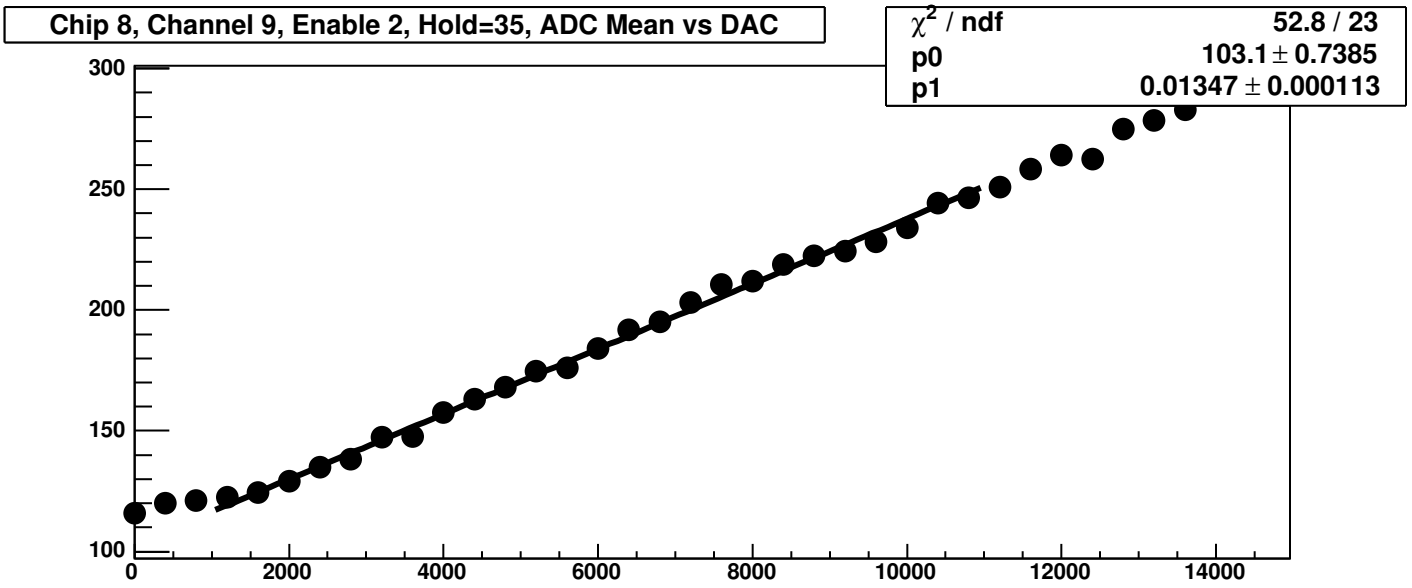

Chip 8, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

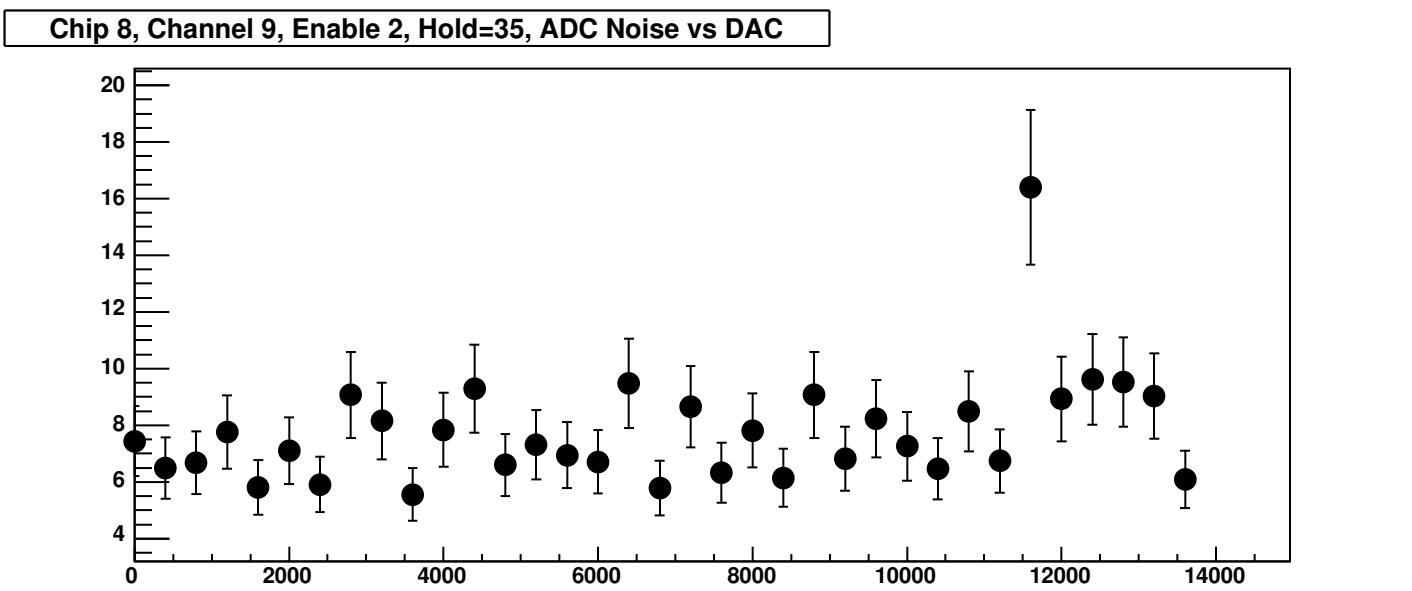

Chip 8, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

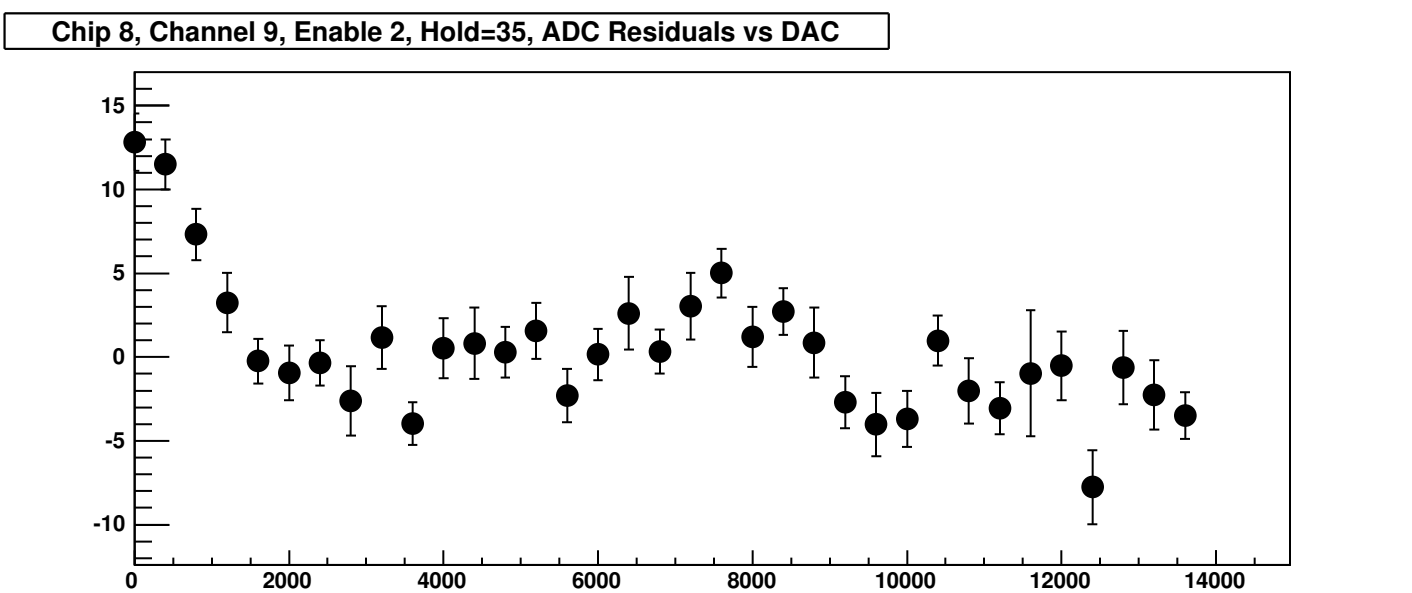

Chip 8, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.82 / 23
p0 101 ± 0.7864
p1 0.01819 ± 0.0001158
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

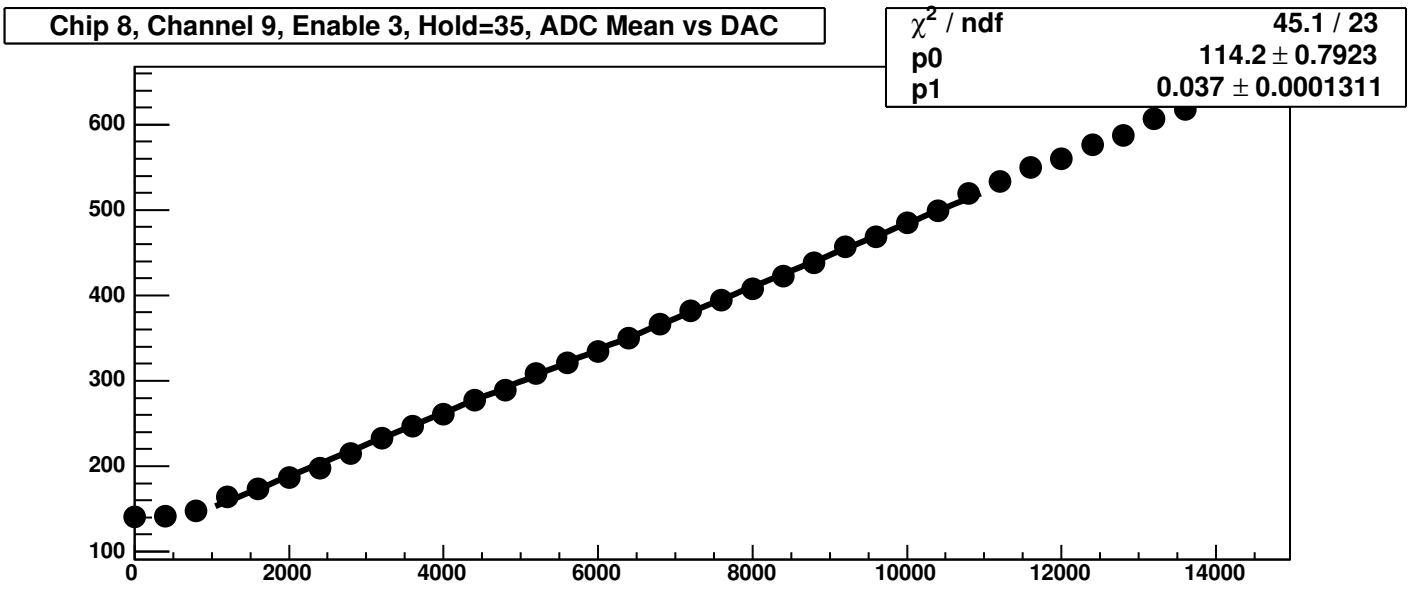

Chip 8, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

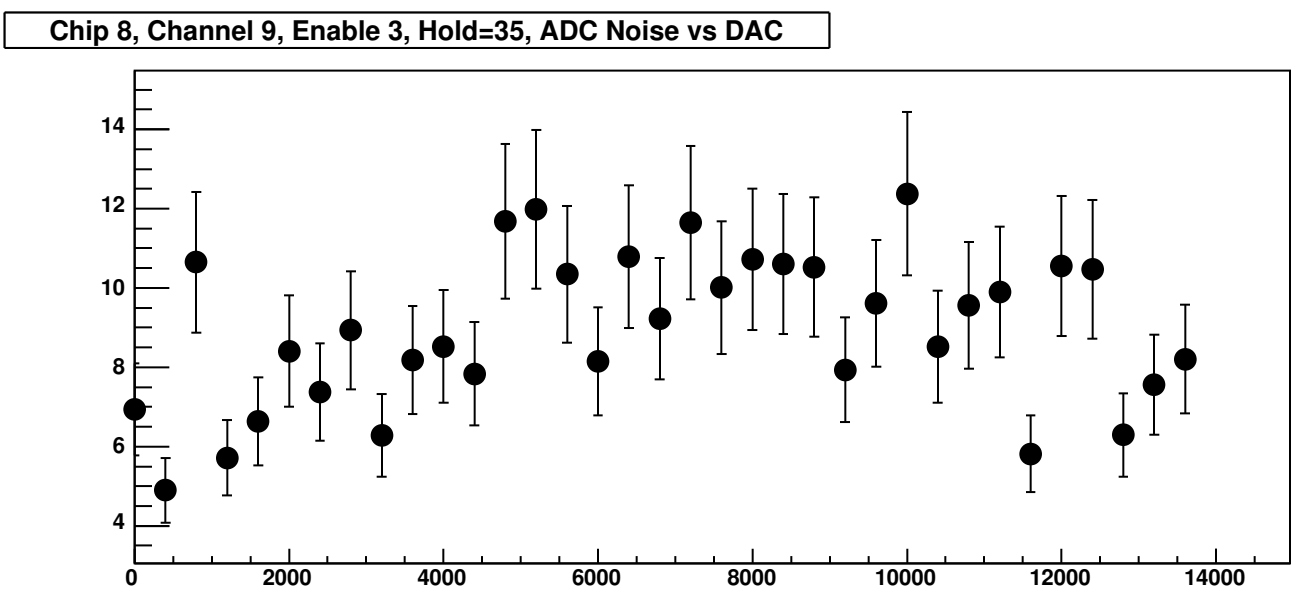

Chip 8, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

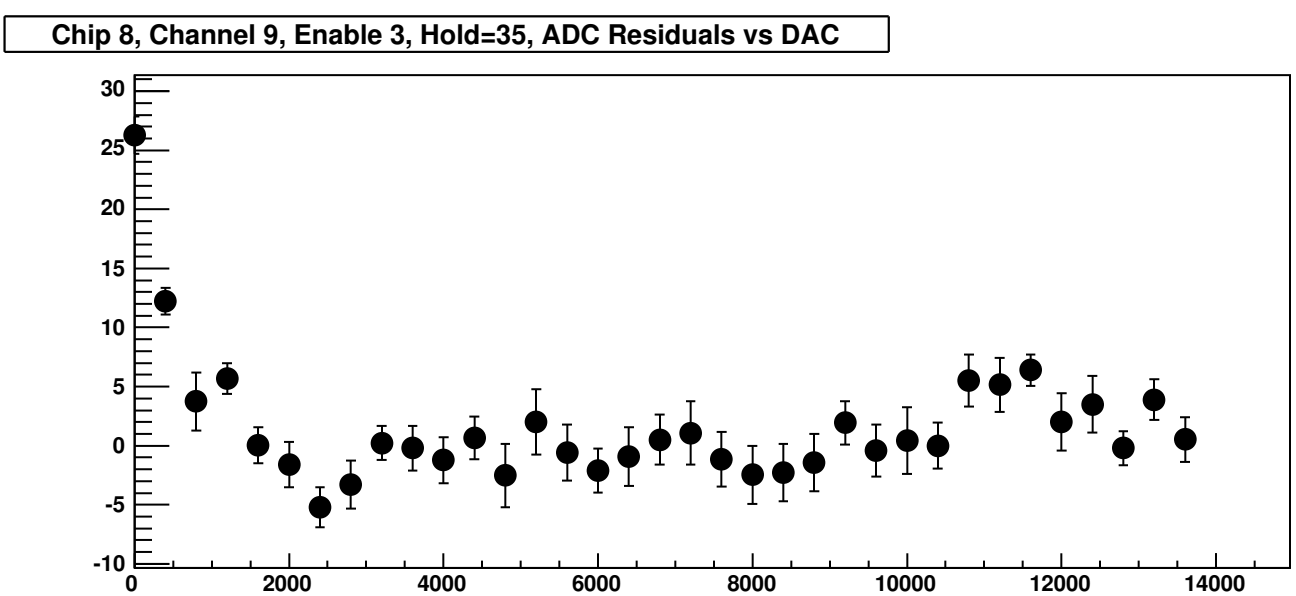

Chip 8, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 52.8 / 23
p0 103.1 ± 0.7385
p1 0.01347 ± 0.000113
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

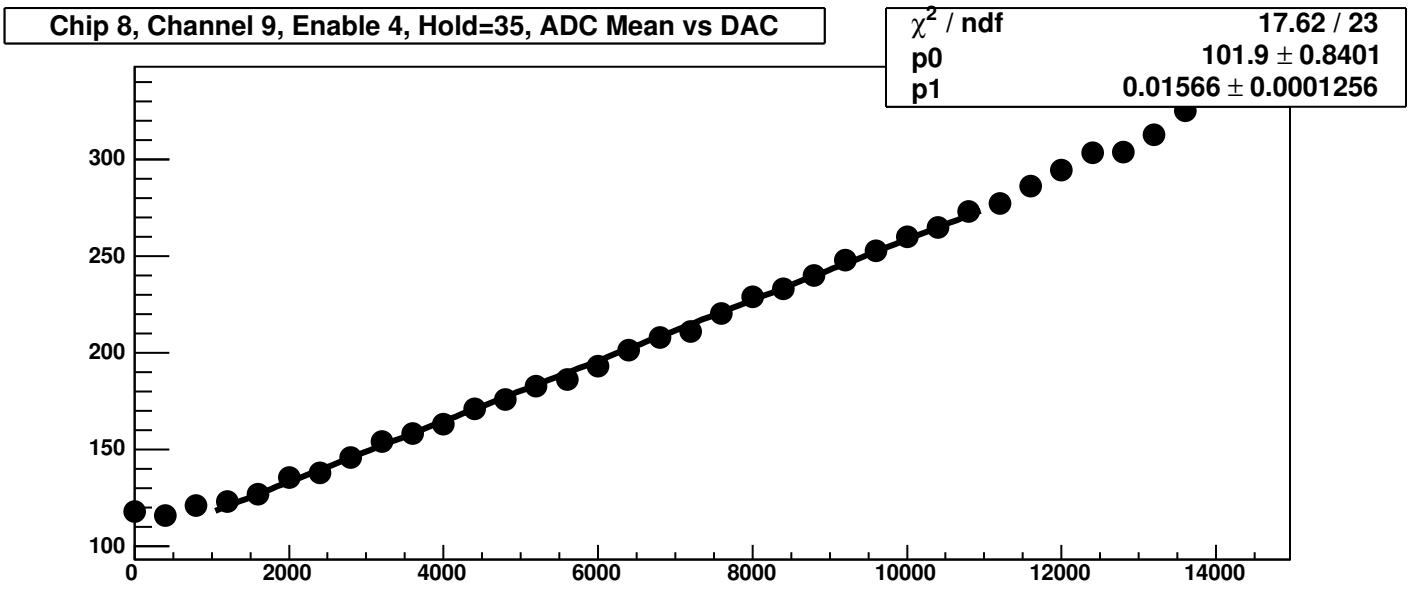

Chip 8, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

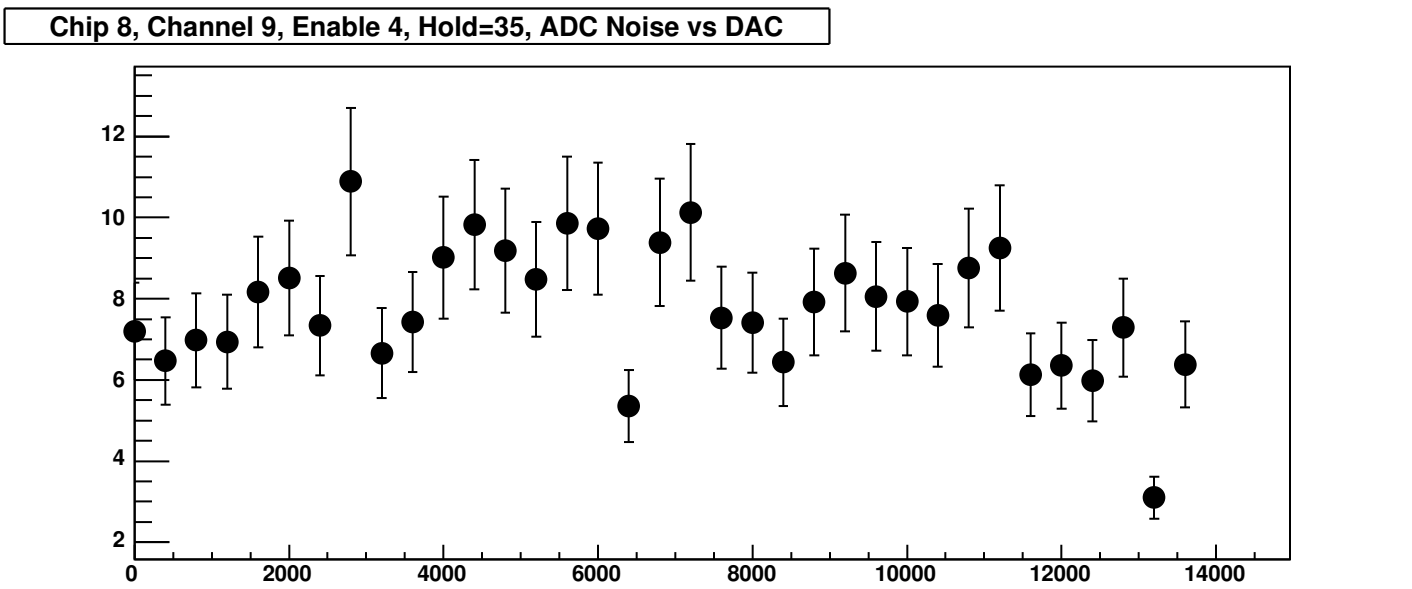

Chip 8, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

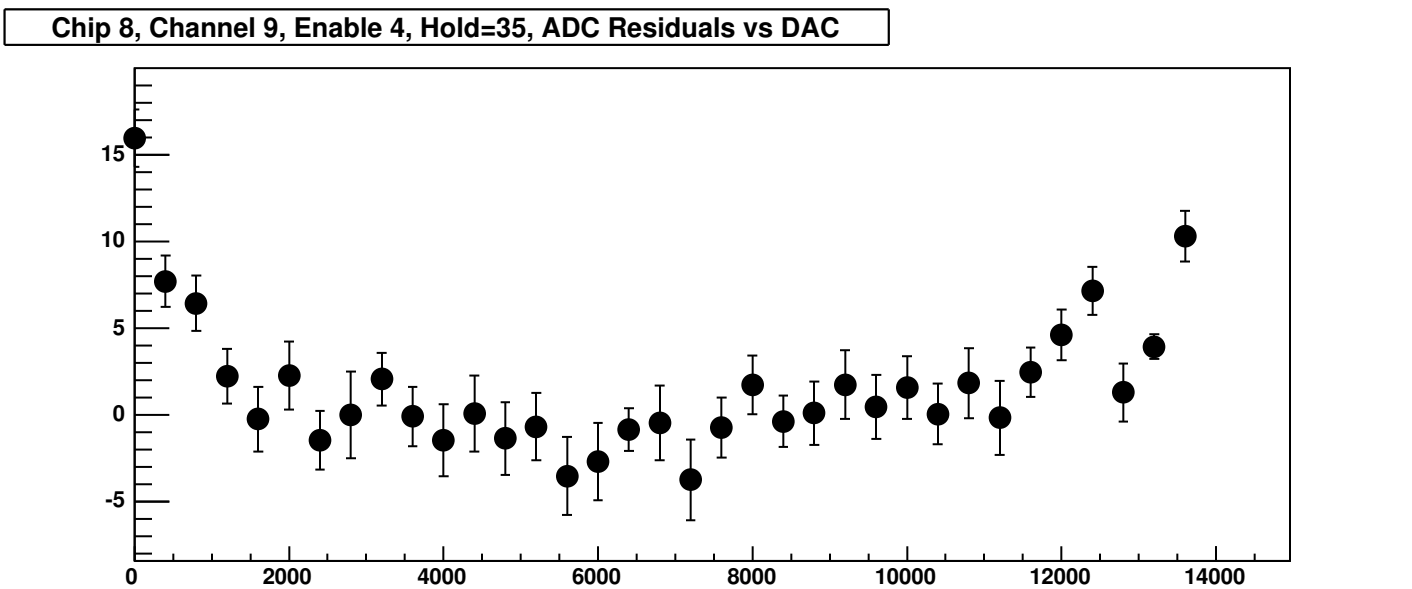

Chip 8, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 45.1 / 23
p0 114.2 ± 0.7923
p1 0.037 ± 0.0001311
600
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000

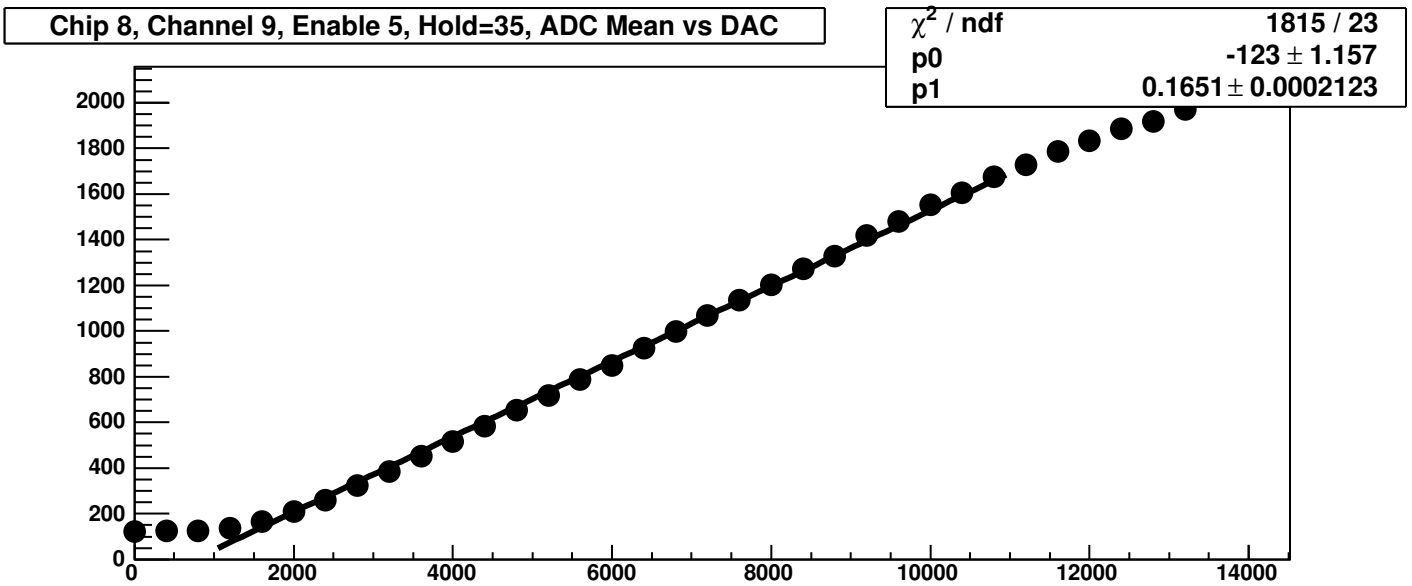

Chip 8, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

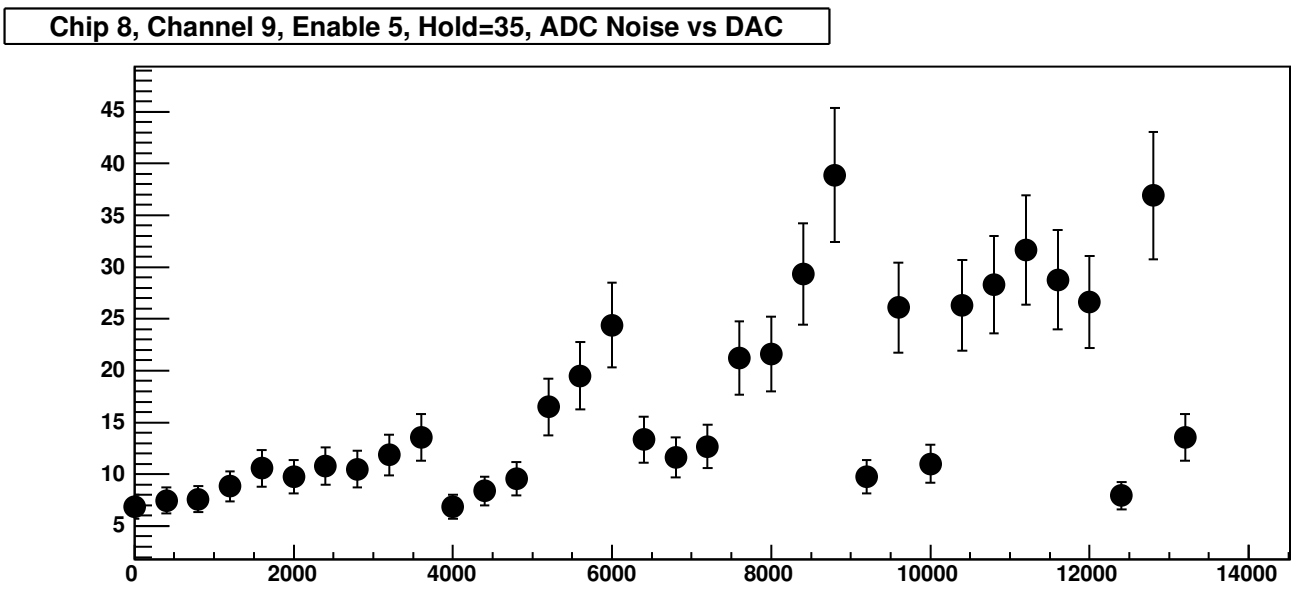

Chip 8, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

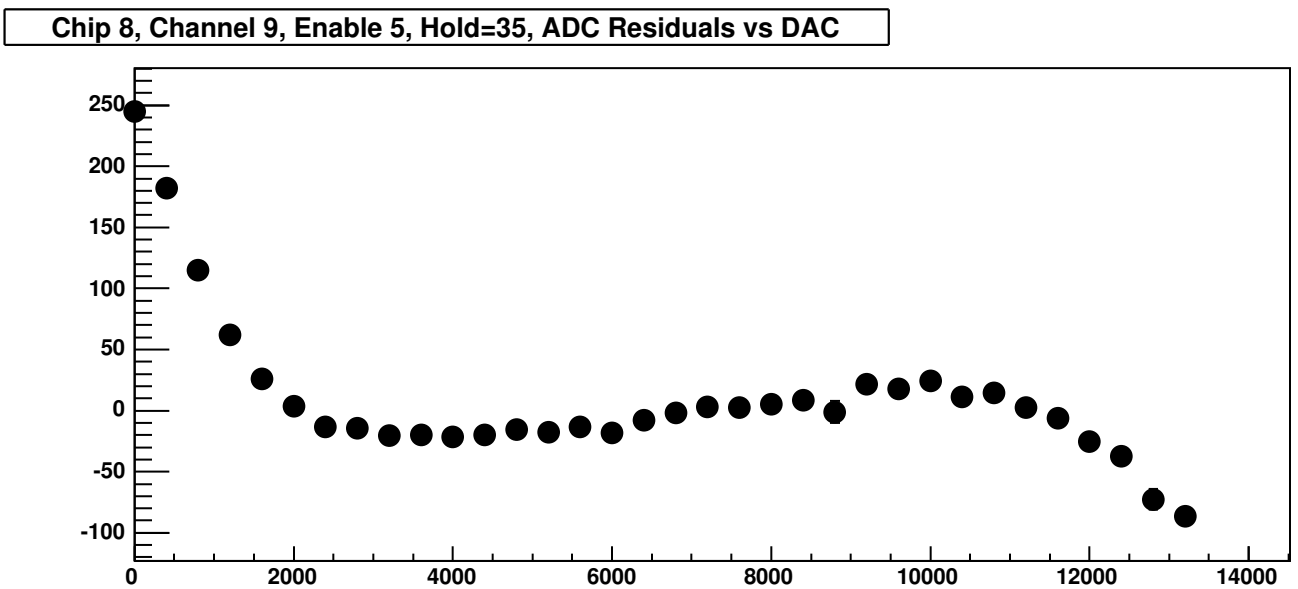

Chip 8, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 17.62 / 23
p0 101.9 ± 0.8401
p1 0.01566 ± 0.0001256
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

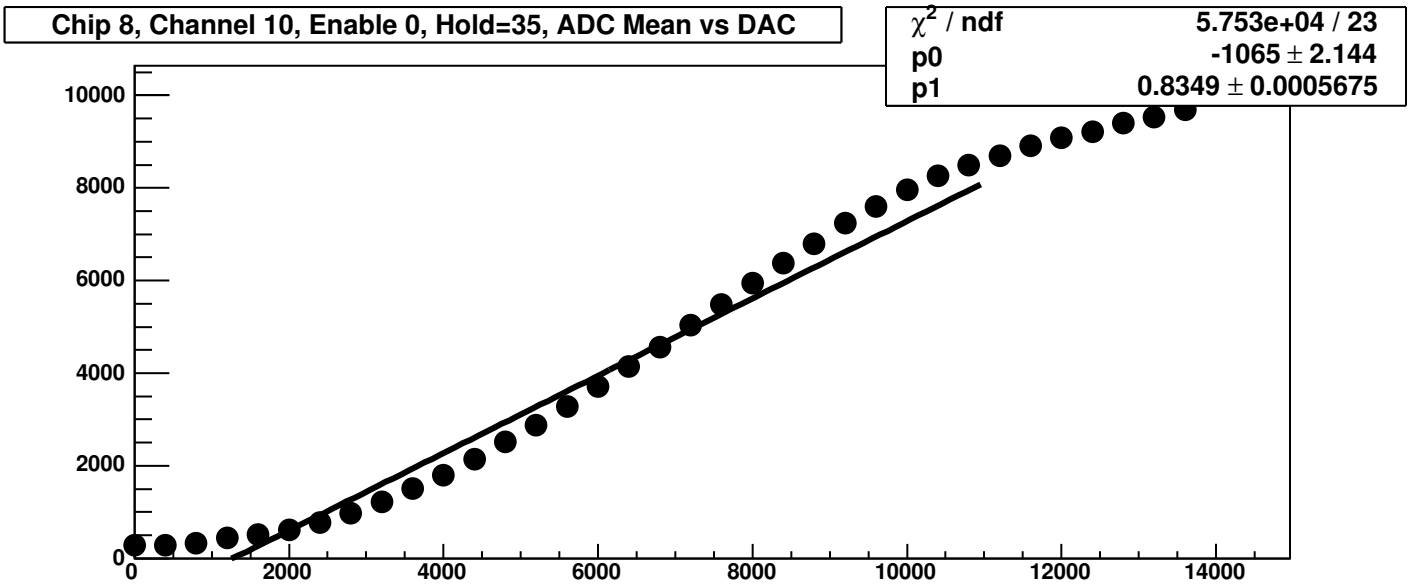

Chip 8, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

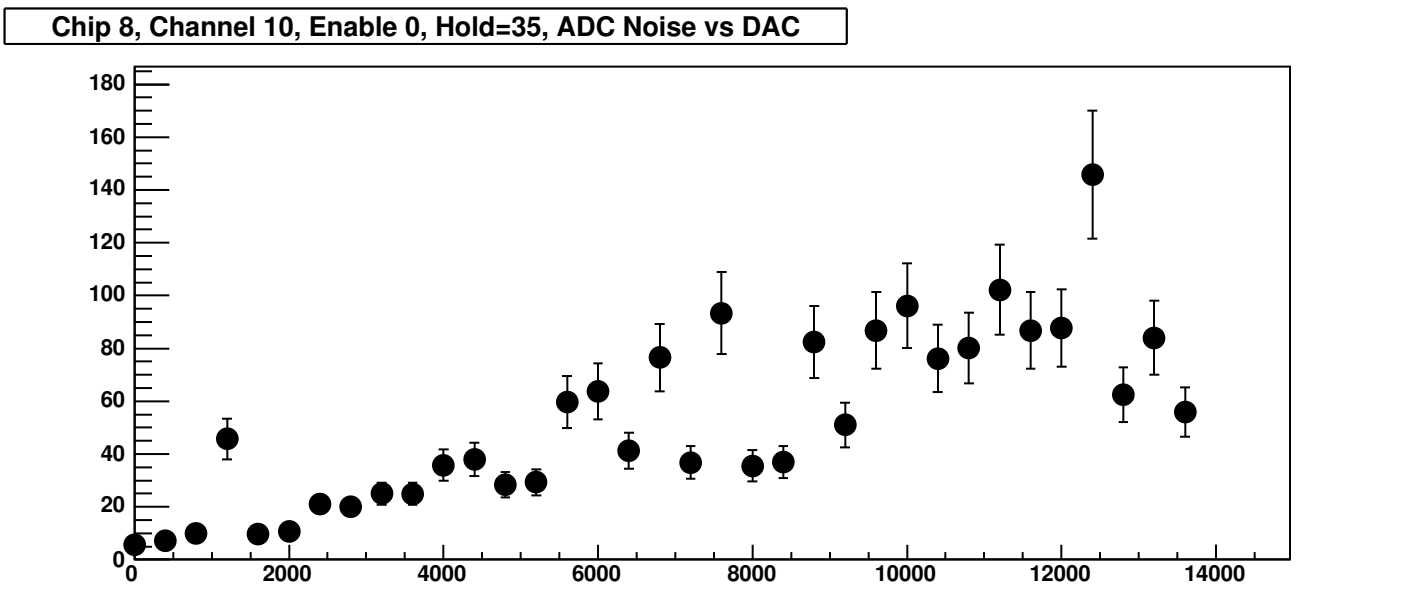

Chip 8, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

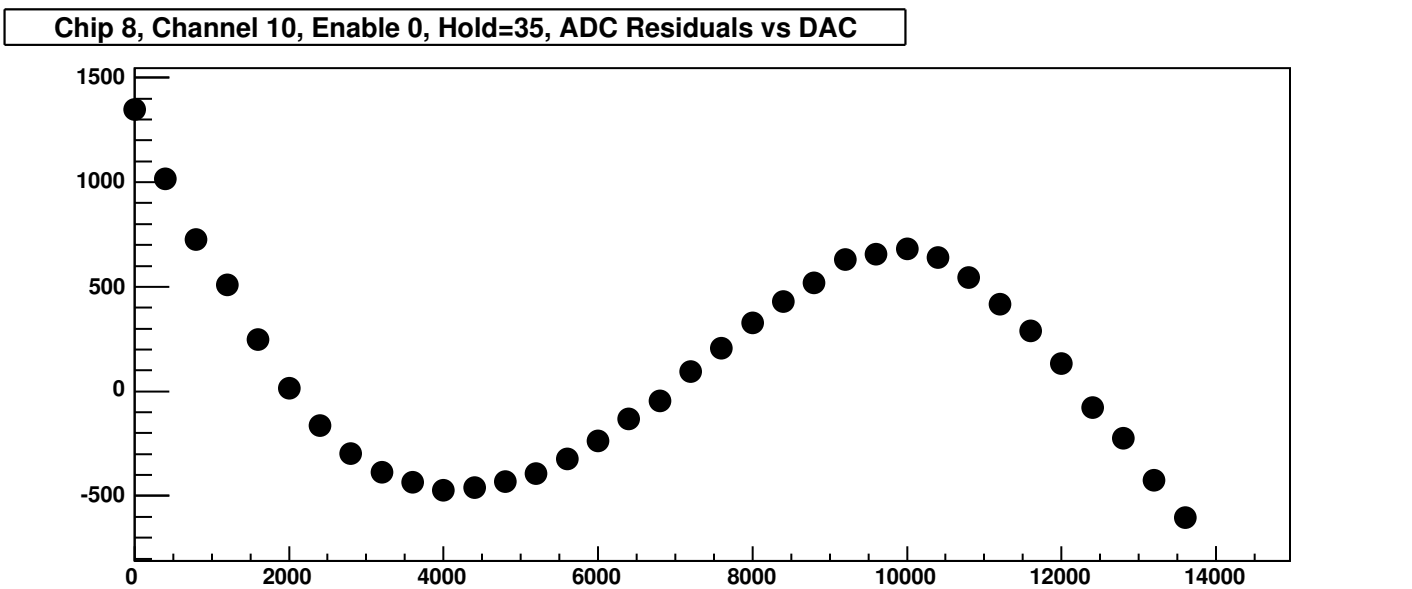

Chip 8, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1815 / 23
p0 -123 ± 1.157
p1 0.1651 ± 0.0002123
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

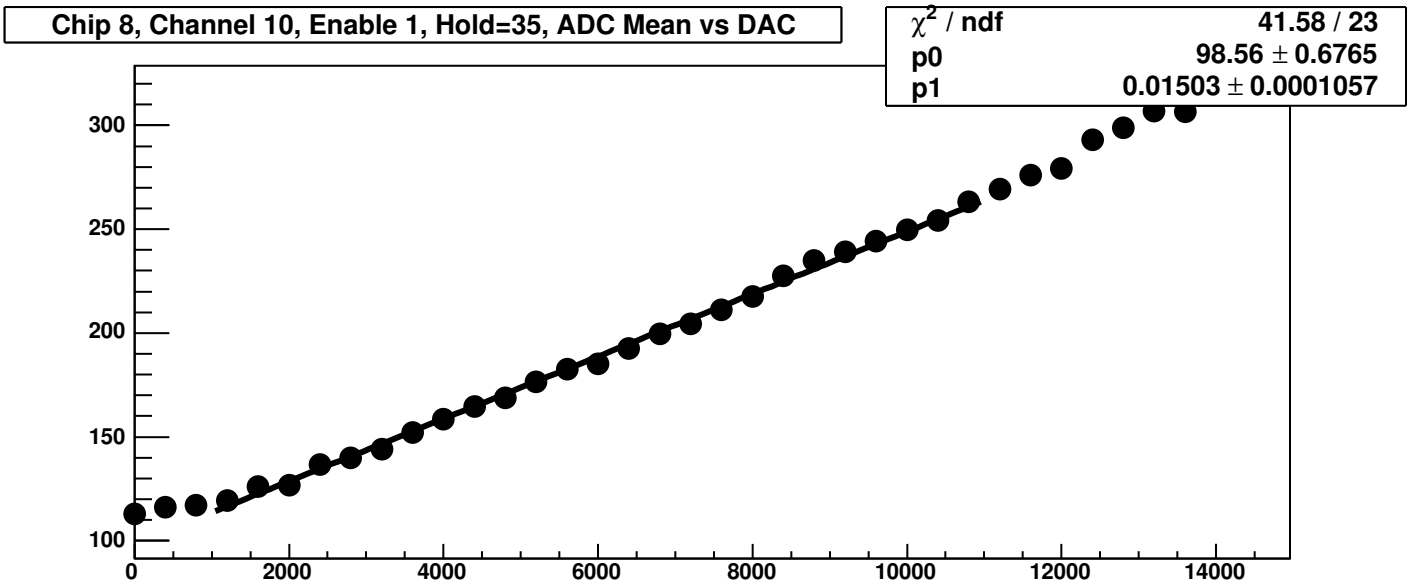

Chip 8, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

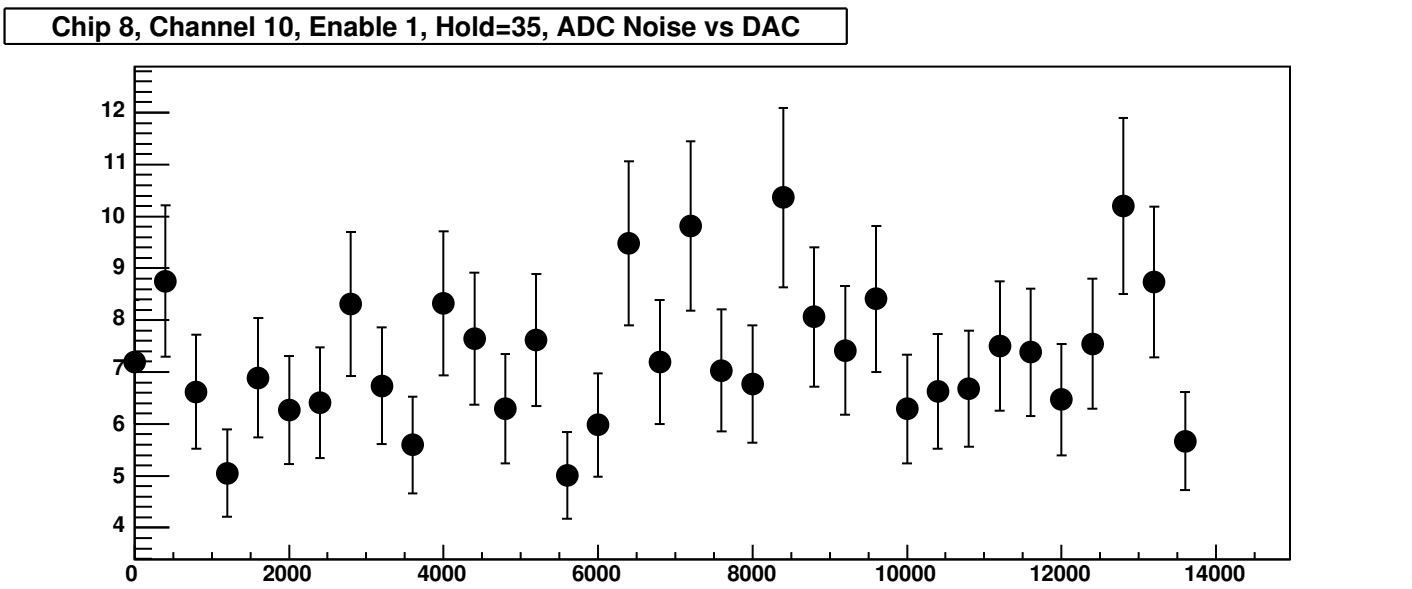

Chip 8, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

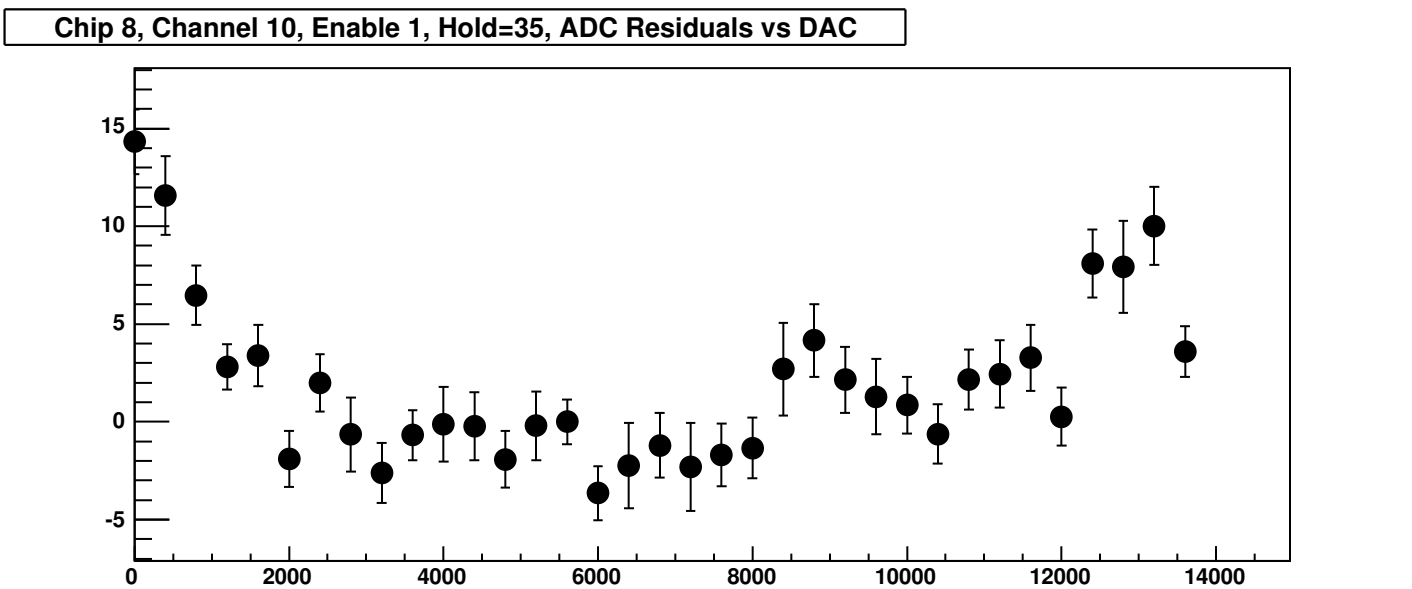

Chip 8, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.753e+04 / 23
p0 -1065 ± 2.144
p1 0.8349 ± 0.0005675
10000
8000
6000
4000
2000
0
0
2000
4000
6000
8000
10000
12000
14000

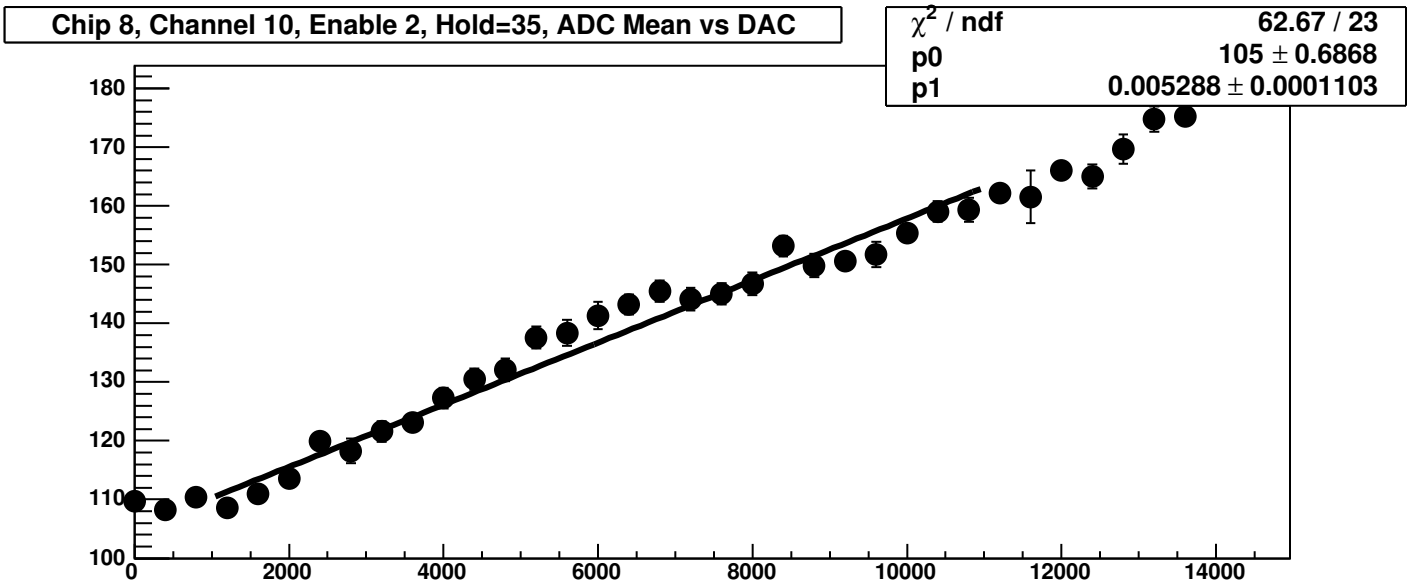

Chip 8, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
180
160
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

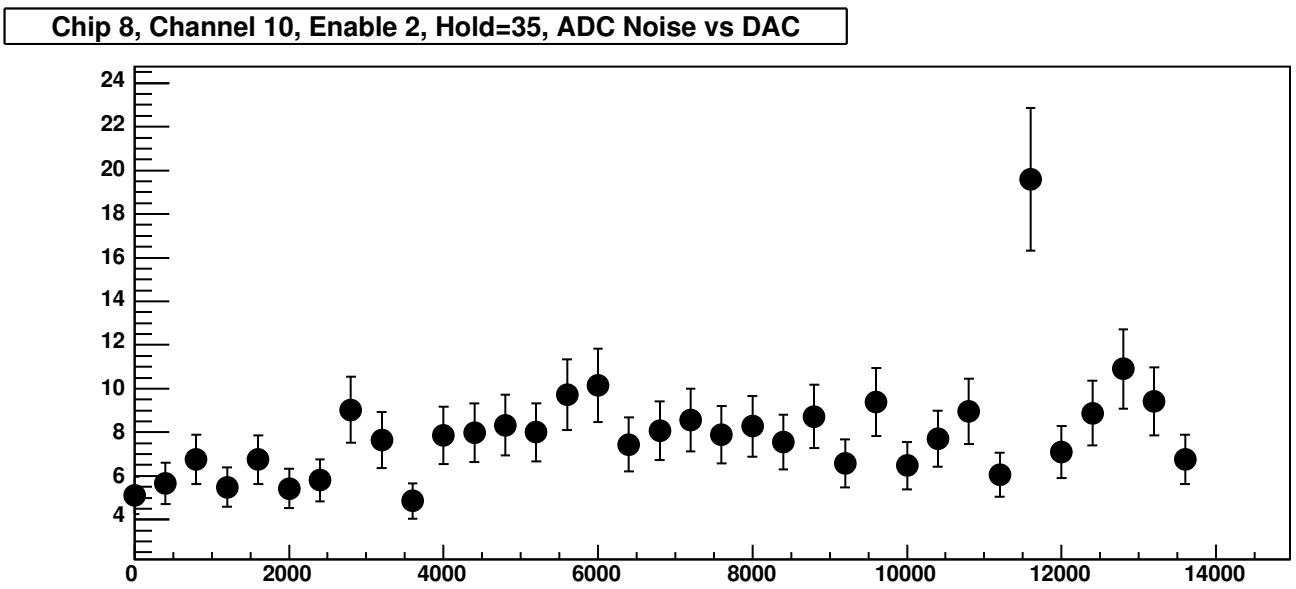

Chip 8, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

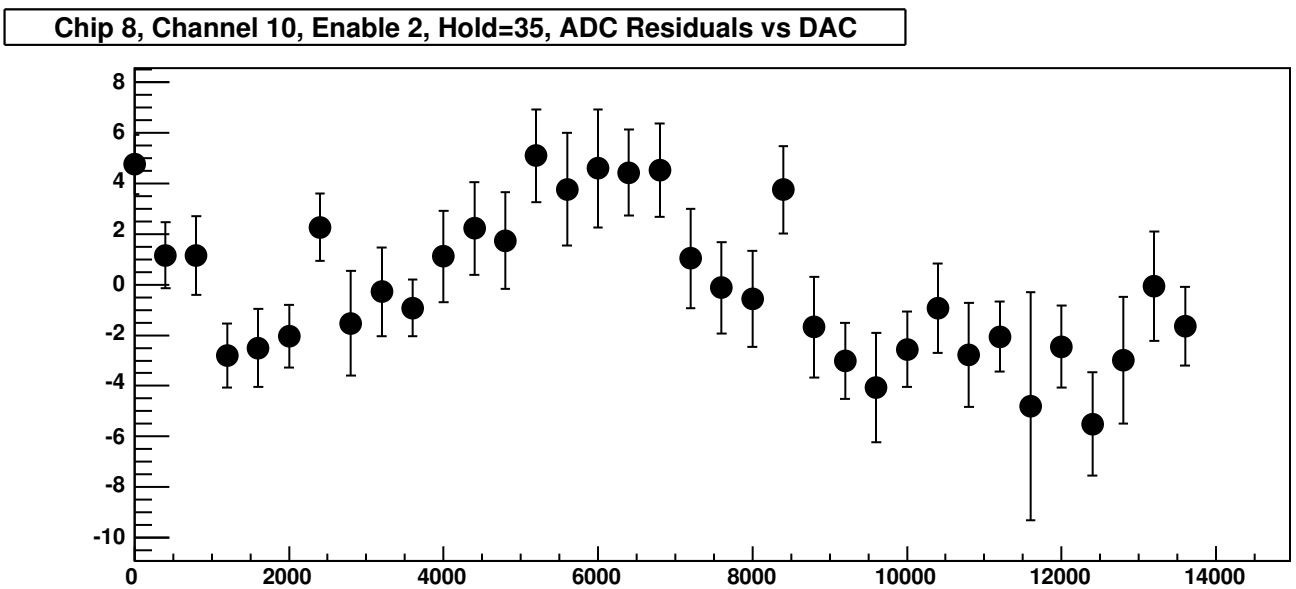

Chip 8, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 41.58 / 23
p0 98.56 ± 0.6765
p1 0.01503 ± 0.0001057
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

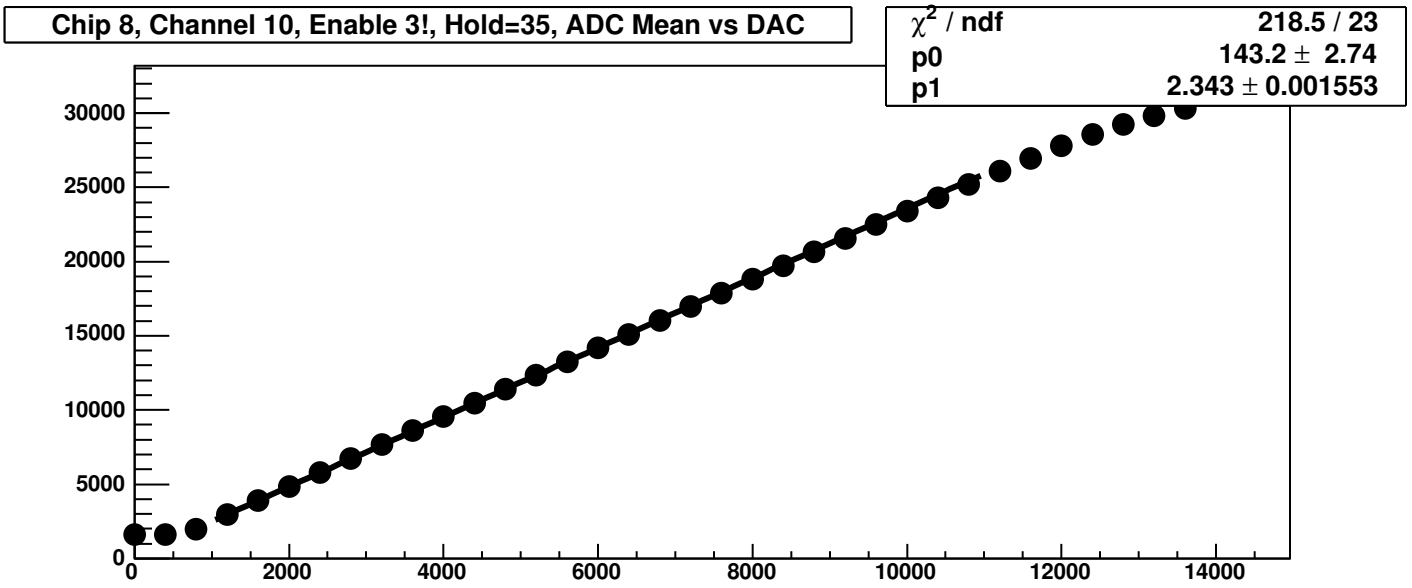

Chip 8, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

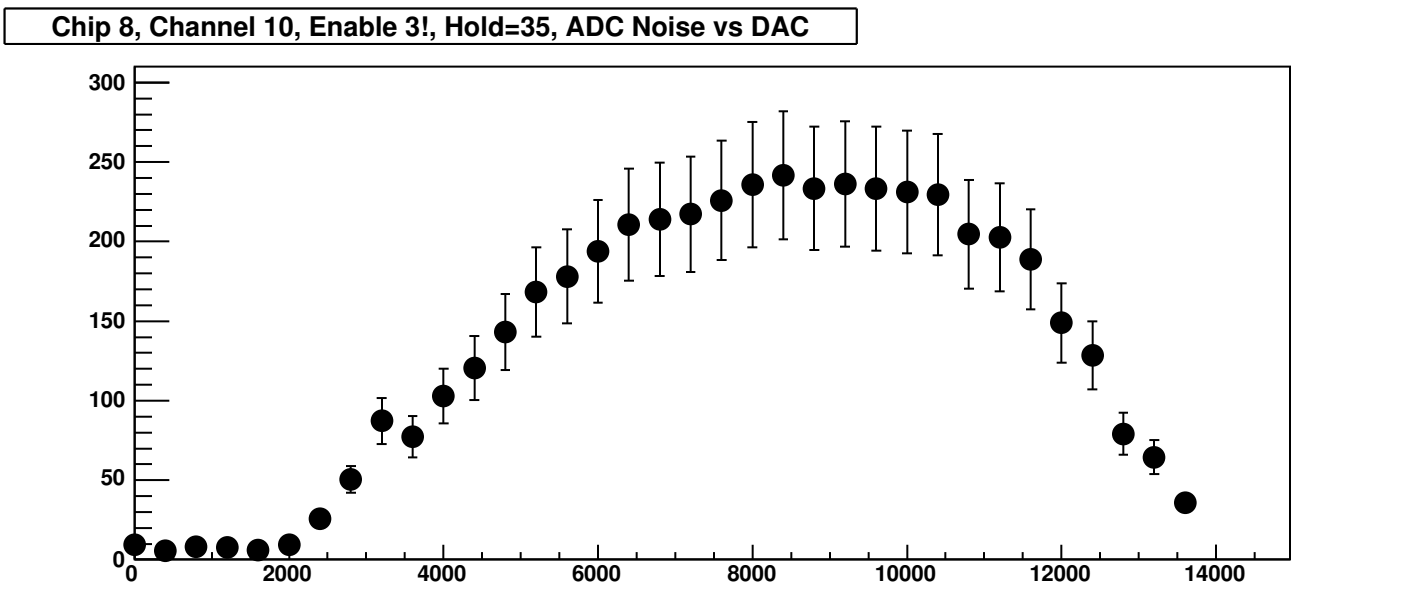

Chip 8, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

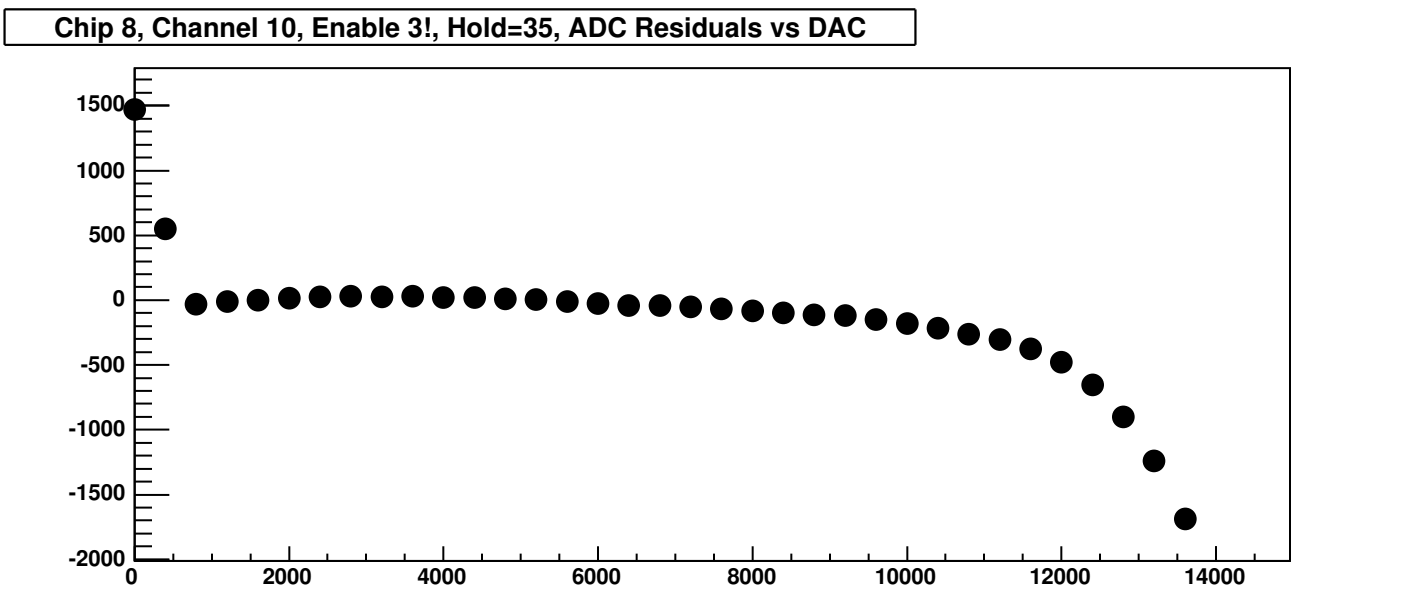

Chip 8, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 62.67 / 23
p0 105 ± 0.6868
p1 0.005288 ± 0.0001103
180
170
160
150
140
130
120
110
100
0
2000
4000
6000
8000
10000
12000
14000

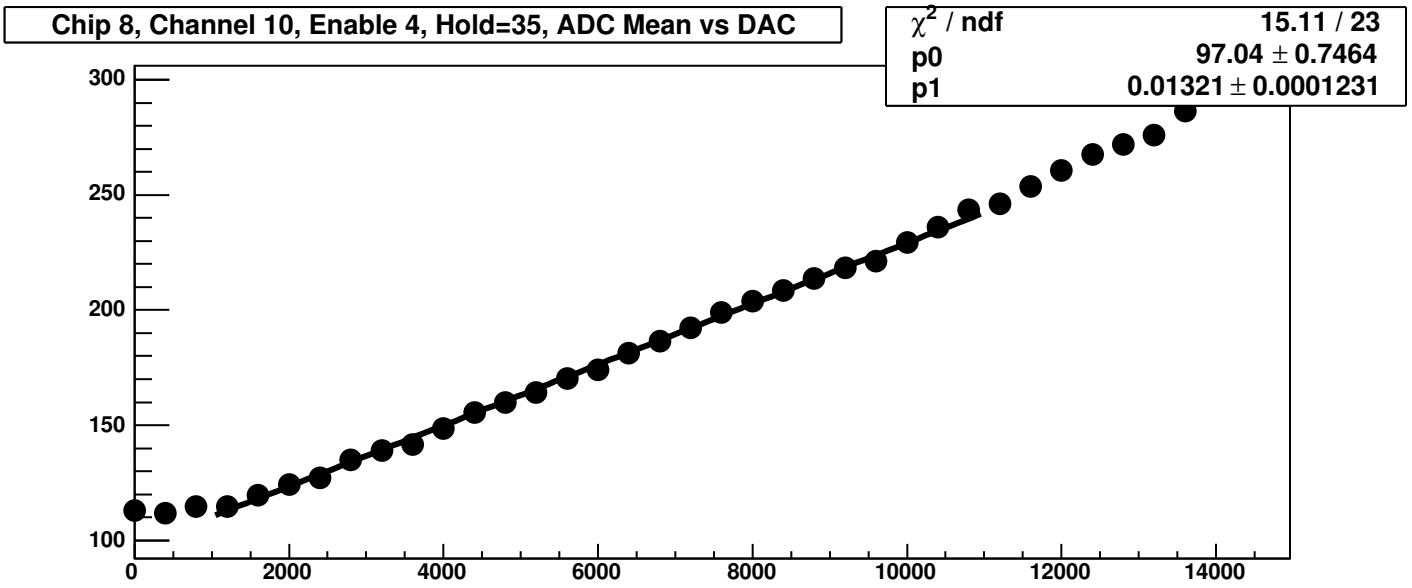

Chip 8, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
24
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

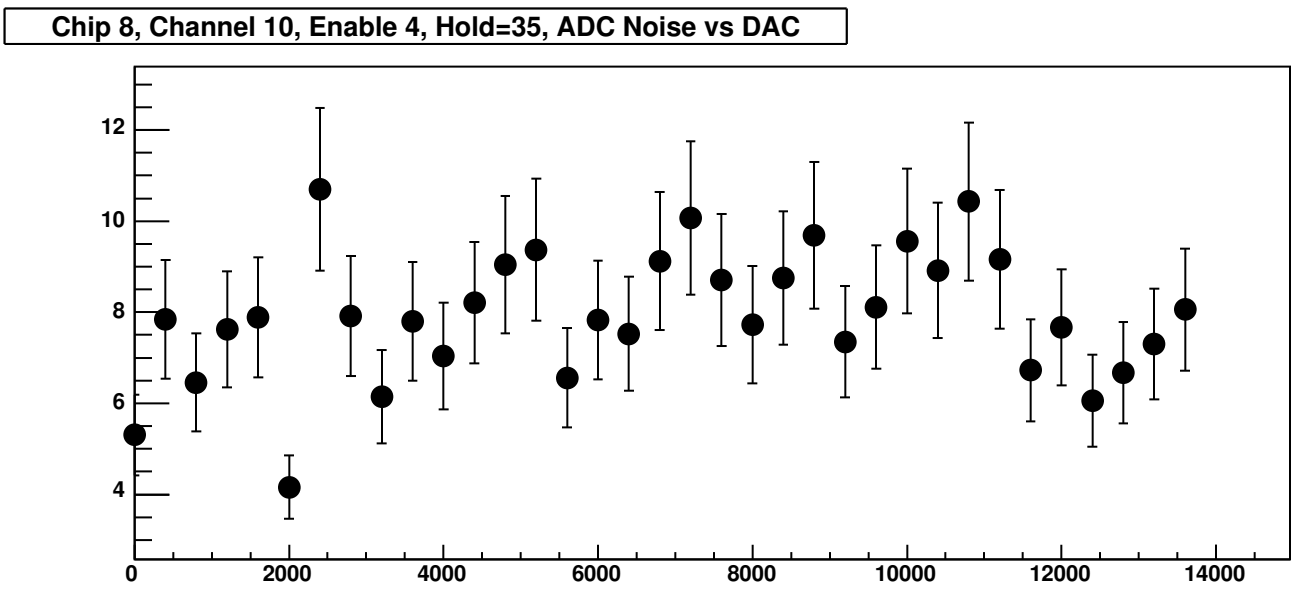

Chip 8, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
0
2000
4000
6000
8000
10000
12000
14000

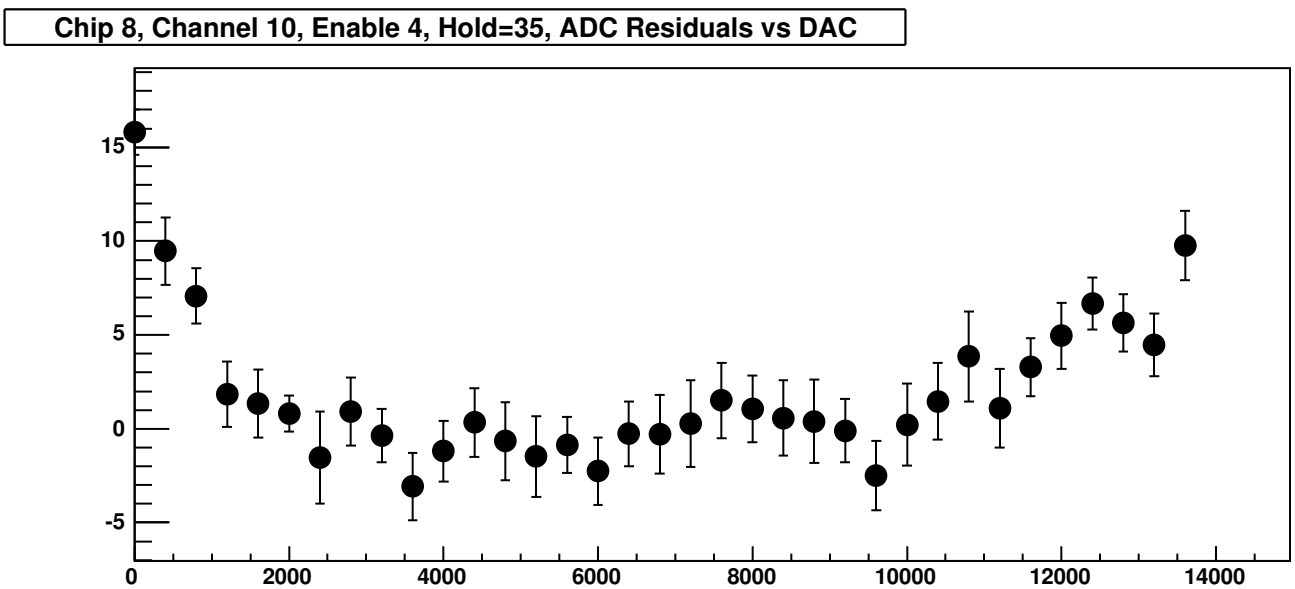

Chip 8, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 218.5 / 23
p0 143.2 ± 2.74
p1 2.343 ± 0.001553
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

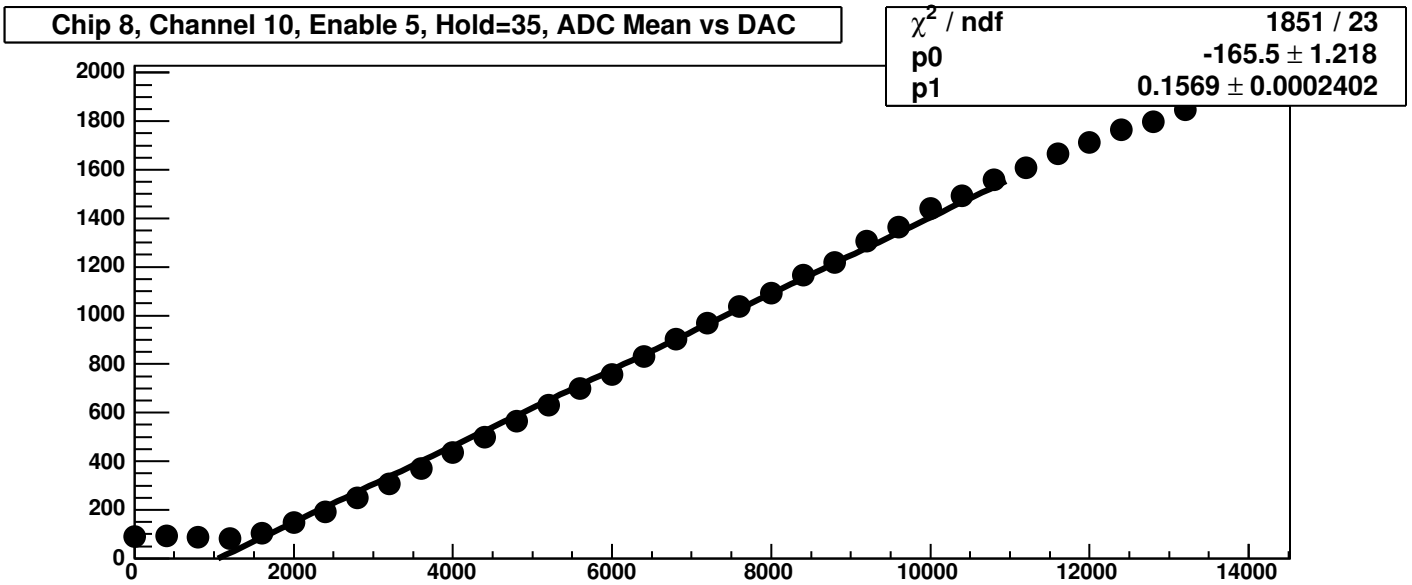

Chip 8, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

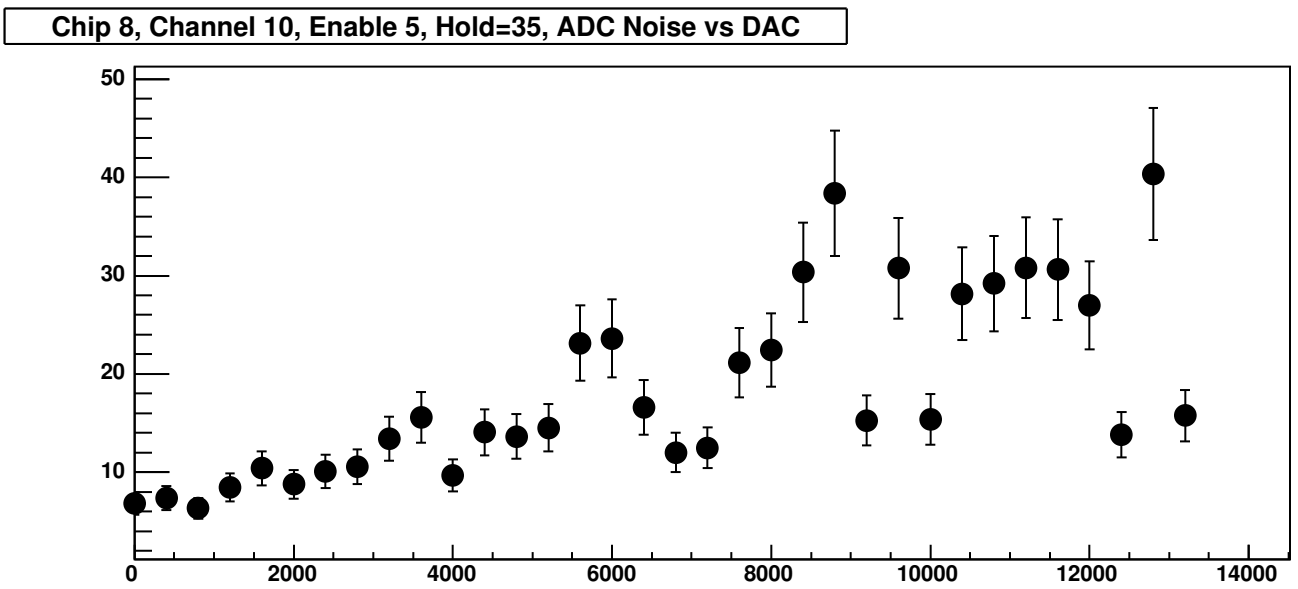

Chip 8, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

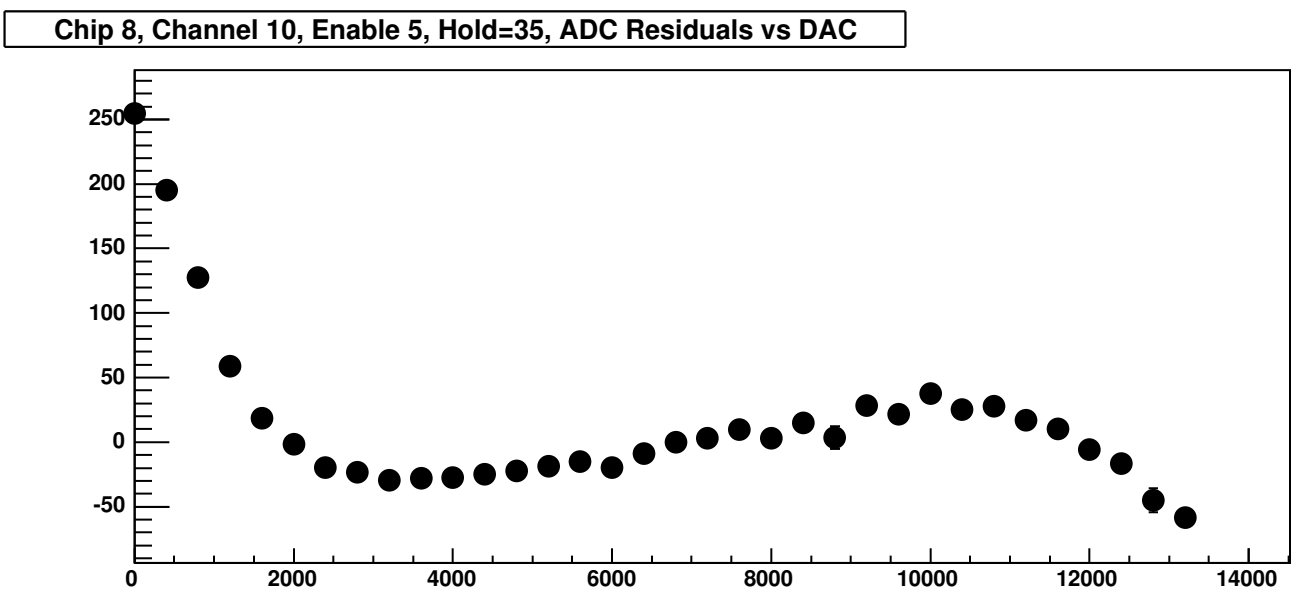

Chip 8, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 15.11 / 23
p0 97.04 ± 0.7464
p1 0.01321 ± 0.0001231
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

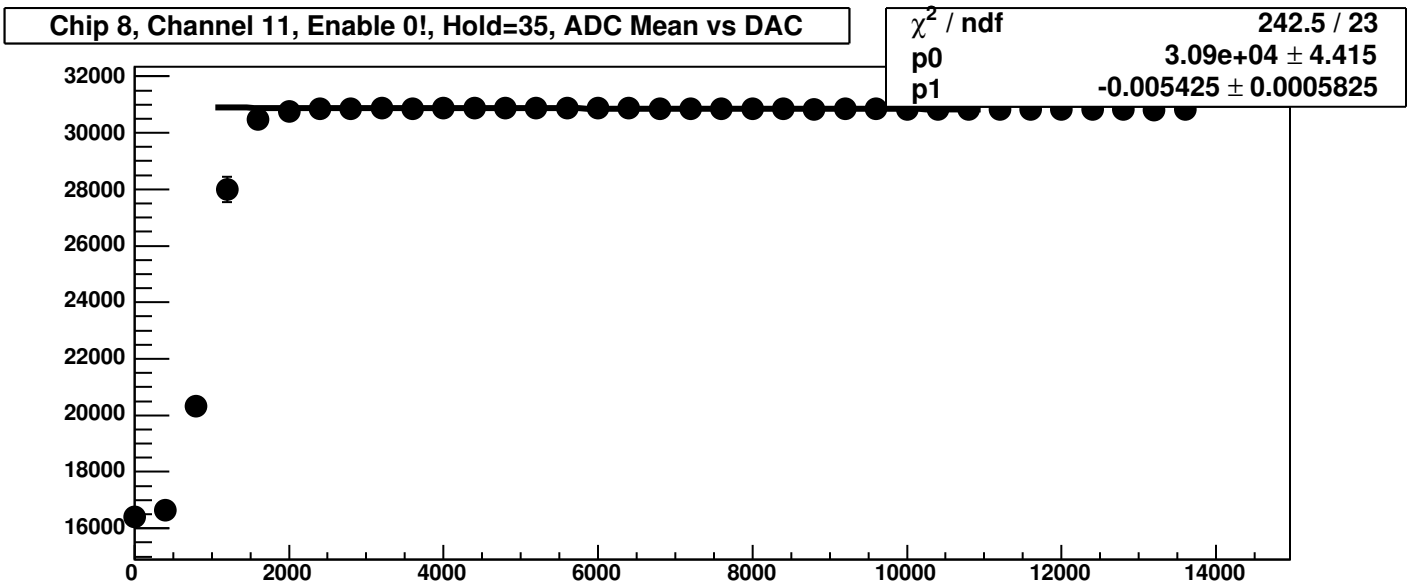

Chip 8, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

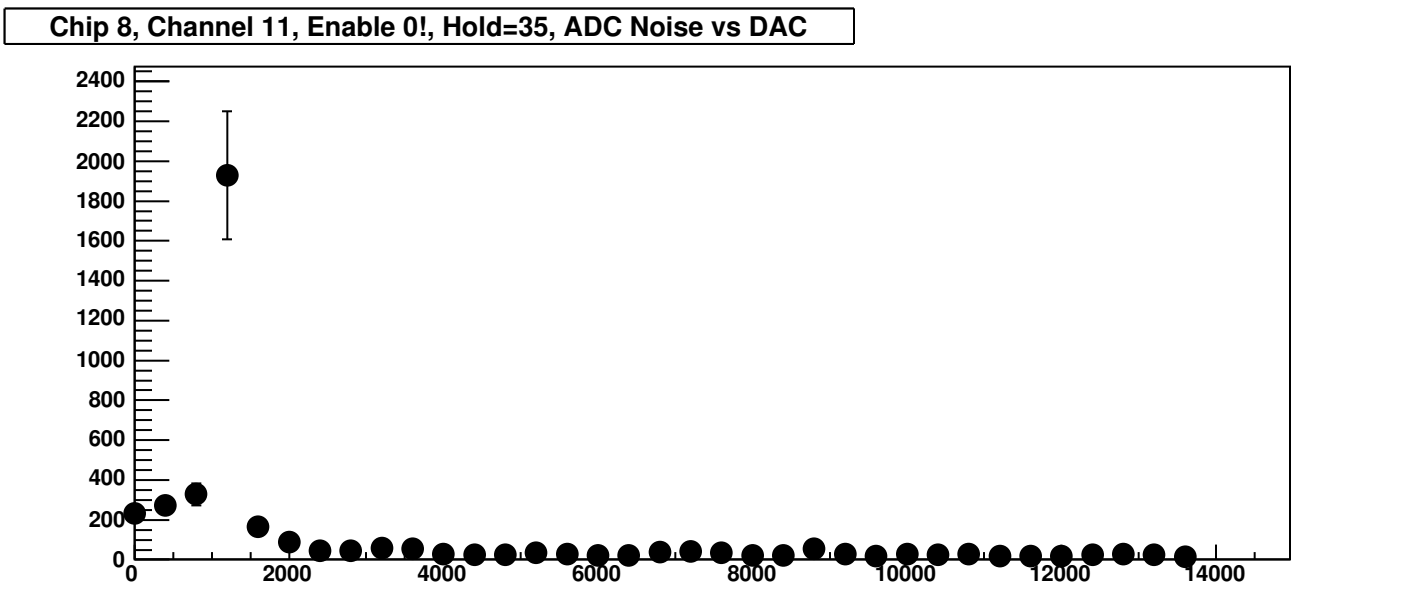

Chip 8, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

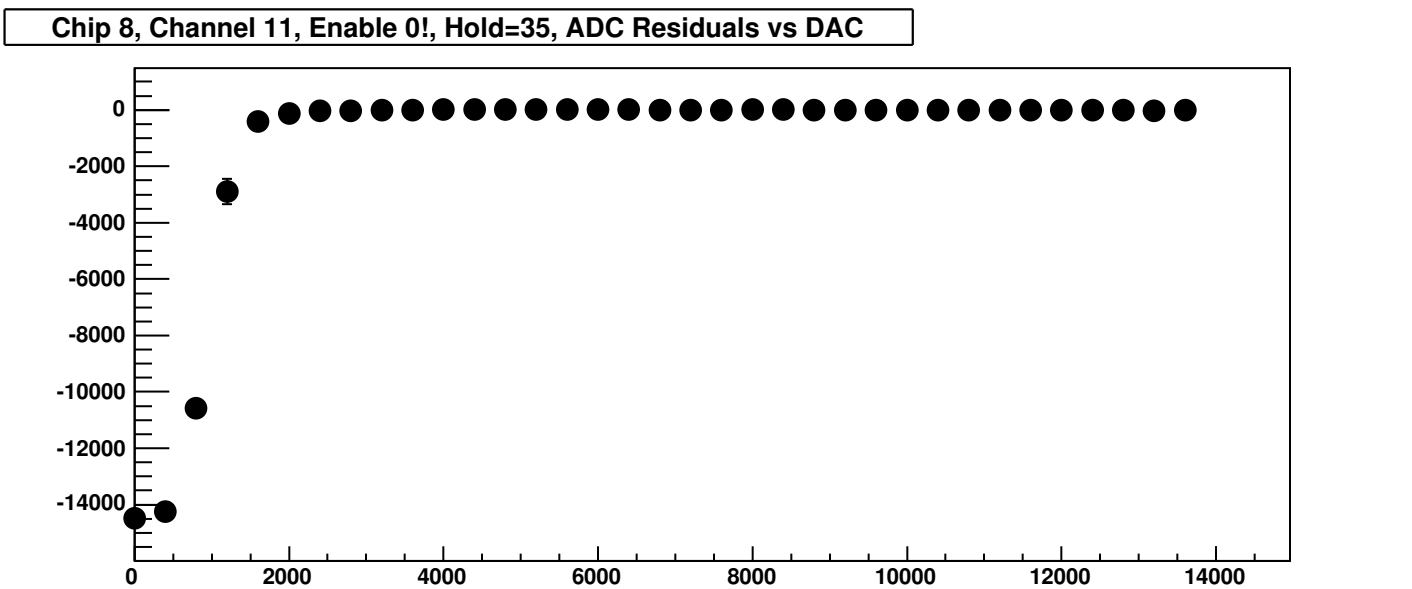

Chip 8, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1851 / 23
p0 -165.5 ± 1.218
p1 0.1569 ± 0.0002402
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

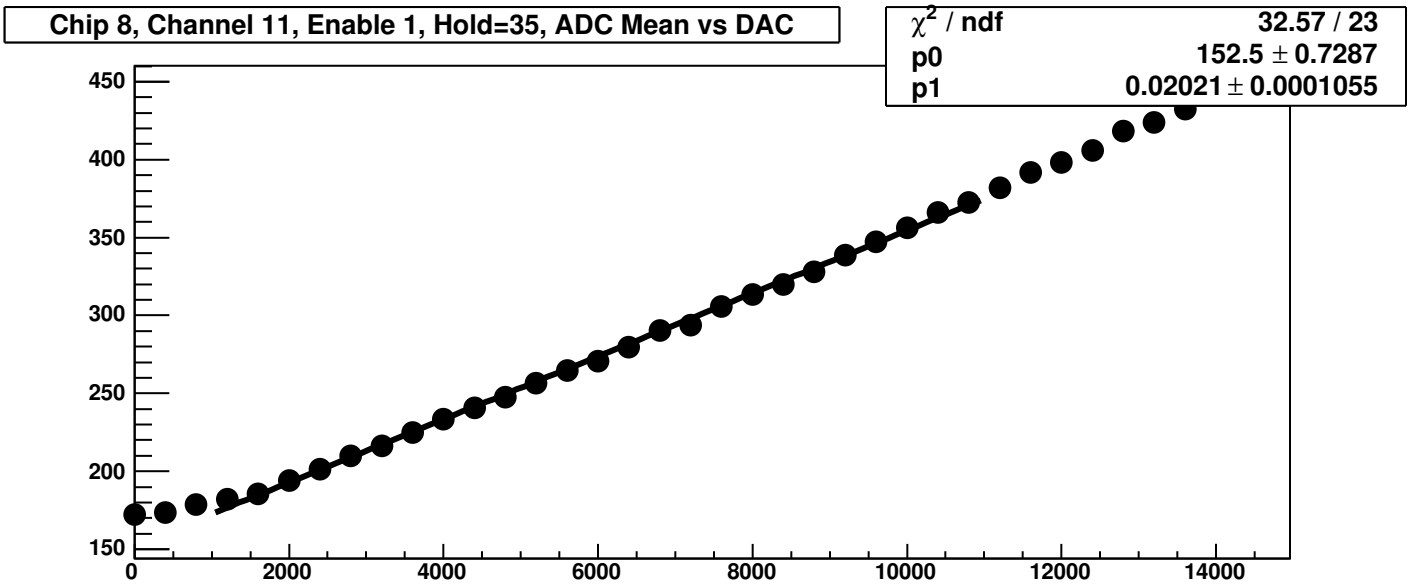

Chip 8, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

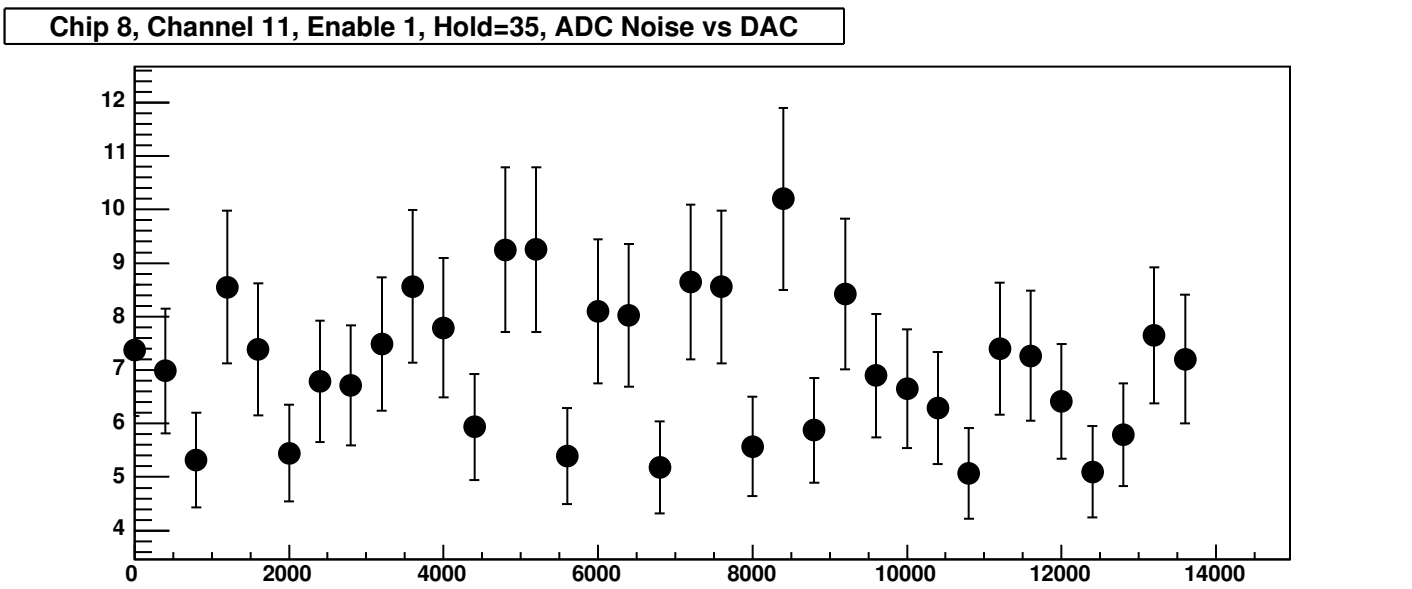

Chip 8, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

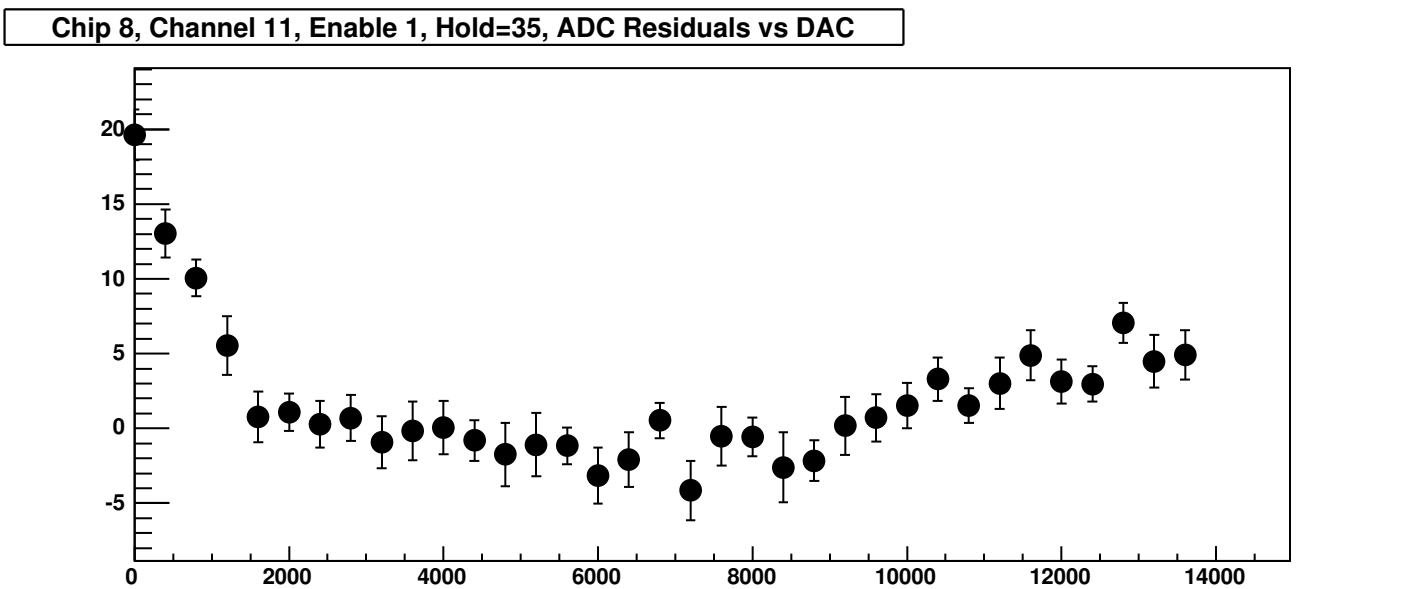

Chip 8, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 242.5 / 23
p0 3.09e+04 ± 4.415
p1 -0.005425 ± 0.0005825
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

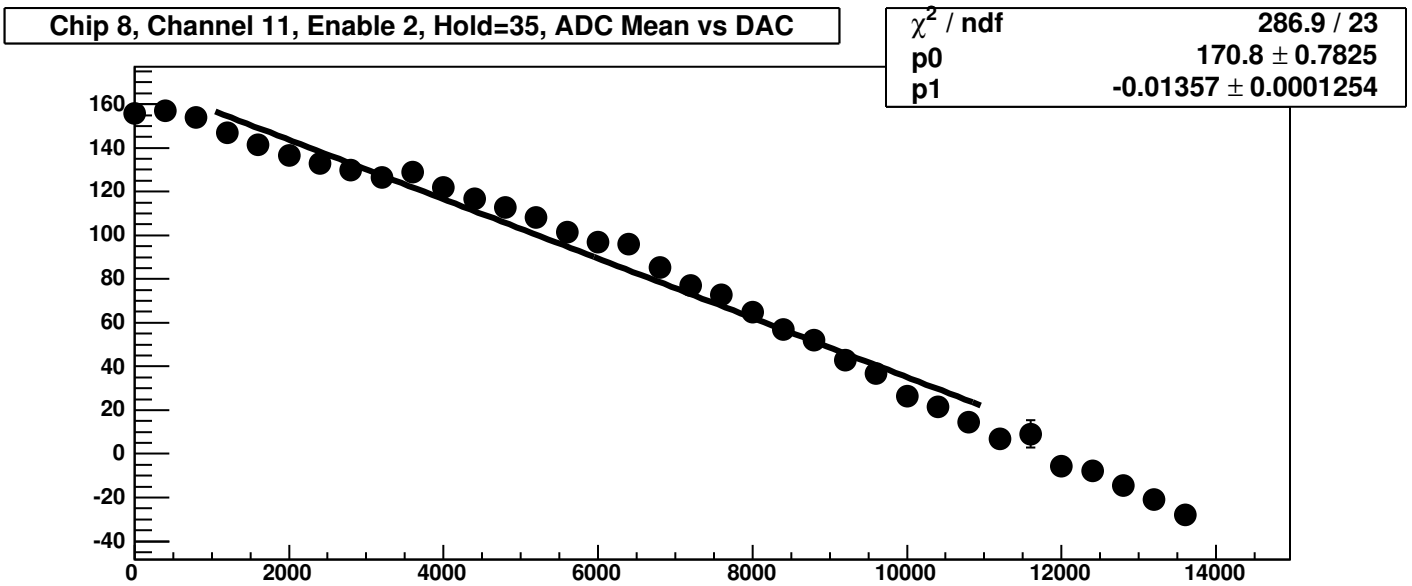

Chip 8, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

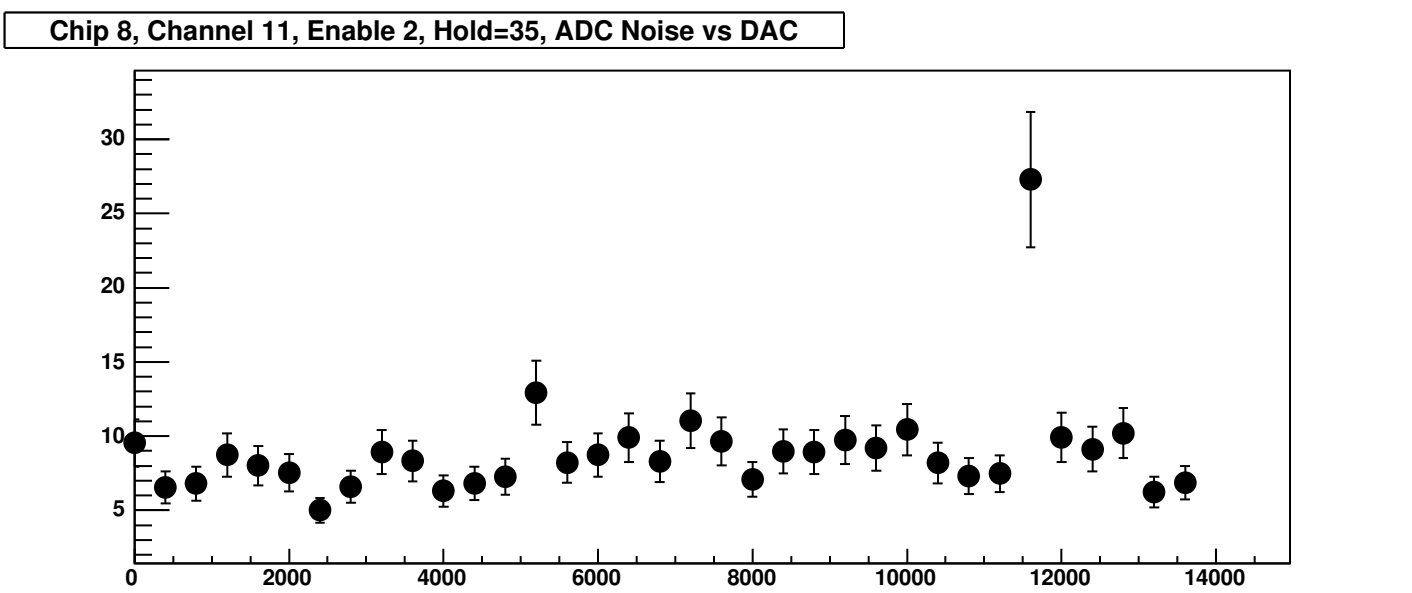

Chip 8, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

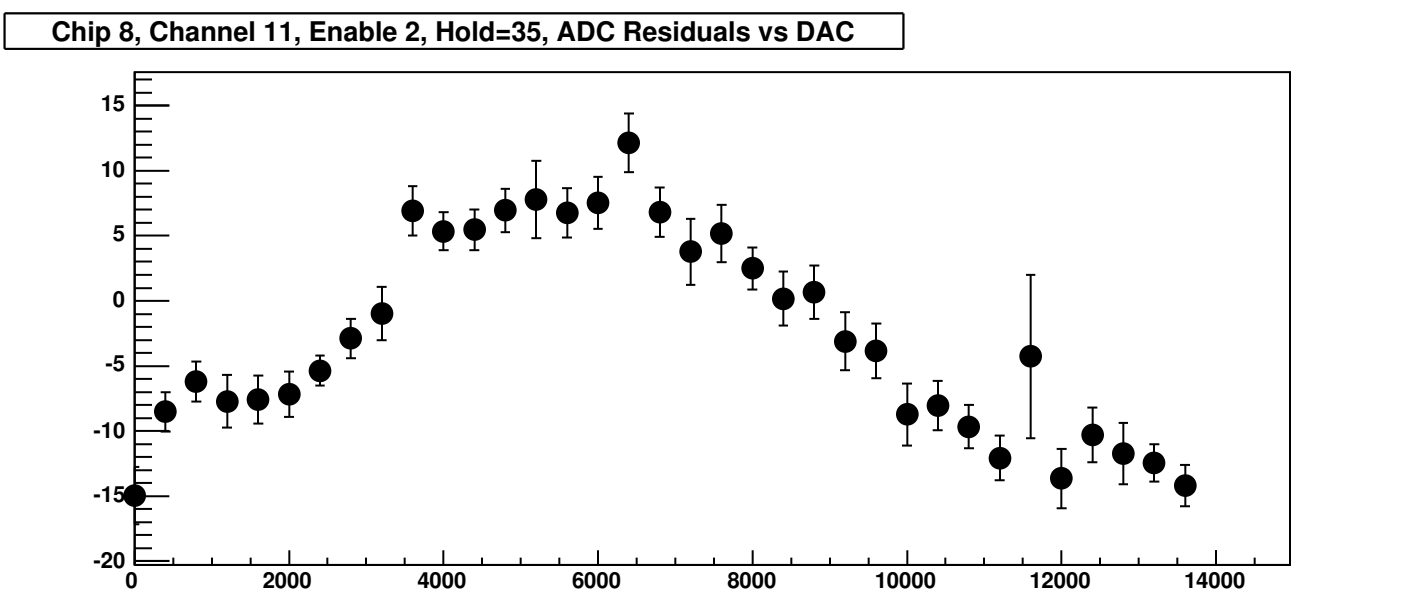

Chip 8, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.57 / 23
p0 152.5 ± 0.7287
p1 0.02021 ± 0.0001055
450
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

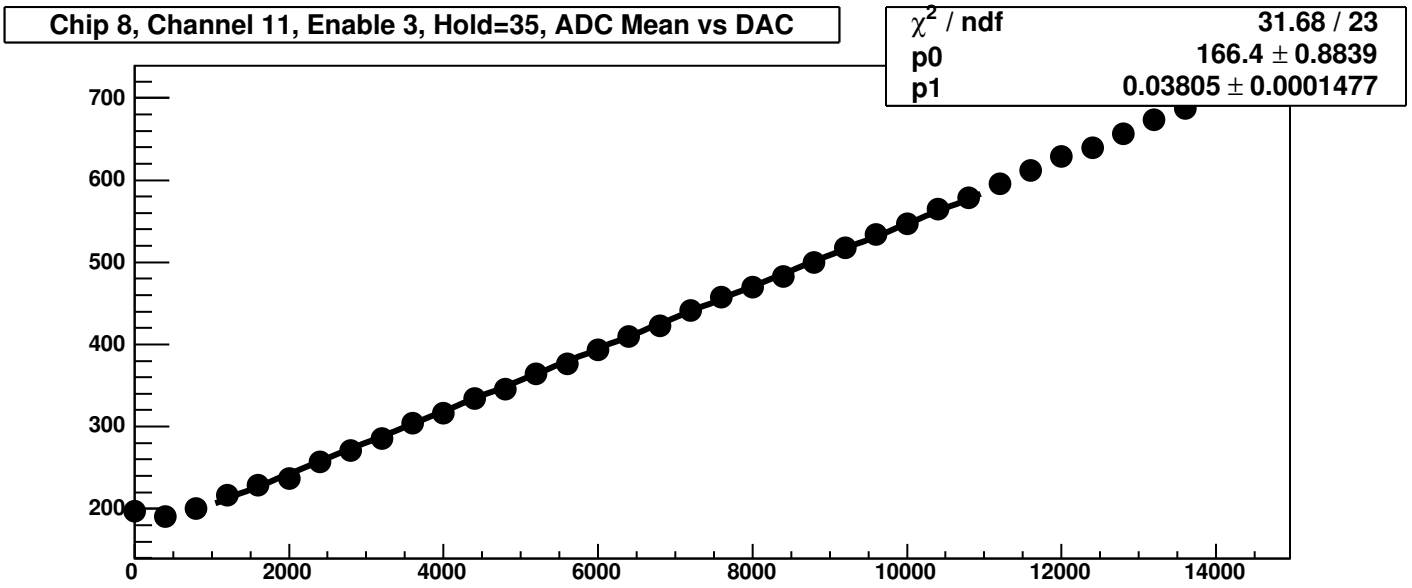

Chip 8, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

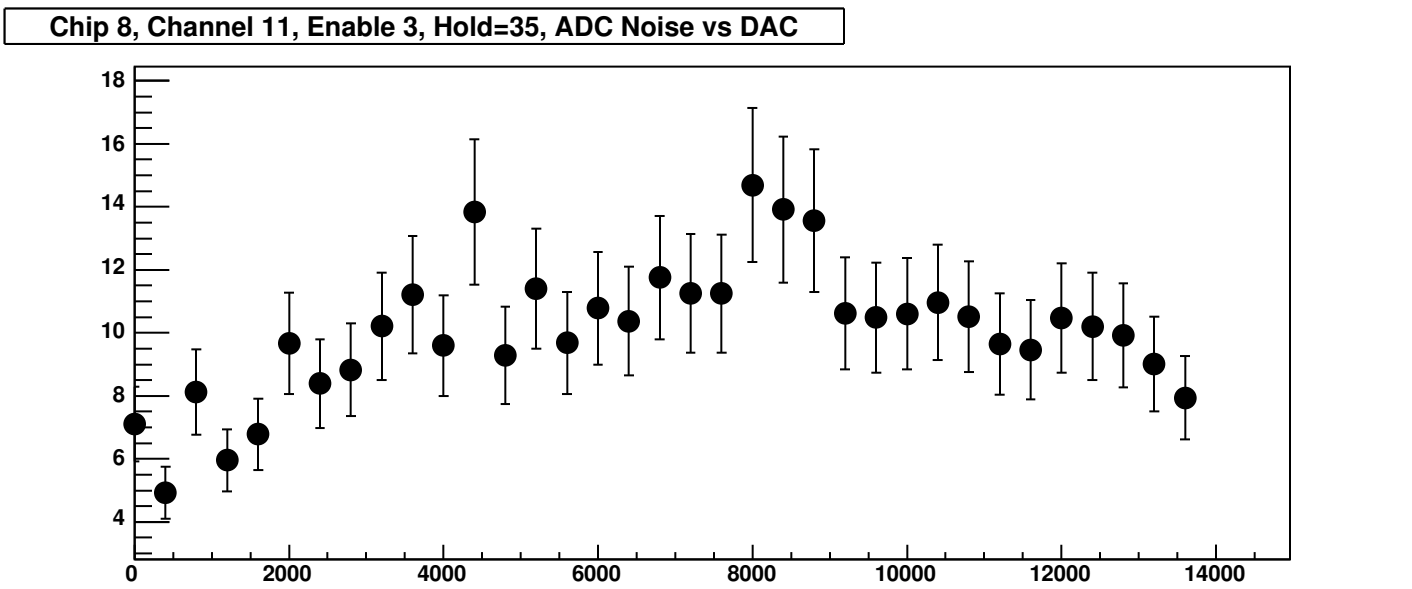

Chip 8, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

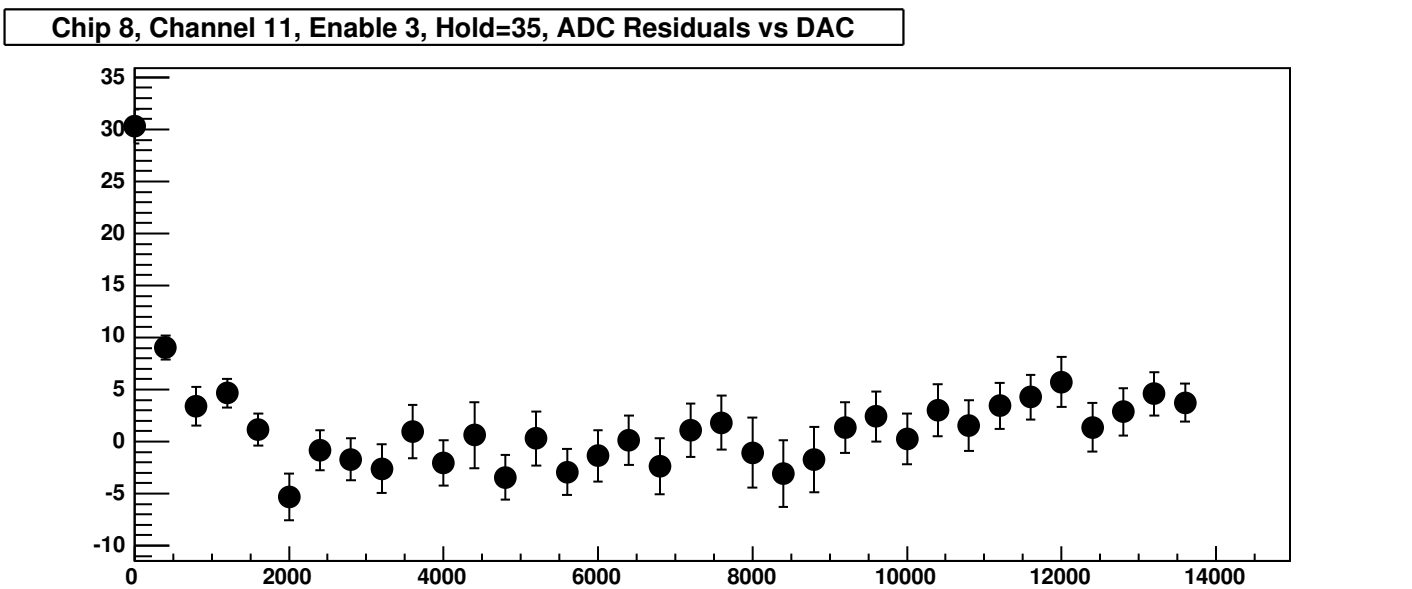

Chip 8, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 286.9 / 23
p0 170.8 ± 0.7825
p1 -0.01357 ± 0.0001254
160
140
120
100
80
60
40
20
0
-20
-40
0
2000
4000
6000
8000
10000
12000
14000

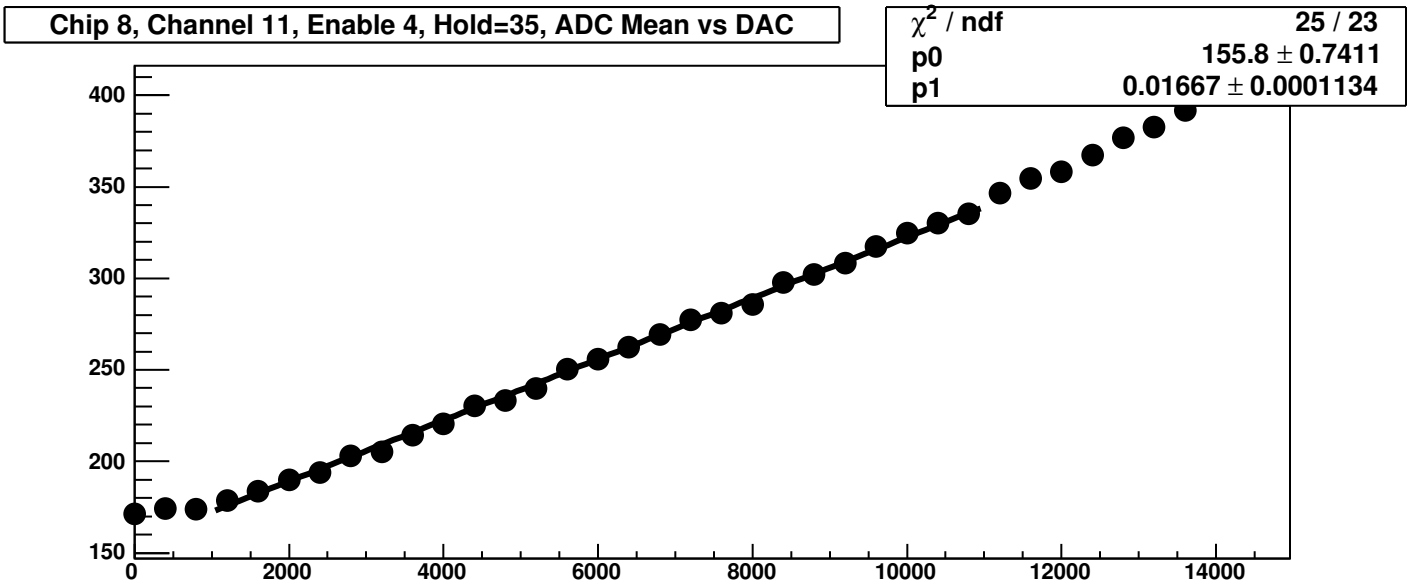

Chip 8, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

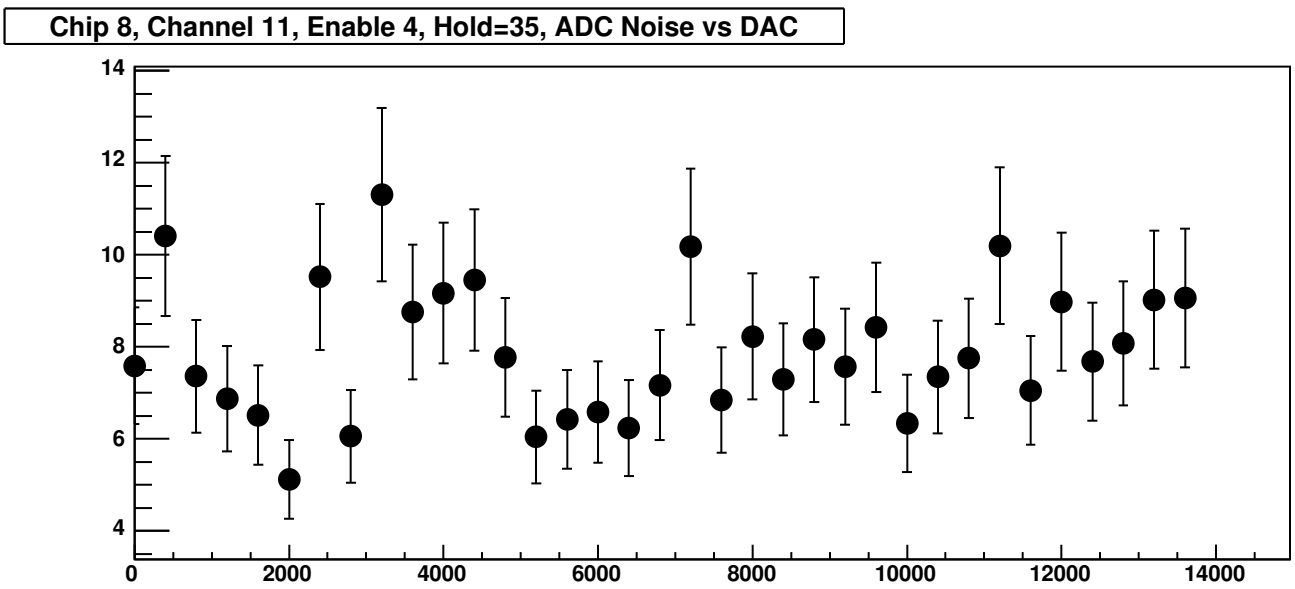

Chip 8, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

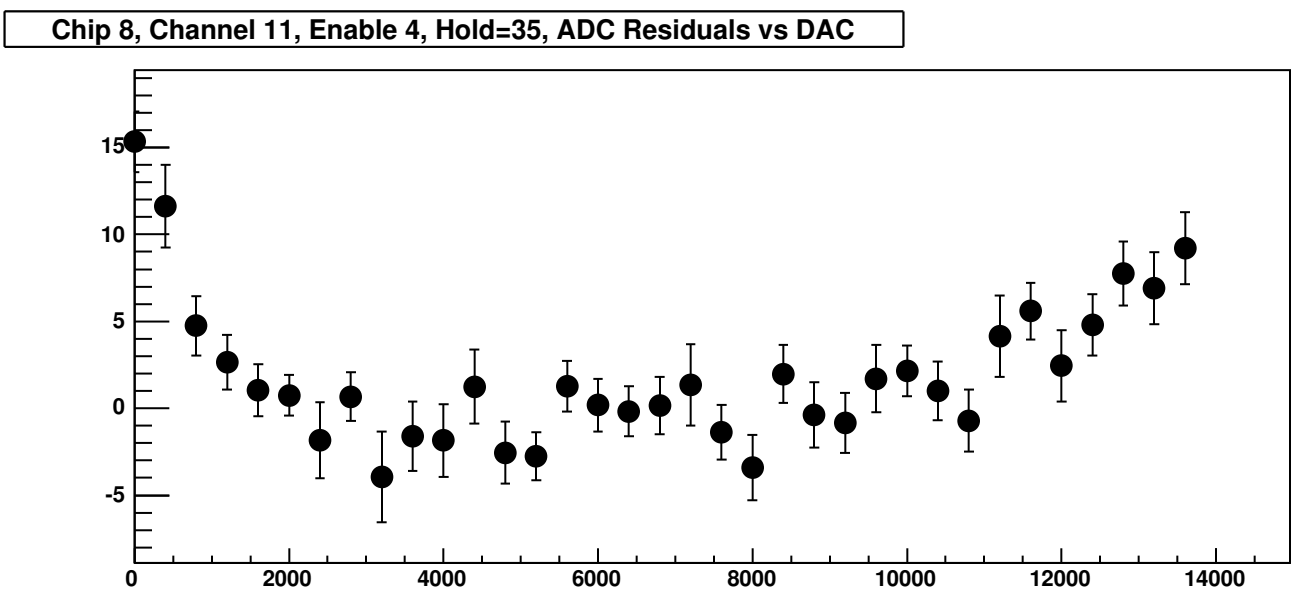

Chip 8, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.68 / 23
p0 166.4 ± 0.8839
p1 0.03805 ± 0.0001477
700
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000

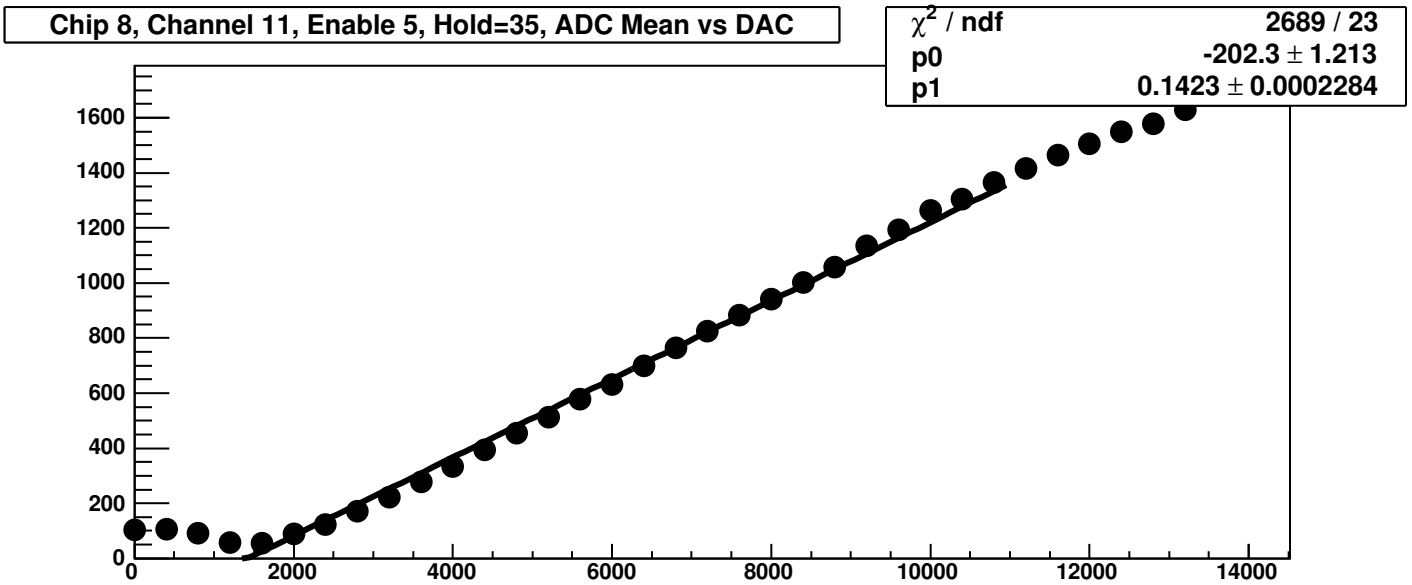

Chip 8, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

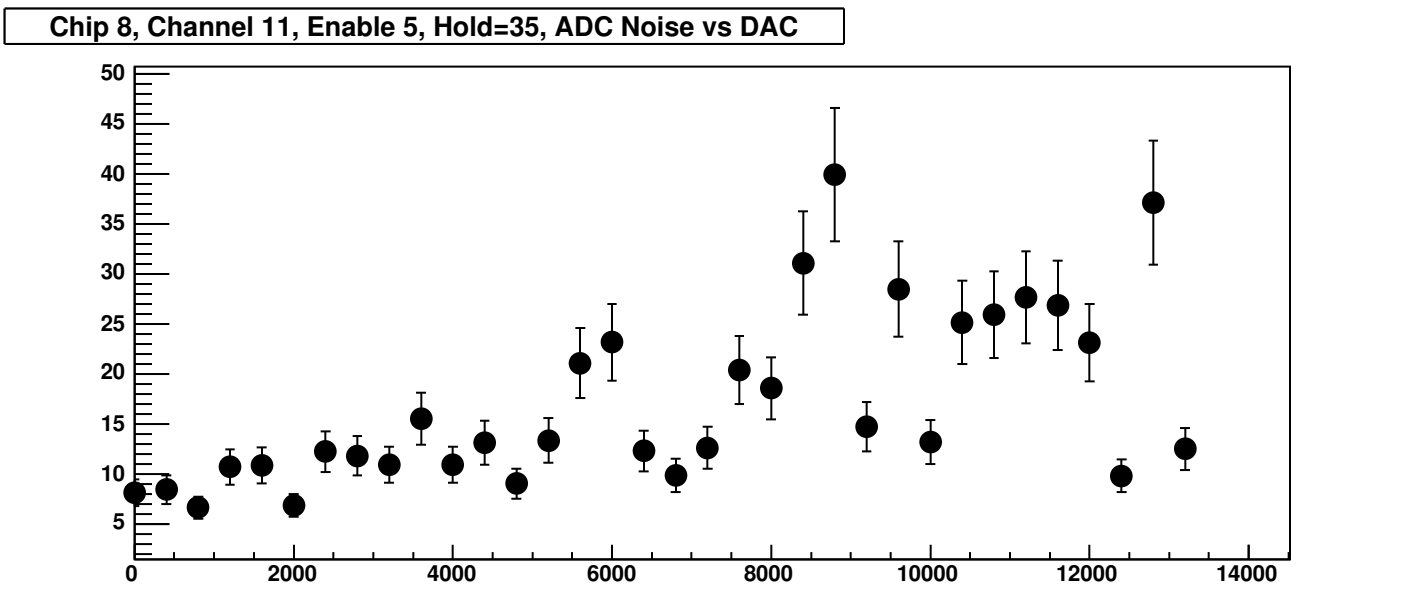

Chip 8, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

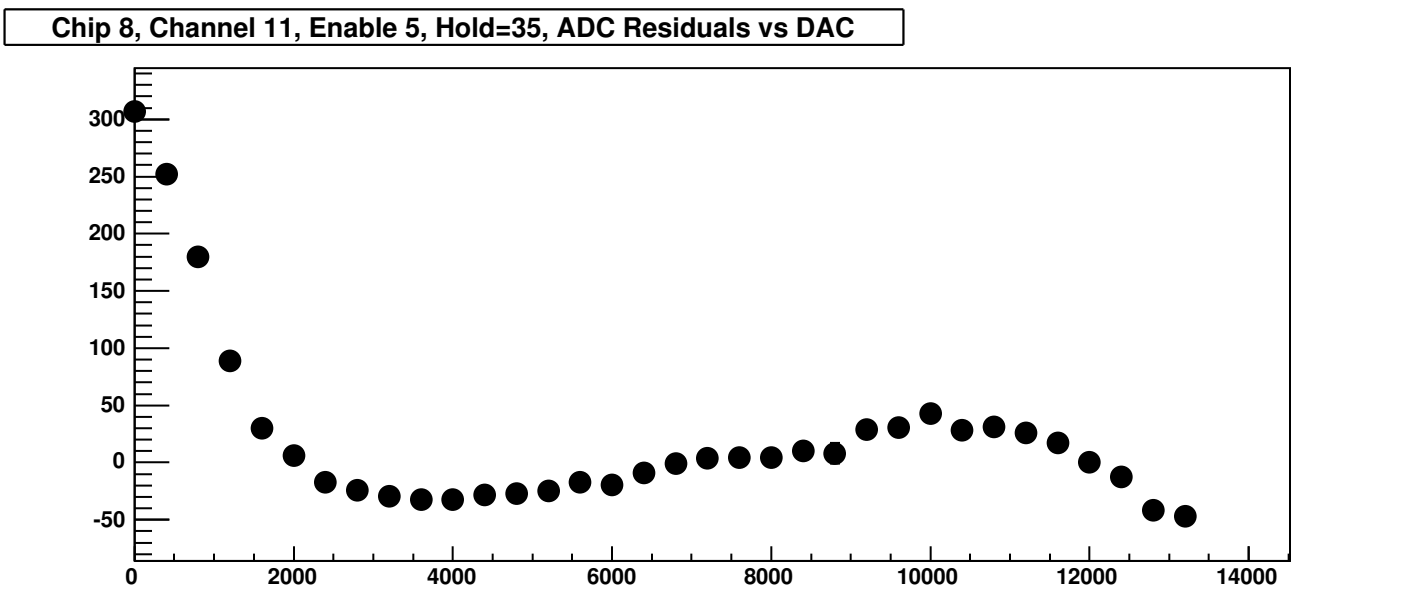

Chip 8, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 25 / 23
p0 155.8 ± 0.7411
p1 0.01667 ± 0.0001134
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

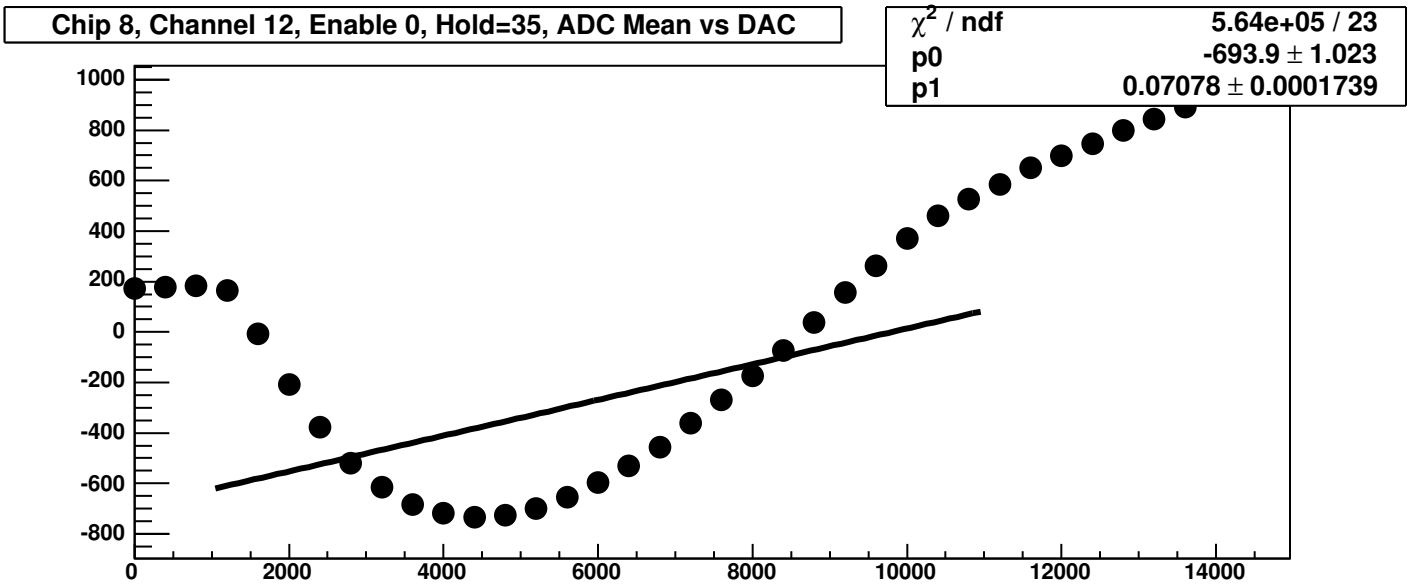

Chip 8, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

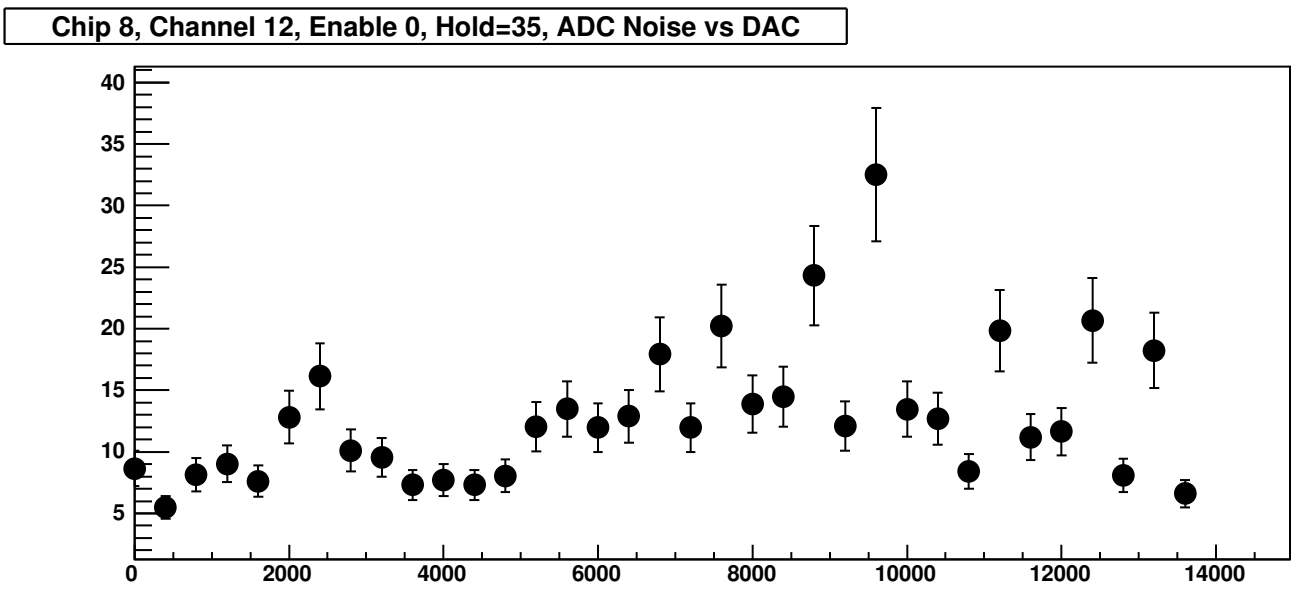

Chip 8, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

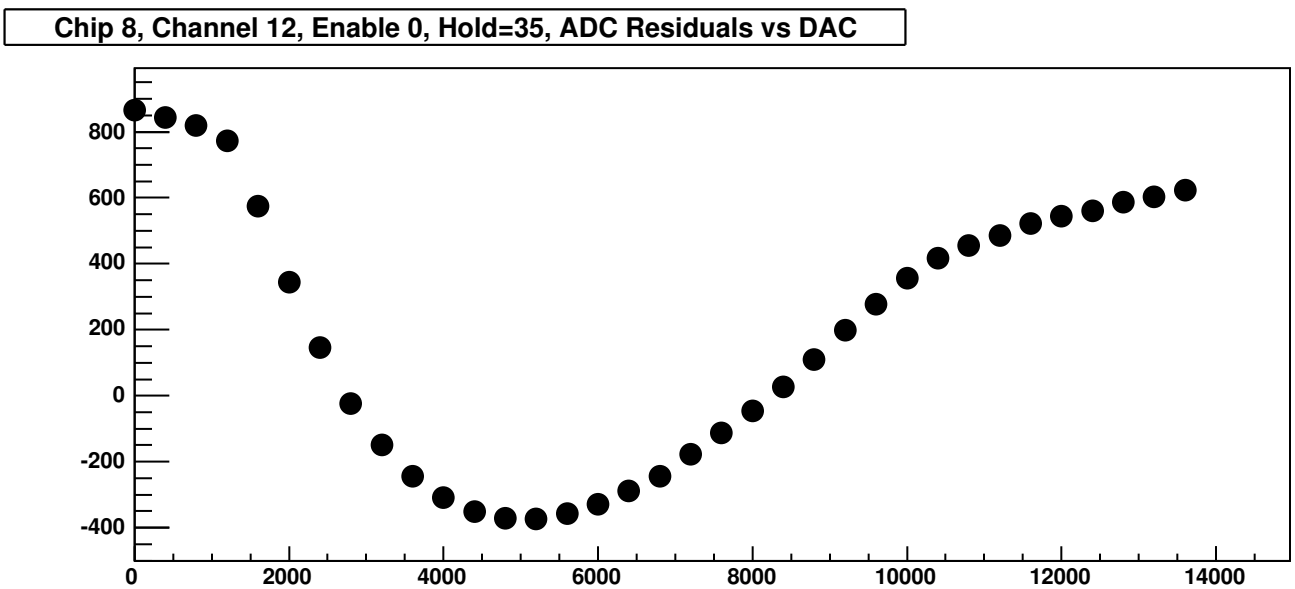

Chip 8, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2689 / 23
p0 -202.3 ± 1.213
p1 0.1423 ± 0.0002284
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

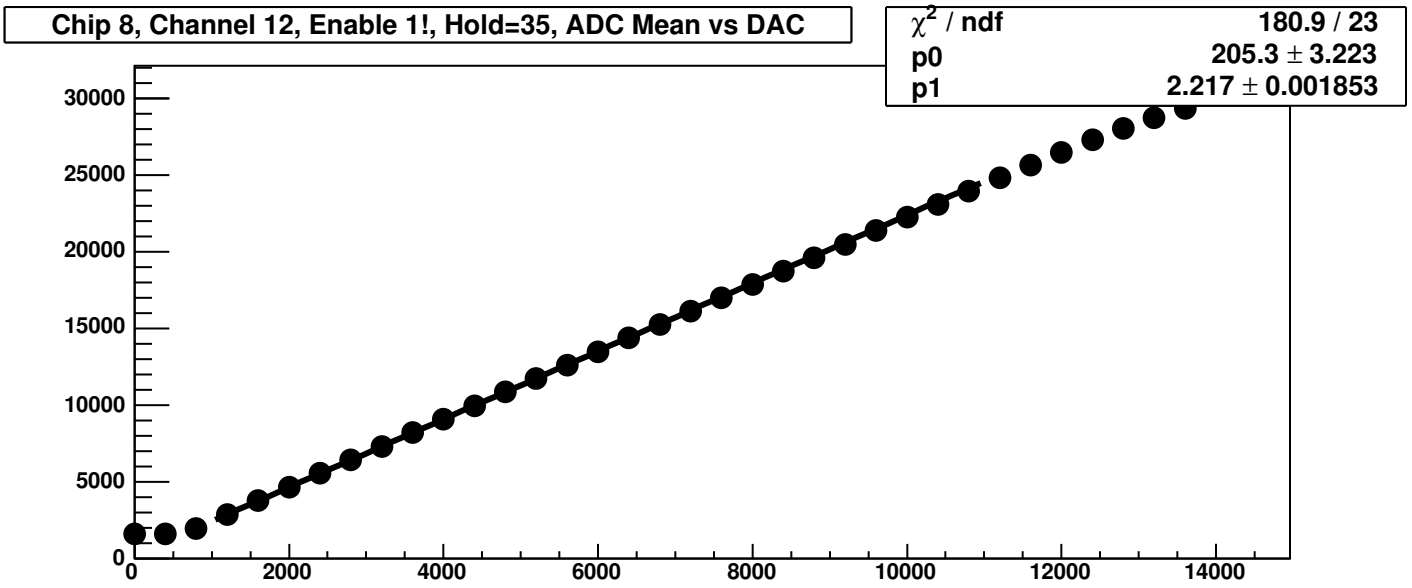

Chip 8, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

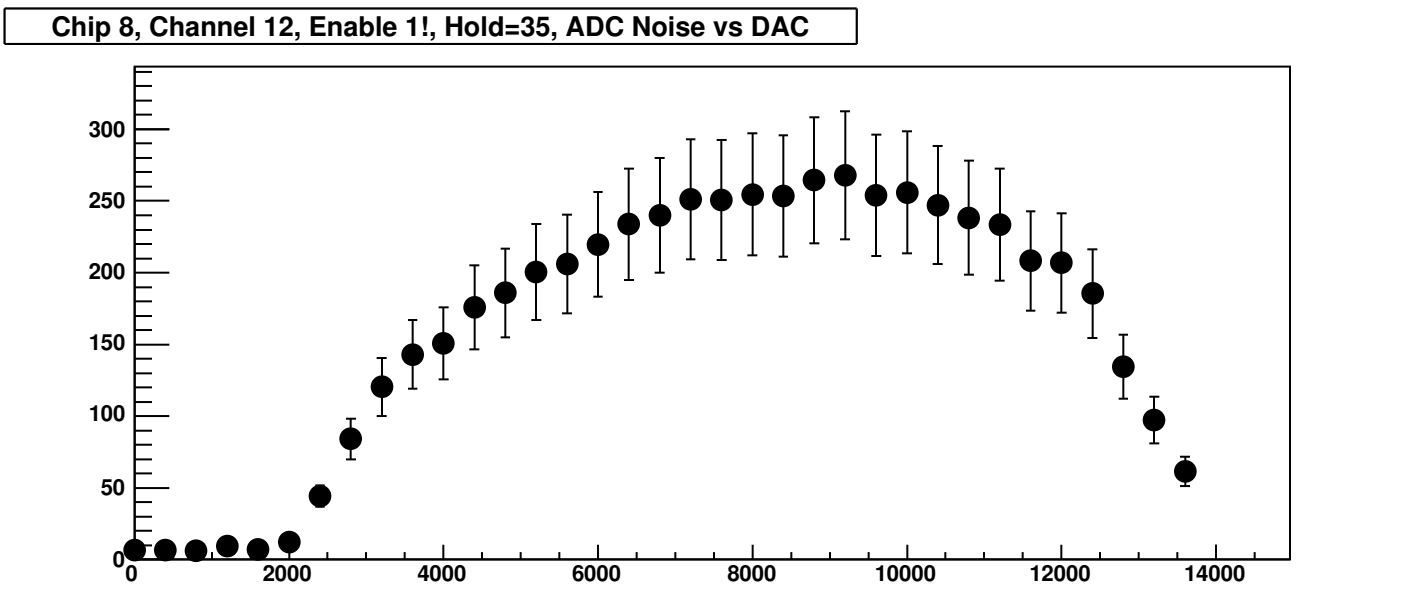

Chip 8, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

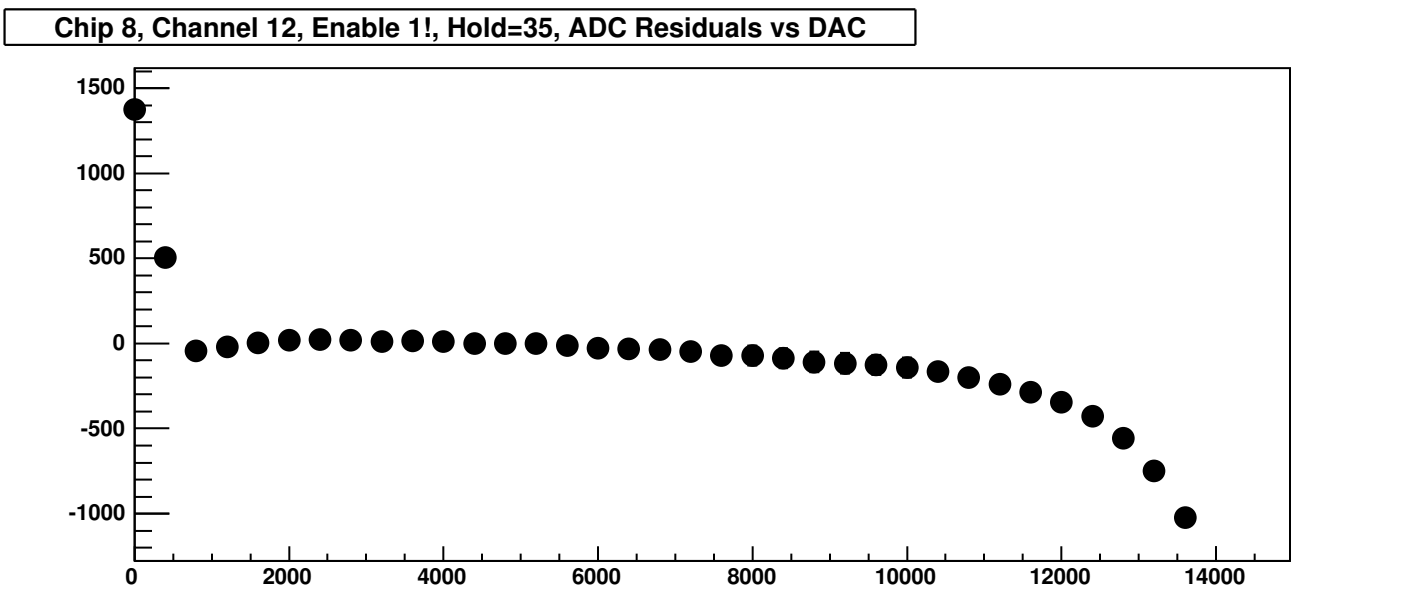

Chip 8, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.64e+05 / 23
p0 -693.9 ± 1.023
p1 0.07078 ± 0.0001739
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

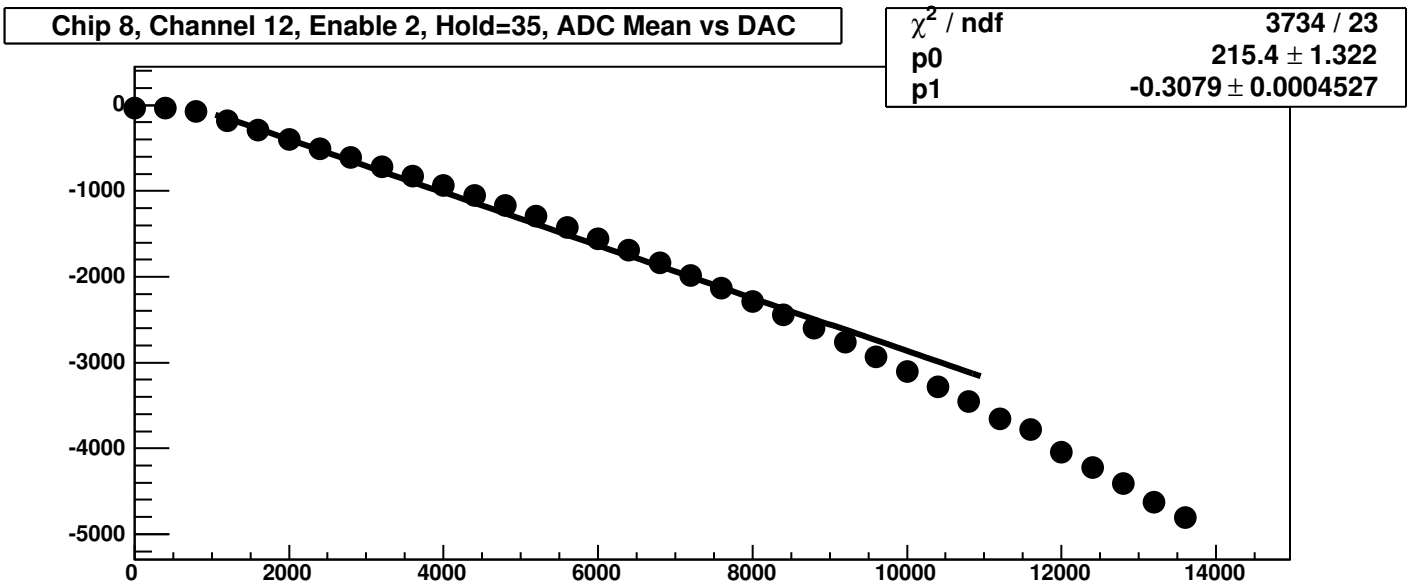

Chip 8, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

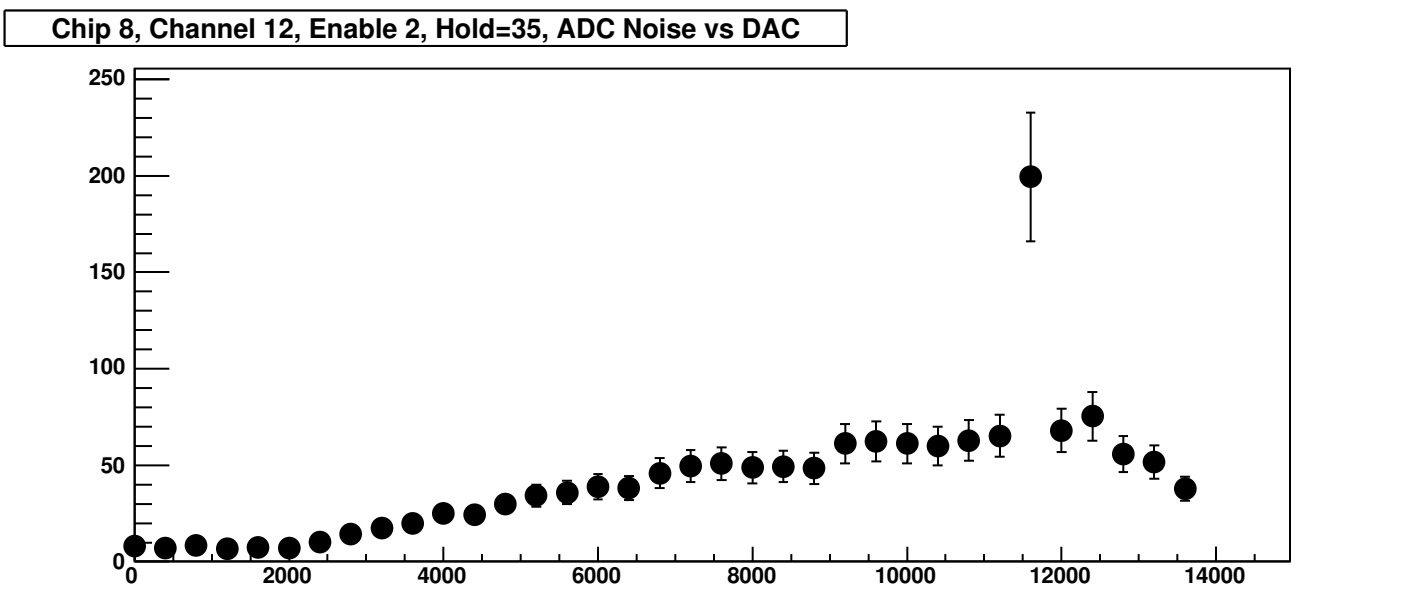

Chip 8, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

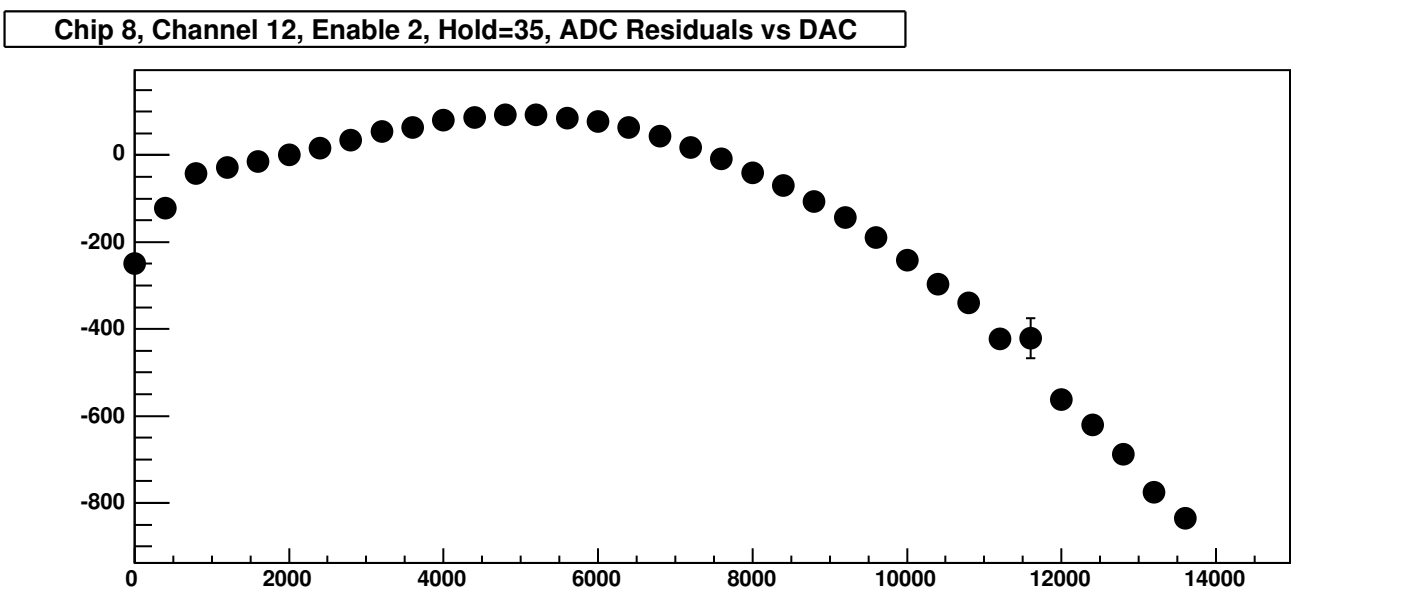

Chip 8, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 180.9 / 23
p0 205.3 ± 3.223
p1 2.217 ± 0.001853
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

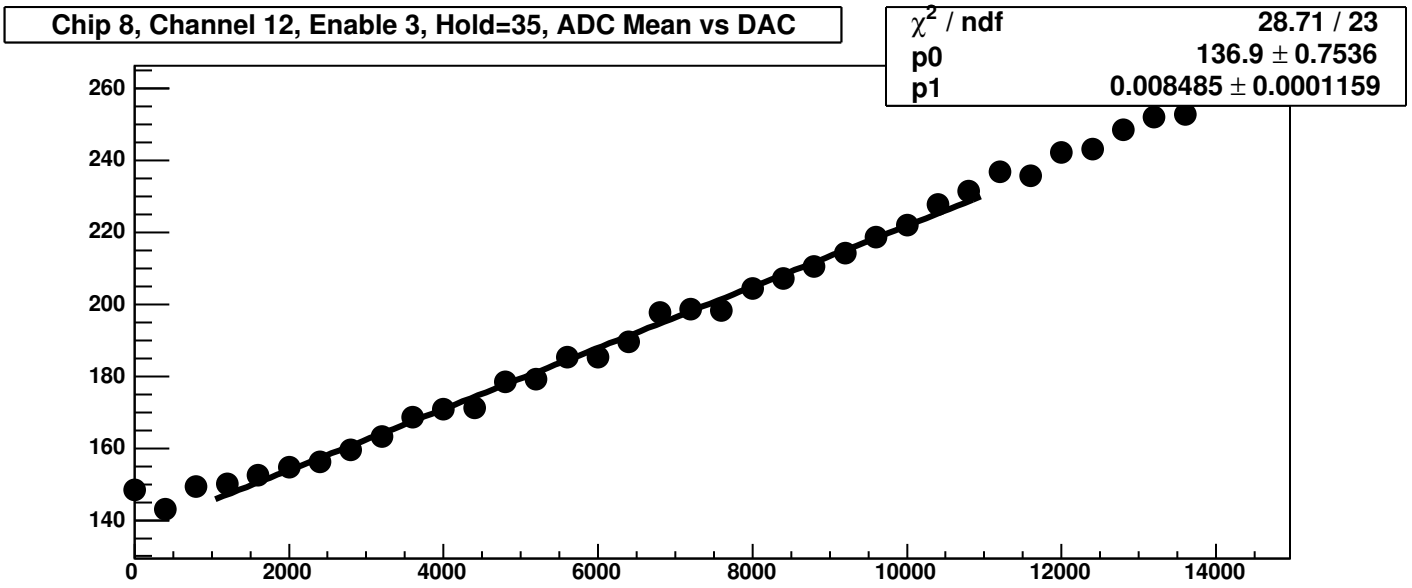

Chip 8, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

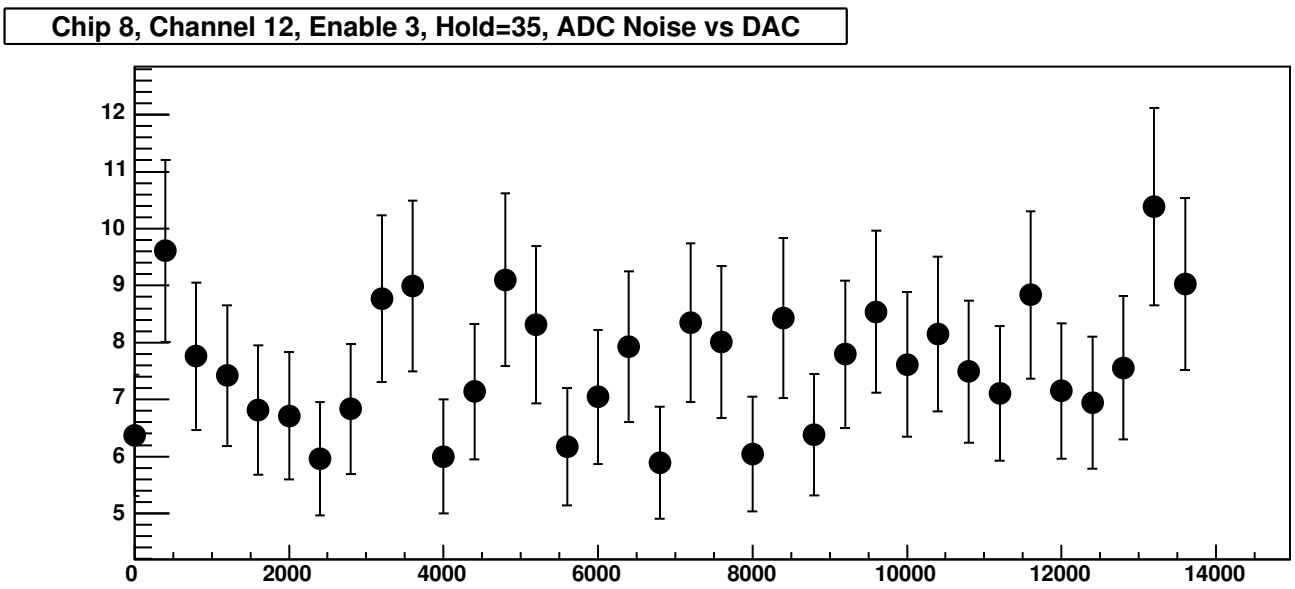

Chip 8, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

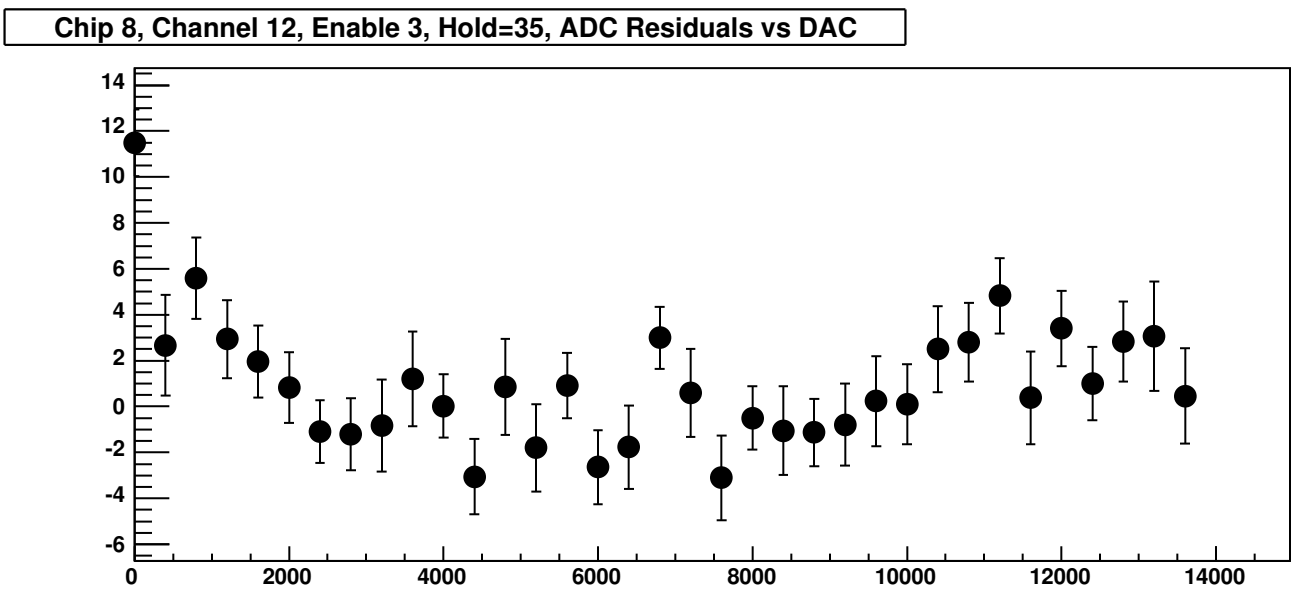

Chip 8, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 3734 / 23
p0 215.4 ± 1.322
p1 -0.3079 ± 0.0004527
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000

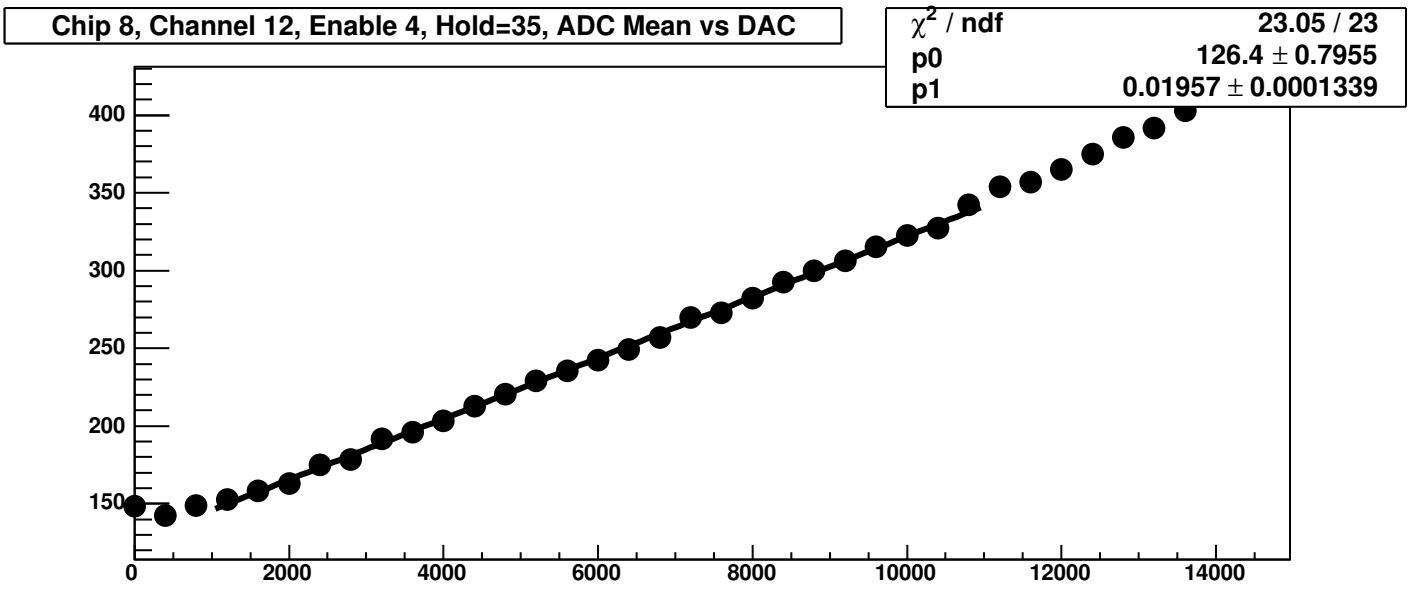

Chip 8, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

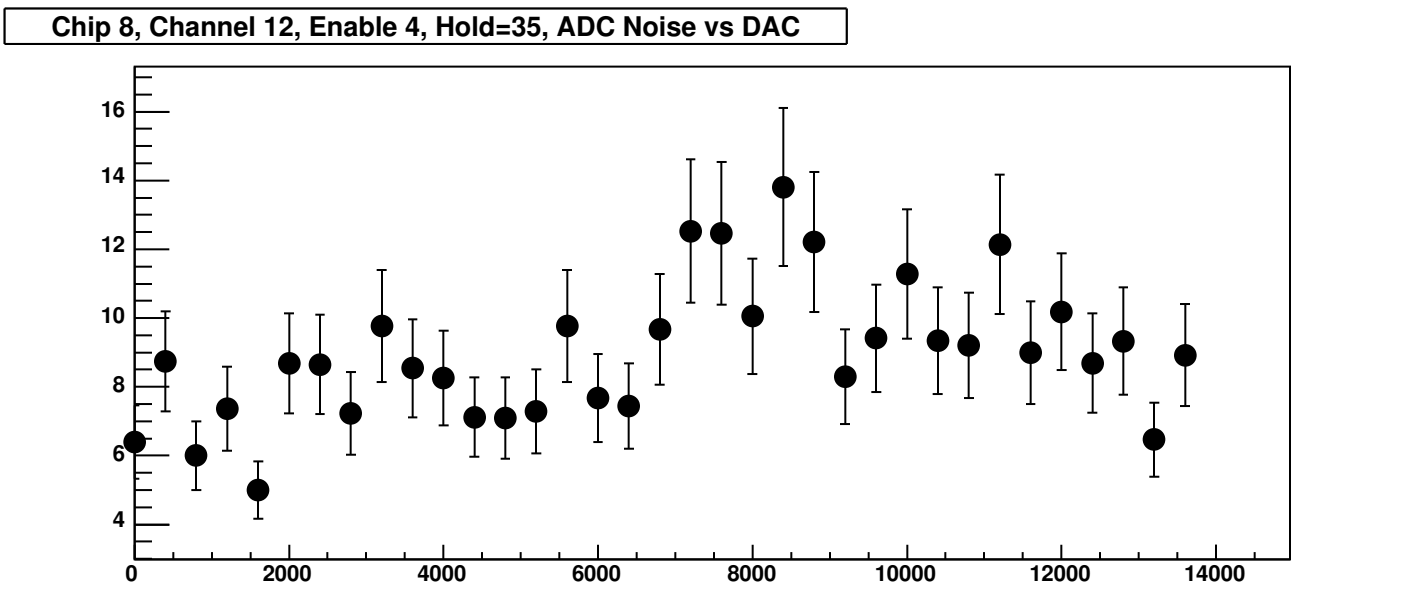

Chip 8, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

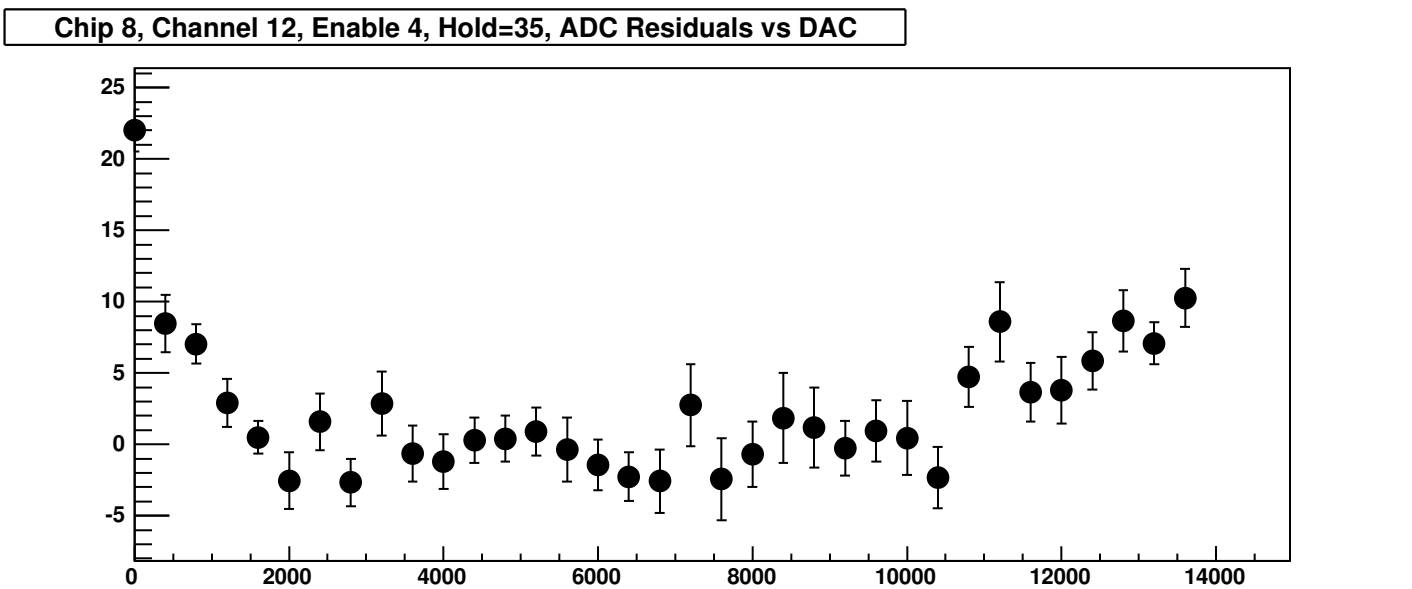

Chip 8, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.71 / 23
p0 136.9 ± 0.7536
p1 0.008485 ± 0.0001159
260
240
220
200
180
160
140
0
2000
4000
6000
8000
10000
12000
14000

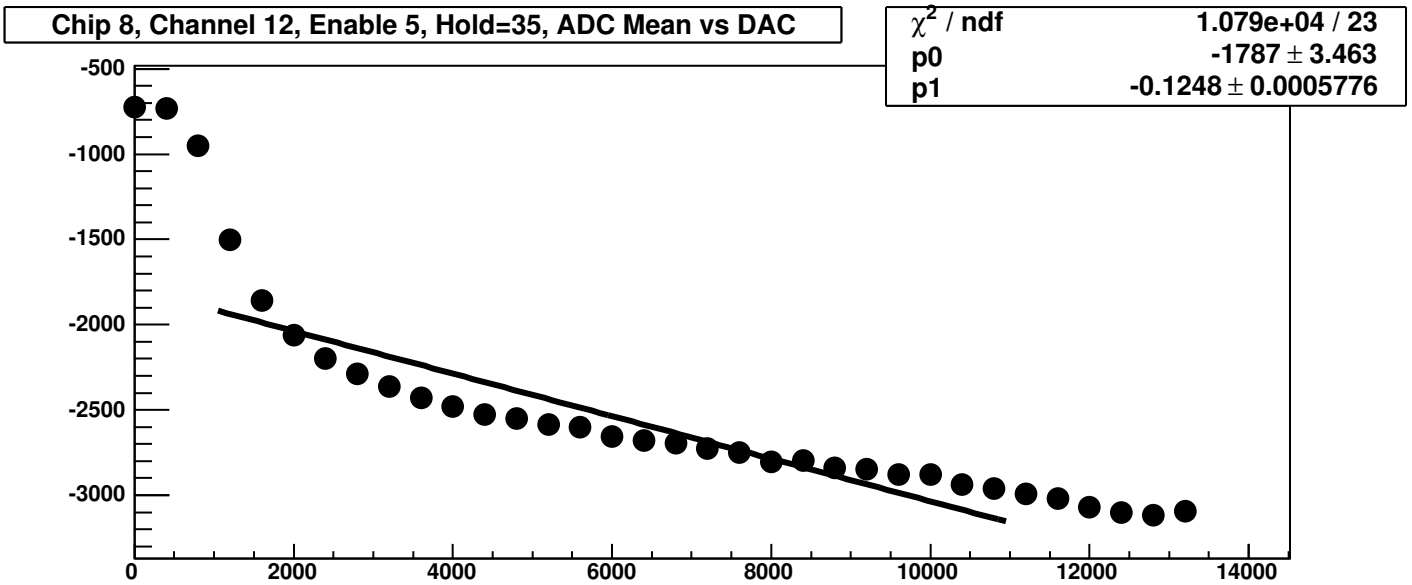

Chip 8, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

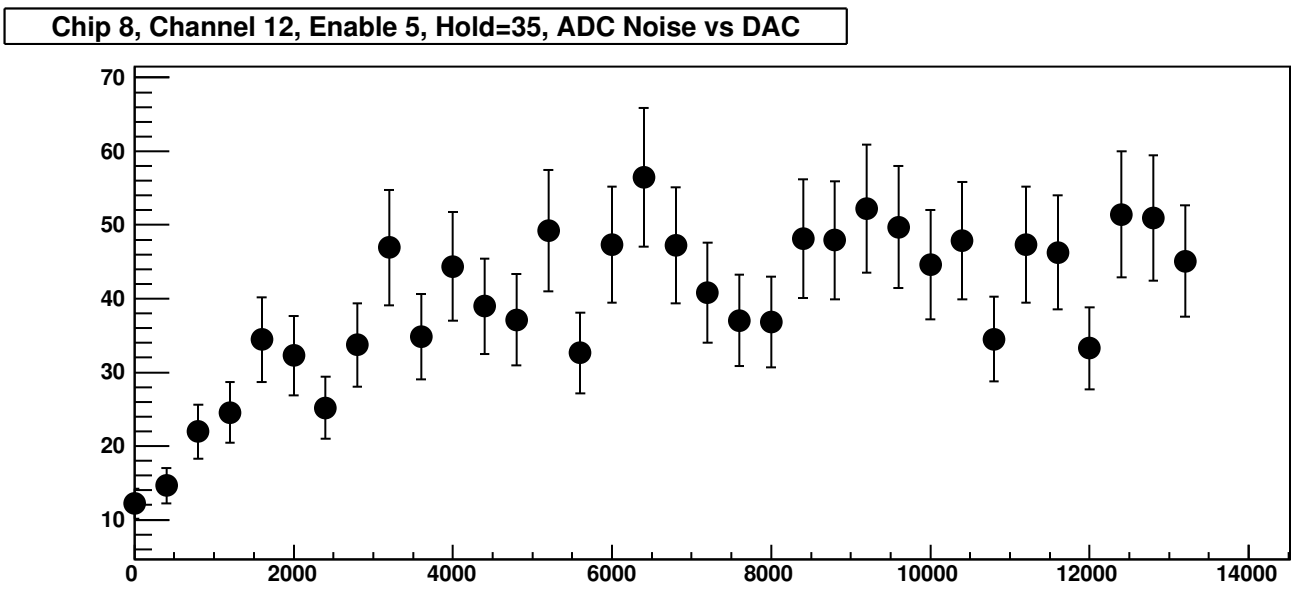

Chip 8, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000

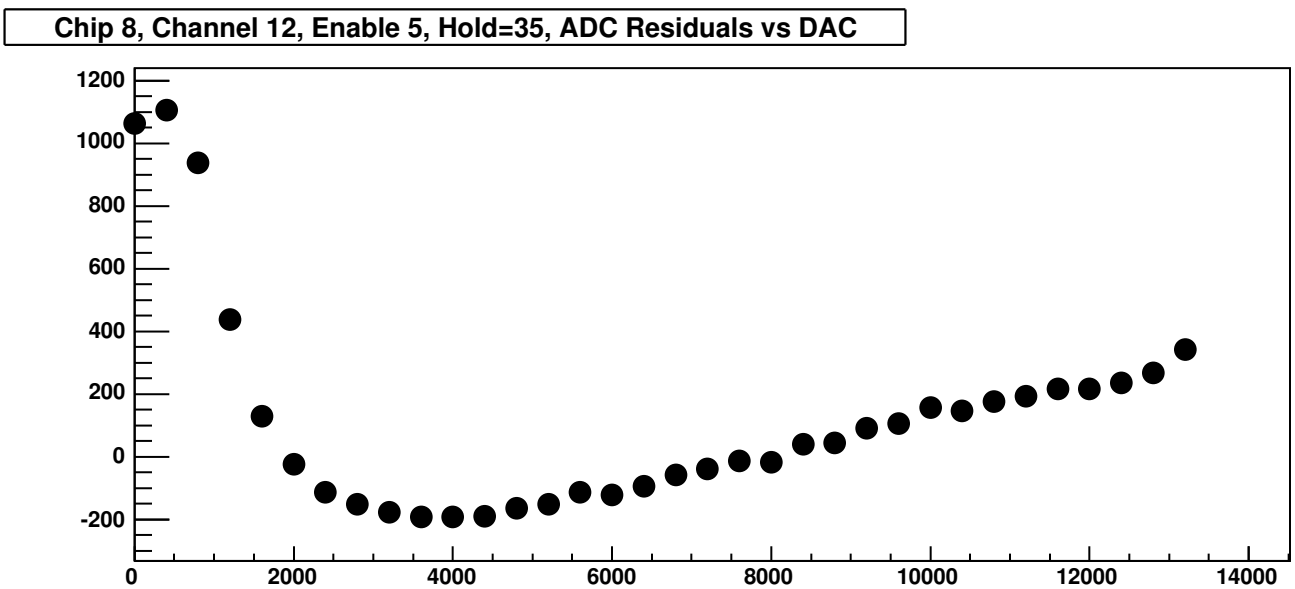

Chip 8, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 23.05 / 23
p0 126.4 ± 0.7955
p1 0.01957 ± 0.0001339
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

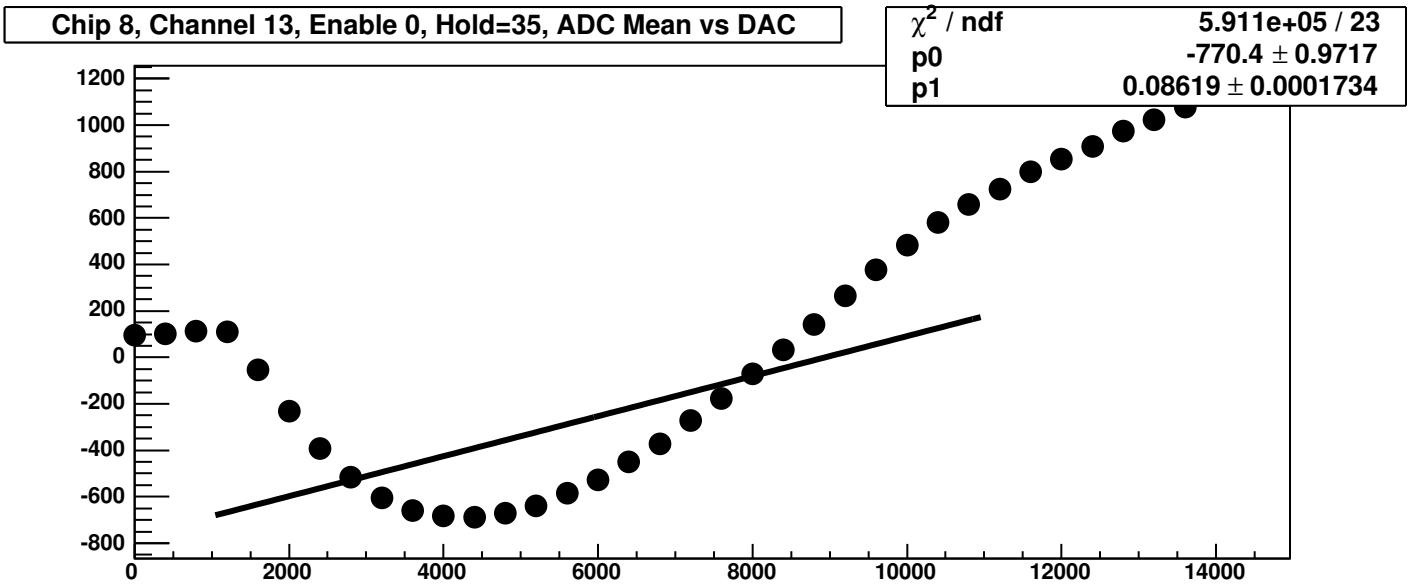

Chip 8, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

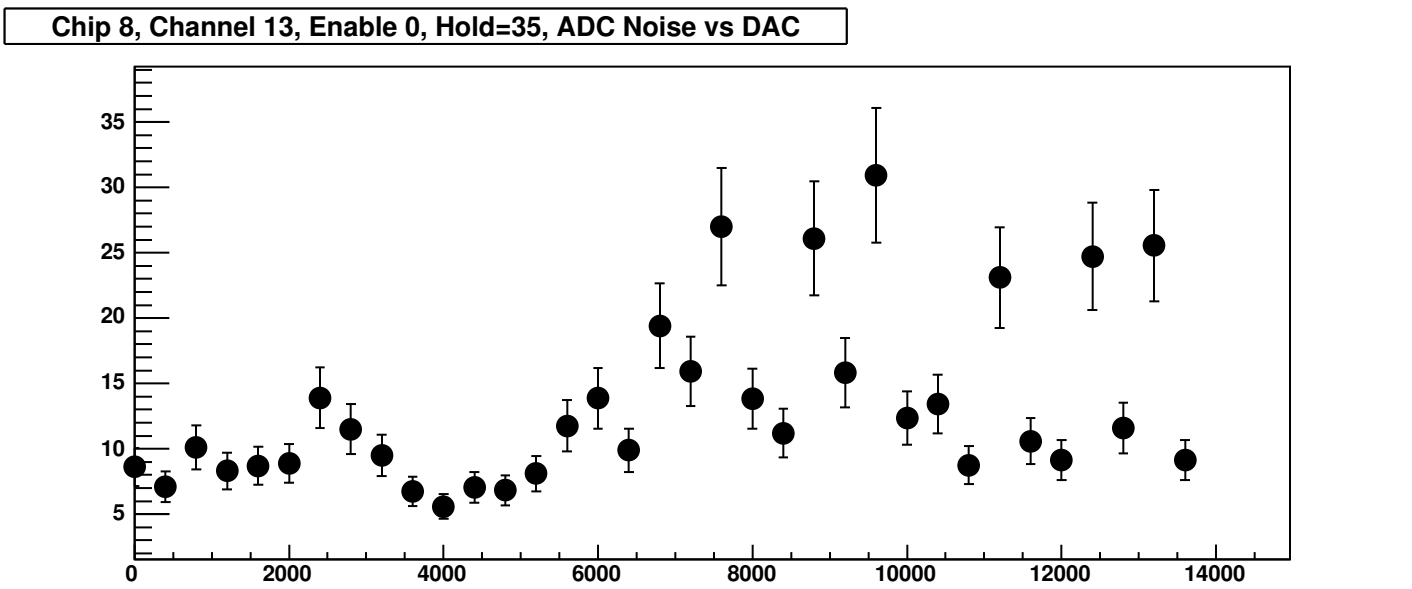

Chip 8, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

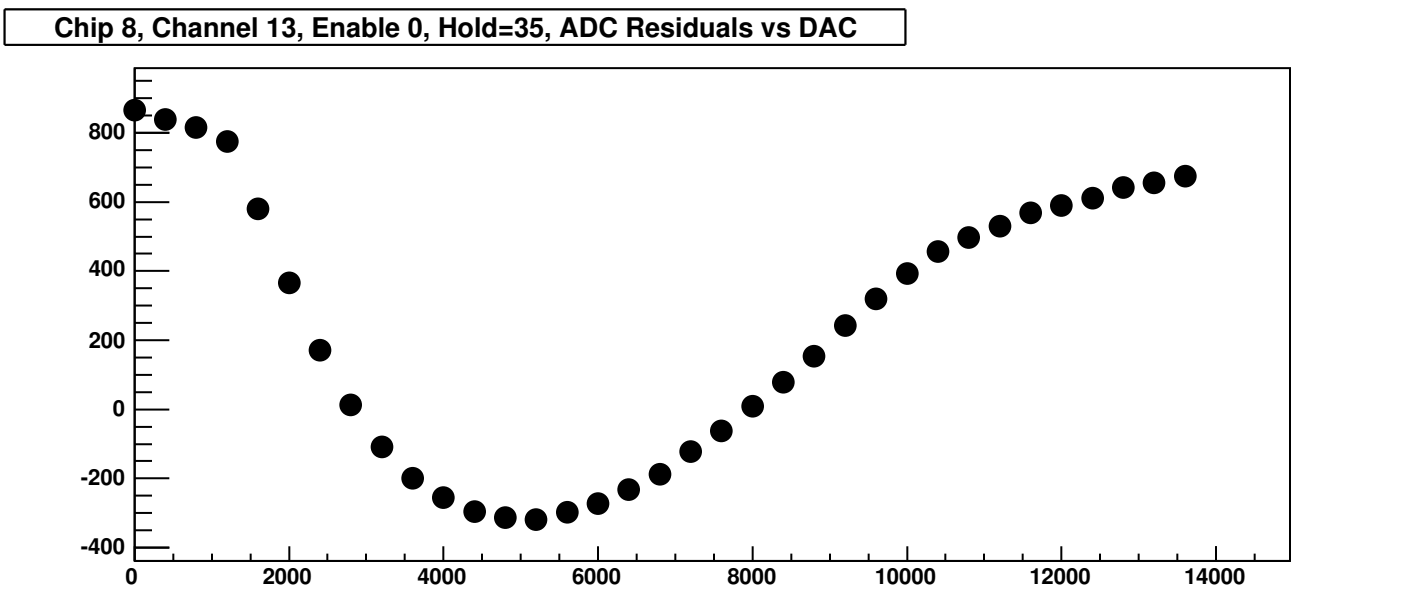

Chip 8, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.079e+04 / 23
p0 -1787 ± 3.463
p1 -0.1248 ± 0.0005776
-500
-1000
-1500
-2000
-2500
-3000
0
2000
4000
6000
8000
10000
12000
14000

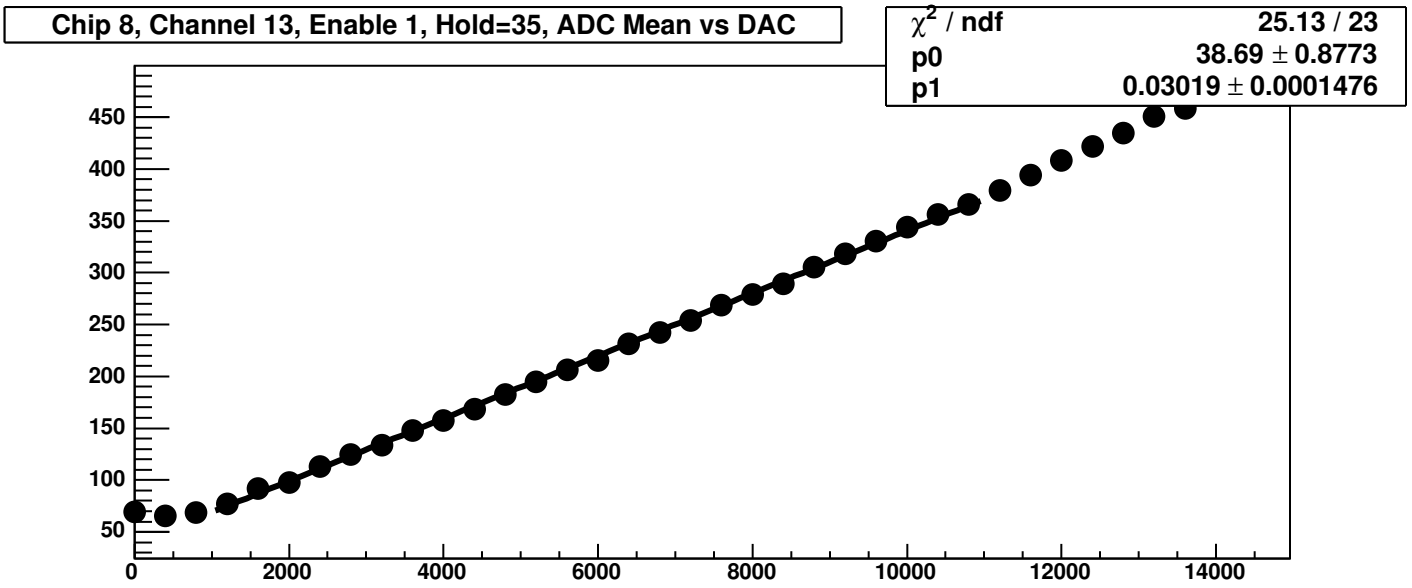

Chip 8, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

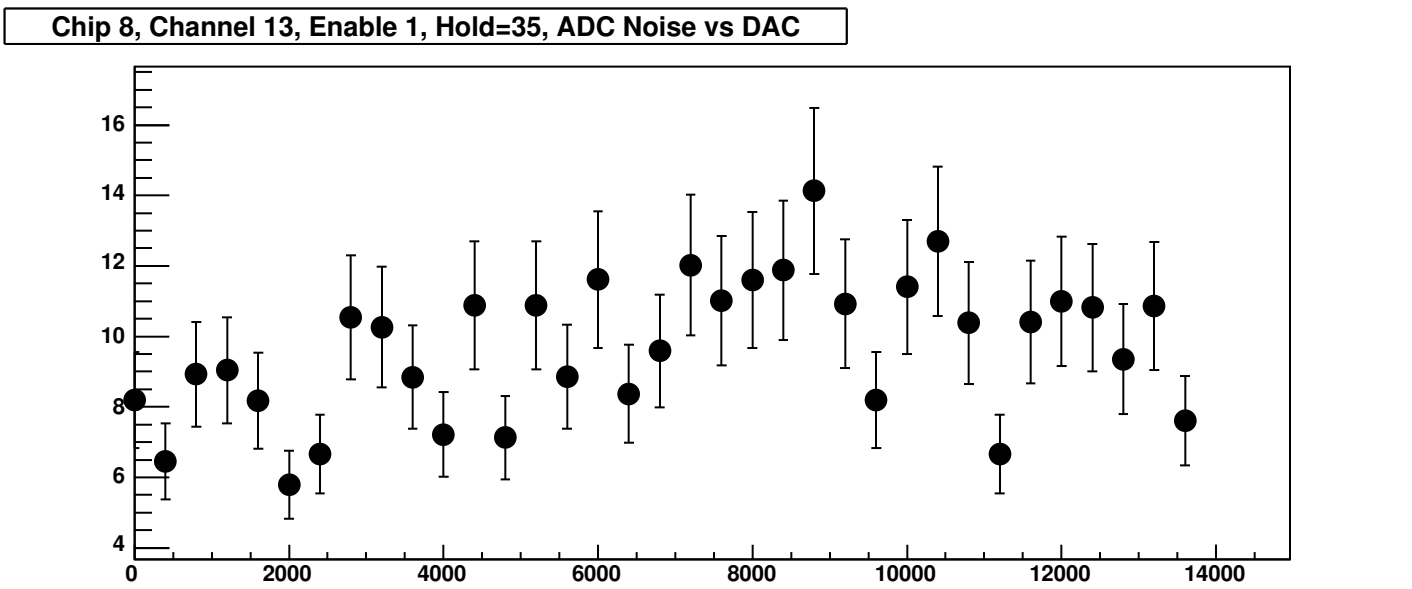

Chip 8, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

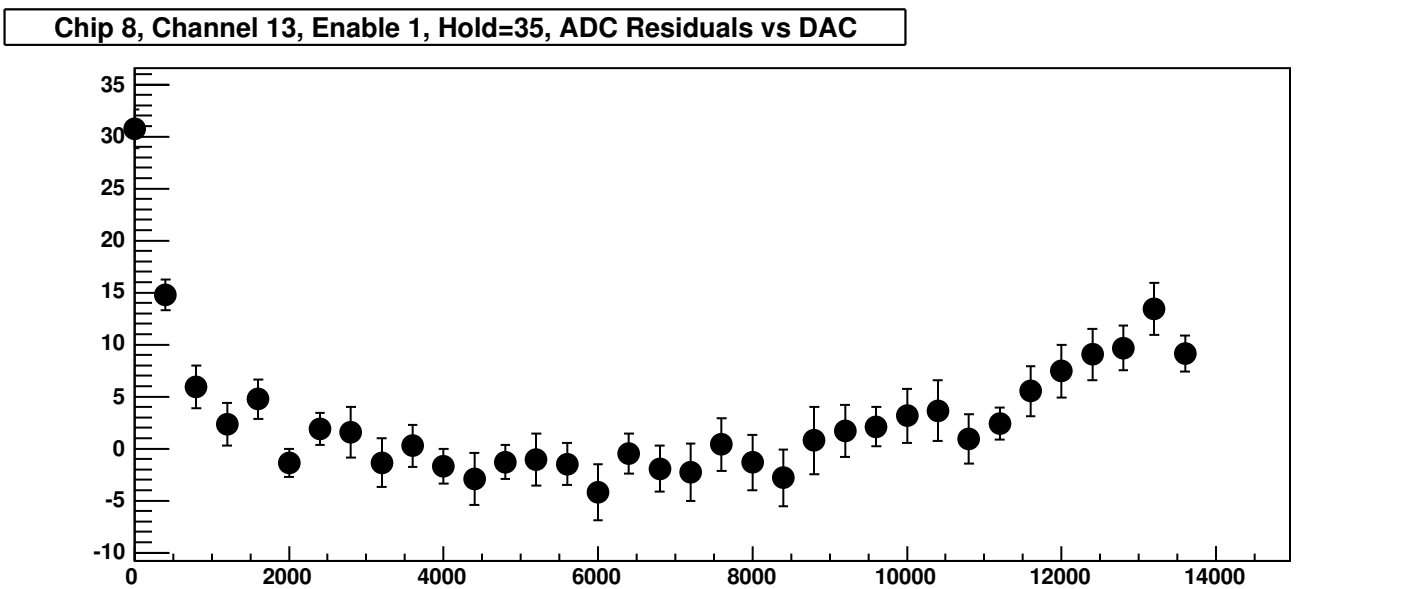

Chip 8, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.911e+05 / 23
p0 -770.4 ± 0.9717
p1 0.08619 ± 0.0001734
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

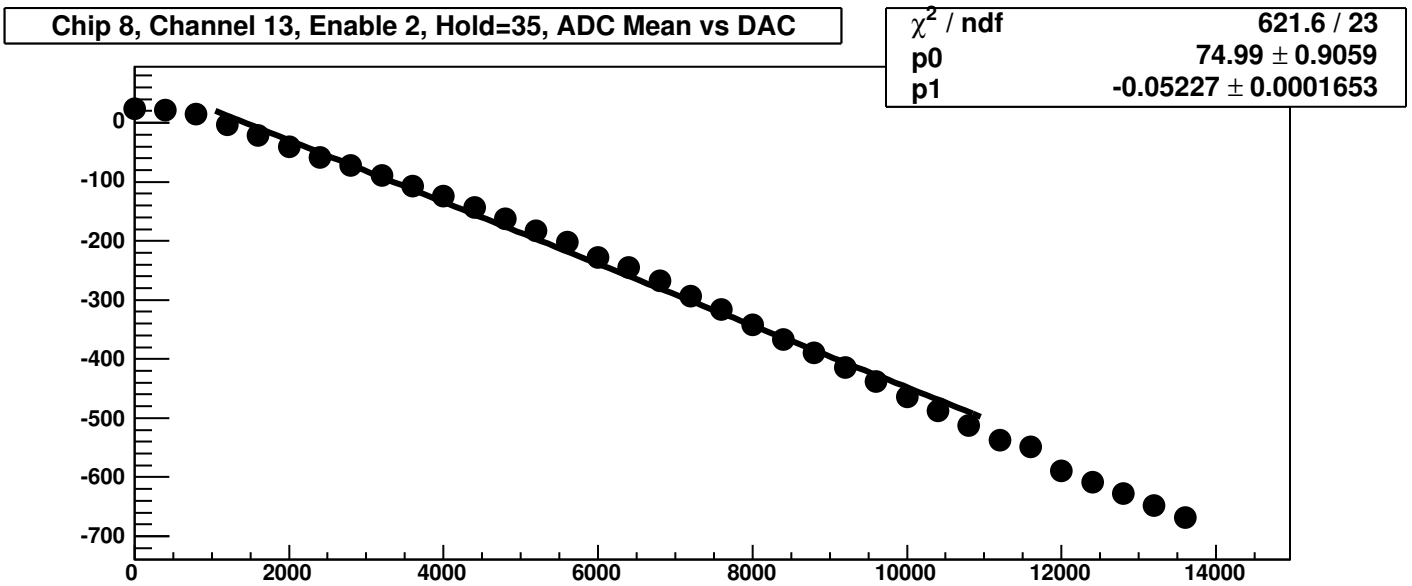

Chip 8, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

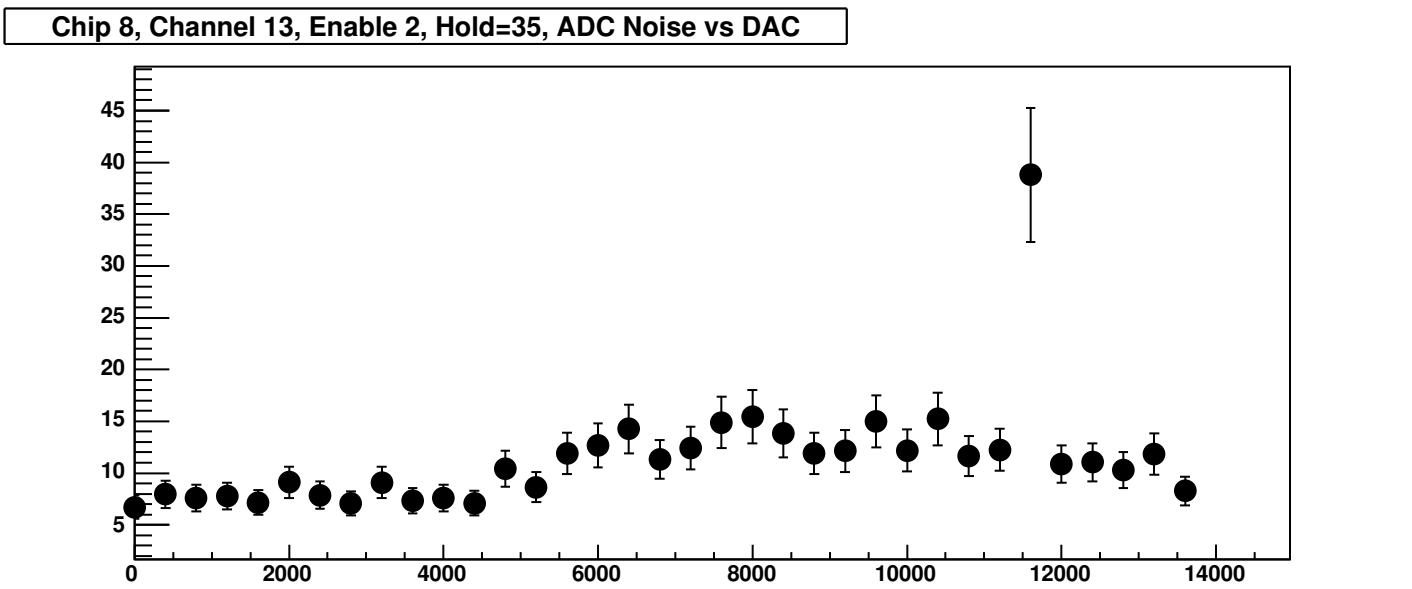

Chip 8, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

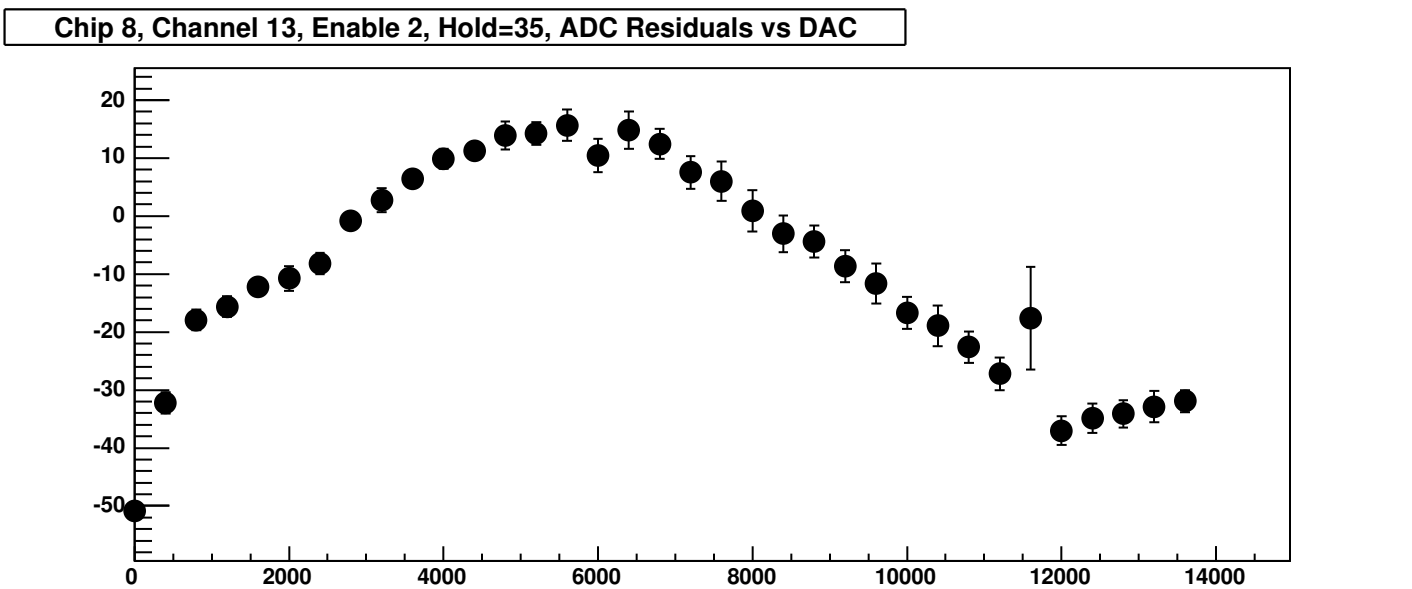

Chip 8, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 25.13 / 23
p0 38.69 ± 0.8773
p1 0.03019 ± 0.0001476
450
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

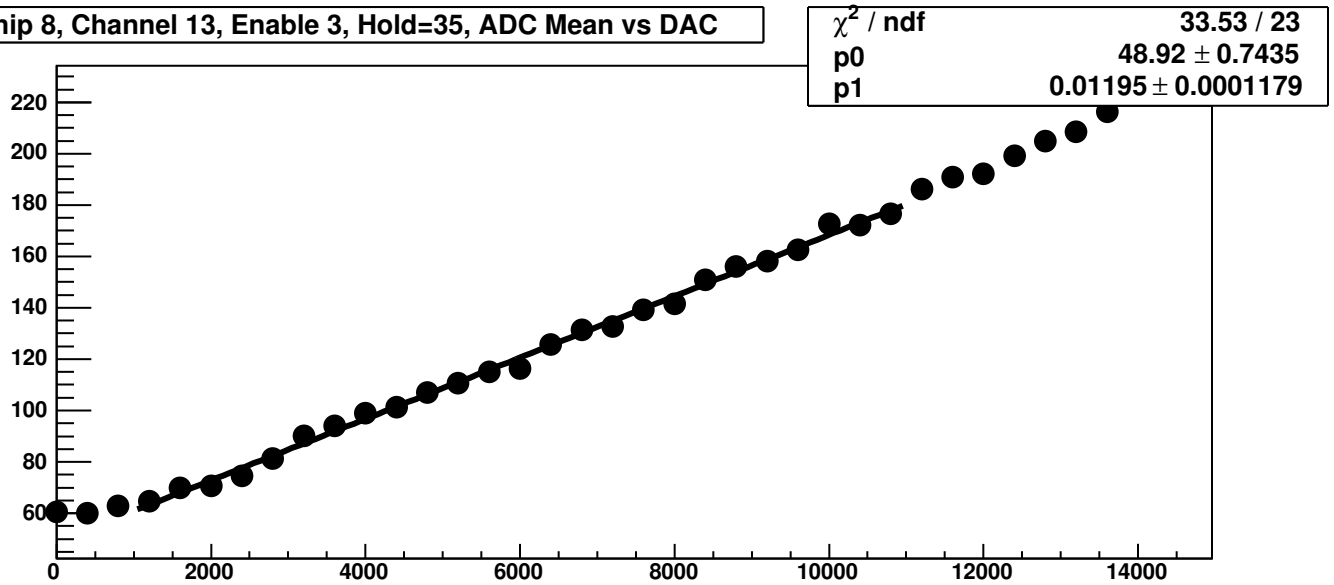

Chip 8, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 621.6 / 23
p0 74.99 ± 0.9059
p1 -0.05227 ± 0.0001653
0
-100
-200
-300
-400
-500
-600
-700
0
2000
4000
6000
8000
10000
12000
14000

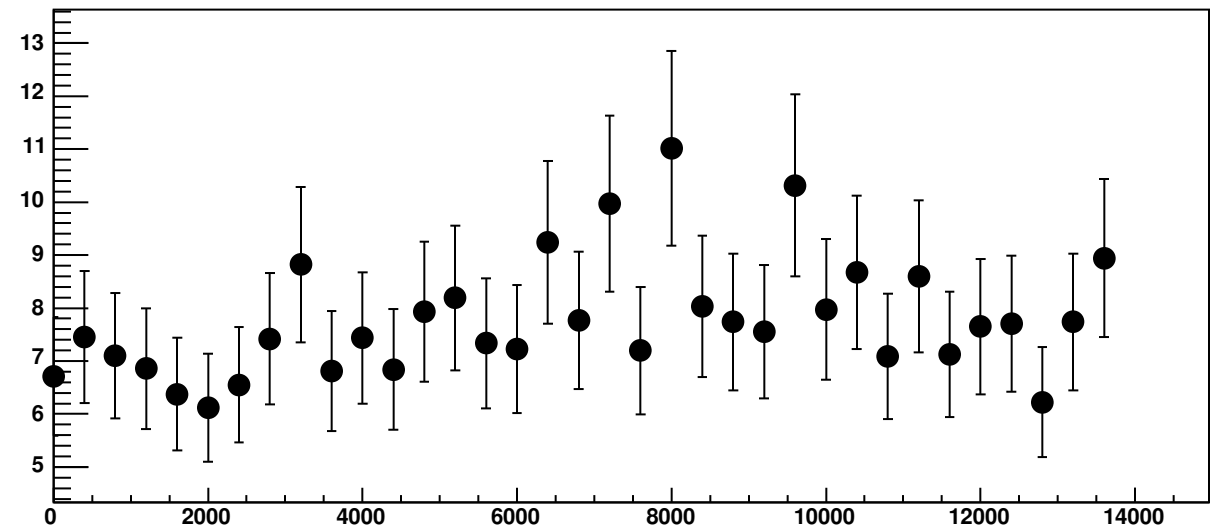

Chip 8, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

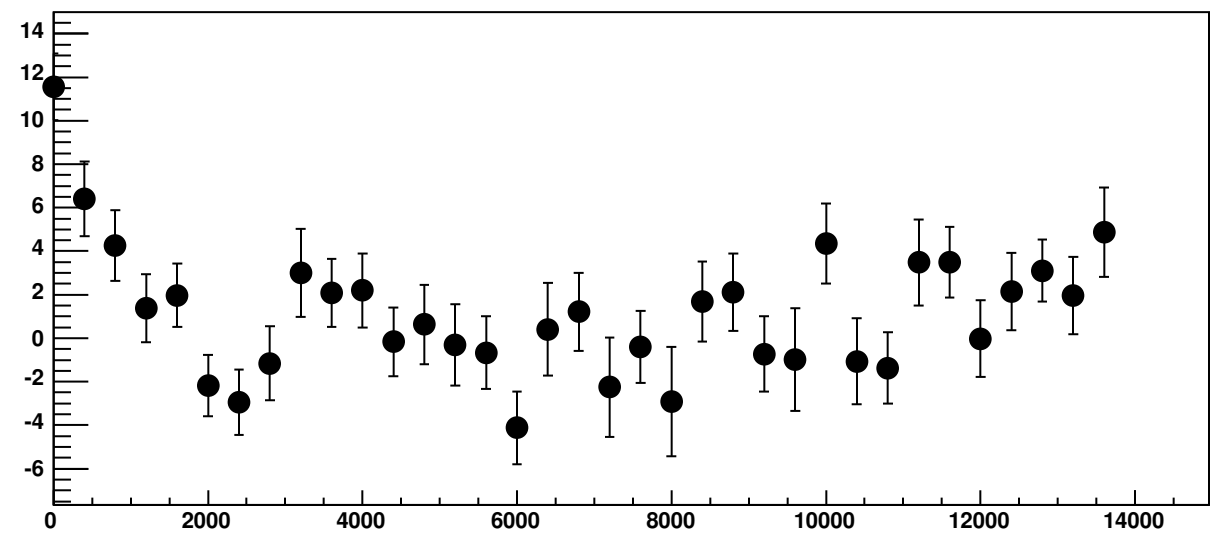
Chip 8, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC

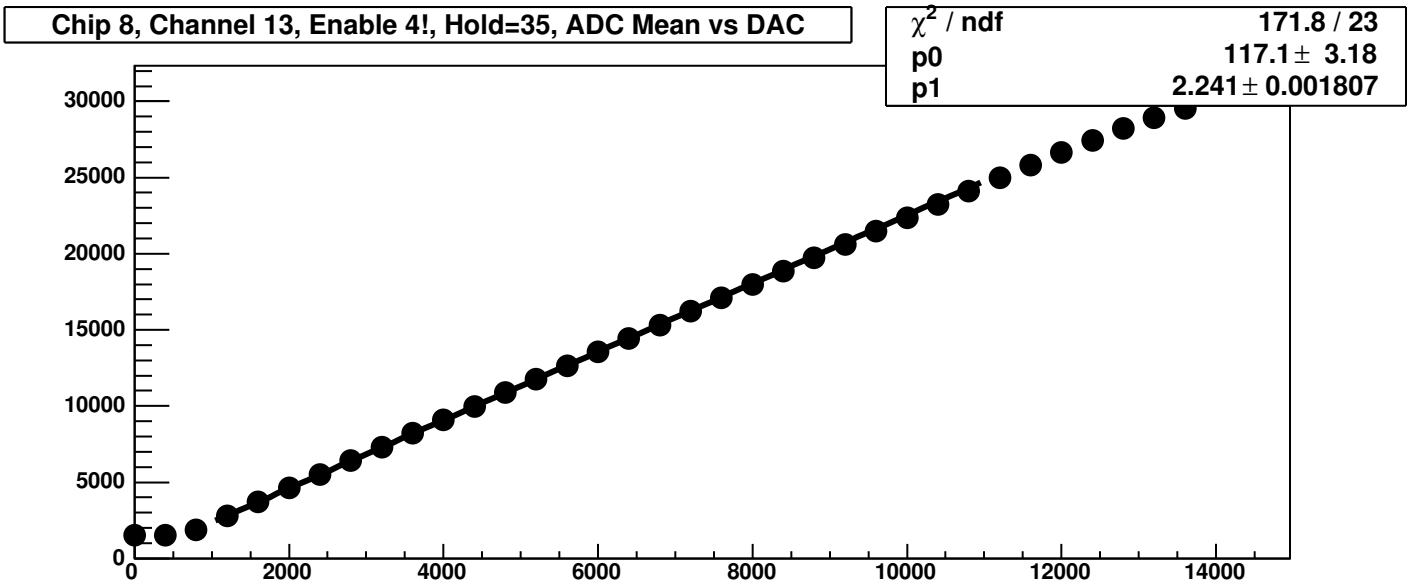


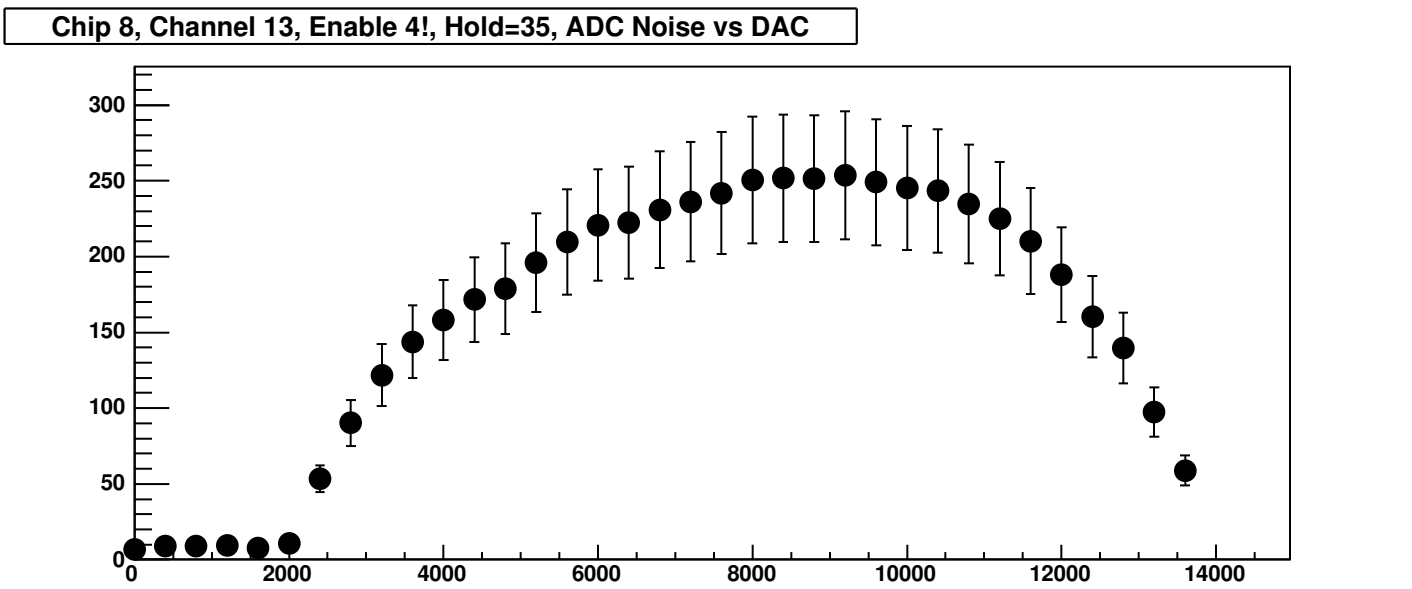
Chip 8, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC

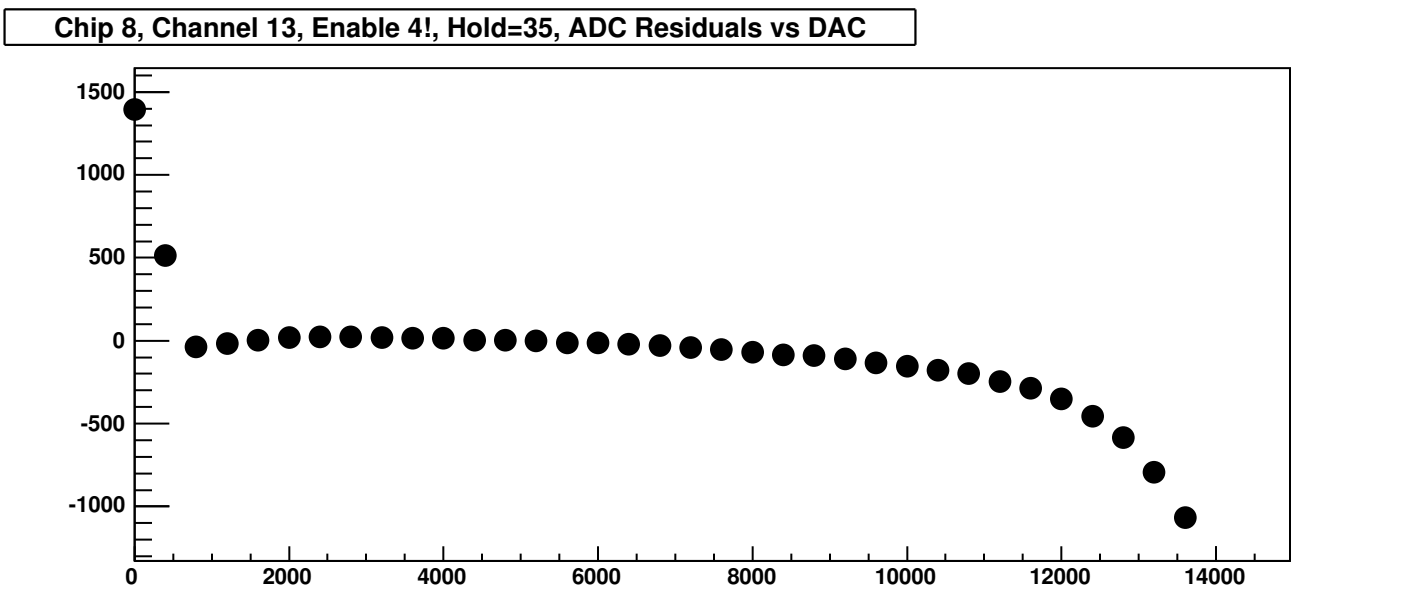


Chip 8, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC

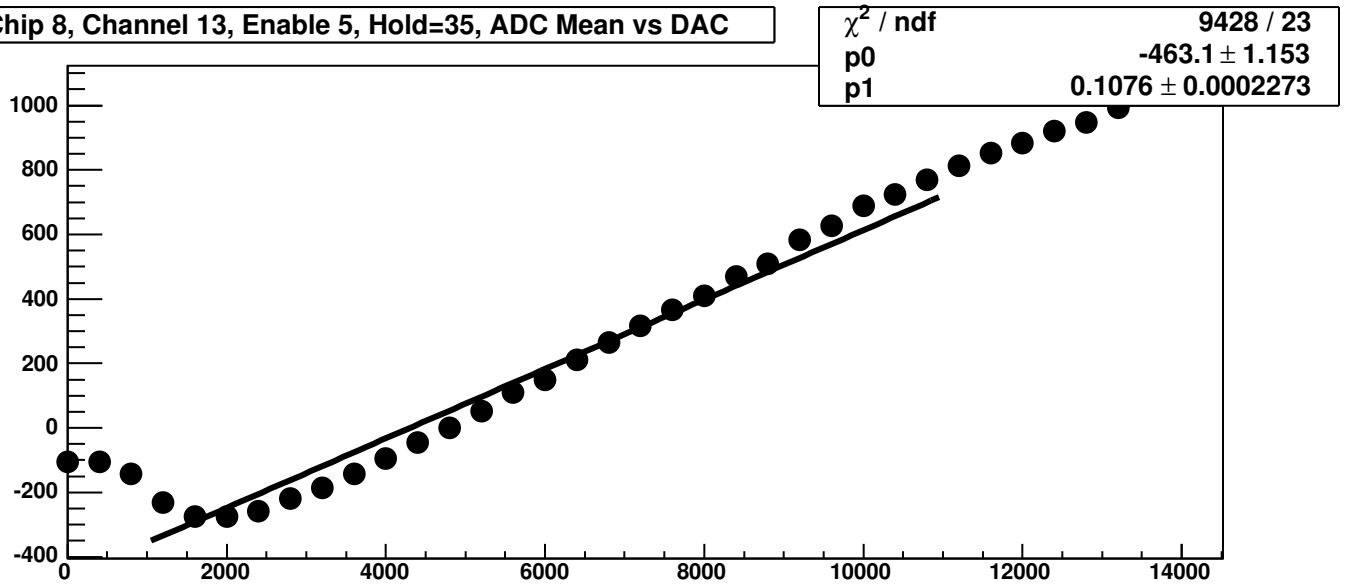



Chip 8, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 171.8 / 23
p0 117.1 ± 3.18
p1 2.241 ± 0.001807
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

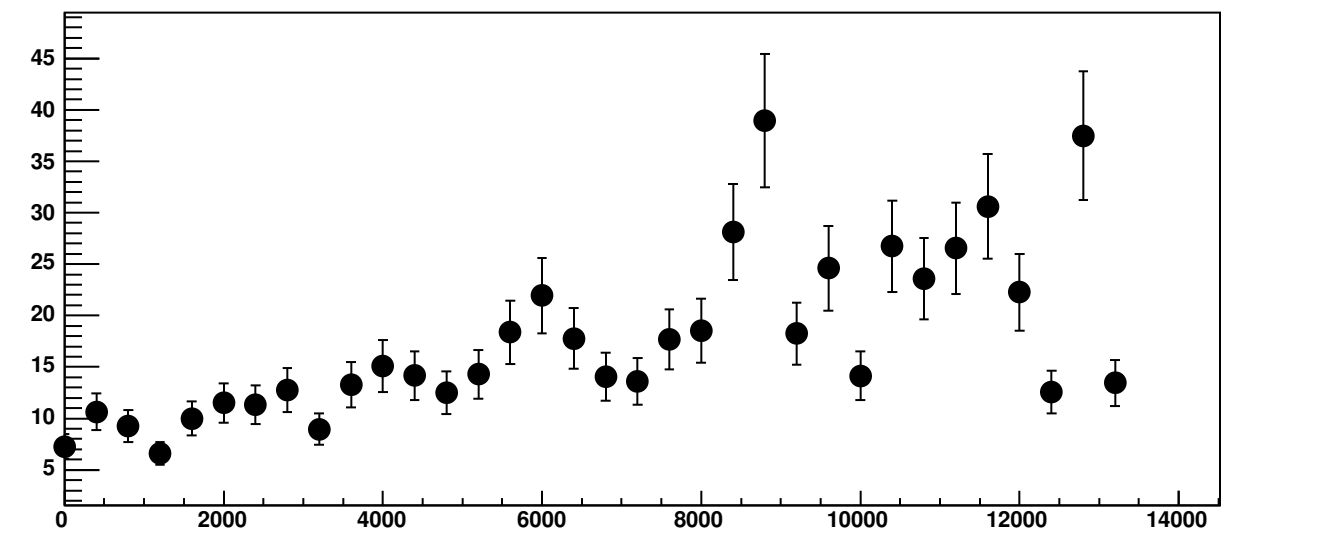

Chip 8, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

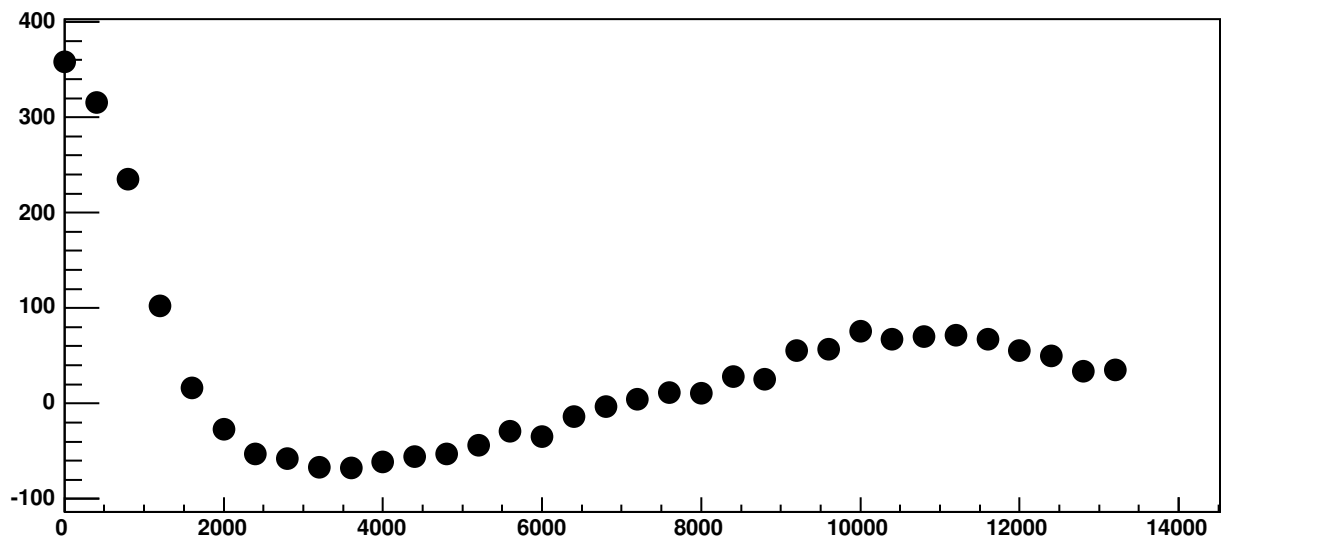
Chip 8, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

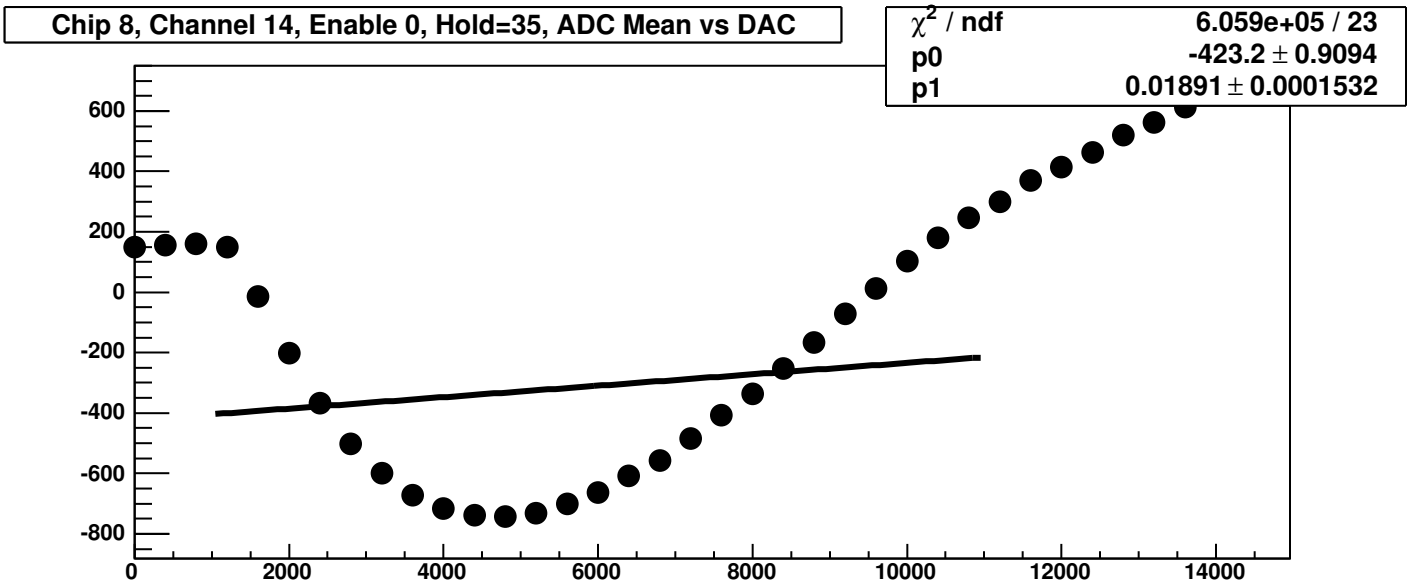


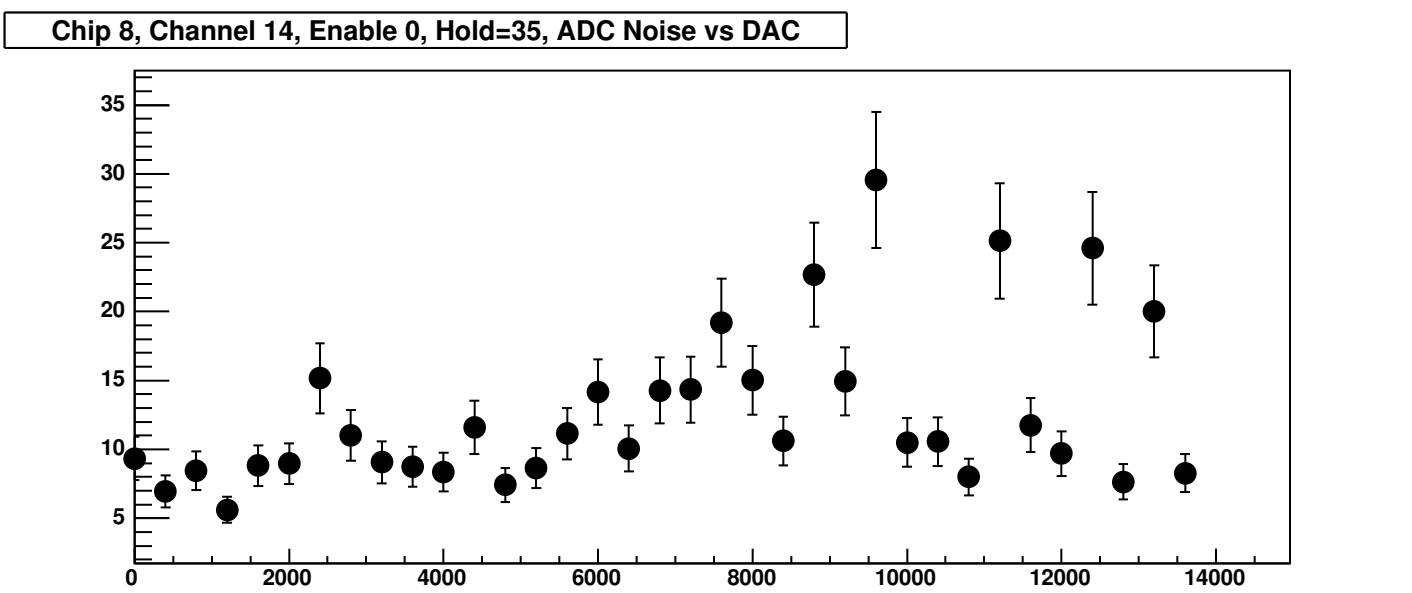
Chip 8, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

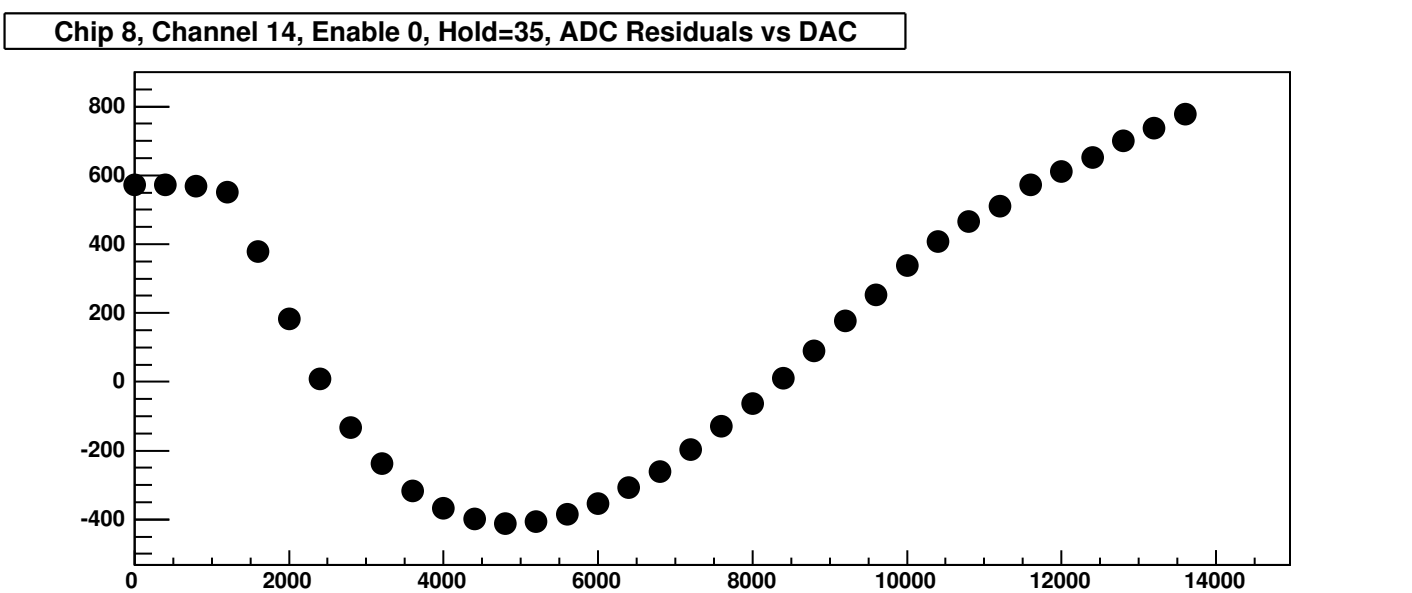


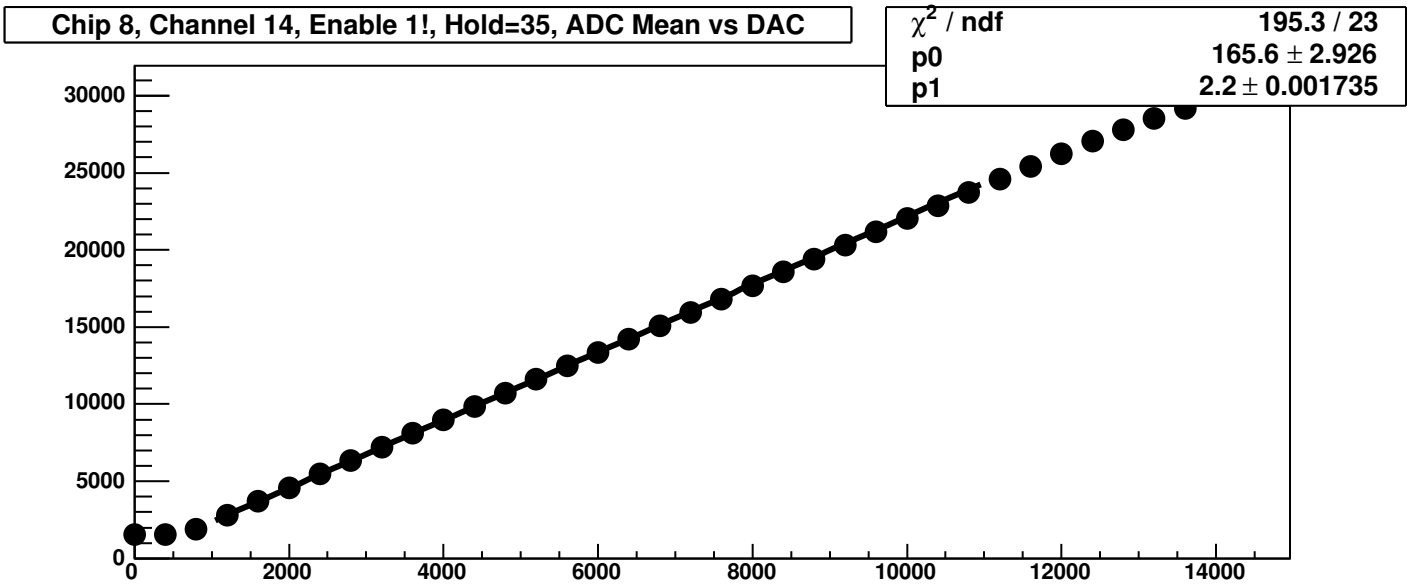
Chip 8, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

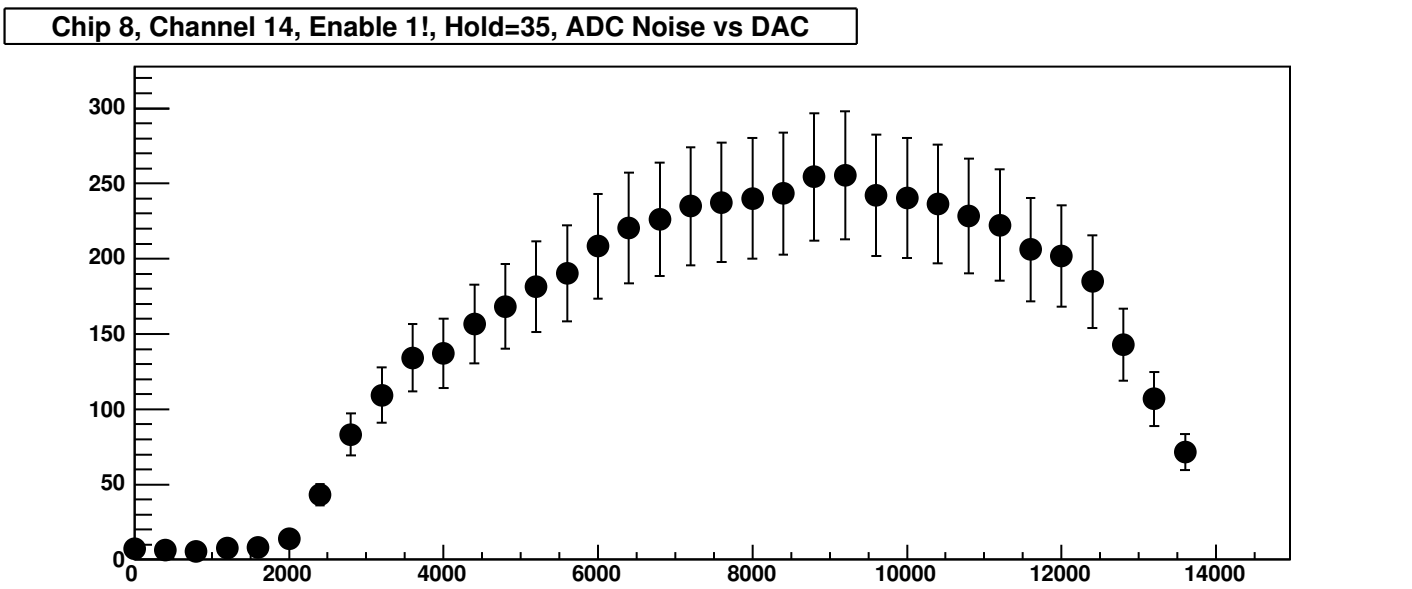


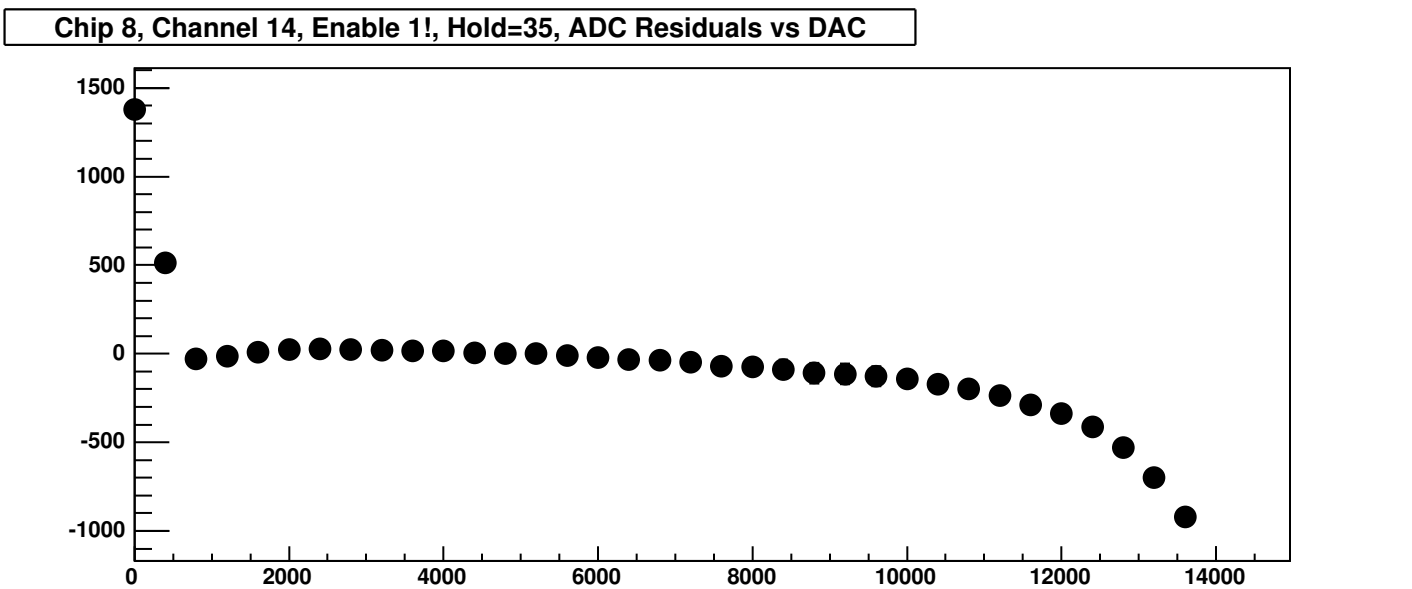

Chip 8, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 6.059e+05 / 23
p0 -423.2 ± 0.9094
p1 0.01891 ± 0.0001532
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

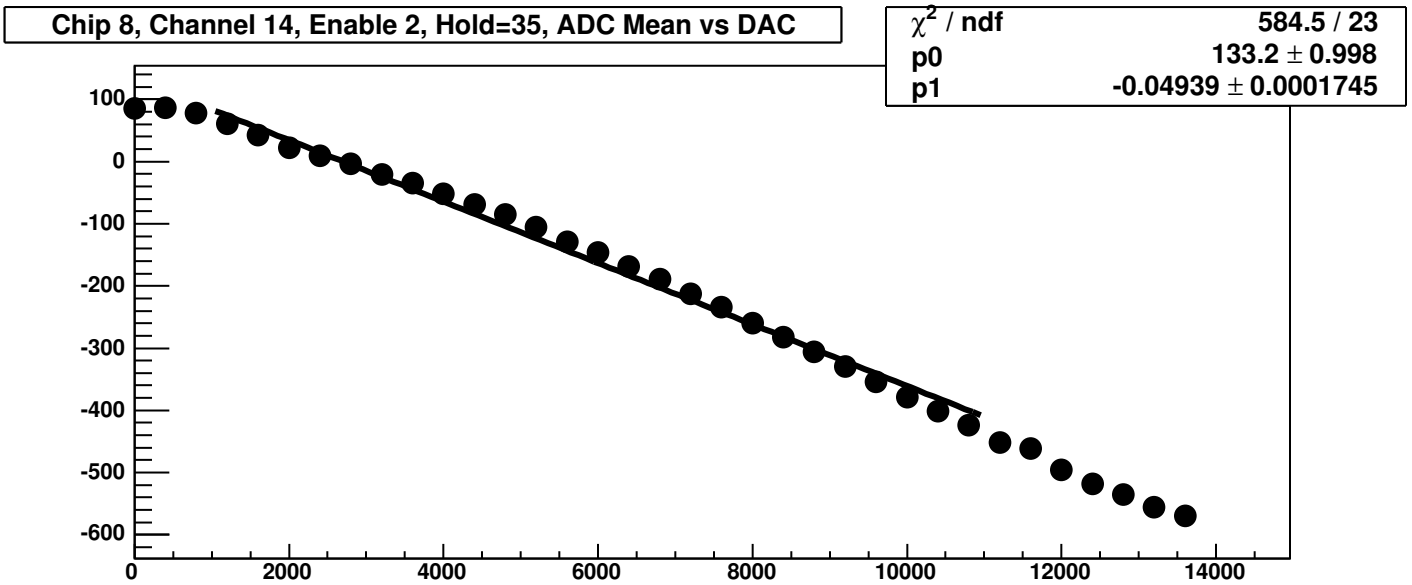

Chip 8, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

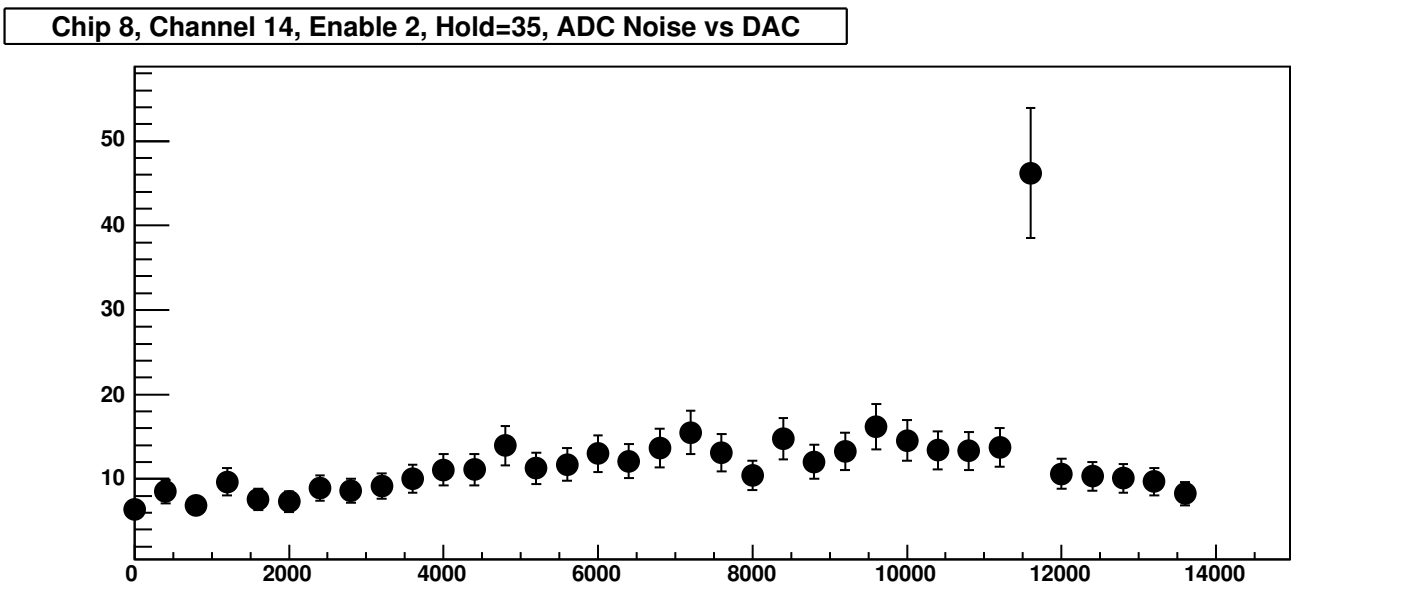

Chip 8, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

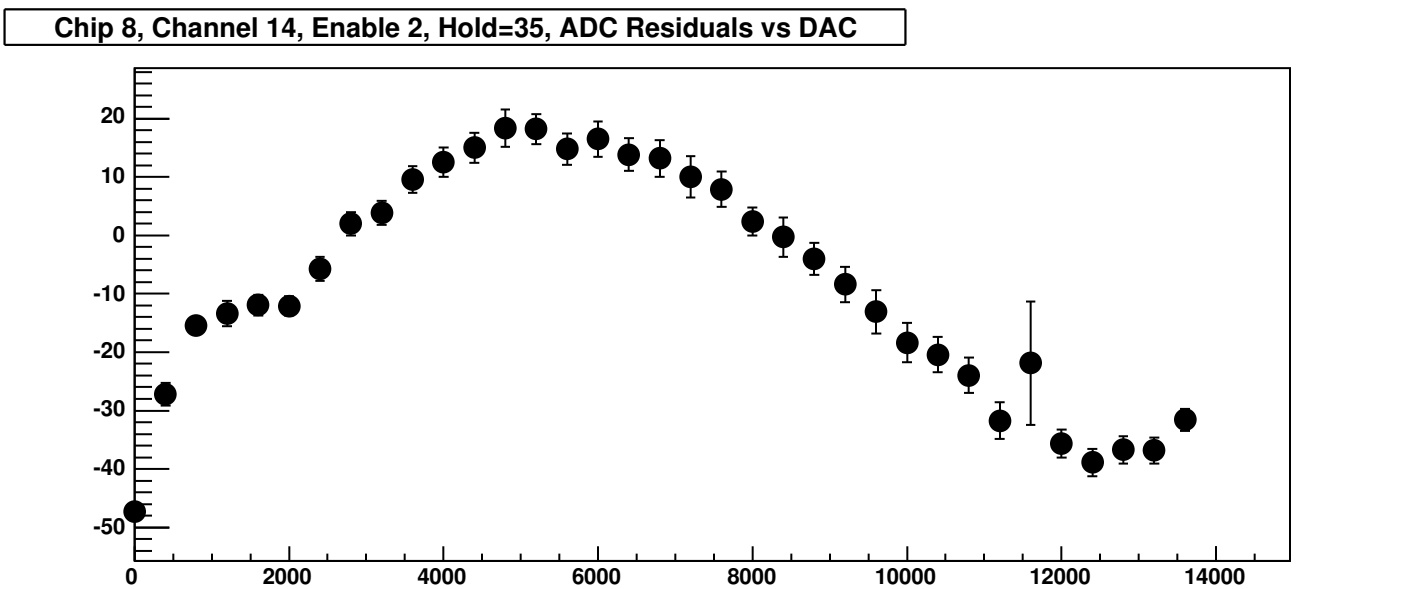

Chip 8, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 195.3 / 23
p0 165.6 ± 2.926
p1 2.2 ± 0.001735
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

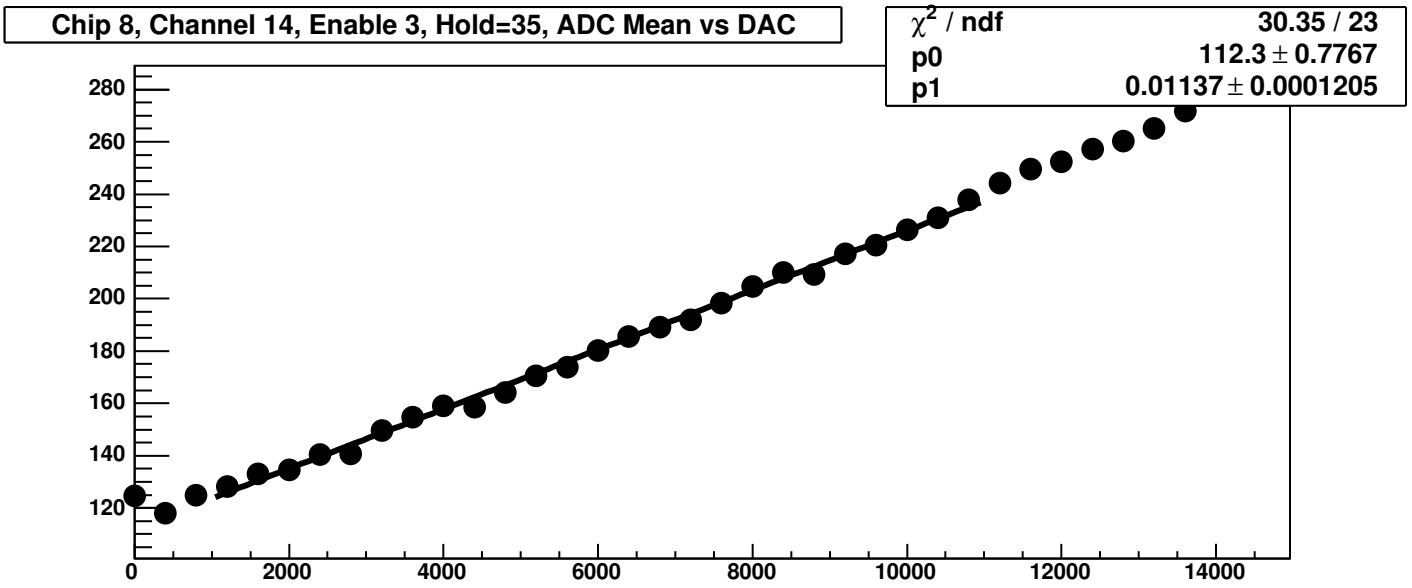

Chip 8, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

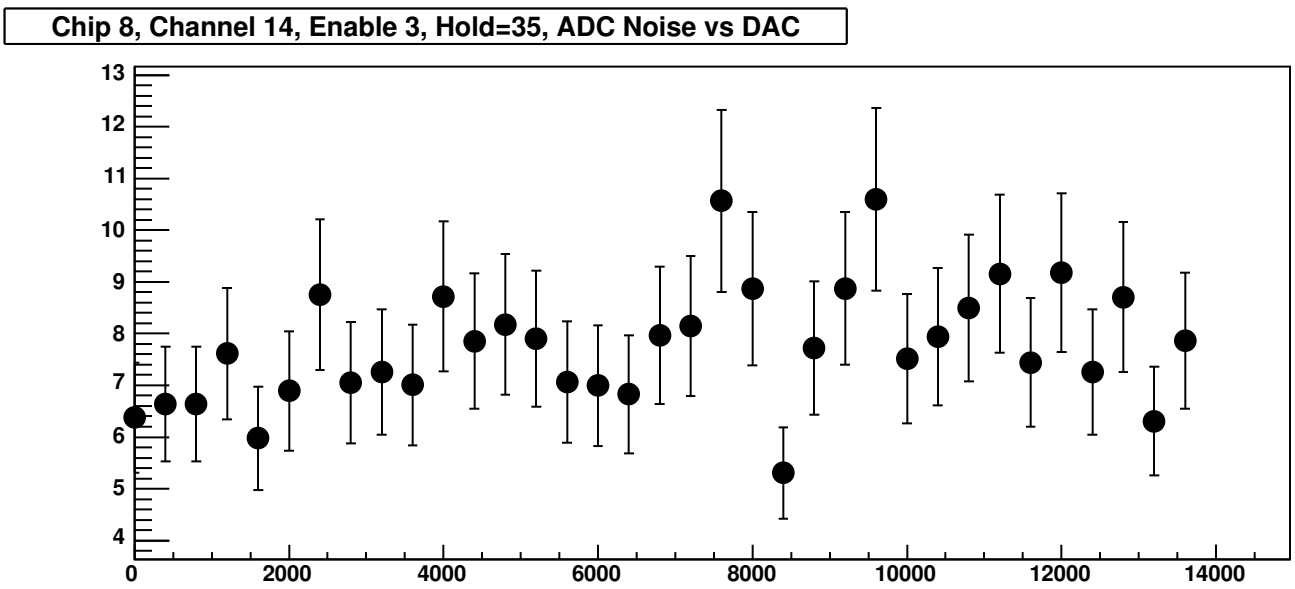

Chip 8, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

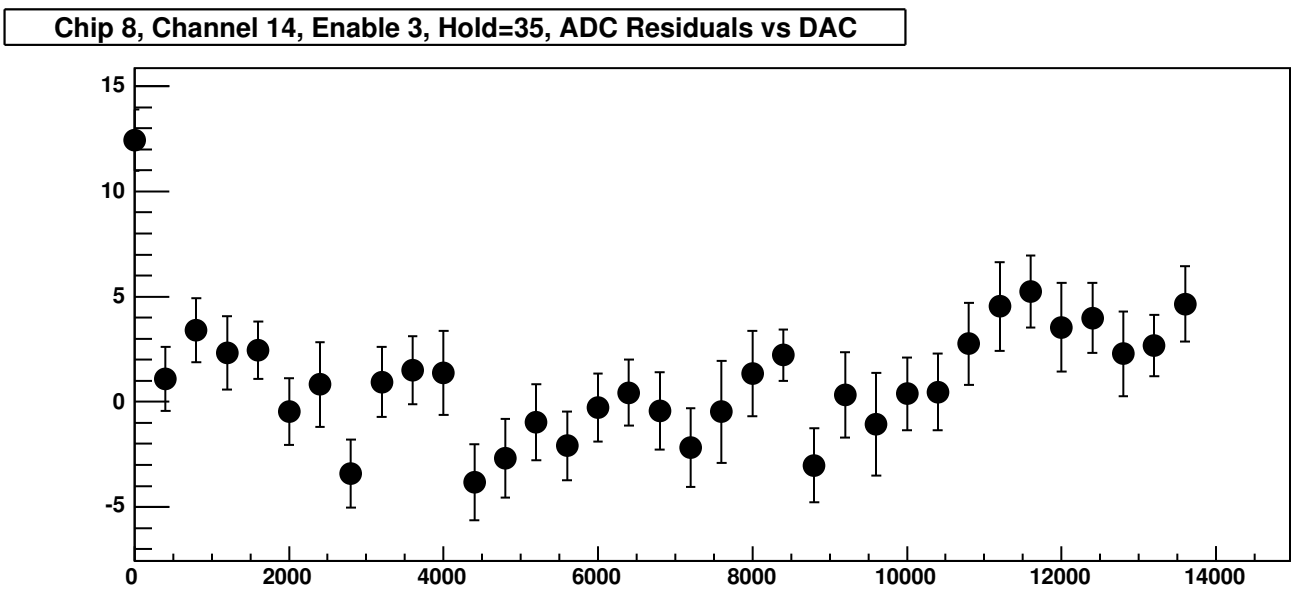

Chip 8, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 584.5 / 23
p0 133.2 ± 0.998
p1 -0.04939 ± 0.0001745
100
0
-100
-200
-300
-400
-500
-600
0
2000
4000
6000
8000
10000
12000
14000

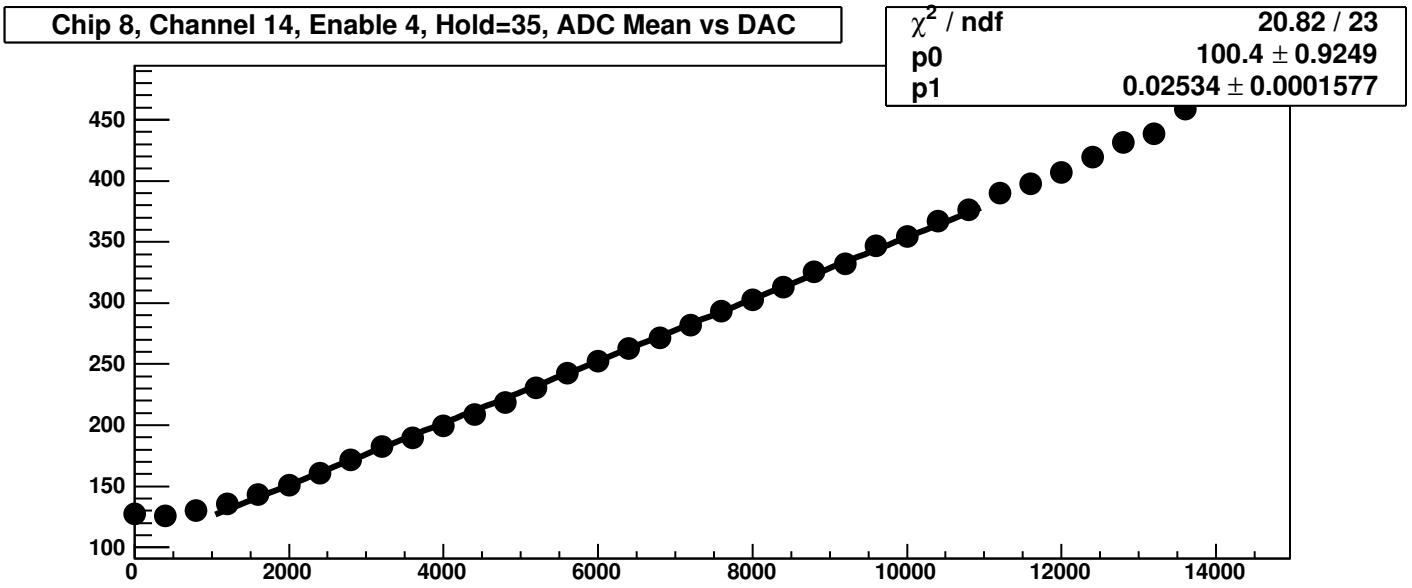

Chip 8, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

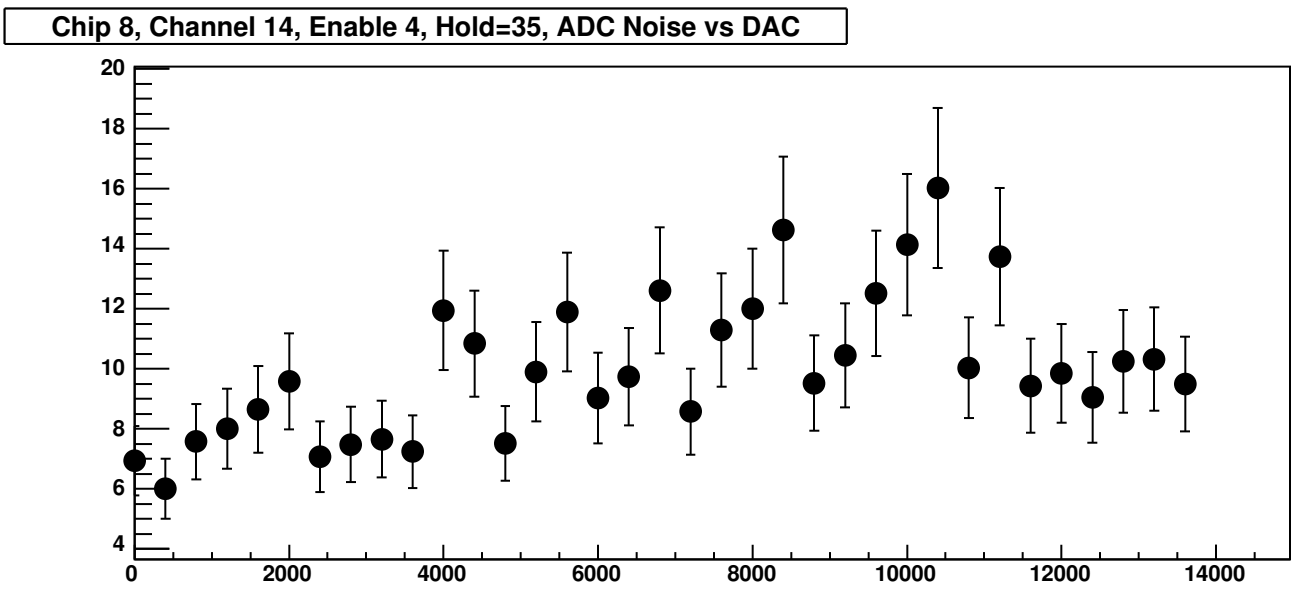

Chip 8, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

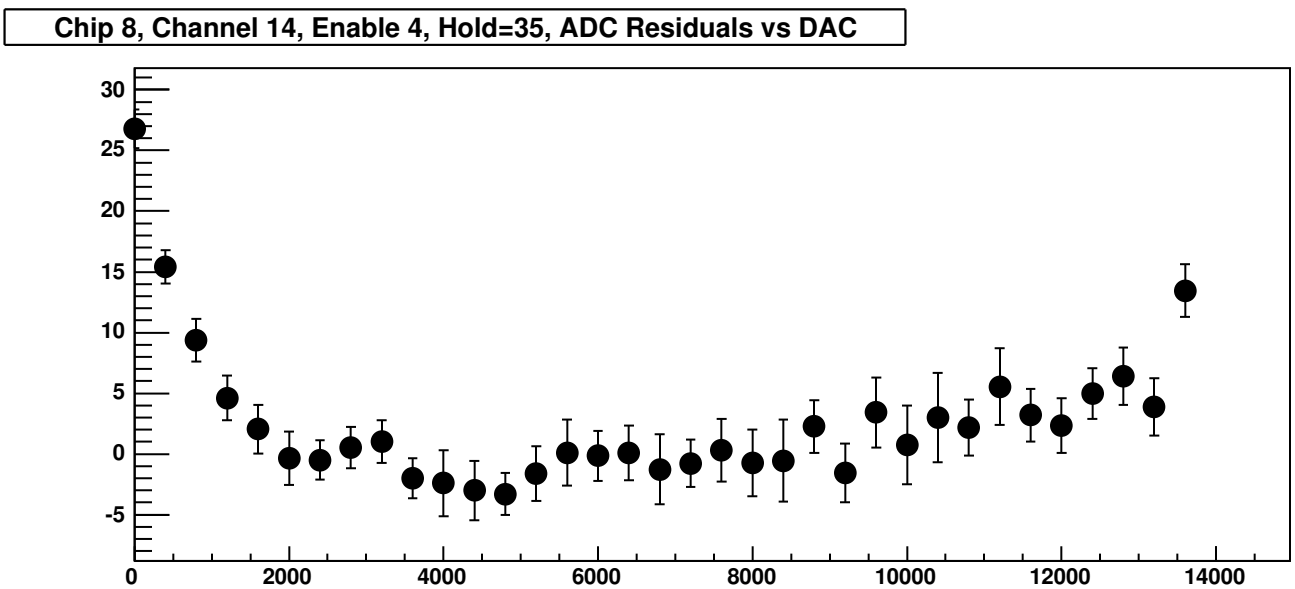

Chip 8, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 30.35 / 23
p0 112.3 ± 0.7767
p1 0.01137 ± 0.0001205
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

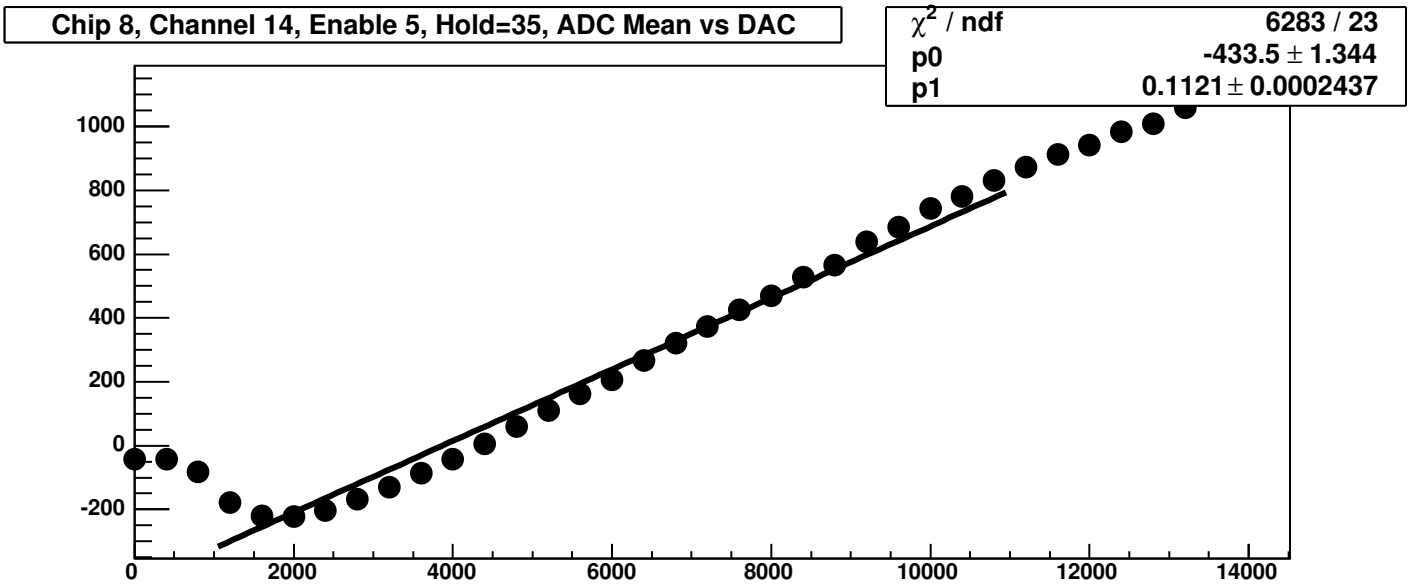

Chip 8, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

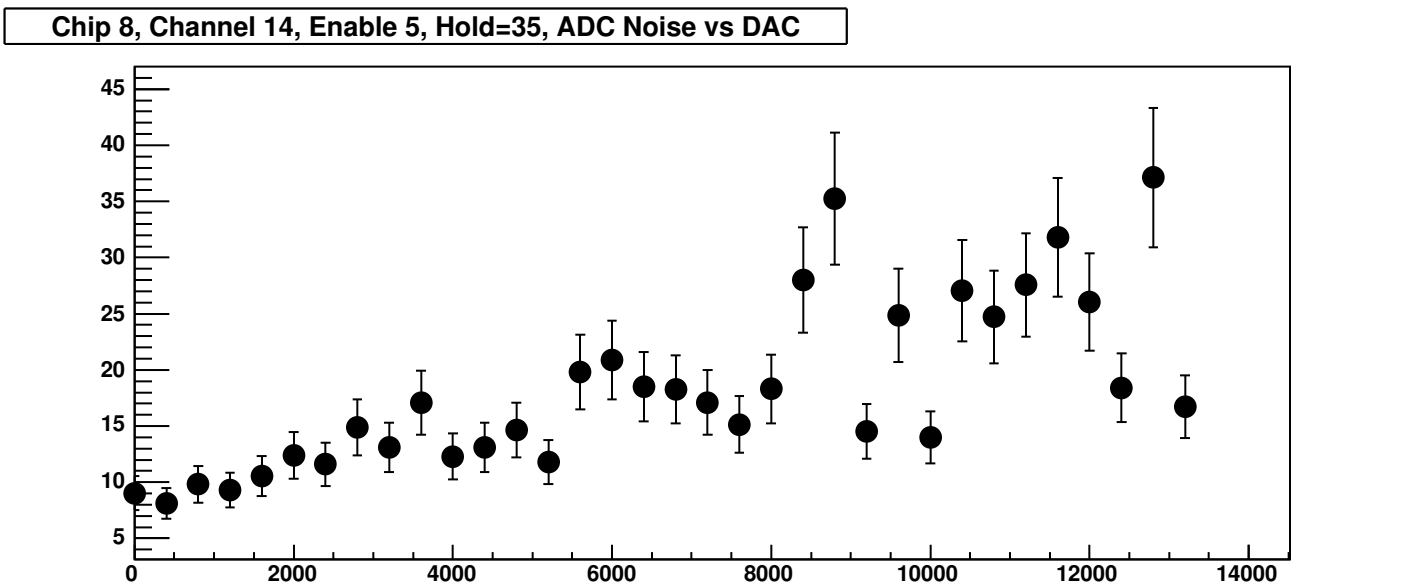

Chip 8, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

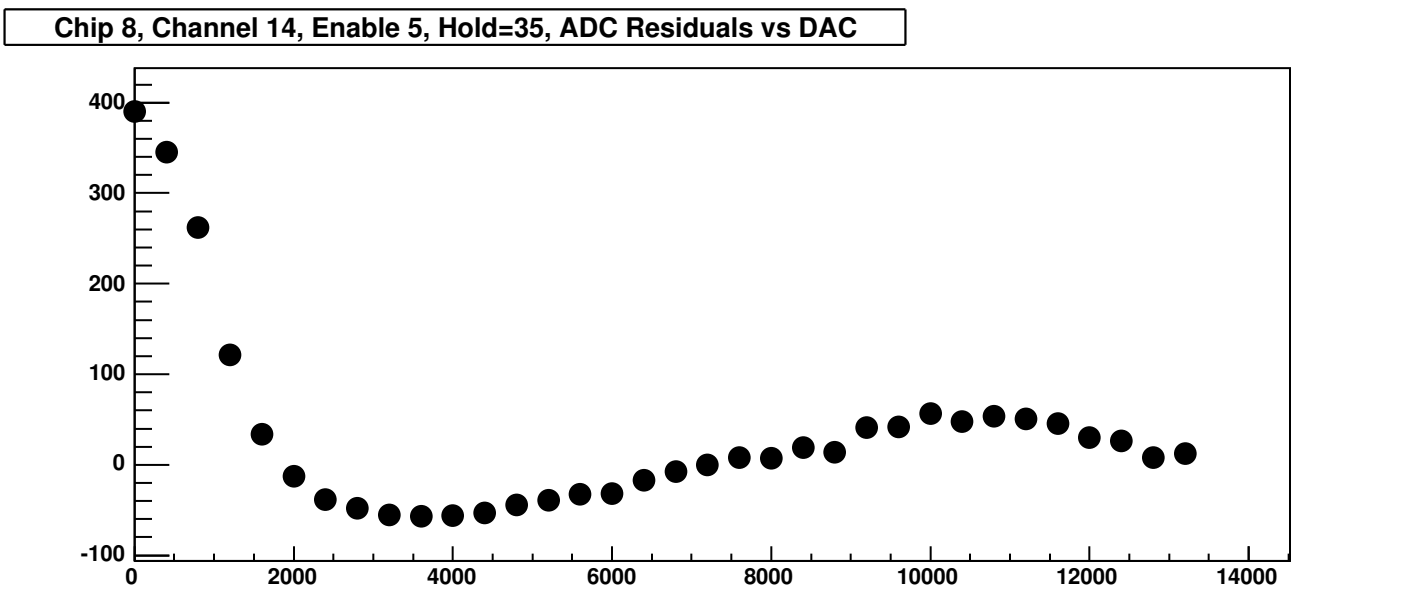

Chip 8, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 20.82 / 23
p0 100.4 ± 0.9249
p1 0.02534 ± 0.0001577
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

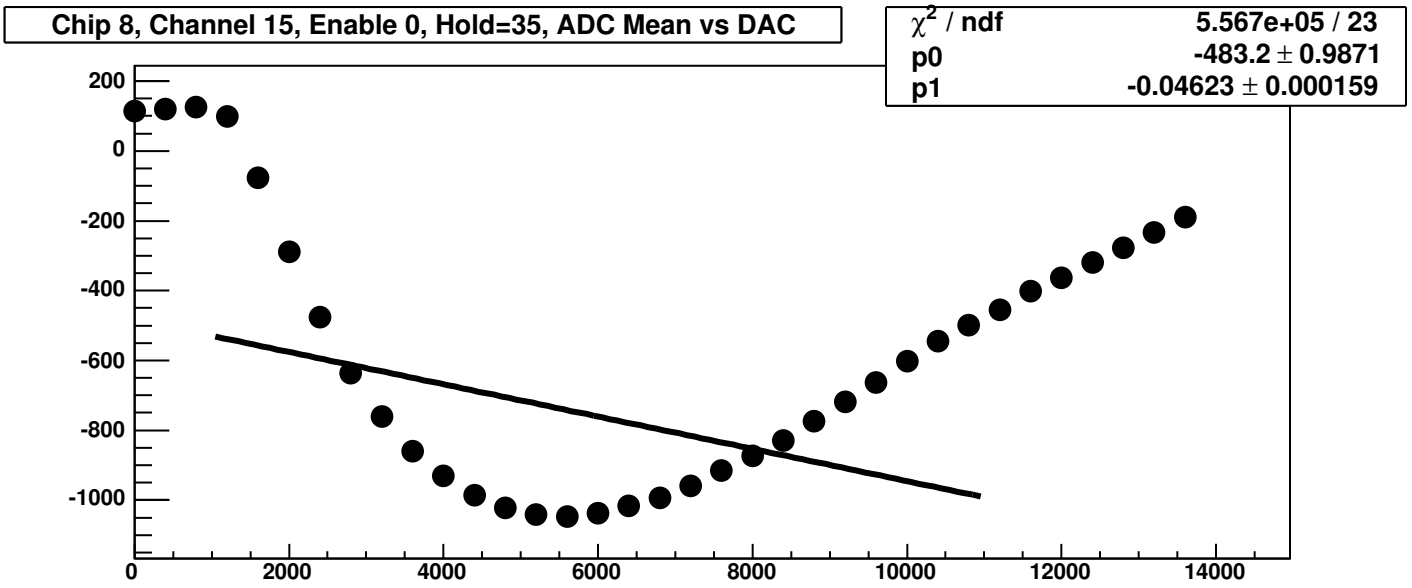

Chip 8, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

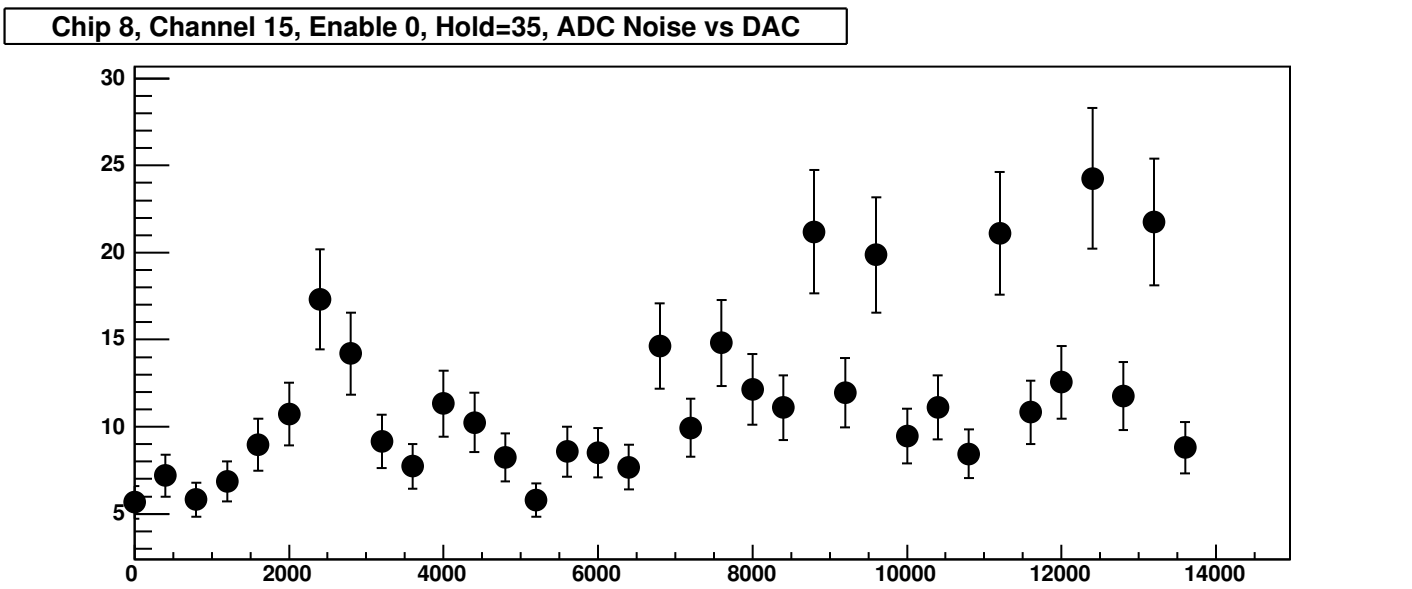

Chip 8, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

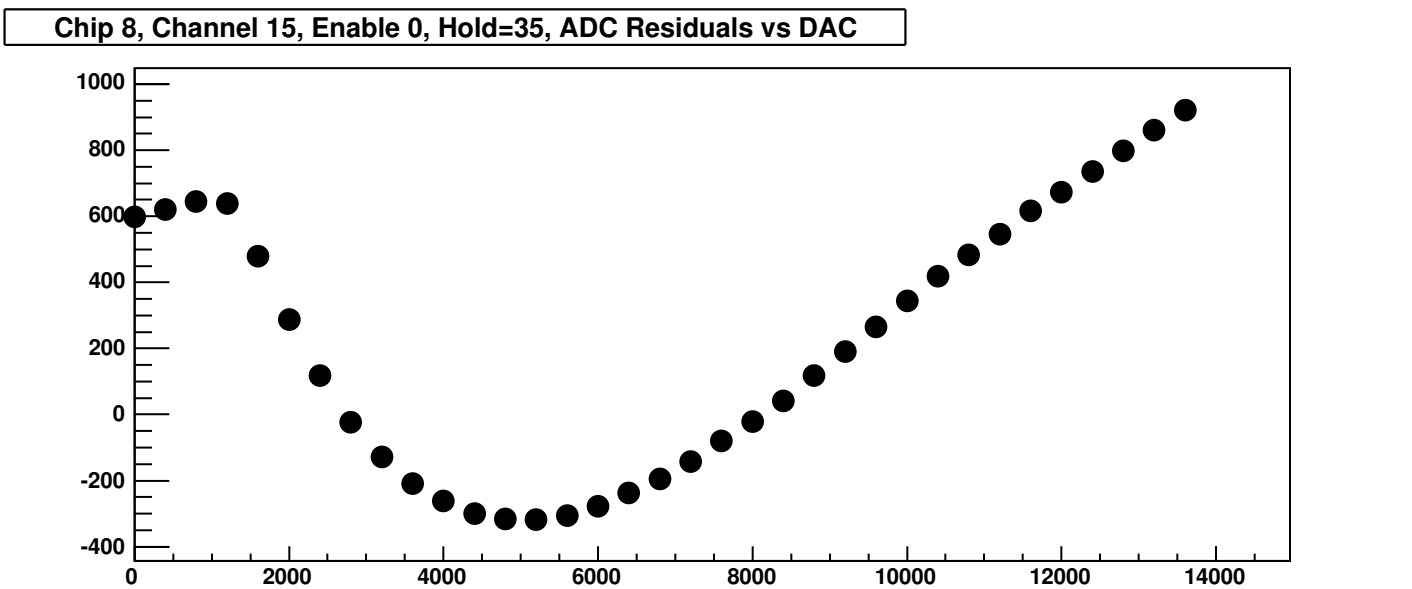

Chip 8, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 6283 / 23
p0 -433.5 ± 1.344
p1 0.1121 ± 0.0002437
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

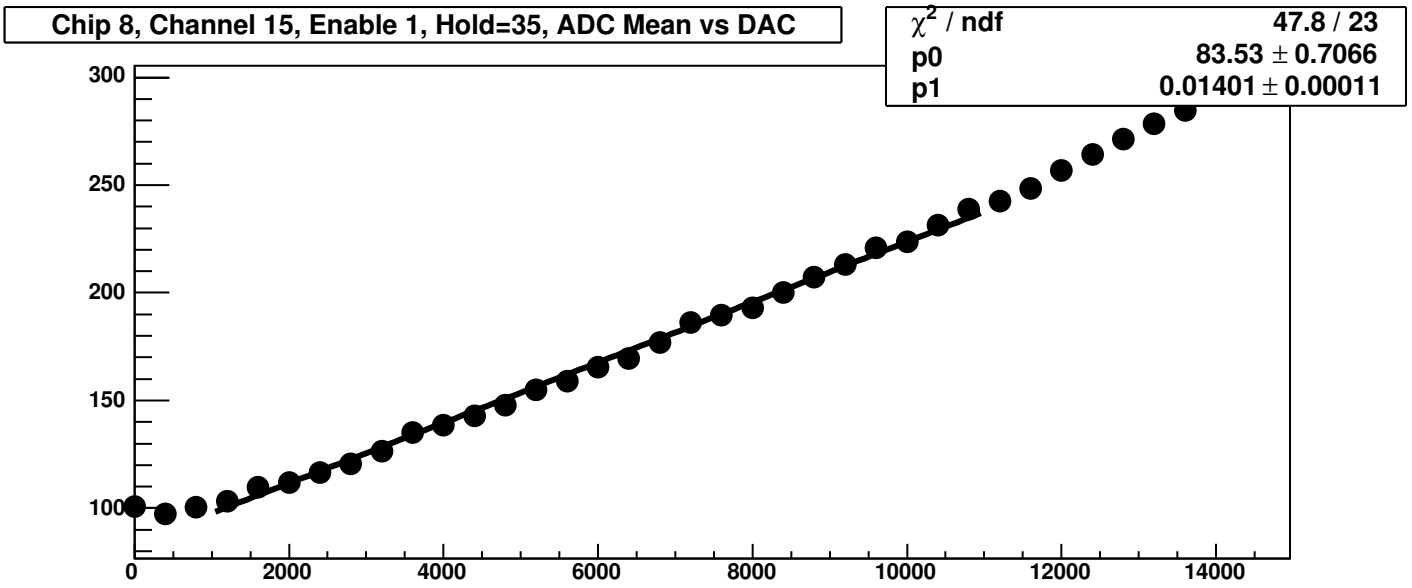

Chip 8, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

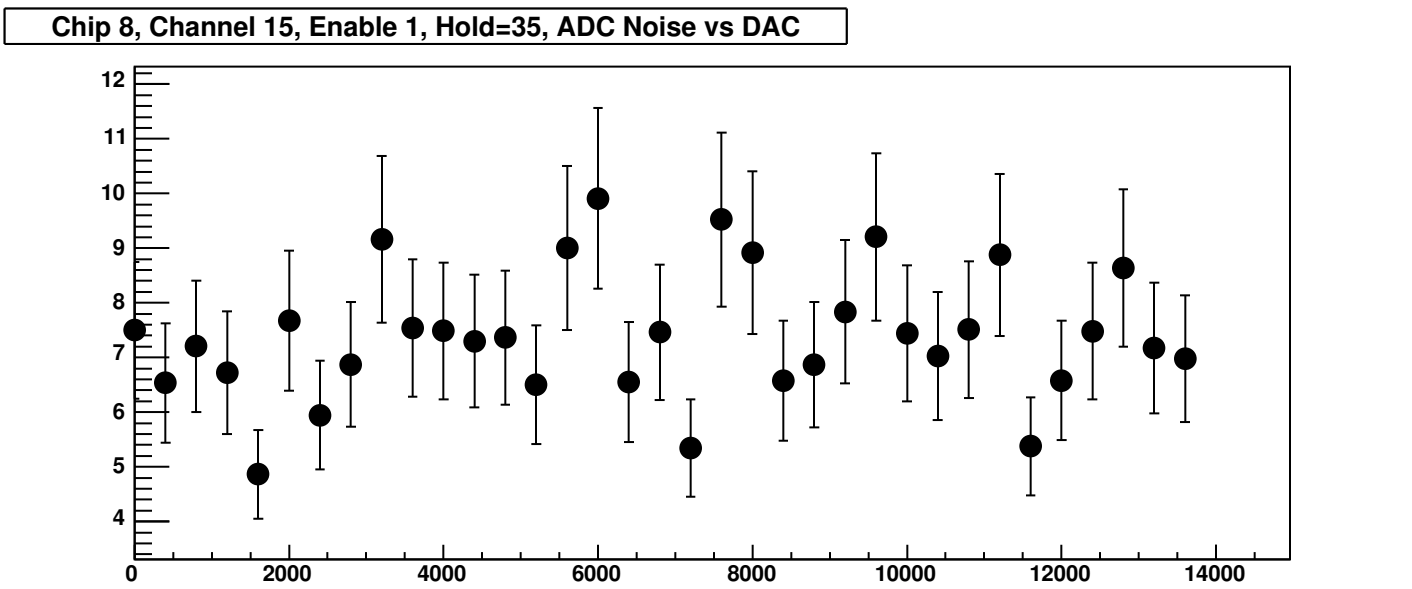

Chip 8, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

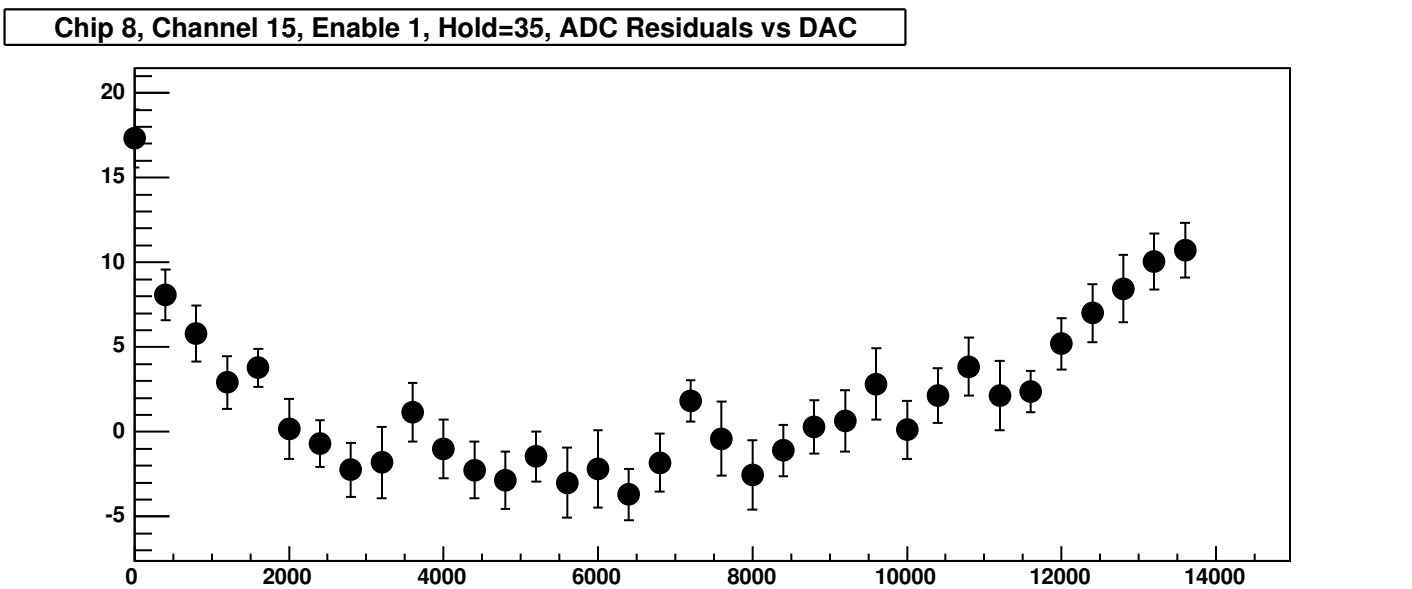

Chip 8, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.567e+05 / 23
p0 -483.2 ± 0.9871
p1 -0.04623 ± 0.000159
200
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

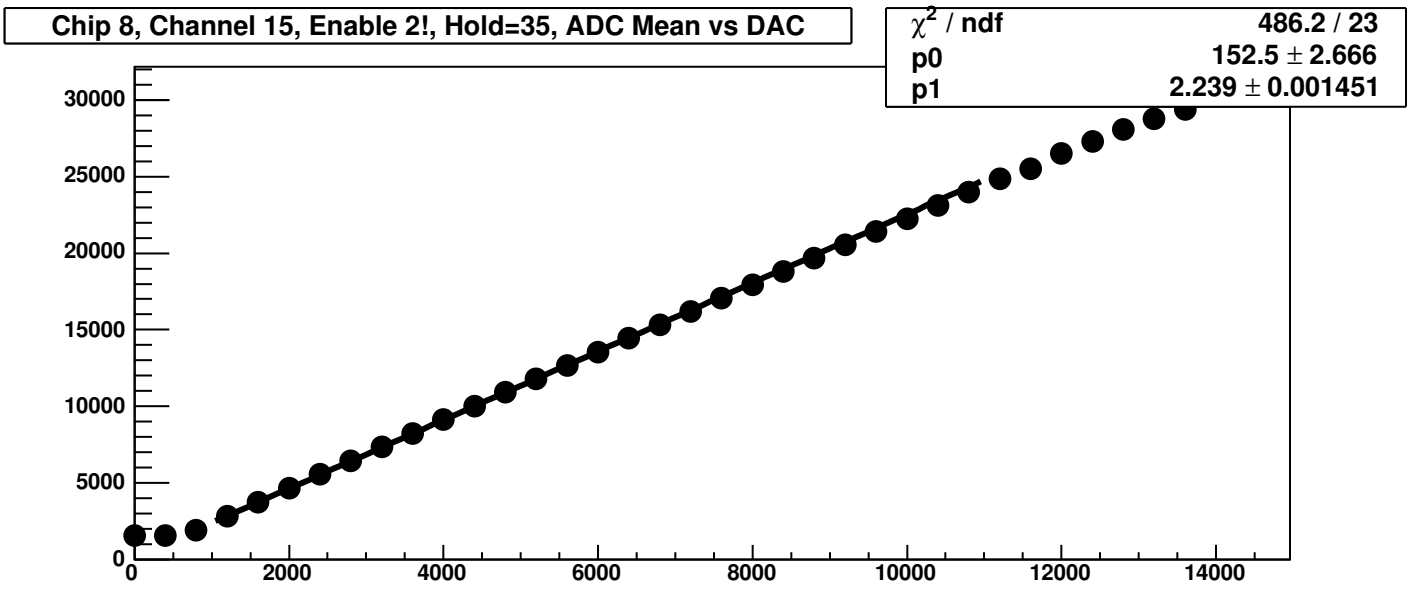

Chip 8, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

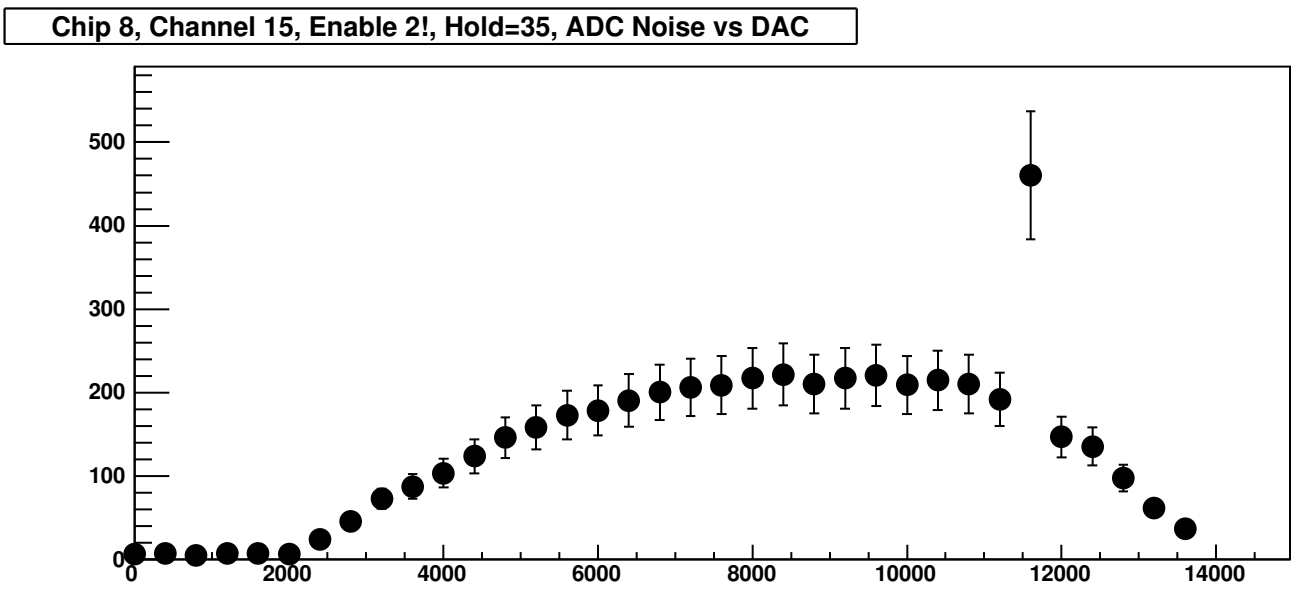

Chip 8, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

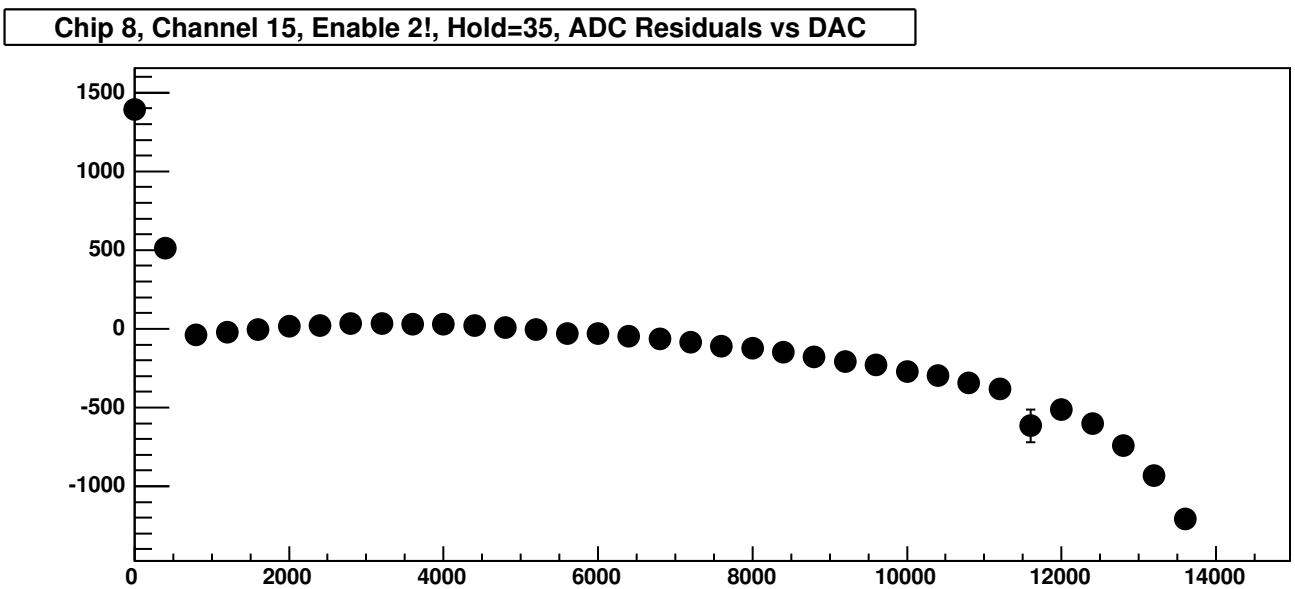

Chip 8, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 47.8 / 23
p0 83.53 ± 0.7066
p1 0.01401 ± 0.00011
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

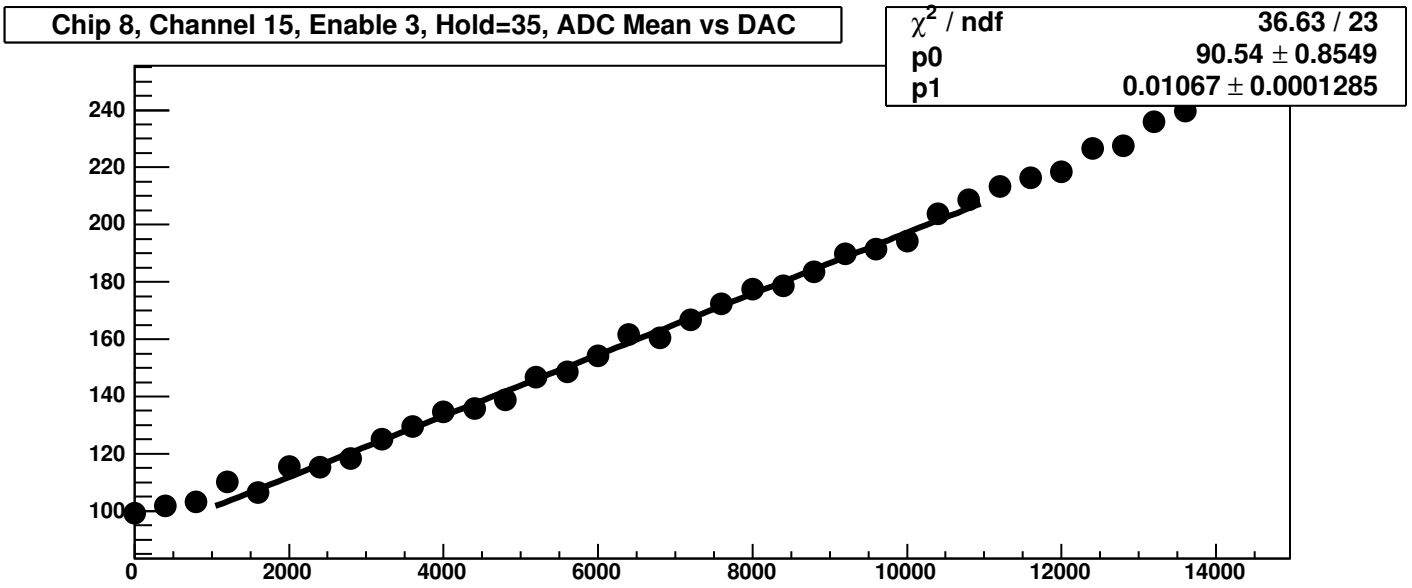

Chip 8, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

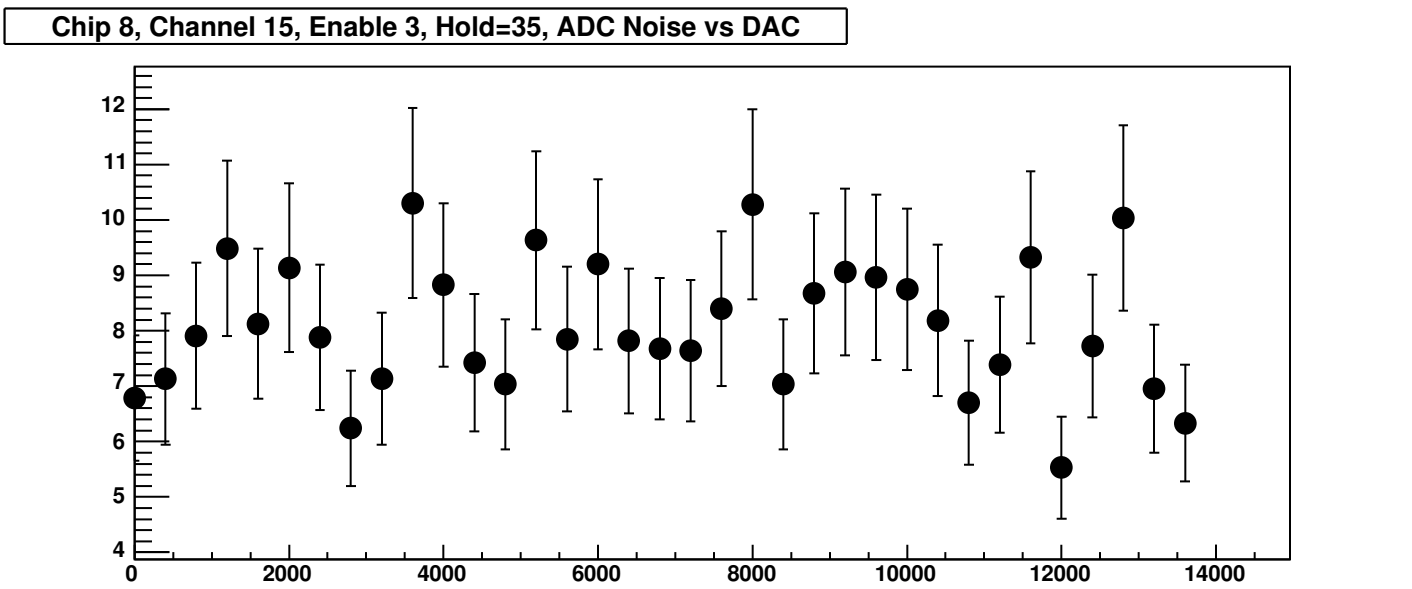

Chip 8, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

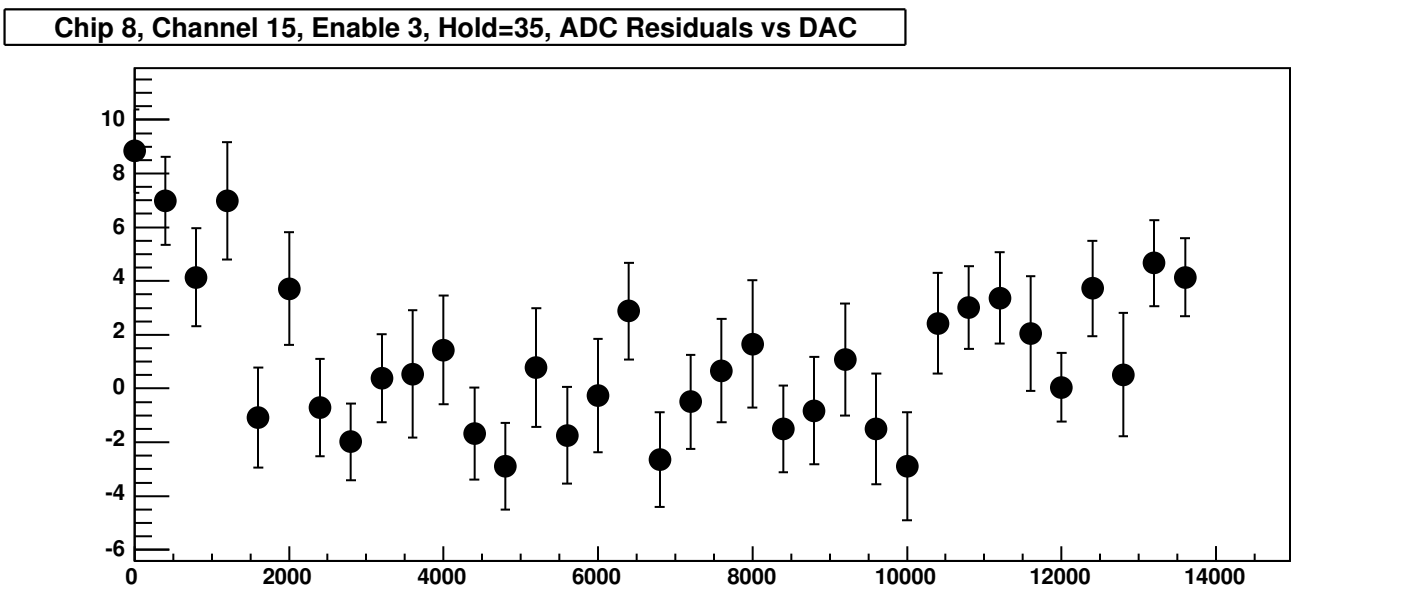

Chip 8, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 486.2 / 23
p0 152.5 ± 2.666
p1 2.239 ± 0.001451
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

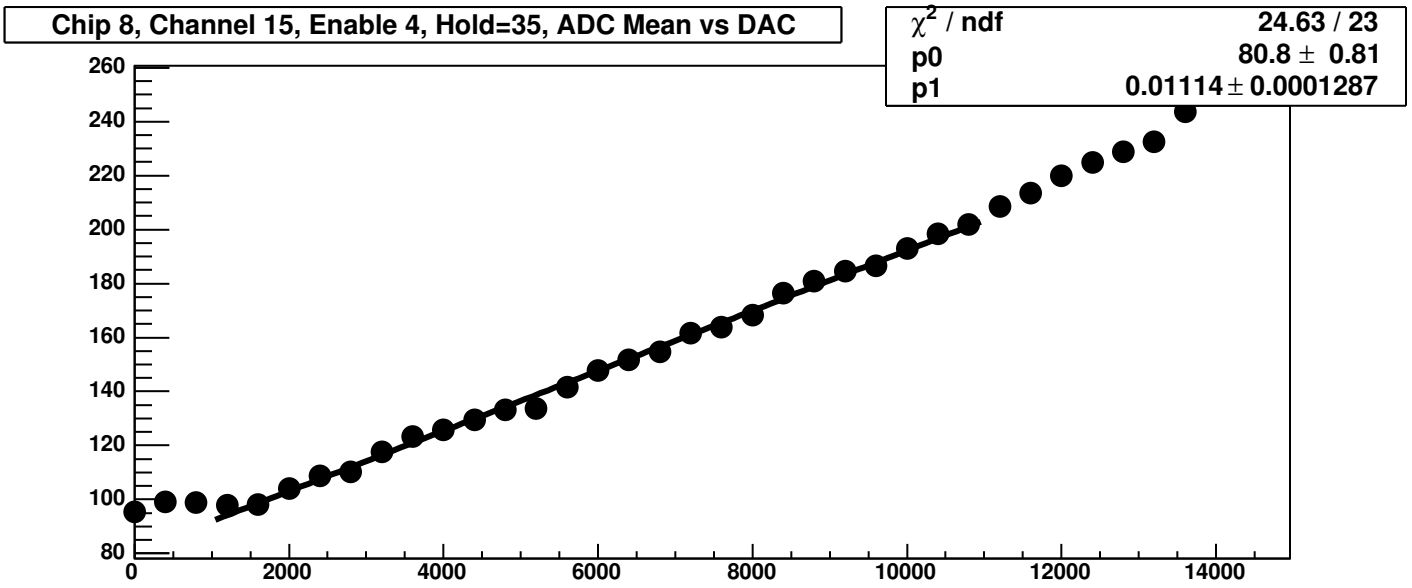

Chip 8, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

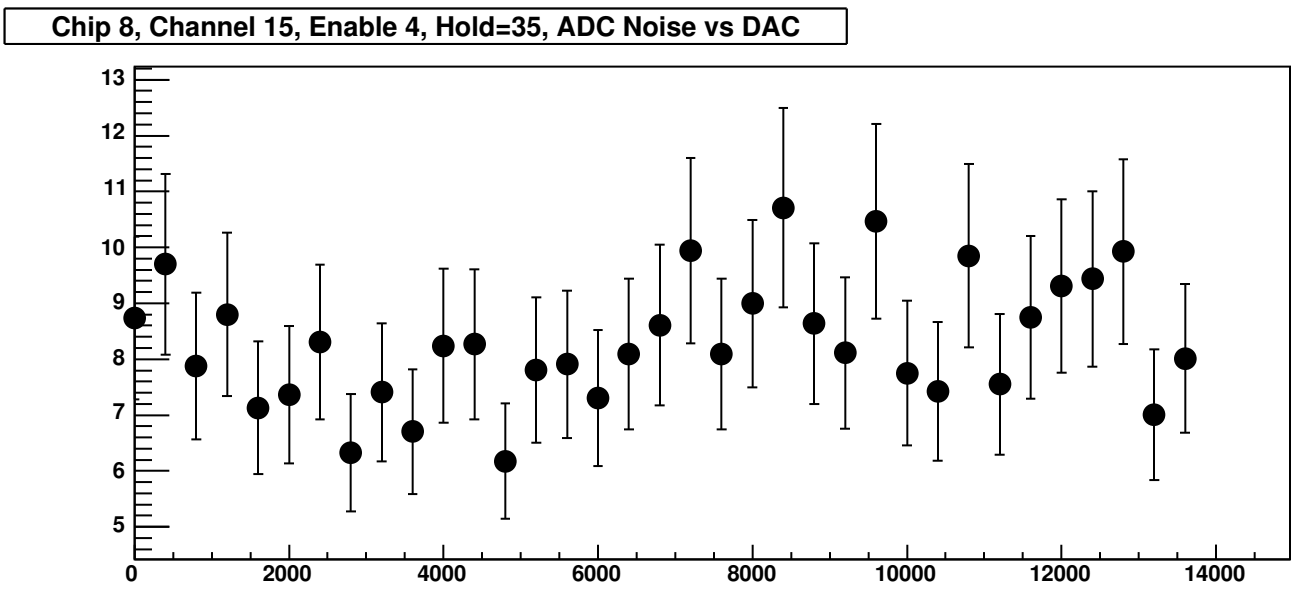

Chip 8, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

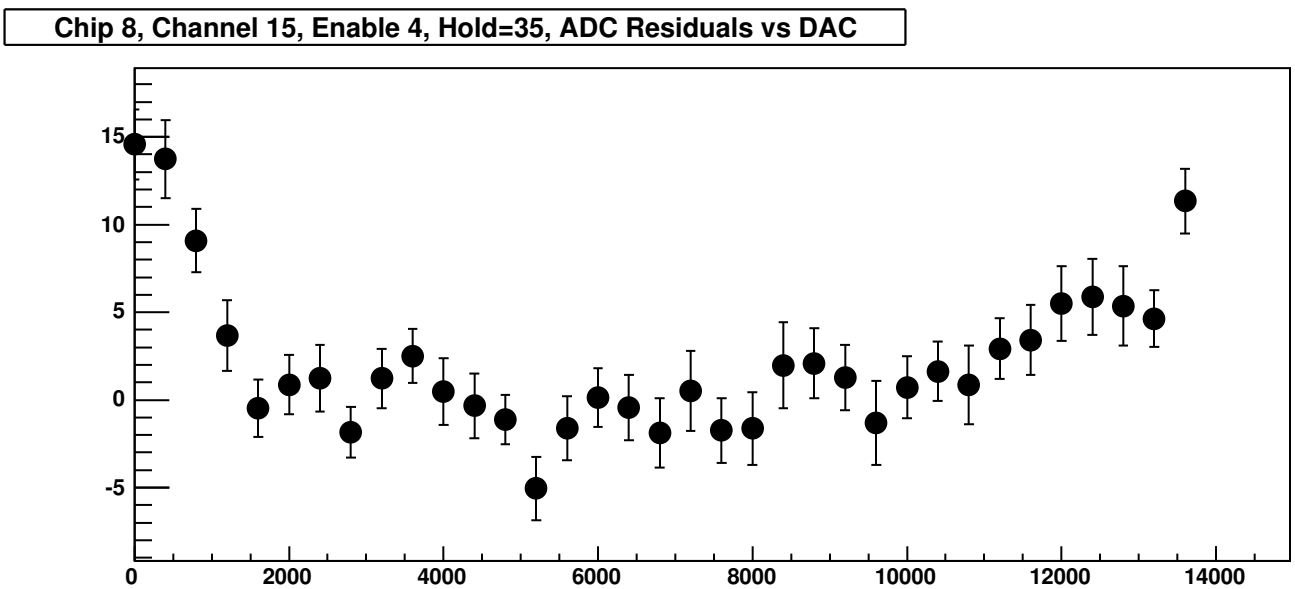

Chip 8, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.63 / 23
p0 90.54 ± 0.8549
p1 0.01067 ± 0.0001285
240
220
200
180
160
140
120
100
0
2000
4000
6000
8000
10000
12000
14000

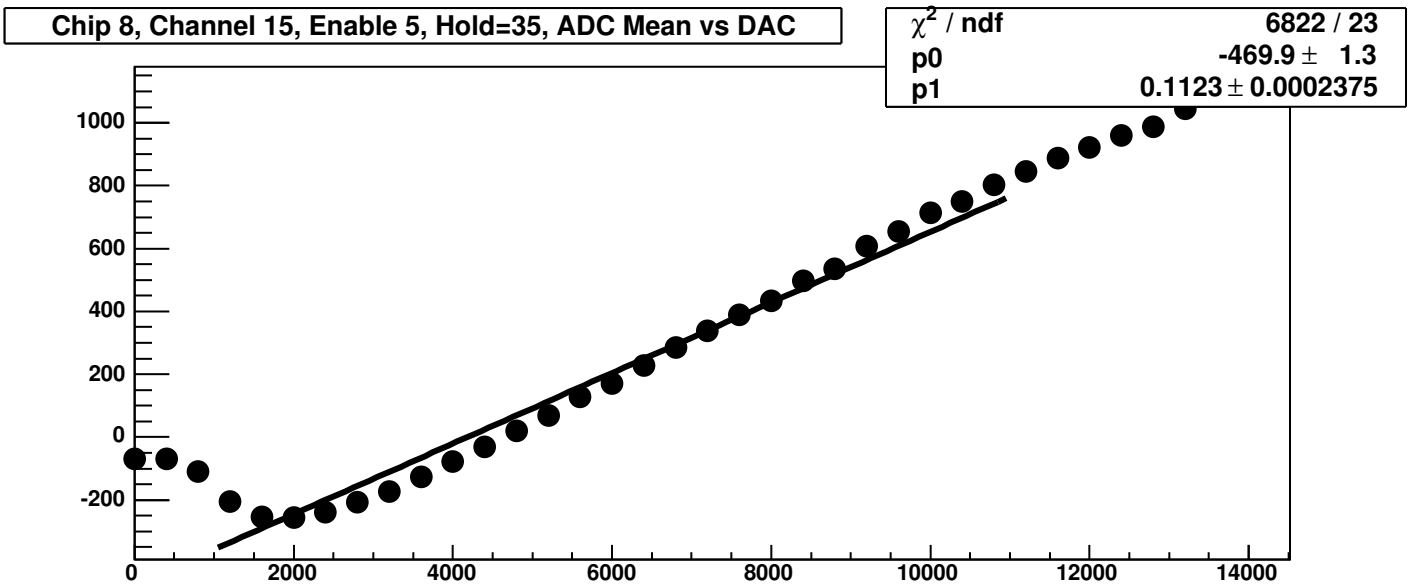

Chip 8, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

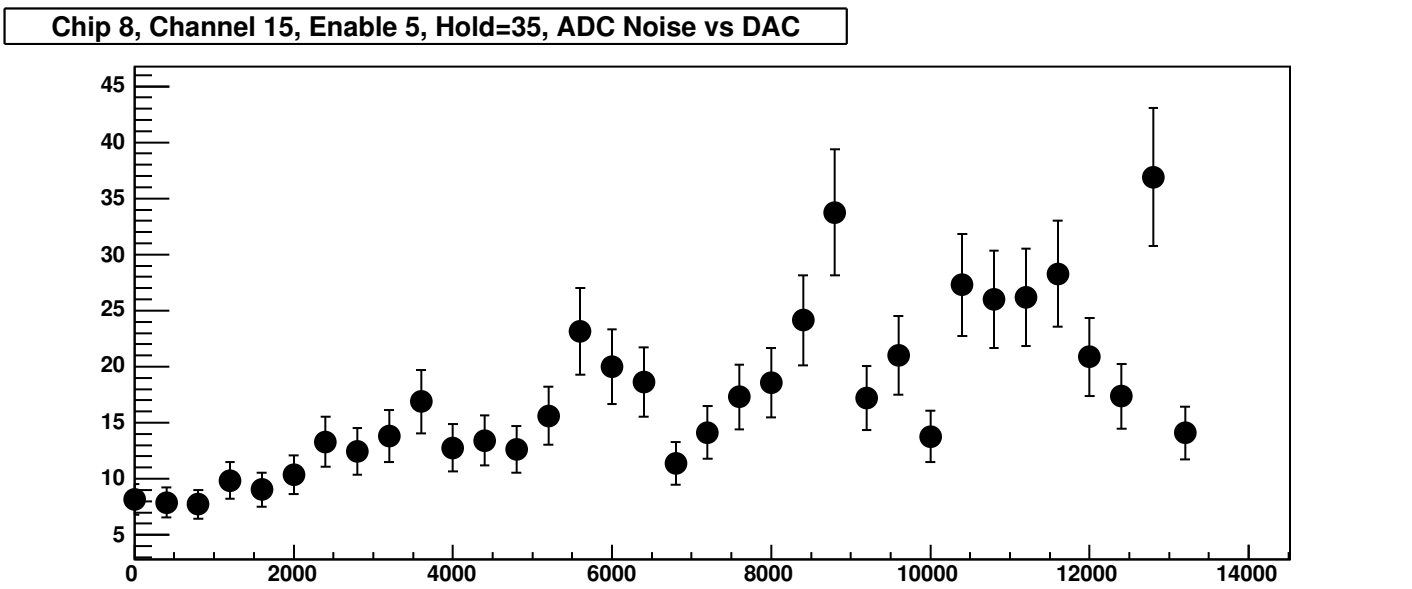

Chip 8, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000

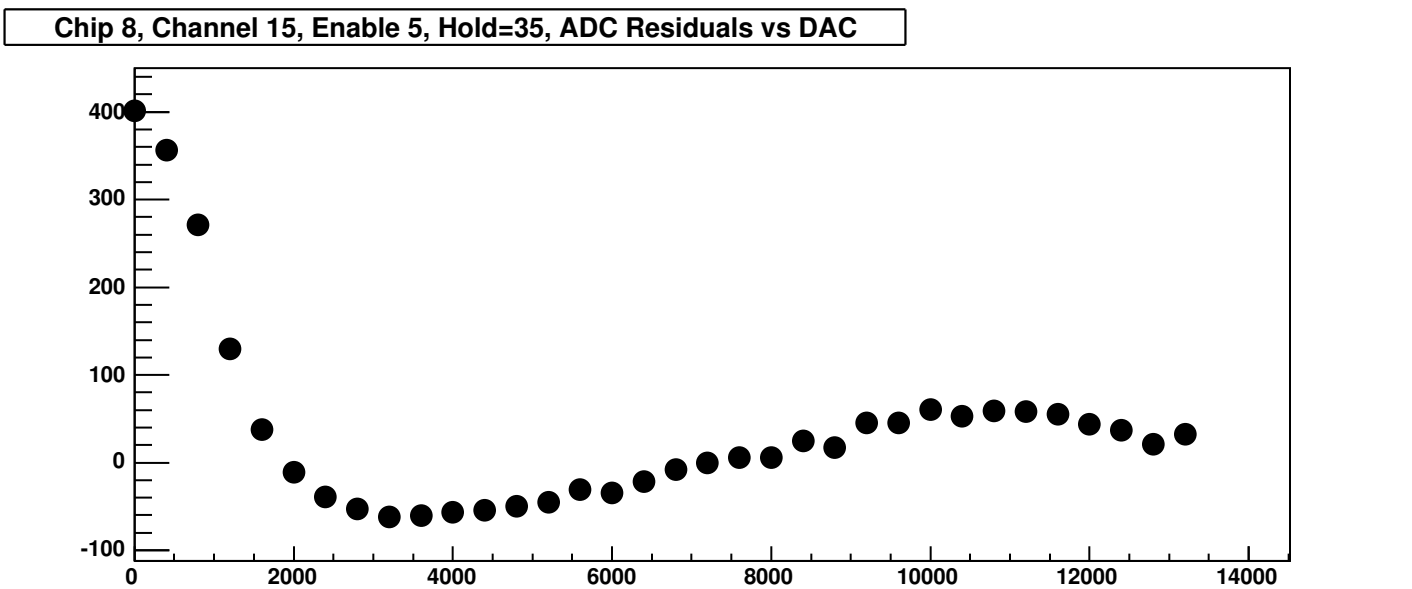

Chip 8, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.63 / 23
p0 80.8 ± 0.81
p1 0.01114 ± 0.0001287
260
240
220
200
180
160
140
120
100
80
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

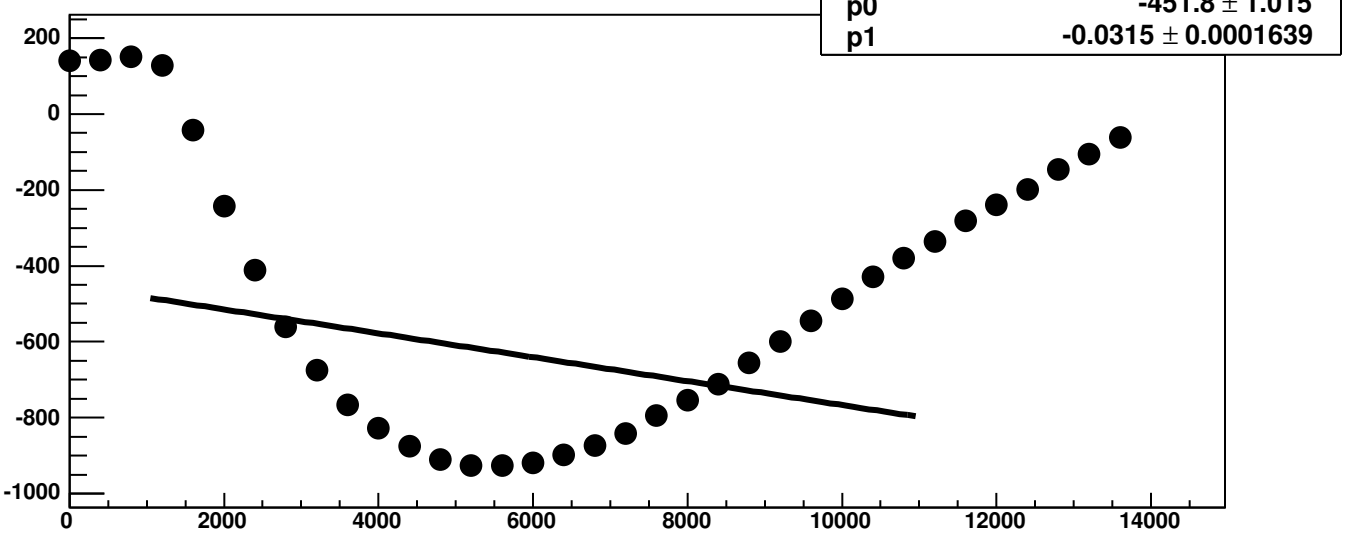

Chip 8, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 6822 / 23
p0 -469.9 ± 1.3
p1 0.1123 ± 0.0002375
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

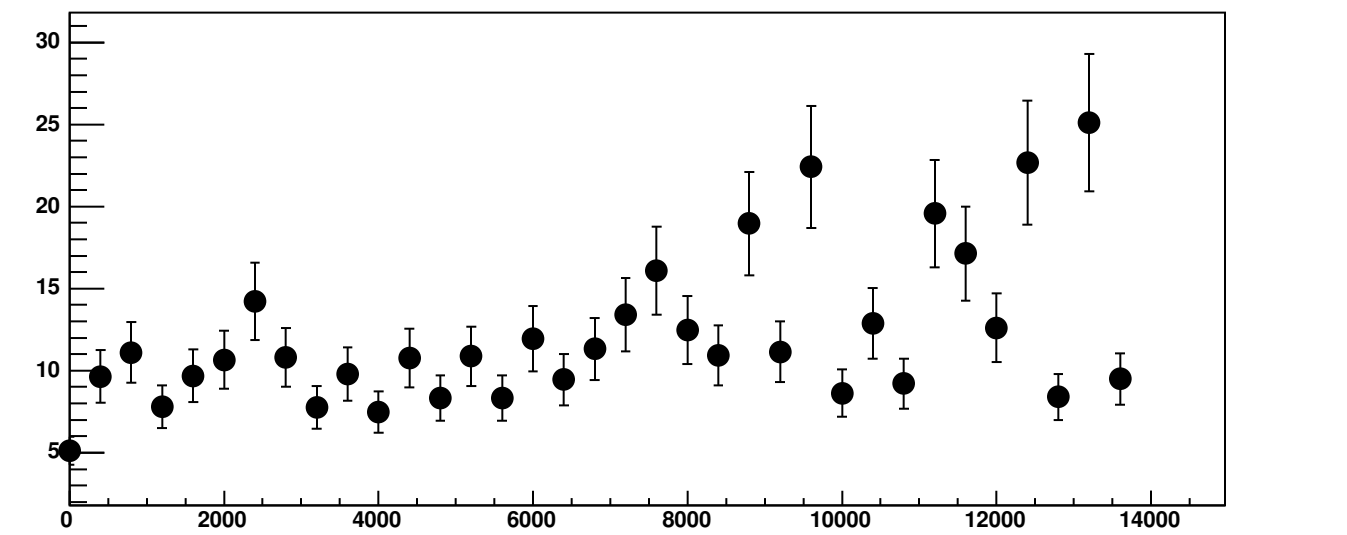

Chip 8, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

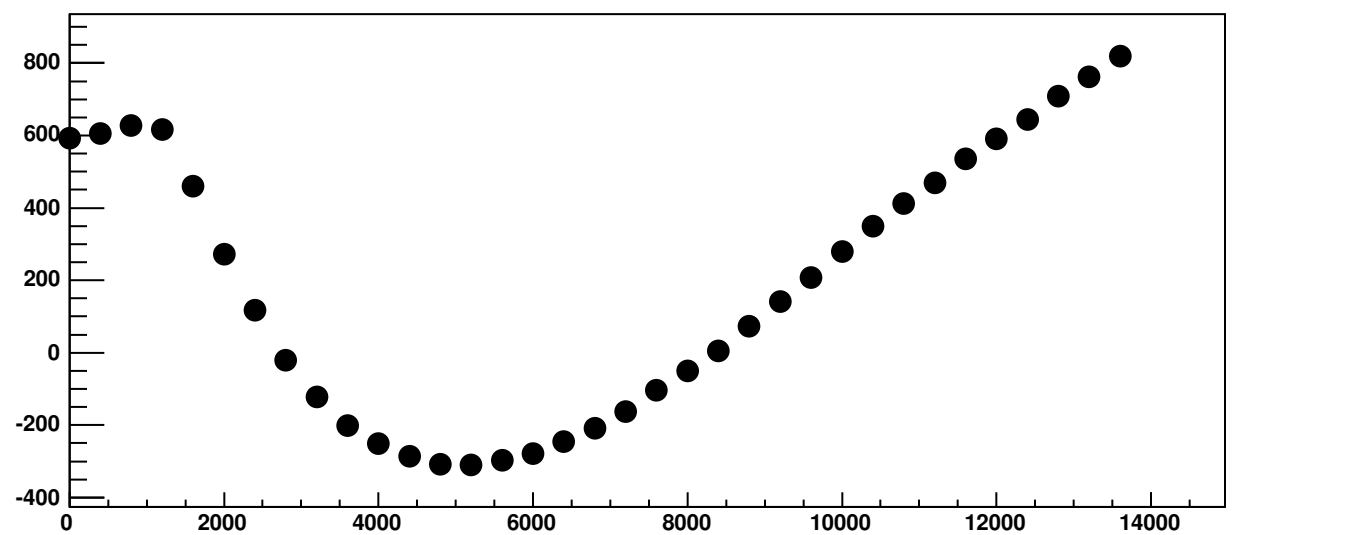
Chip 8, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

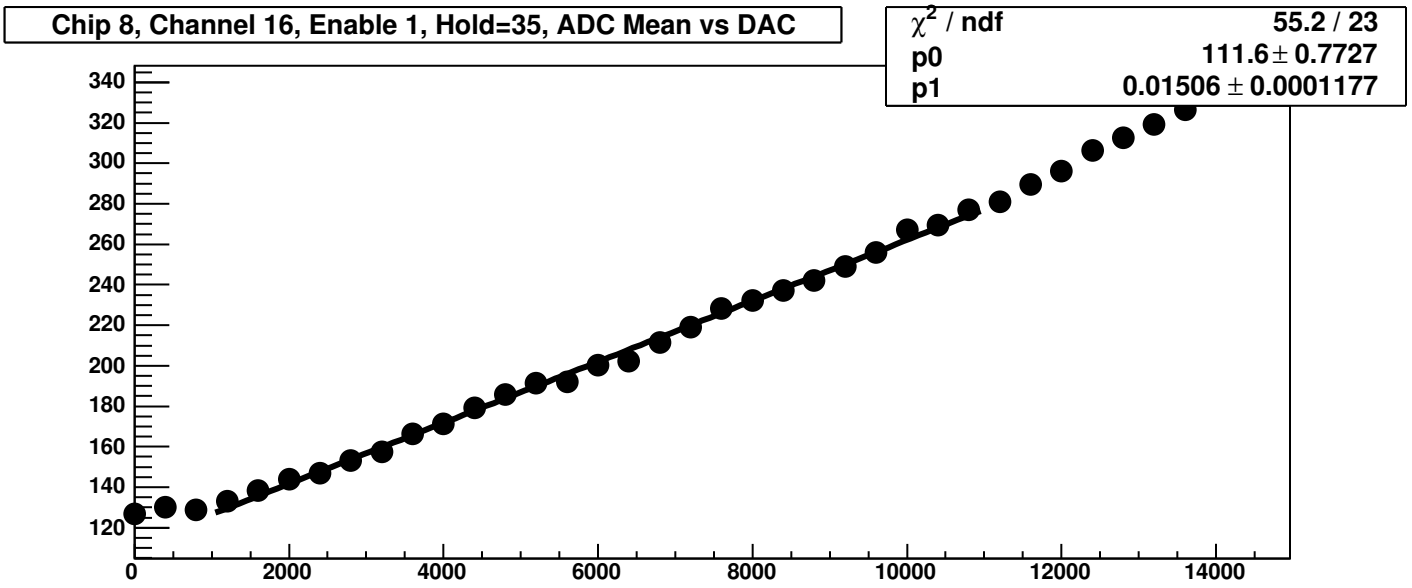


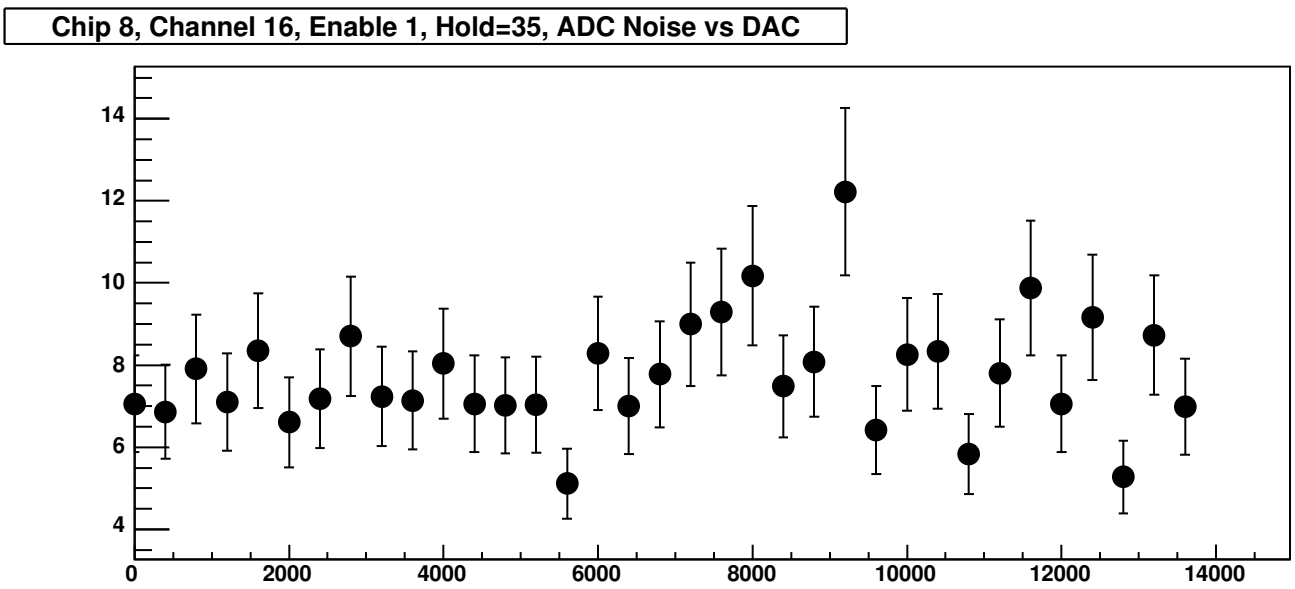
Chip 8, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC

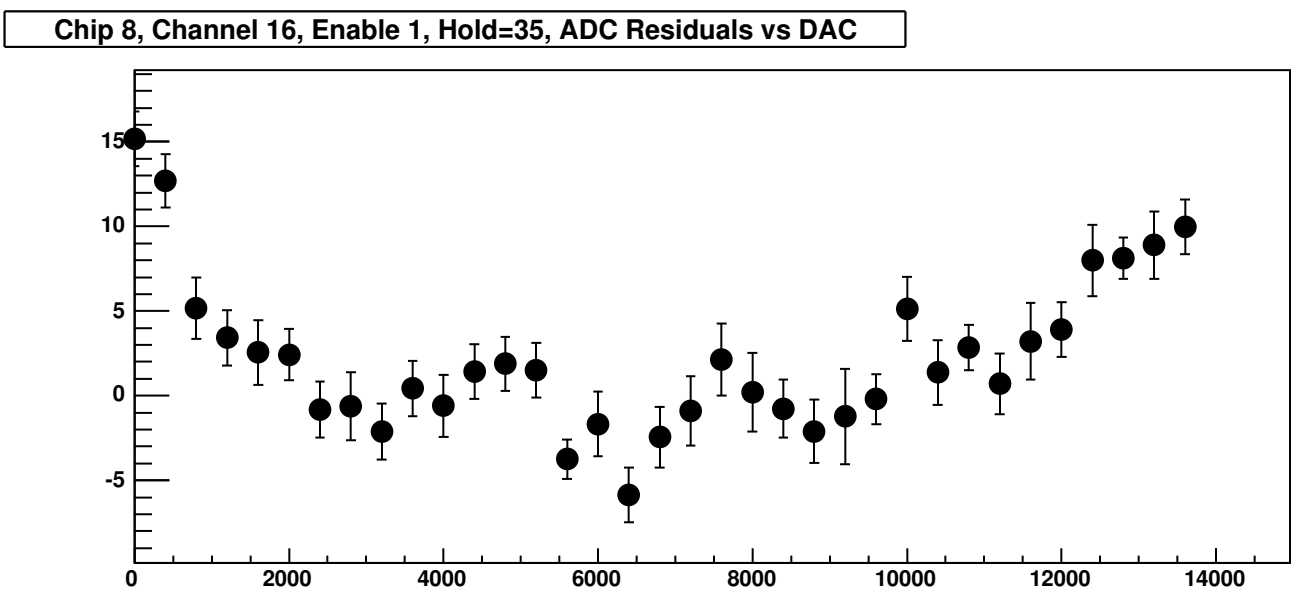


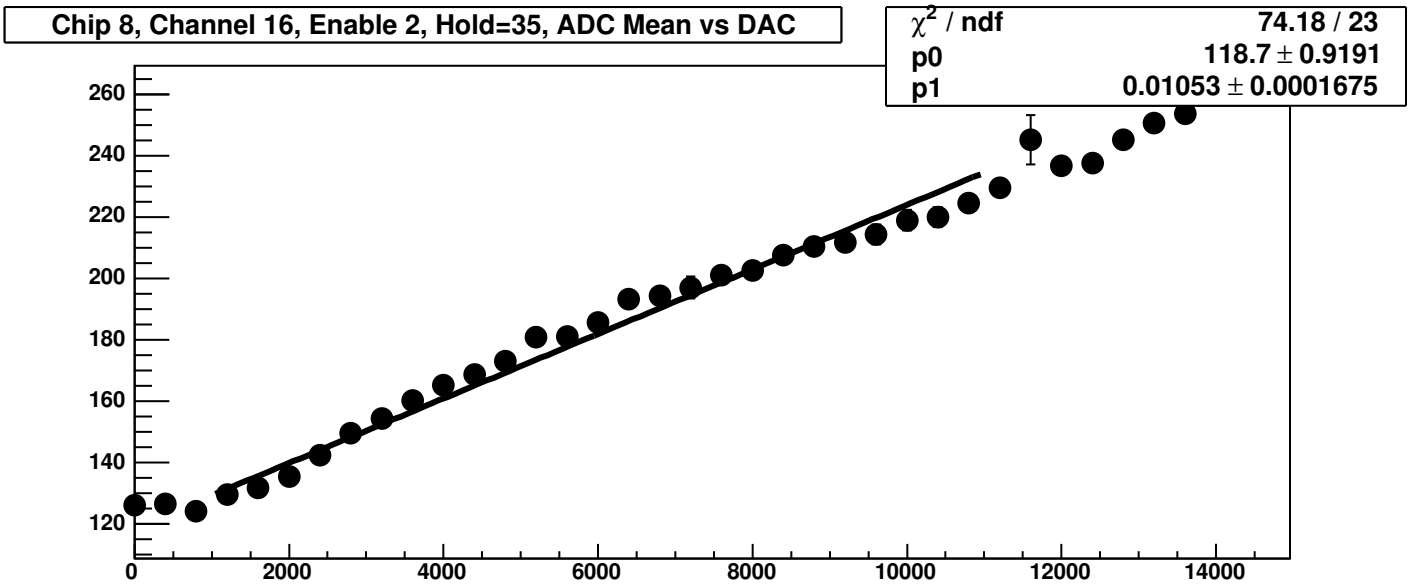
Chip 8, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

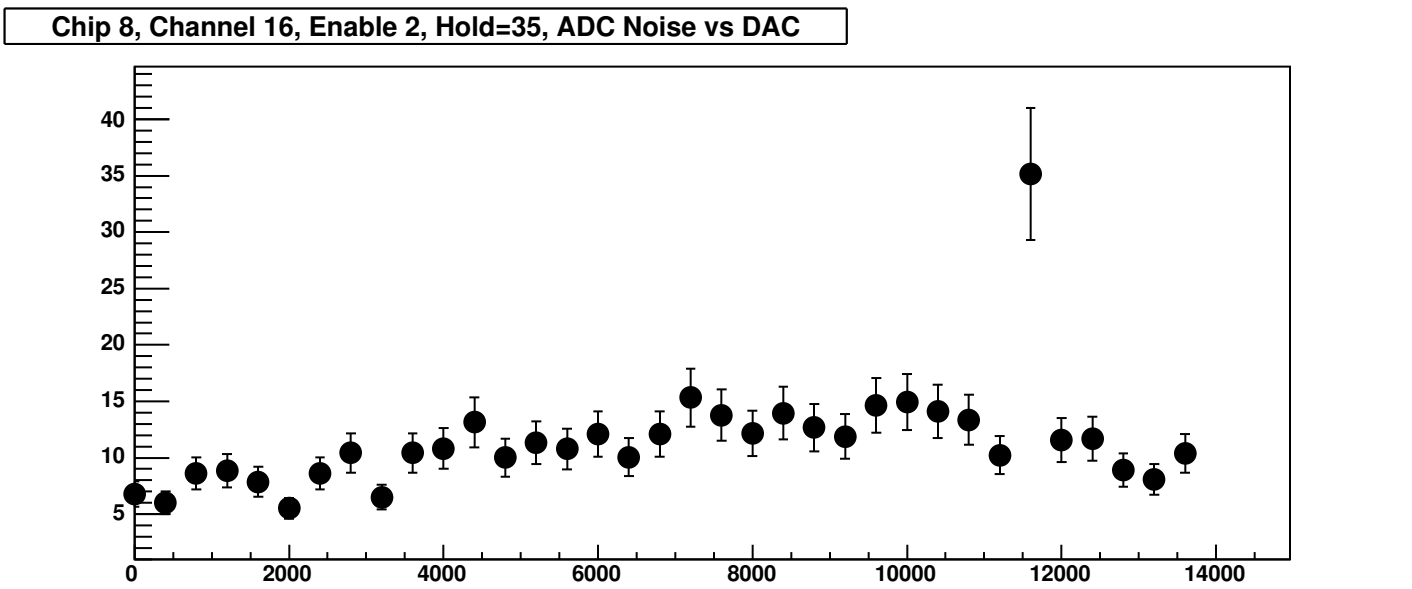


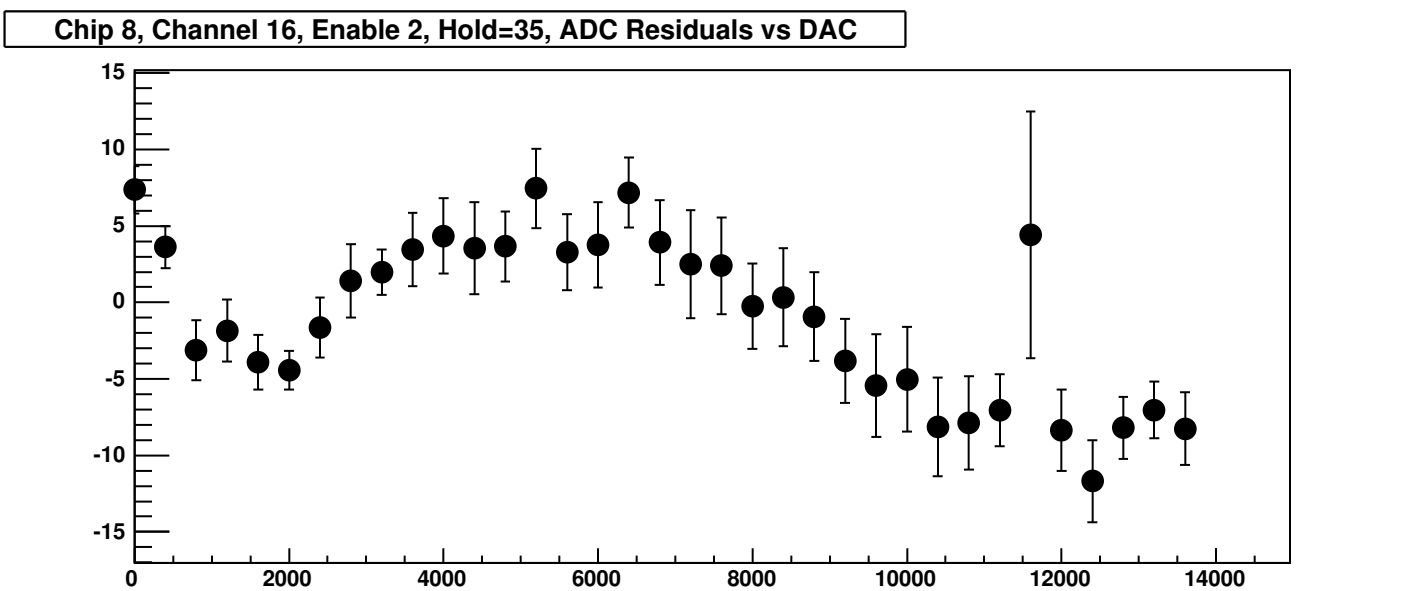

Chip 8, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 55.2 / 23
p0 111.6 ± 0.7727
p1 0.01506 ± 0.0001177
340
320
300
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

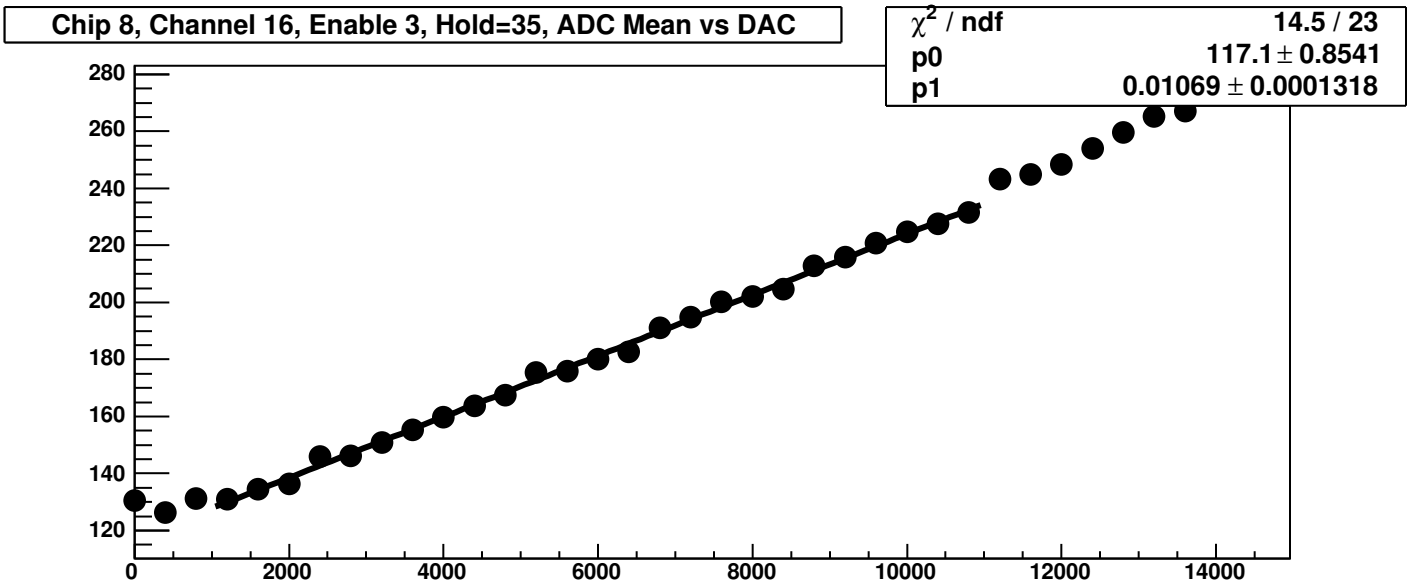

Chip 8, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

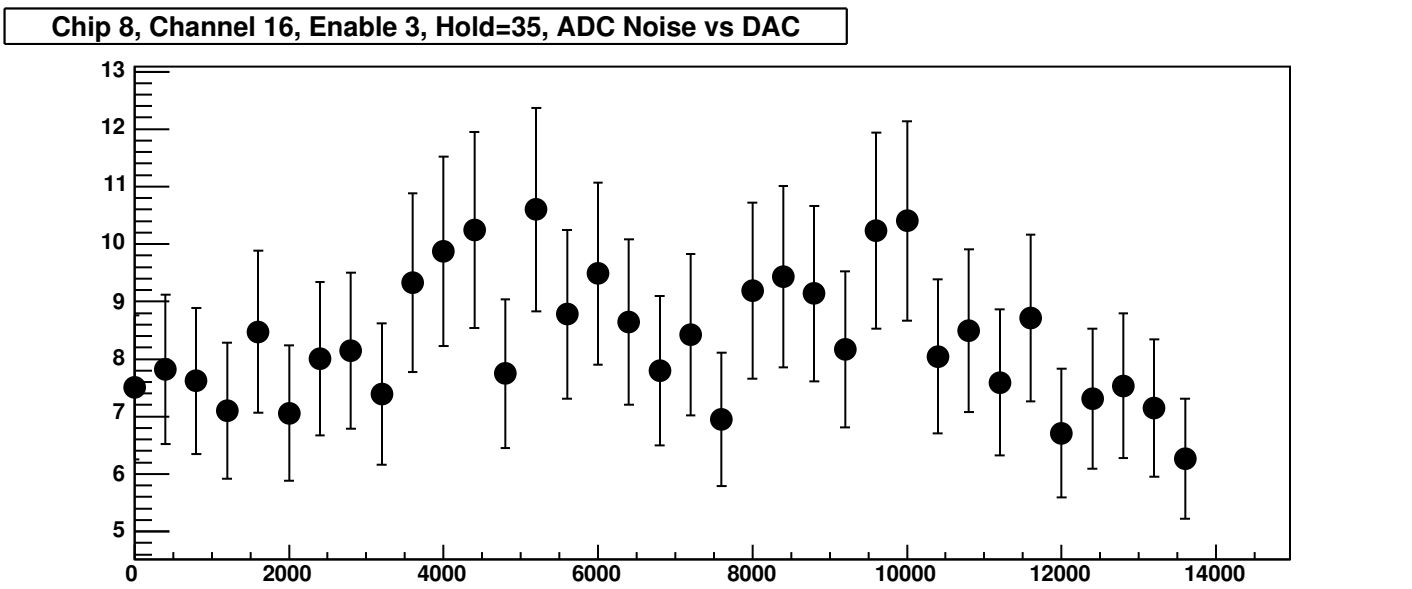

Chip 8, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

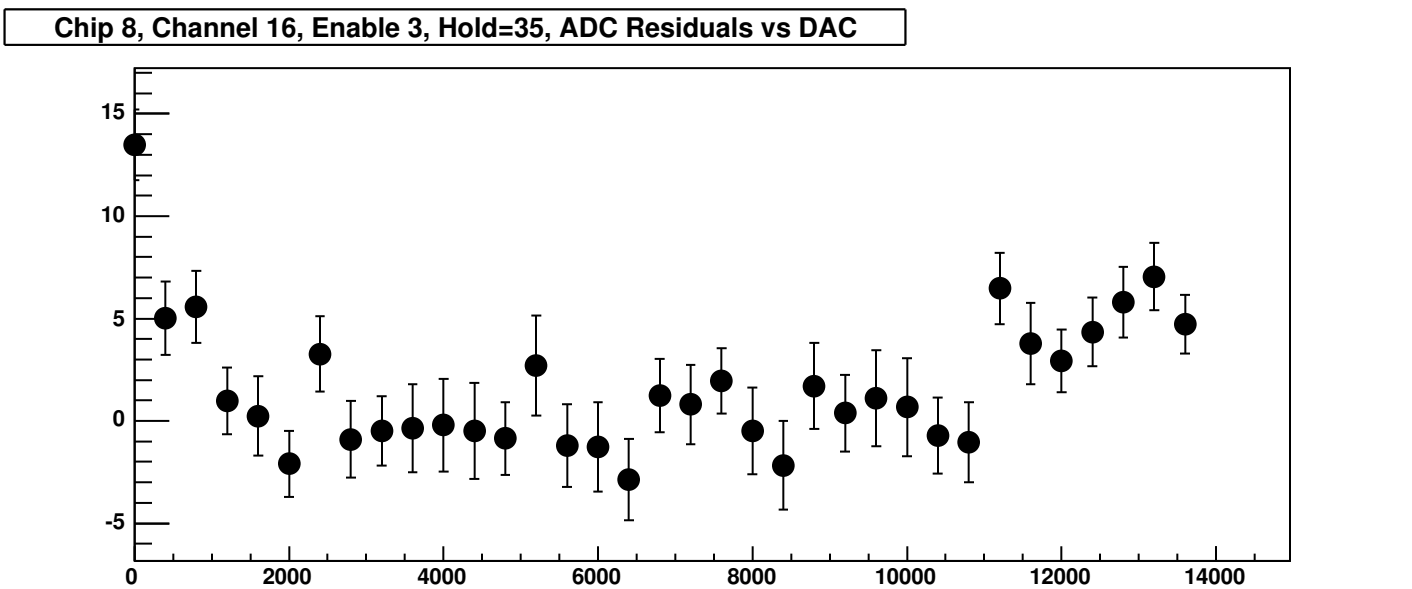

Chip 8, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 74.18 / 23
p0 118.7 ± 0.9191
p1 0.01053 ± 0.0001675
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

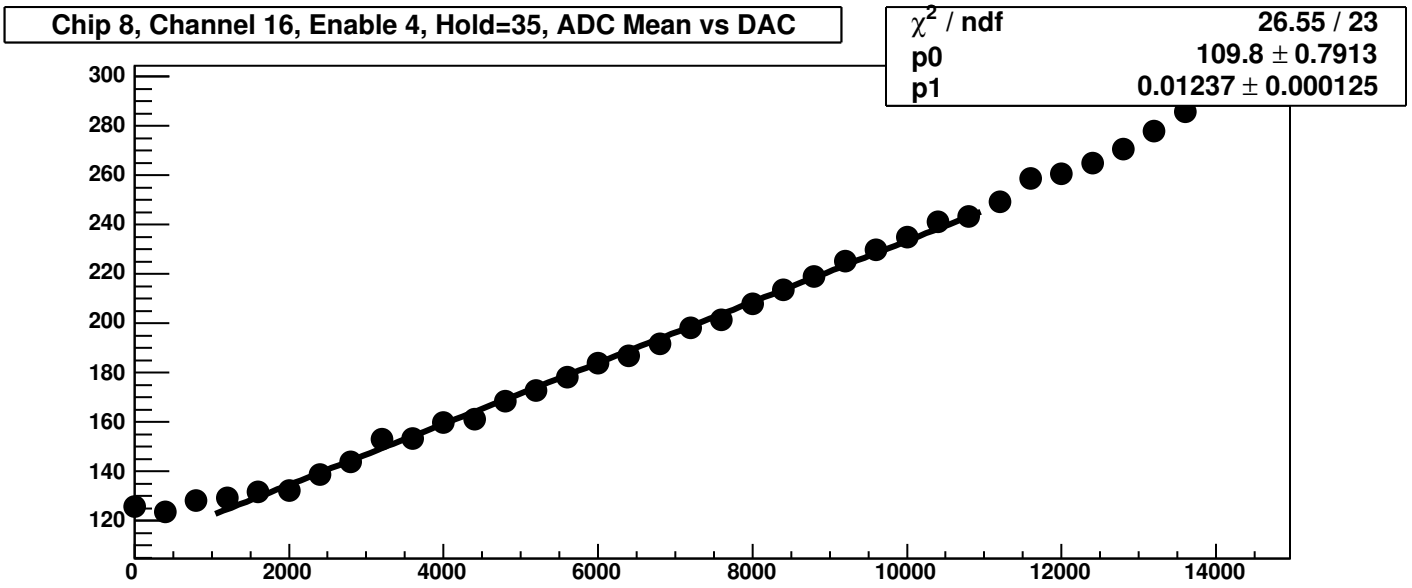

Chip 8, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

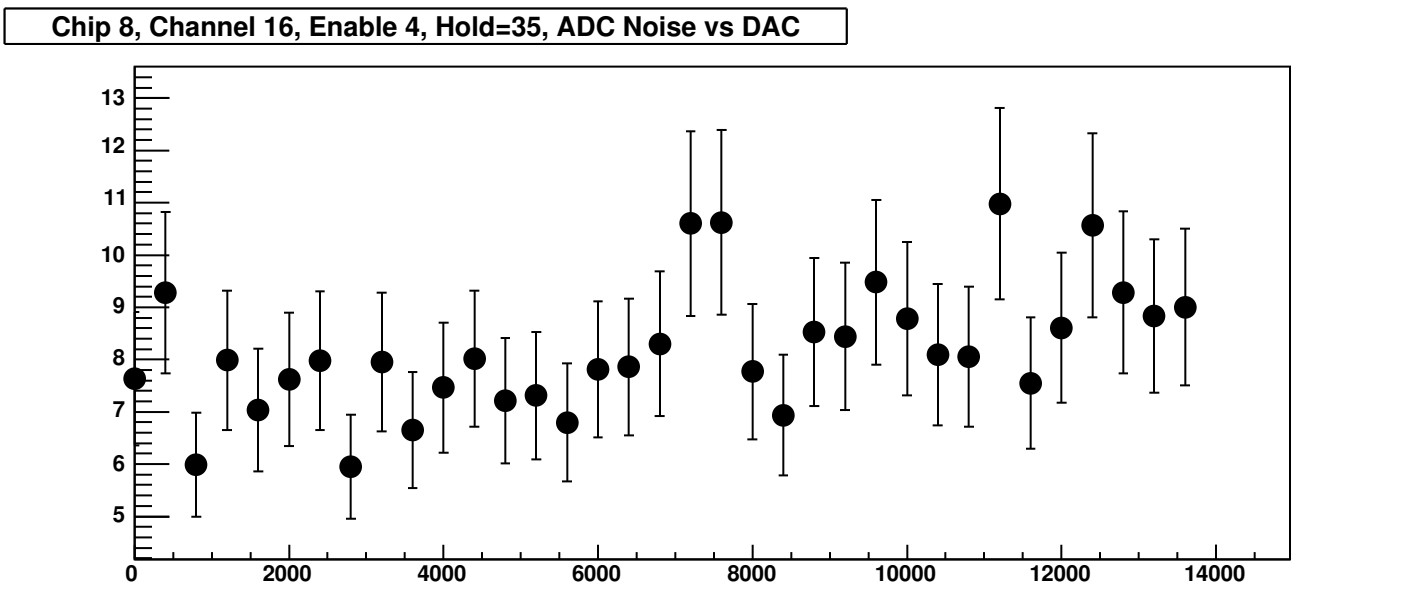

Chip 8, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

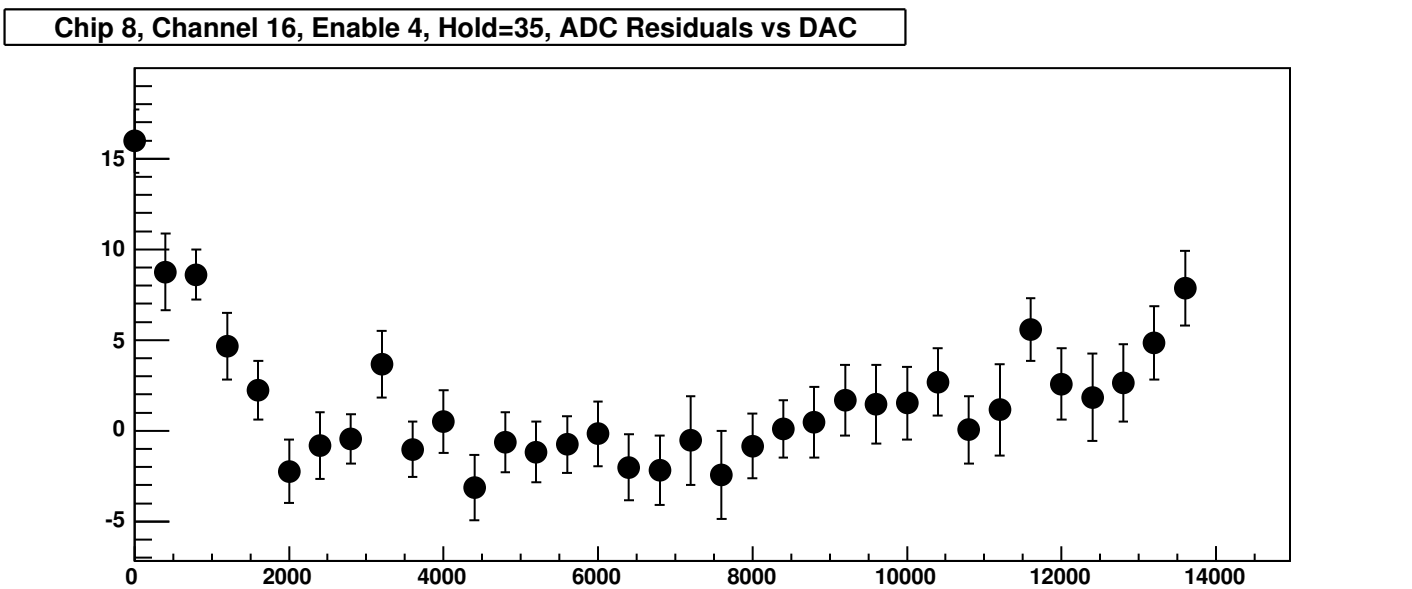

Chip 8, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 14.5 / 23
p0 117.1 ± 0.8541
p1 0.01069 ± 0.0001318
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

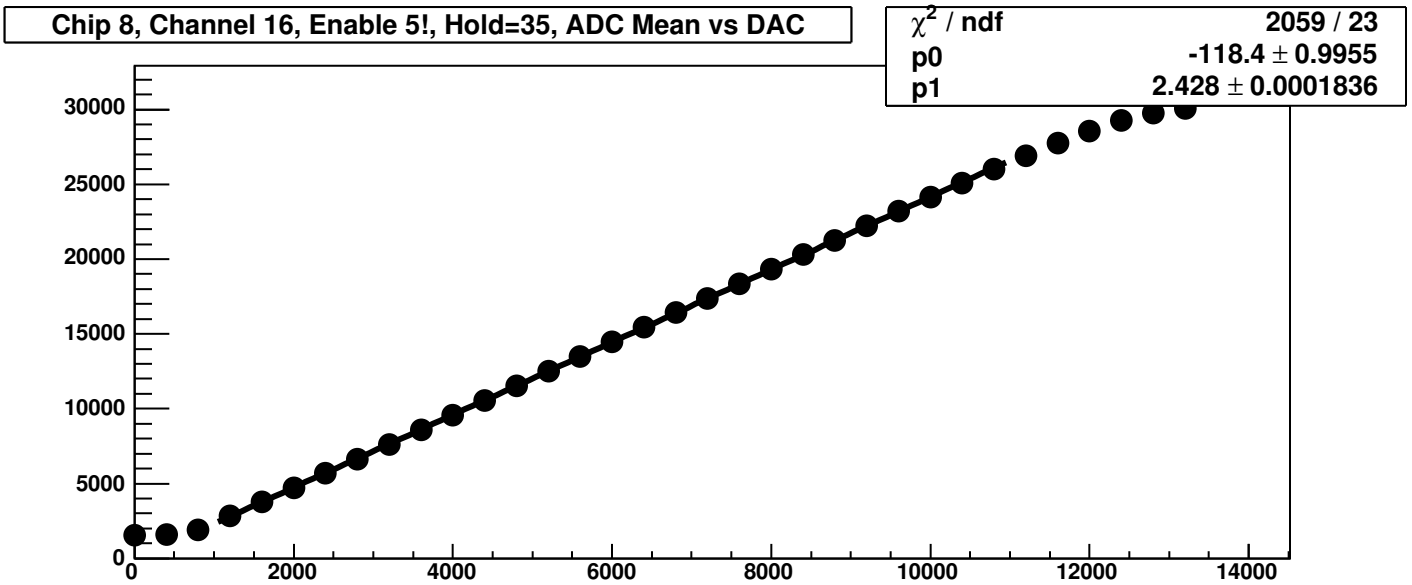

Chip 8, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

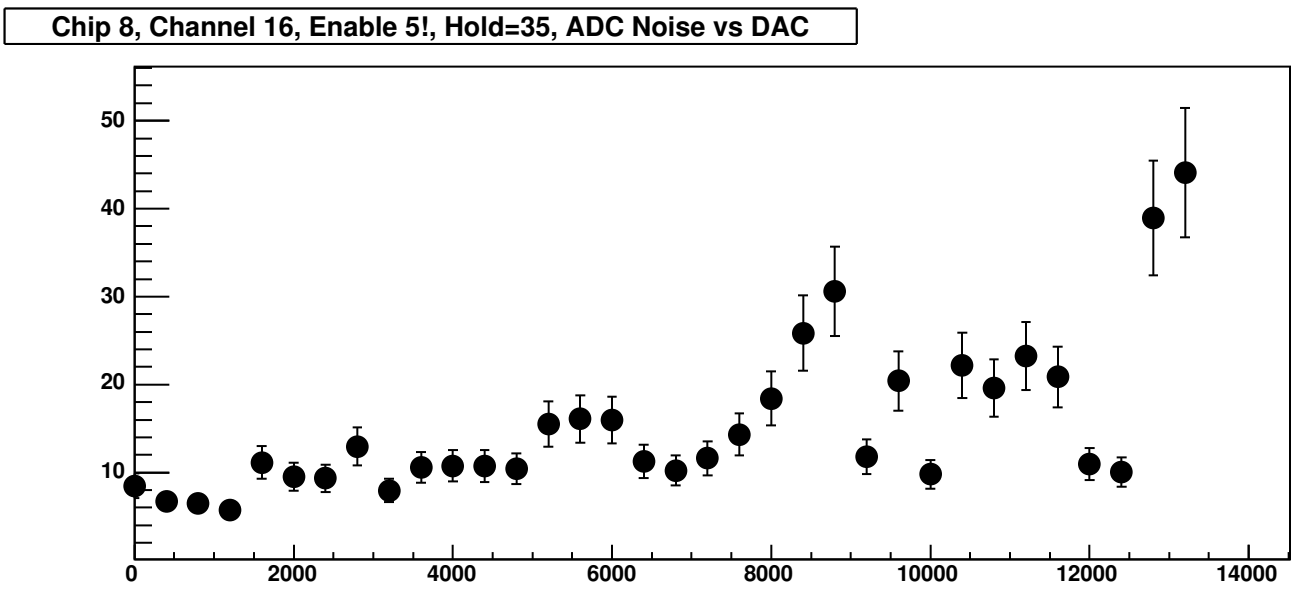

Chip 8, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

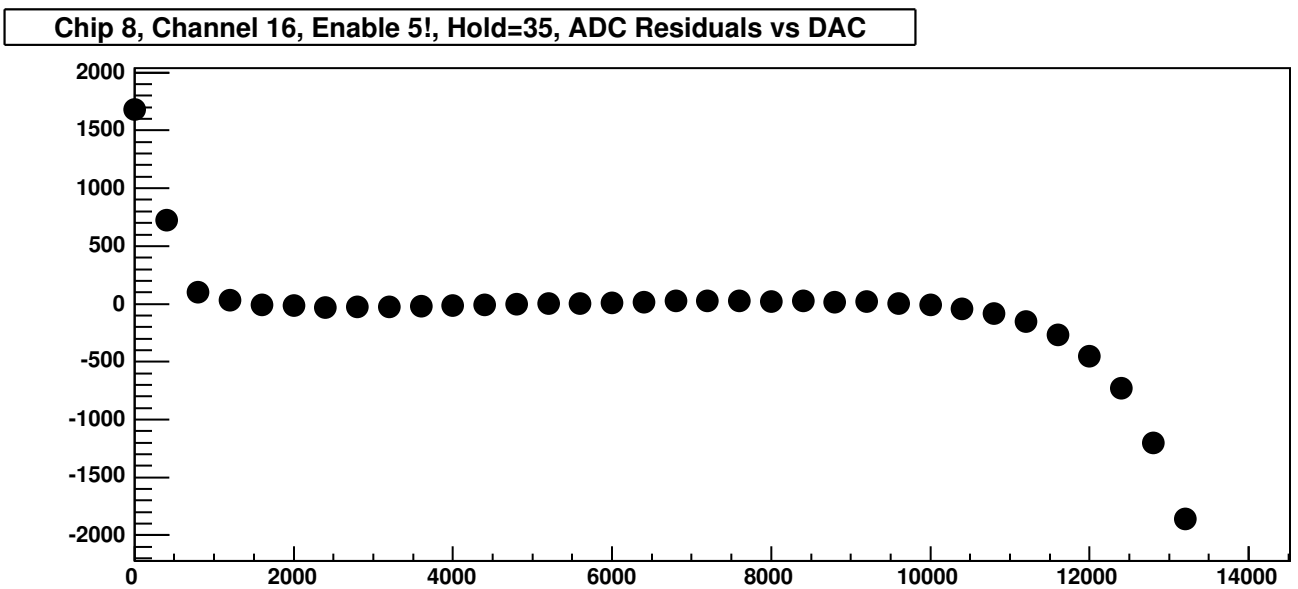

Chip 8, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.55 / 23
p0 109.8 ± 0.7913
p1 0.01237 ± 0.000125
300
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

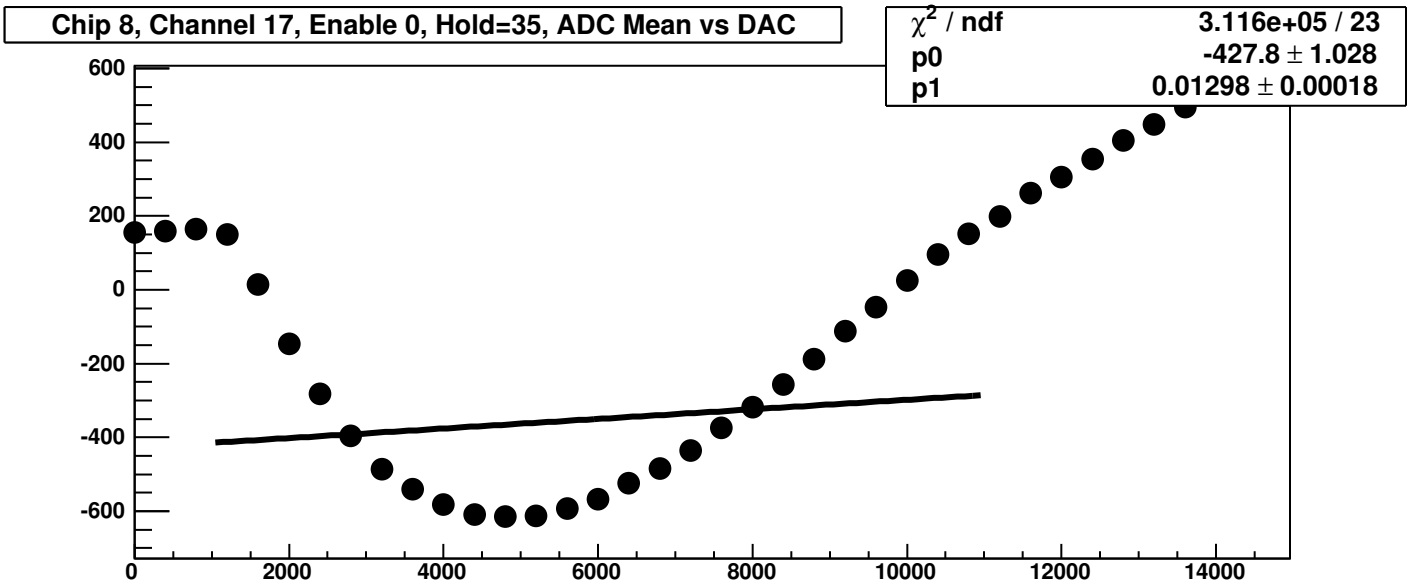

Chip 8, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

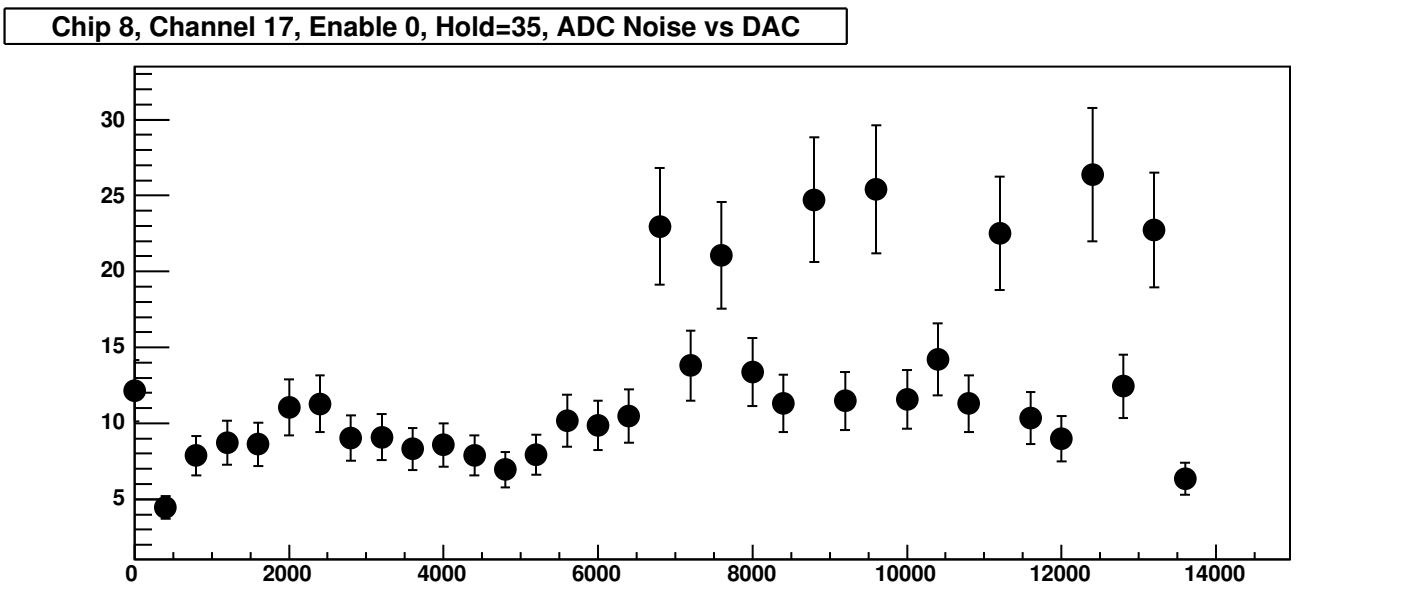

Chip 8, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

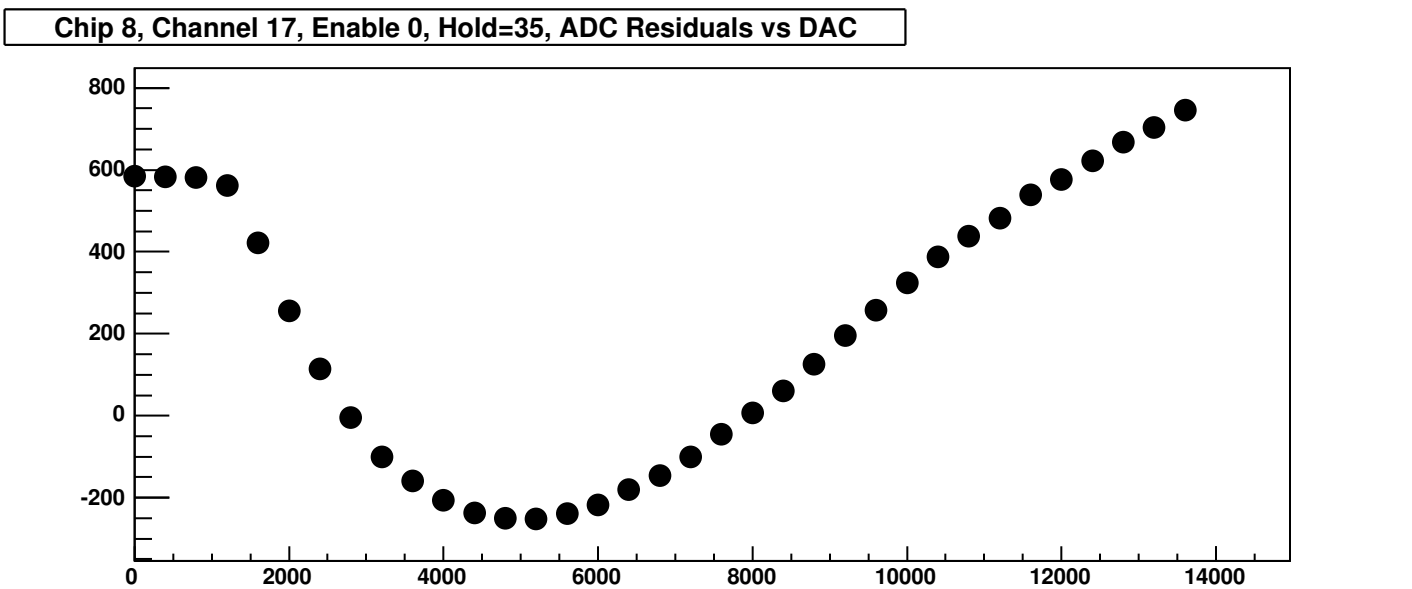

Chip 8, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2059 / 23
p0 -118.4 ± 0.9955
p1 2.428 ± 0.0001836
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

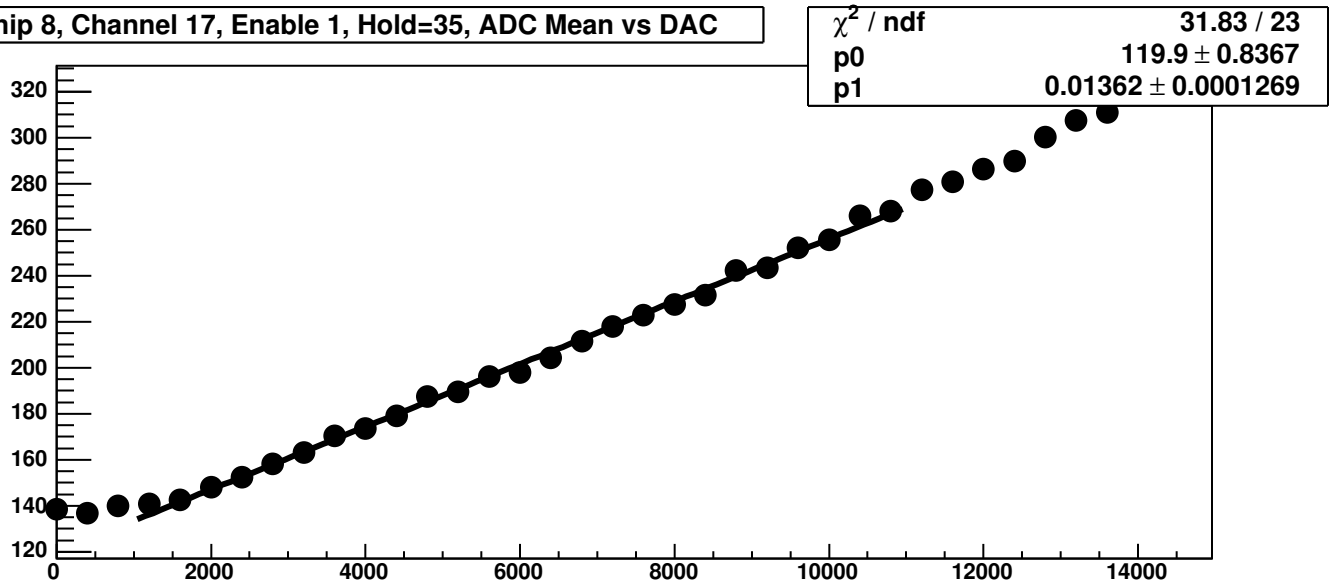

Chip 8, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.116e+05 / 23
p0 -427.8 ± 1.028
p1 0.01298 ± 0.00018
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

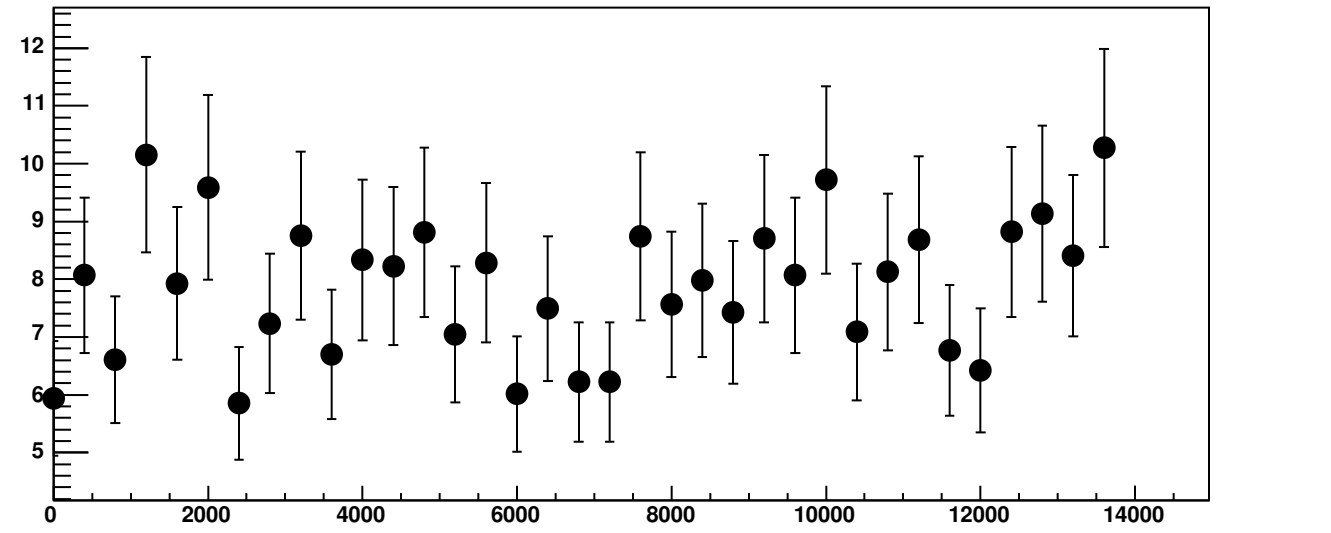

Chip 8, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 8, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

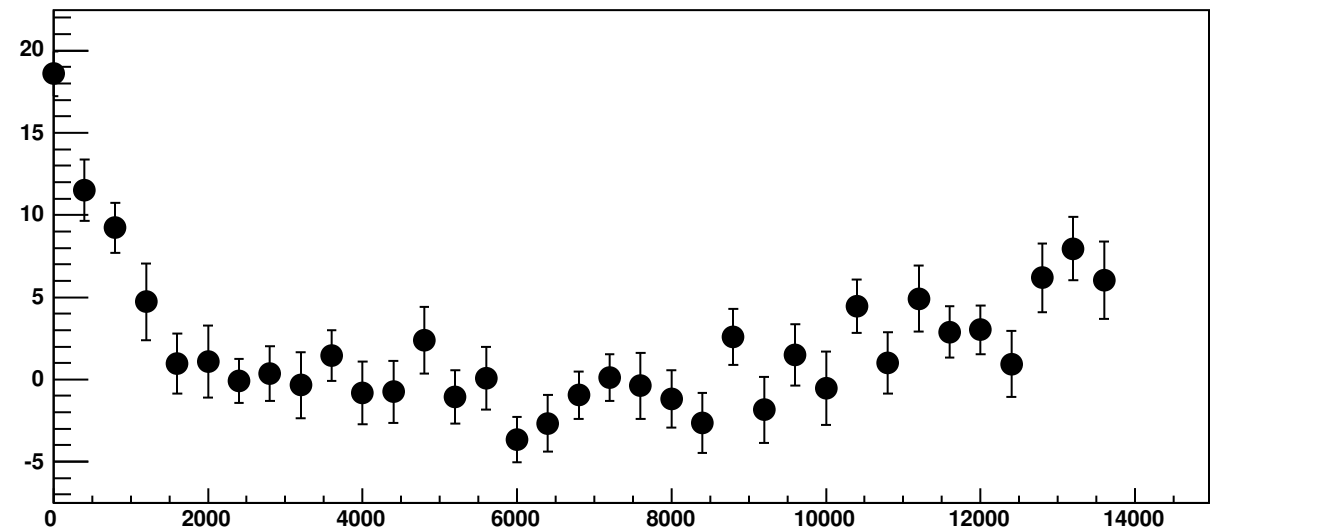
Chip 8, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

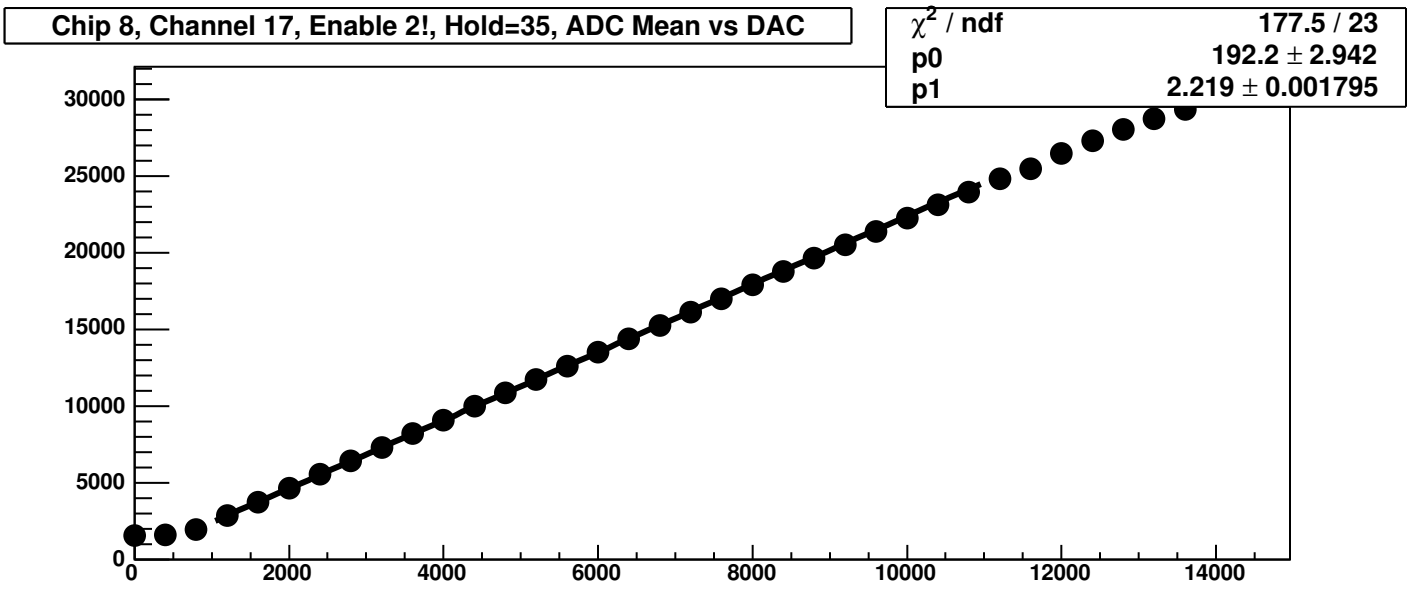


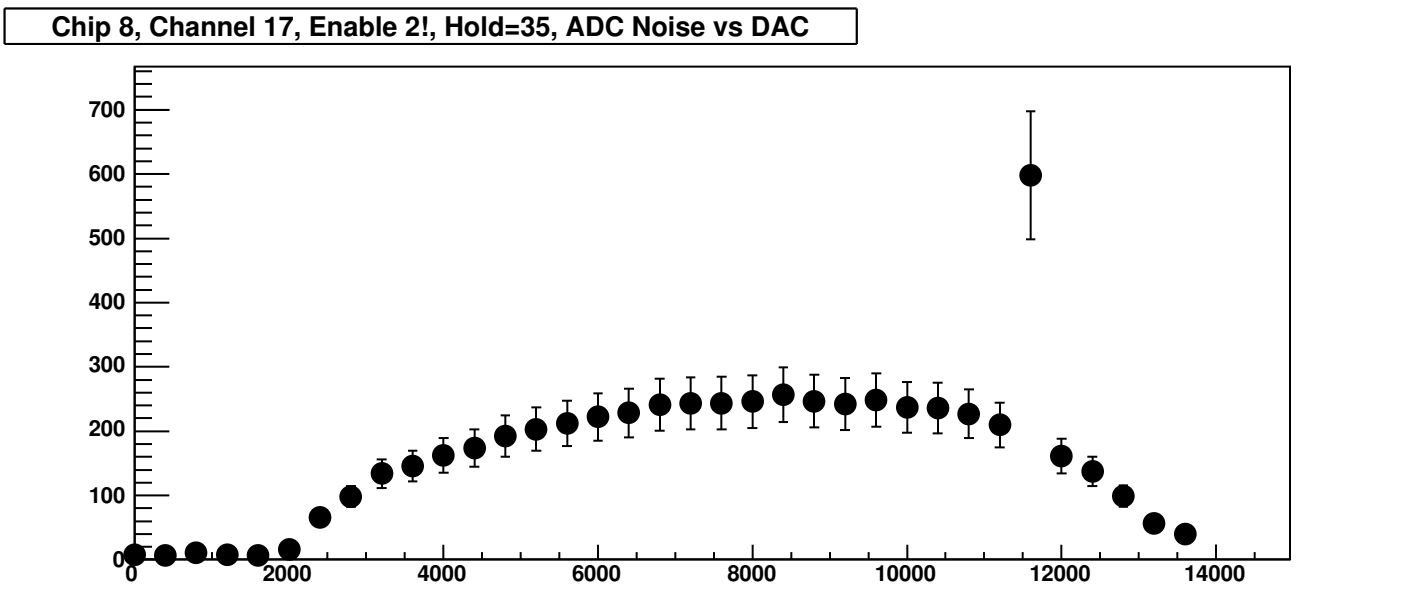
Chip 8, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

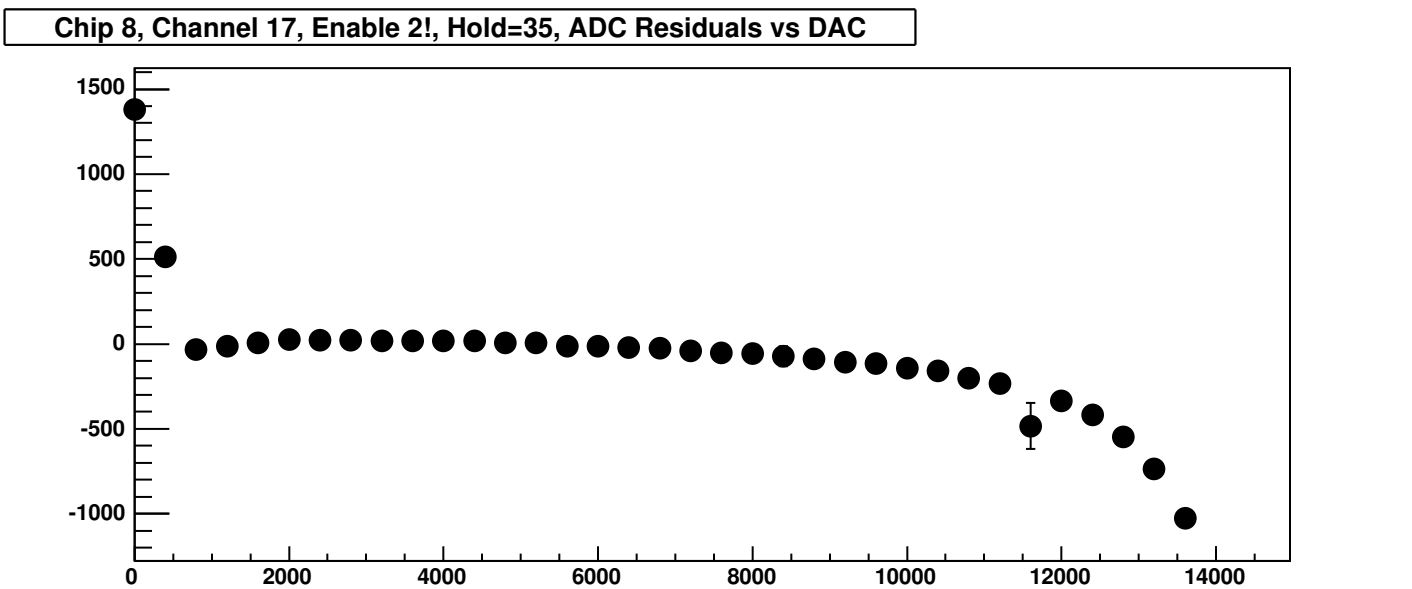


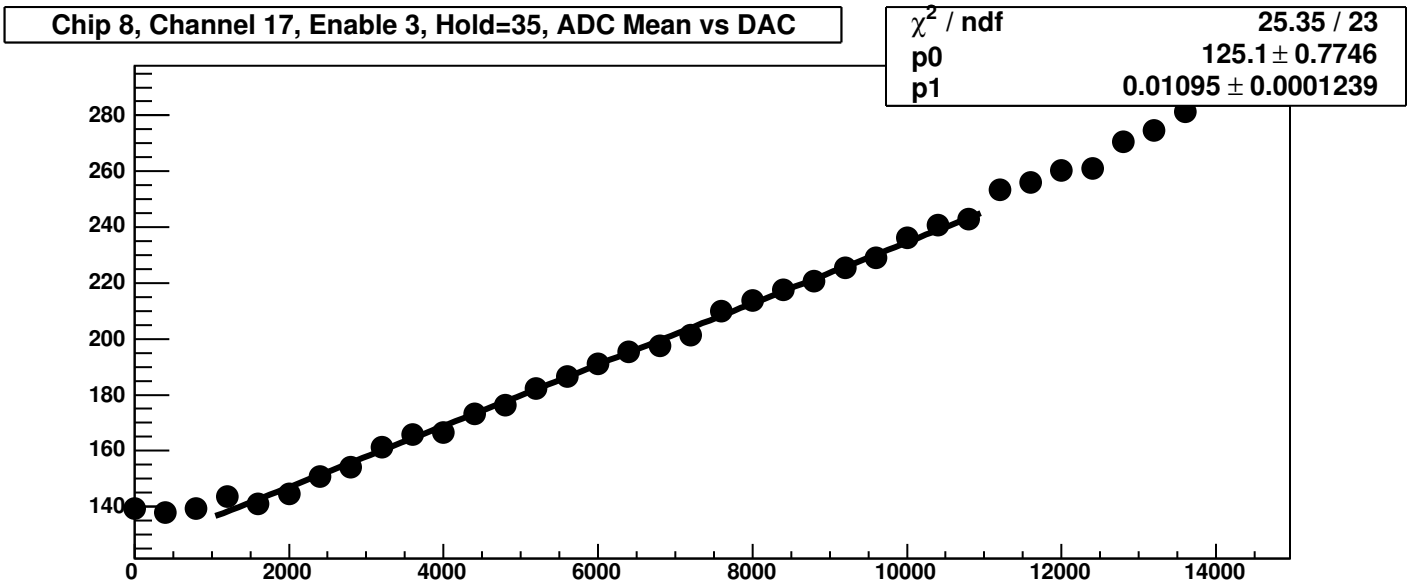
Chip 8, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

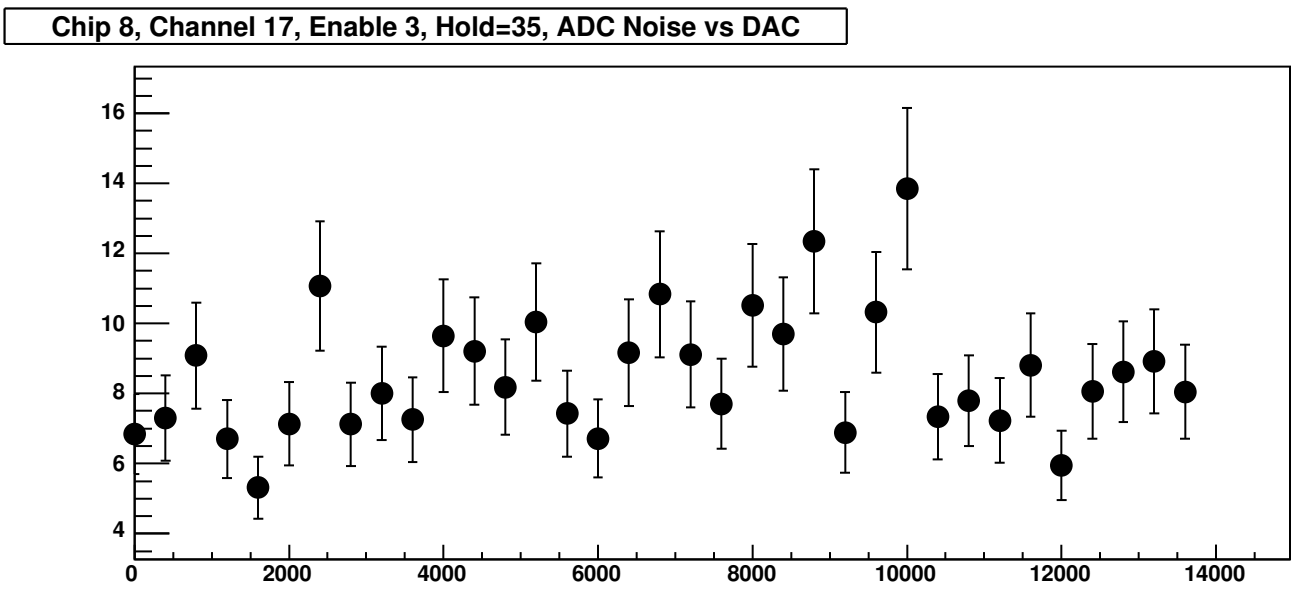


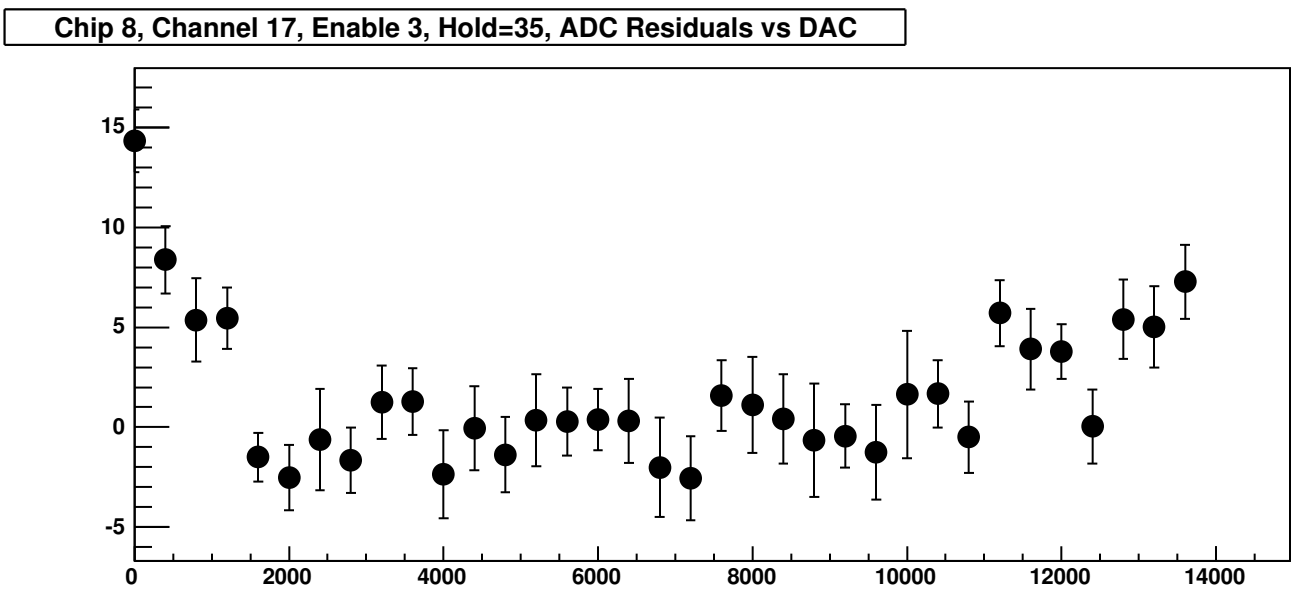

Chip 8, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 177.5 / 23
p0 192.2 ± 2.942
p1 2.219 ± 0.001795
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

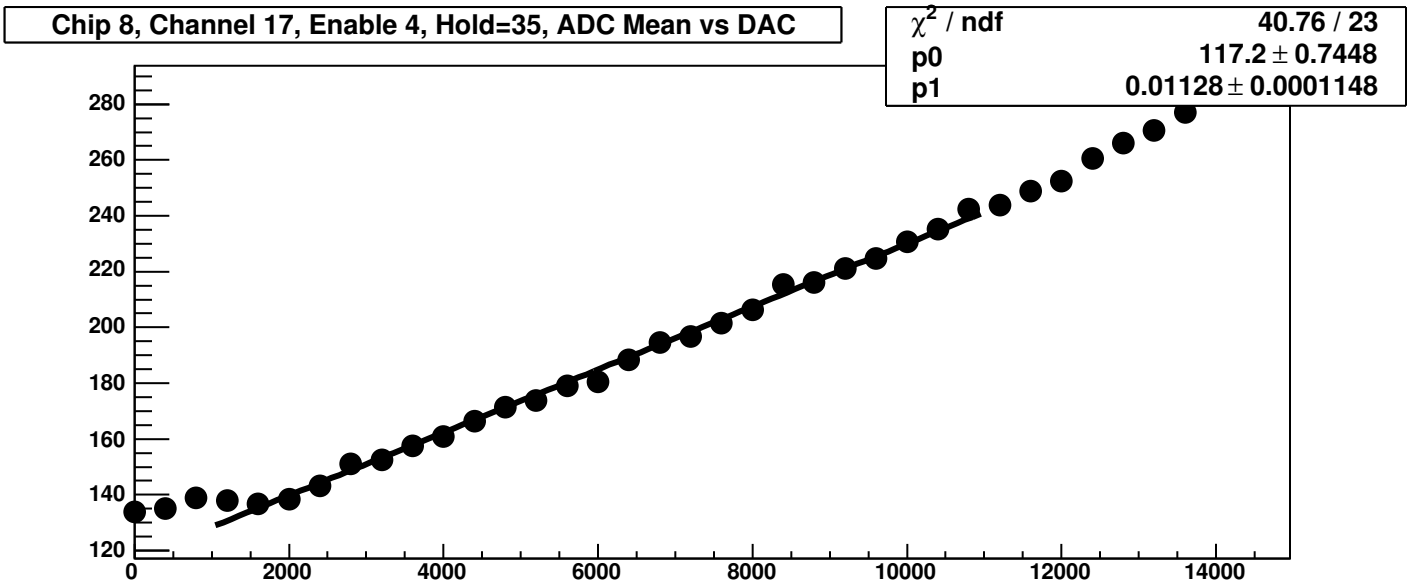

Chip 8, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

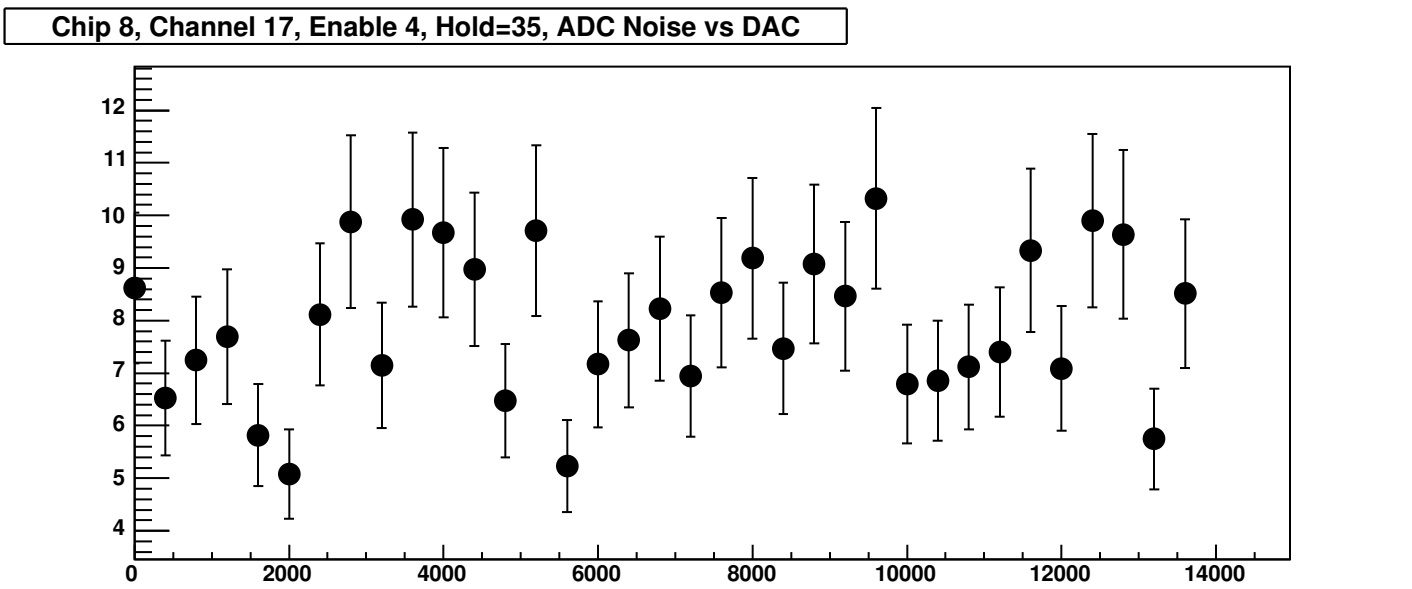

Chip 8, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

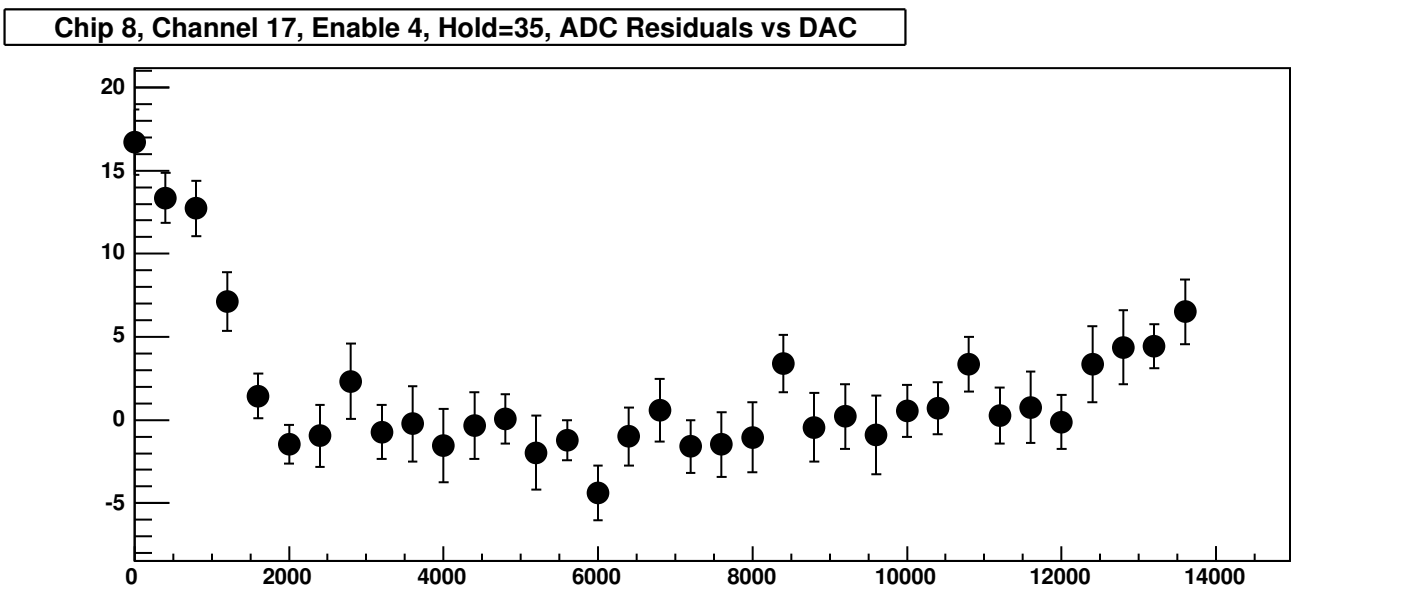

Chip 8, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.35 / 23
p0 125.1 ± 0.7746
p1 0.01095 ± 0.0001239
280
260
240
220
200
180
160
140
0
2000
4000
6000
8000
10000
12000
14000

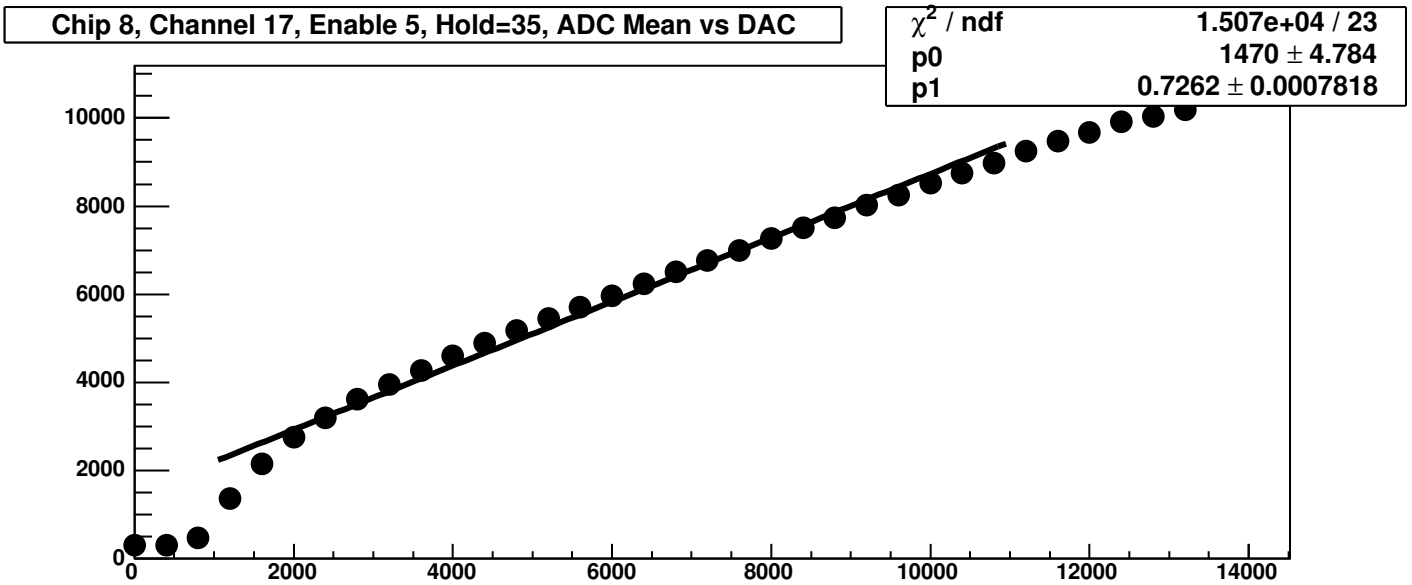

Chip 8, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

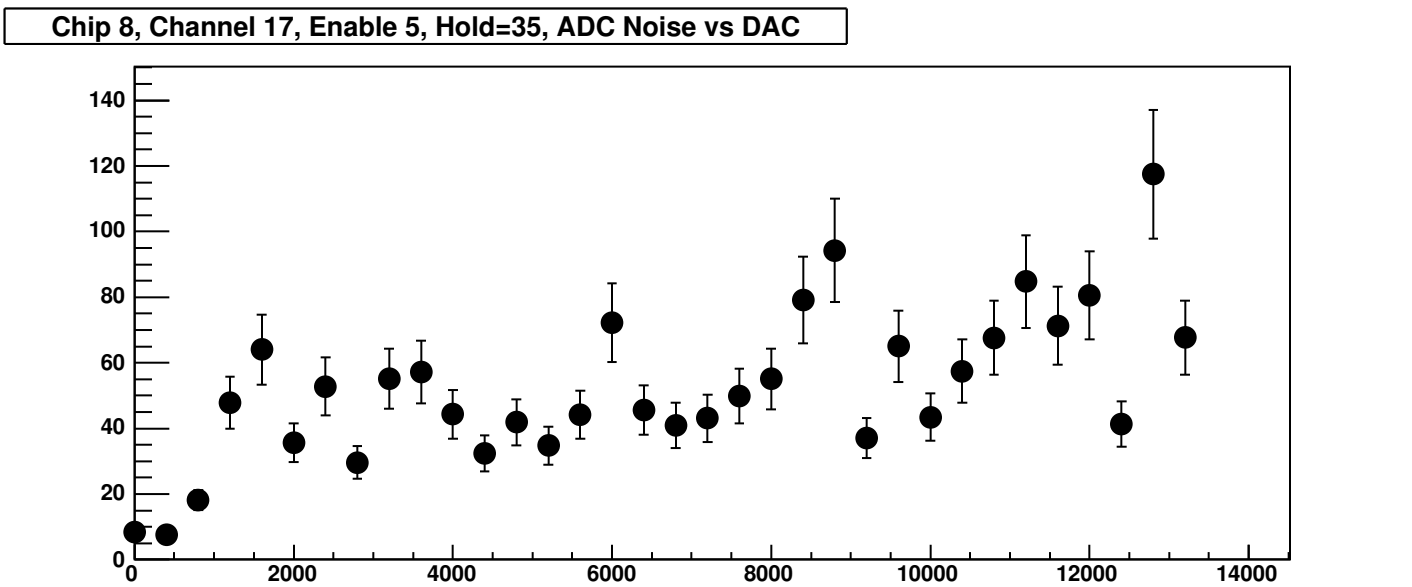

Chip 8, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

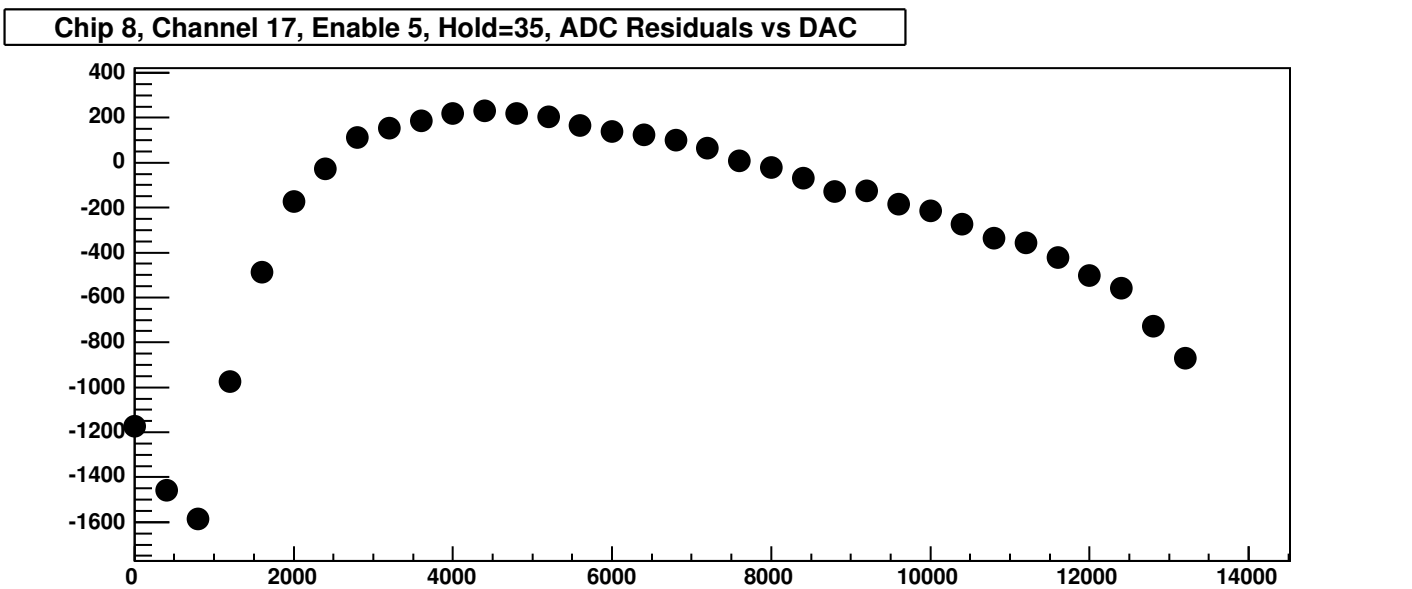

Chip 8, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 40.76 / 23
p0 117.2 ± 0.7448
p1 0.01128 ± 0.0001148
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

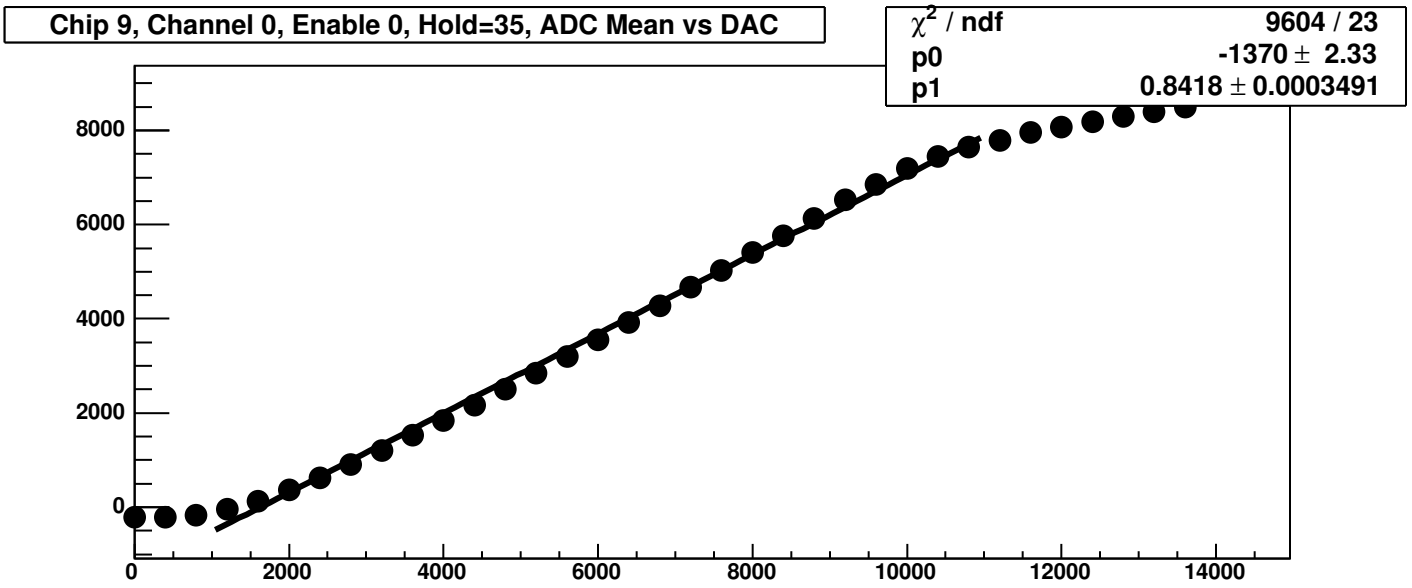

Chip 8, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

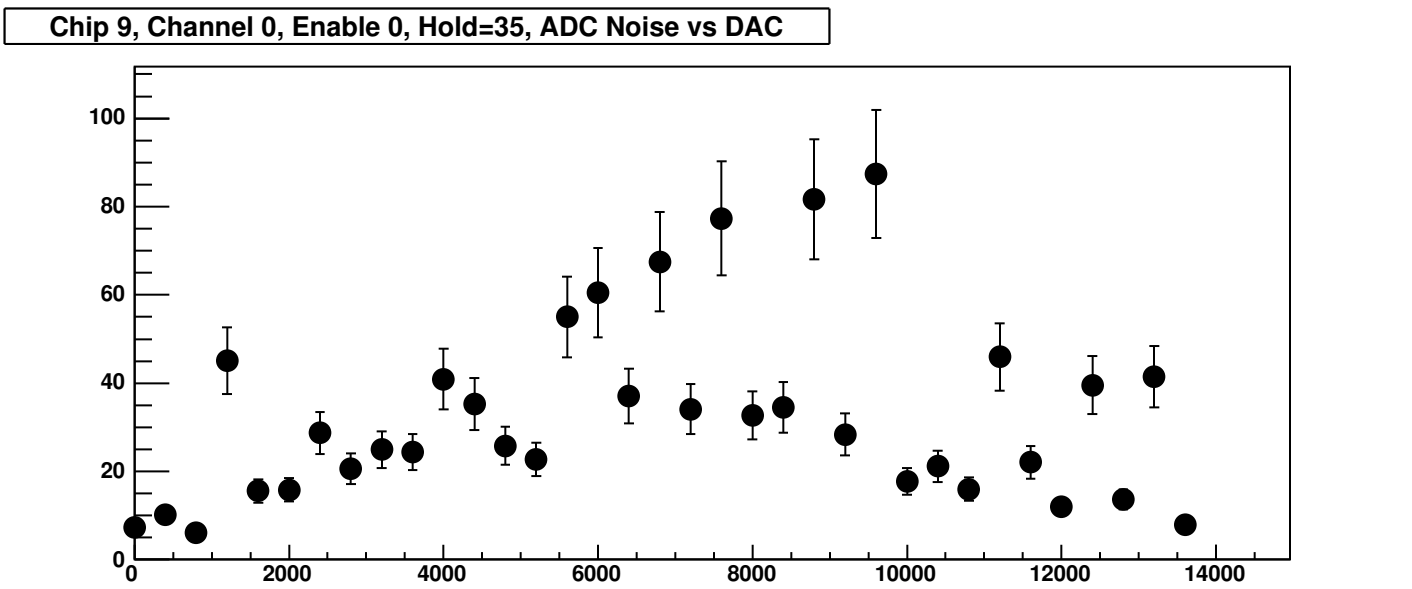

Chip 8, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

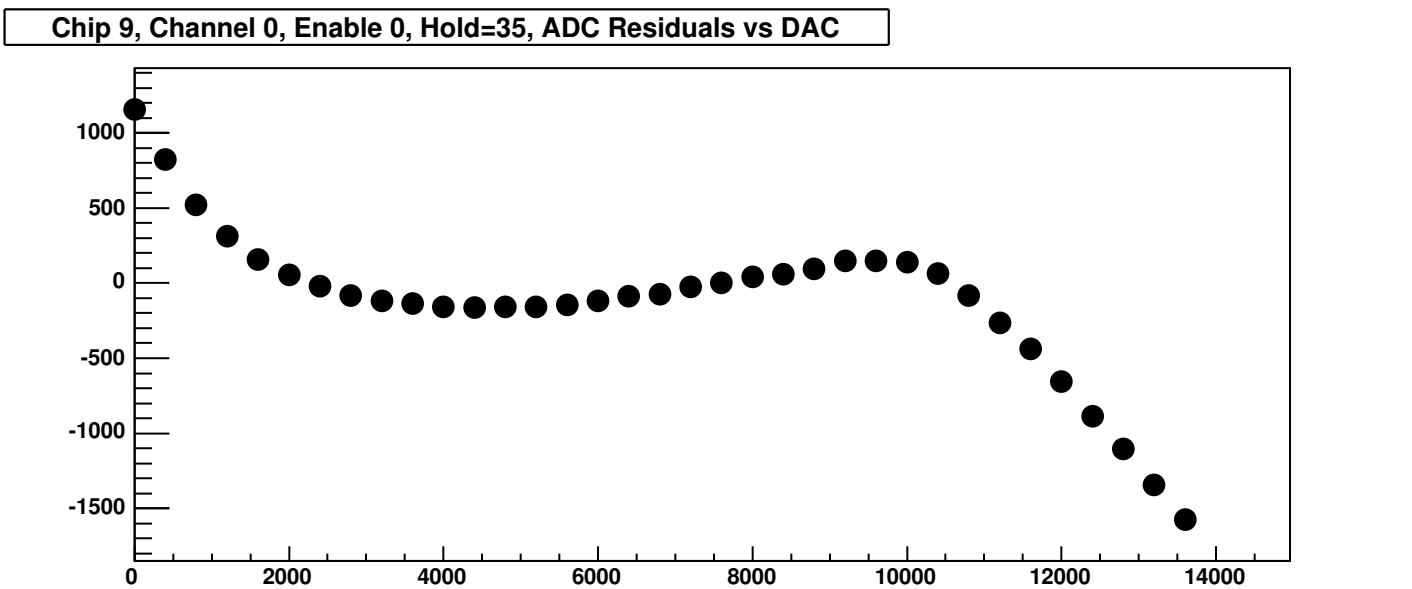

Chip 8, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.507e+04 / 23
p0 1470 ± 4.784
p1 0.7262 ± 0.0007818
10000
8000
6000
4000
2000
0
0
2000
4000
6000
8000
10000
12000
14000

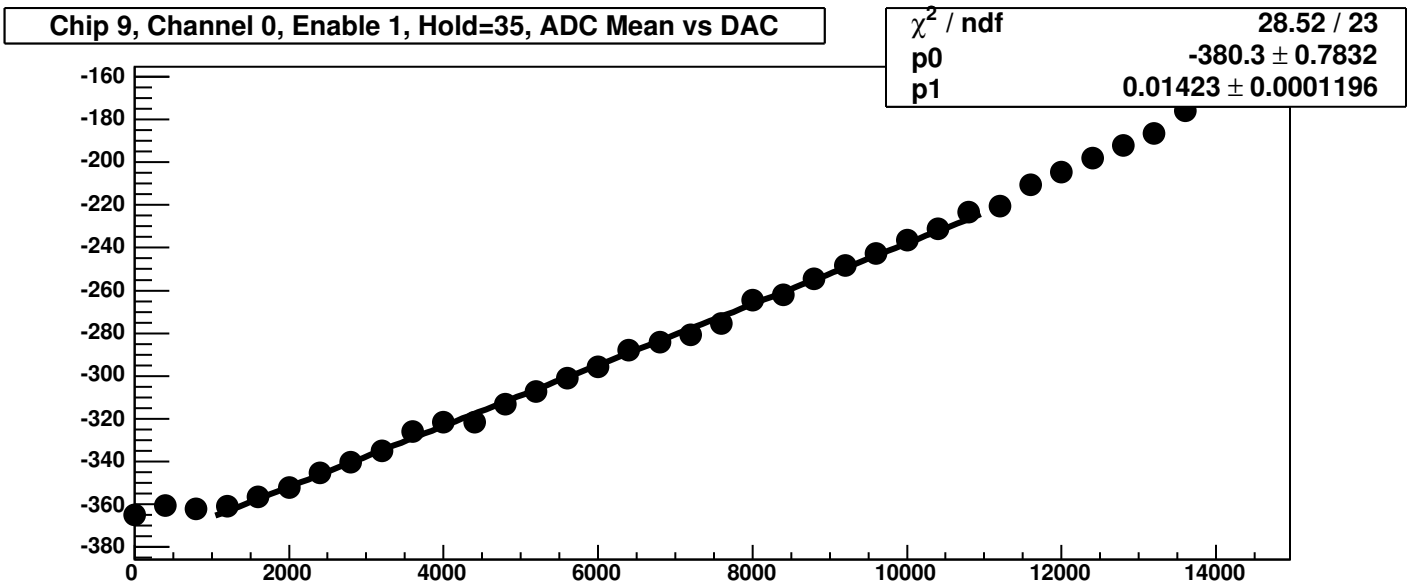

Chip 8, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

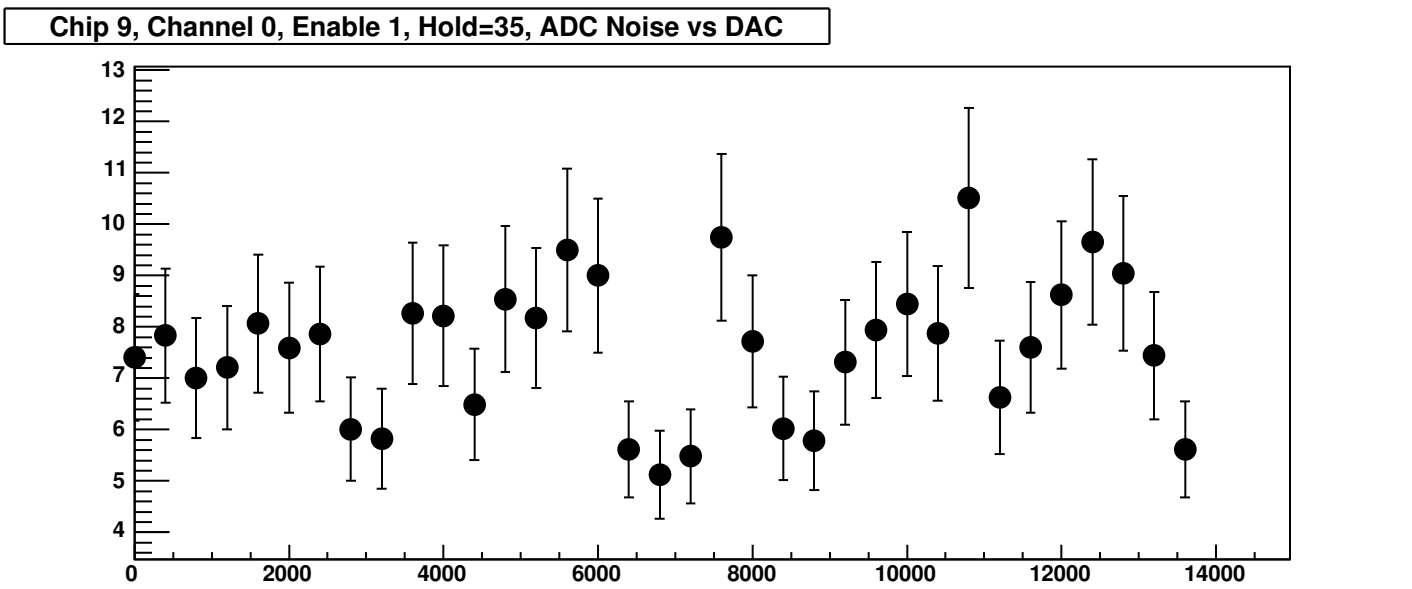

Chip 8, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
400
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
0
2000
4000
6000
8000
10000
12000
14000

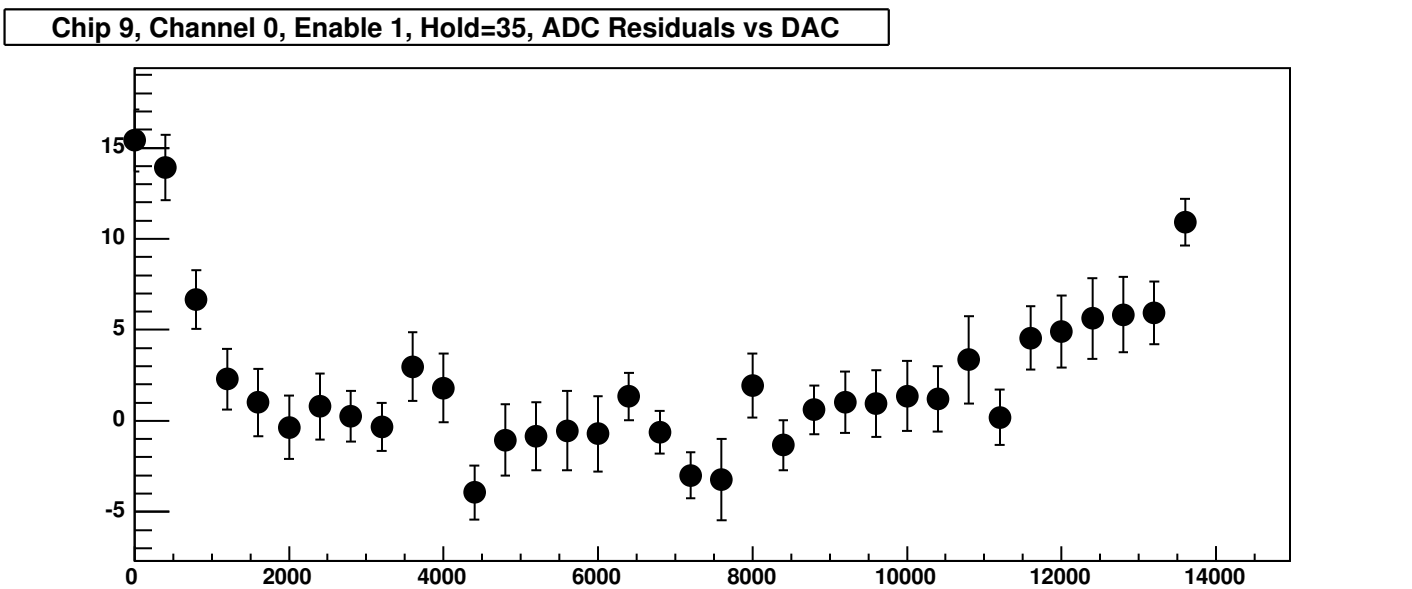

Chip 9, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 9604 / 23
p0 -1370 ± 2.33
p1 0.8418 ± 0.0003491
8000
6000
4000
2000
0
0
2000
4000
6000
8000
10000
12000
14000

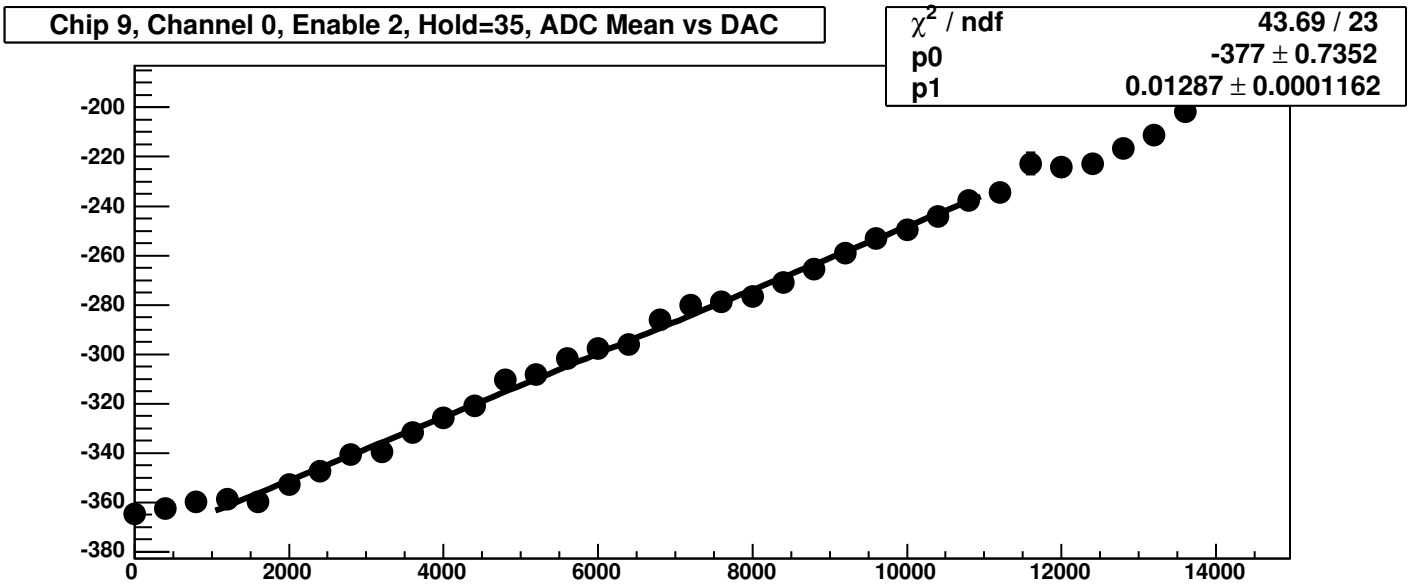

Chip 9, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

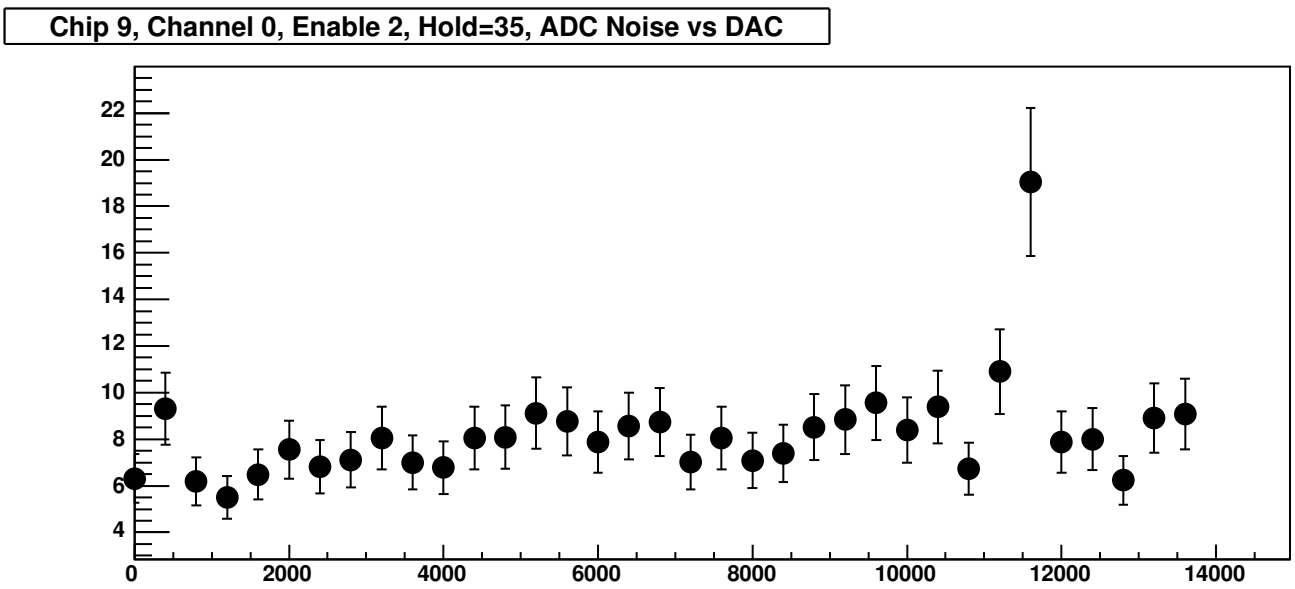

Chip 9, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

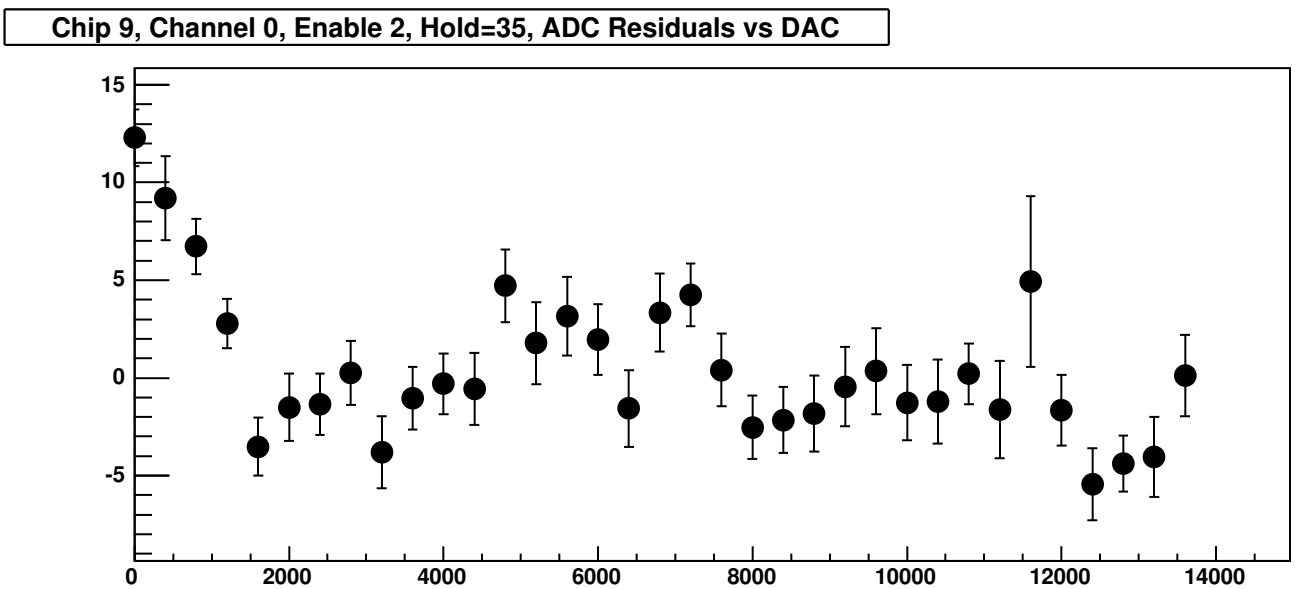

Chip 9, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 28.52 / 23
p0 -380.3 ± 0.7832
p1 0.01423 ± 0.0001196
-160
-180
-200
-220
-240
-260
-280
-300
-320
-340
-360
-380
0
2000
4000
6000
8000
10000
12000
14000

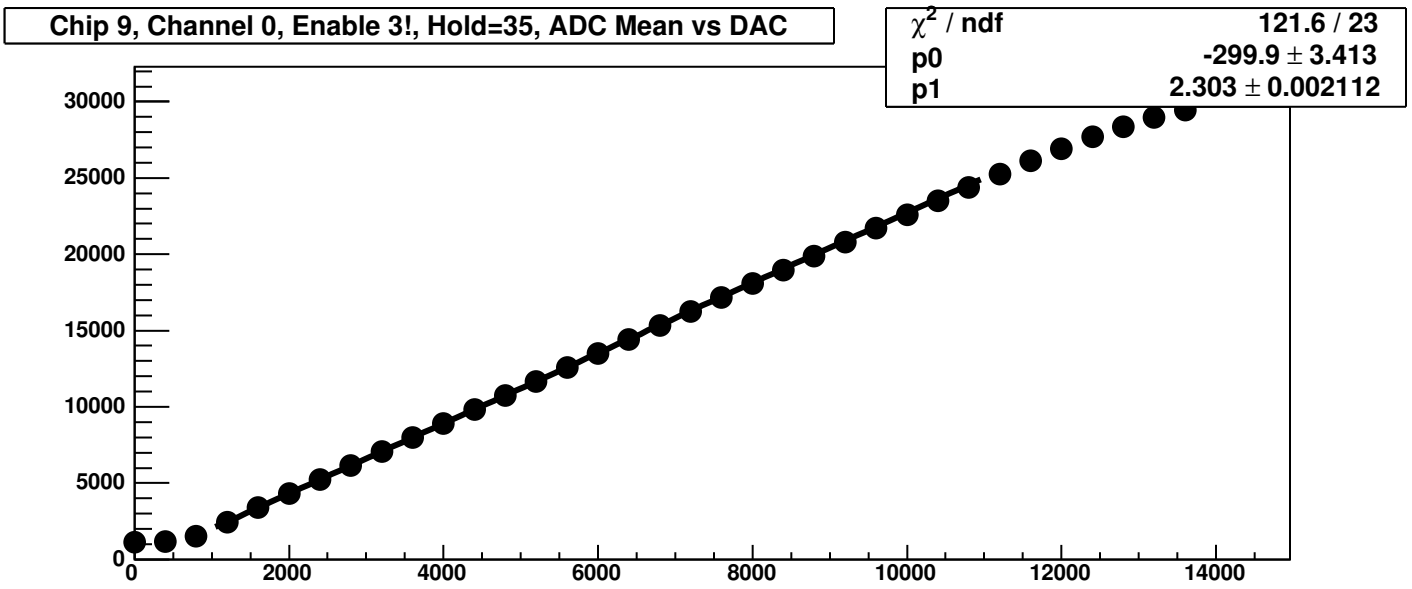

Chip 9, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

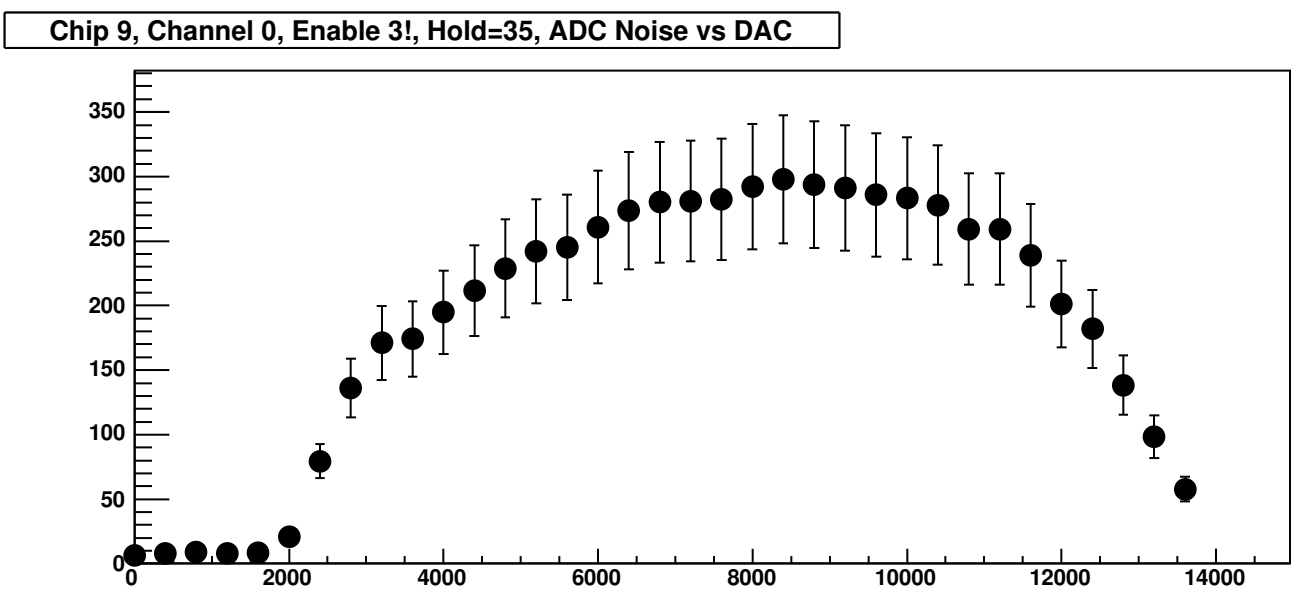

Chip 9, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

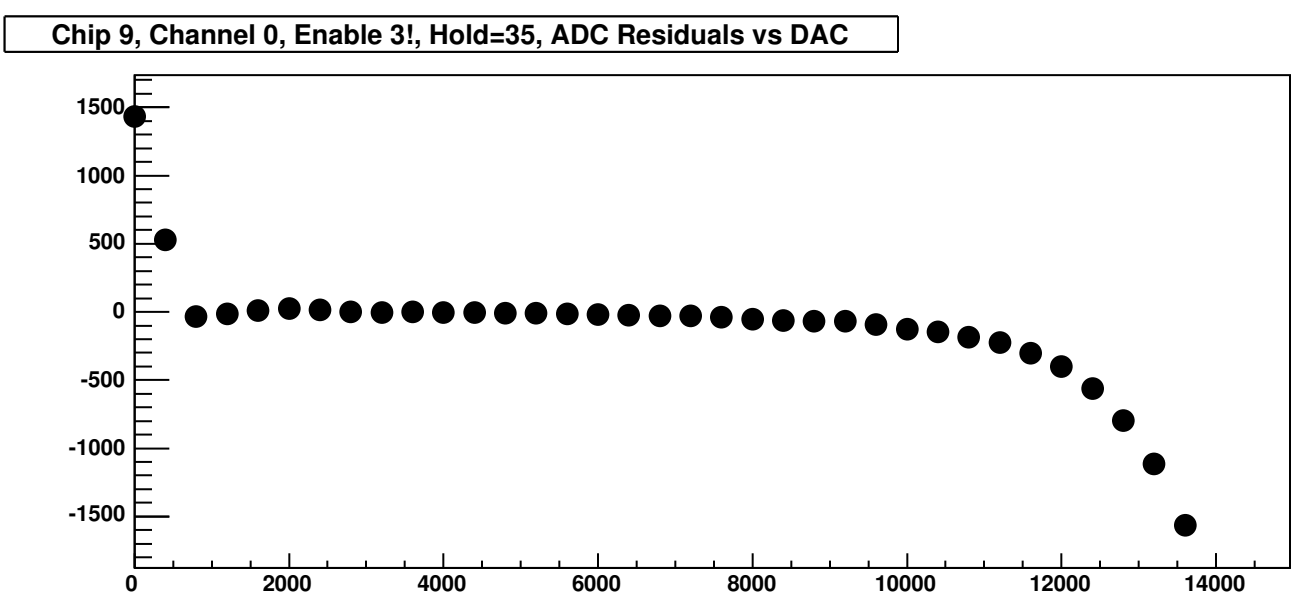

Chip 9, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 43.69 / 23
p0 -377 ± 0.7352
p1 0.01287 ± 0.0001162
-200
-220
-240
-260
-280
-300
-320
-340
-360
-380
0
2000
4000
6000
8000
10000
12000
14000

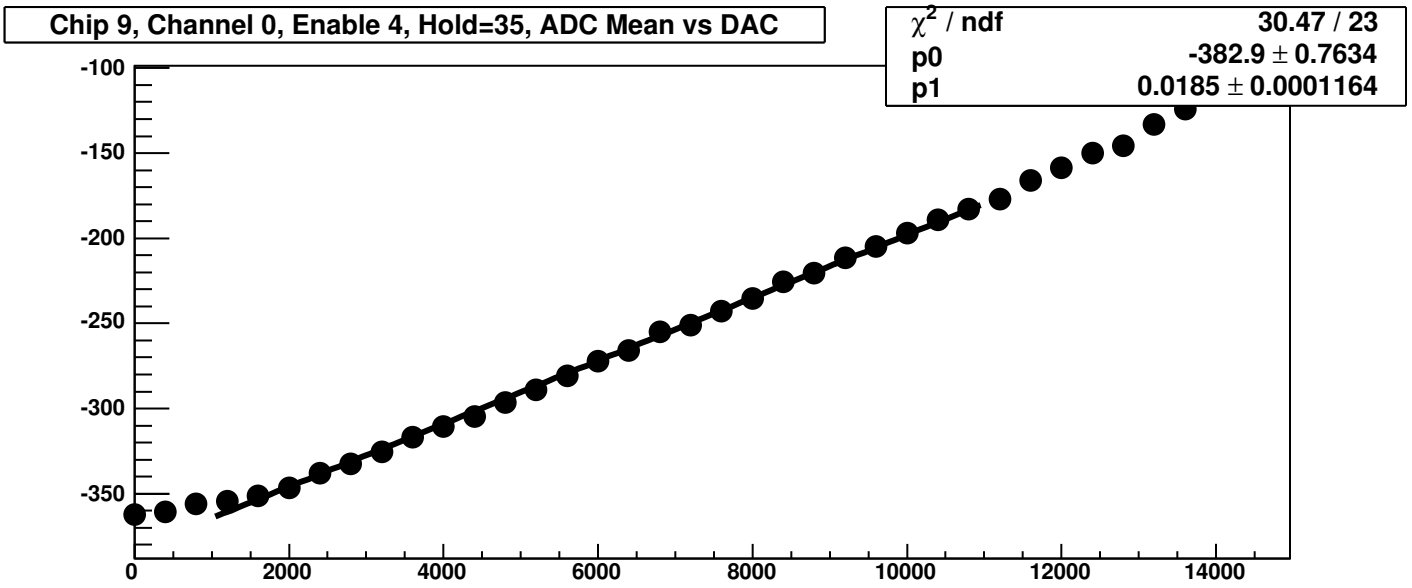

Chip 9, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

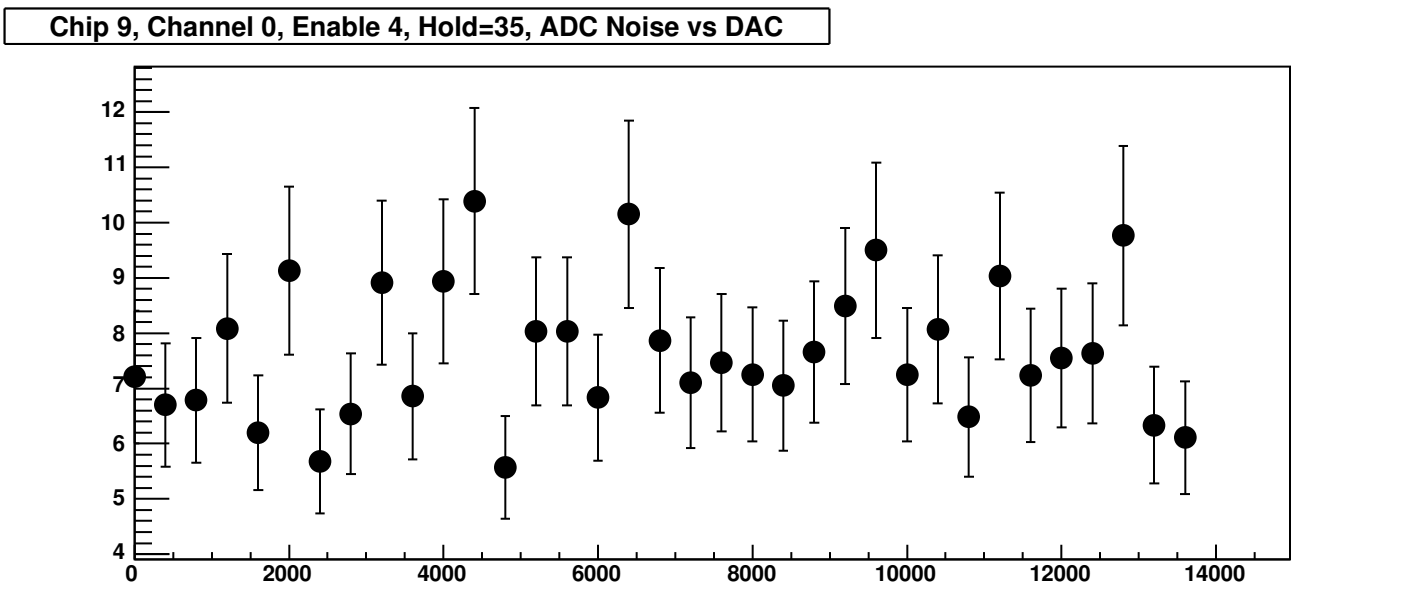

Chip 9, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

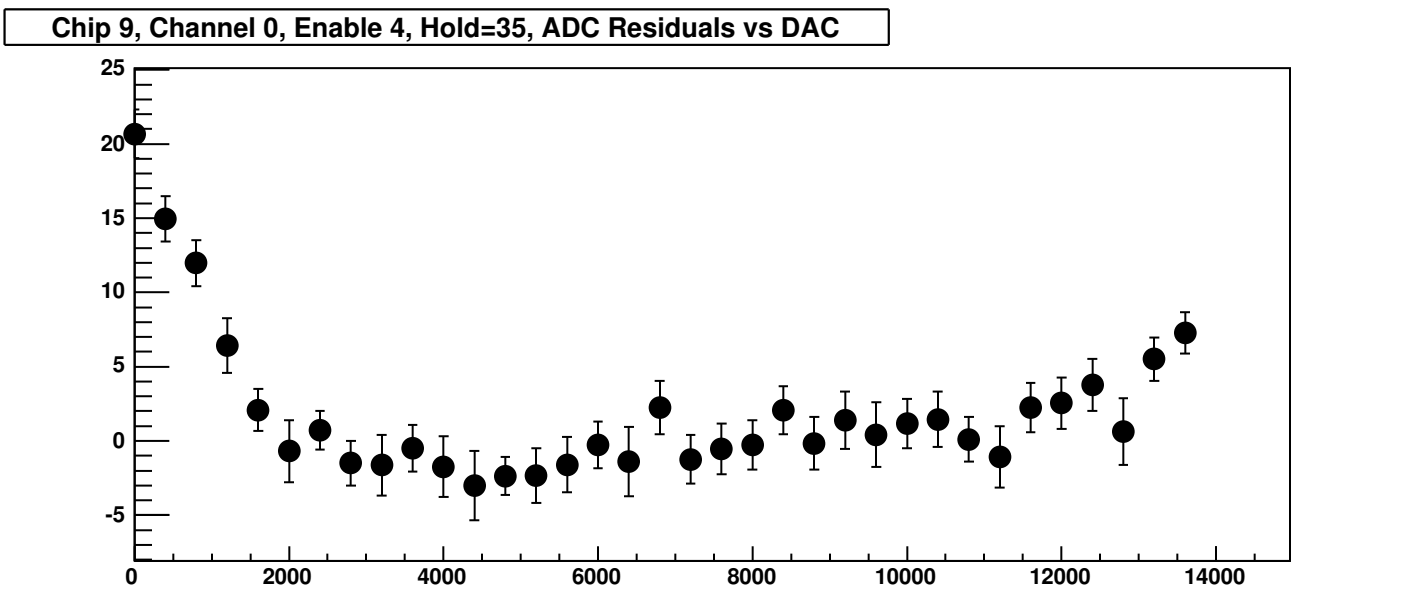

Chip 9, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 121.6 / 23
p0 -299.9 ± 3.413
p1 2.303 ± 0.002112
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

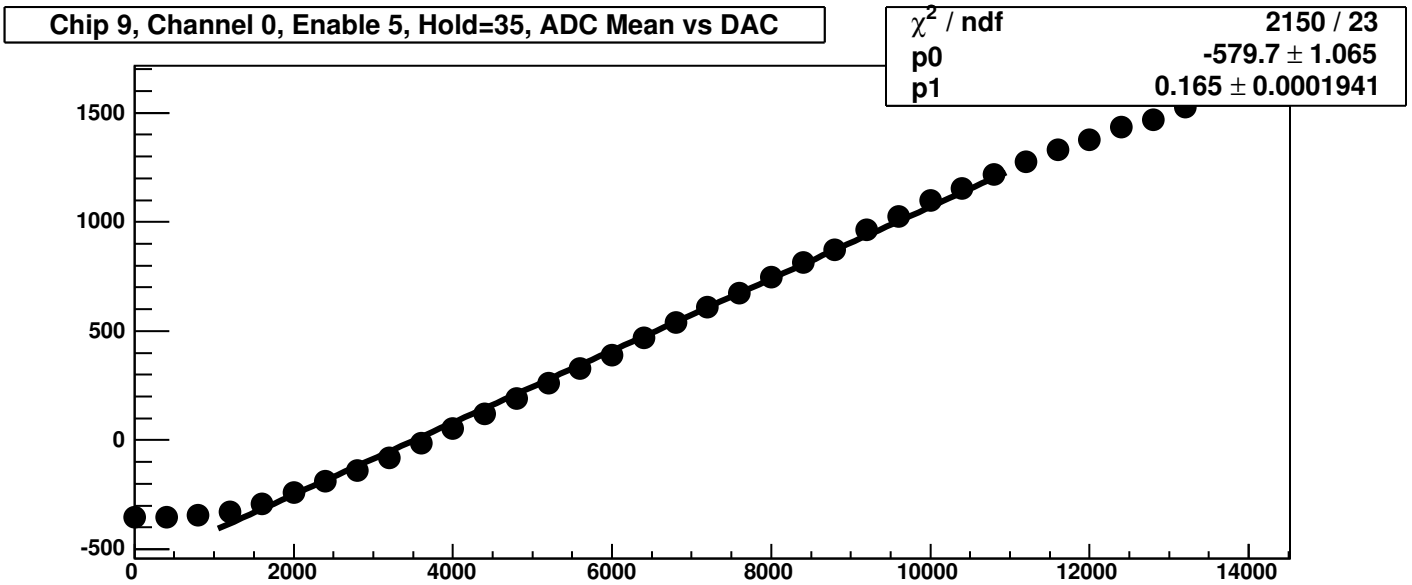

Chip 9, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

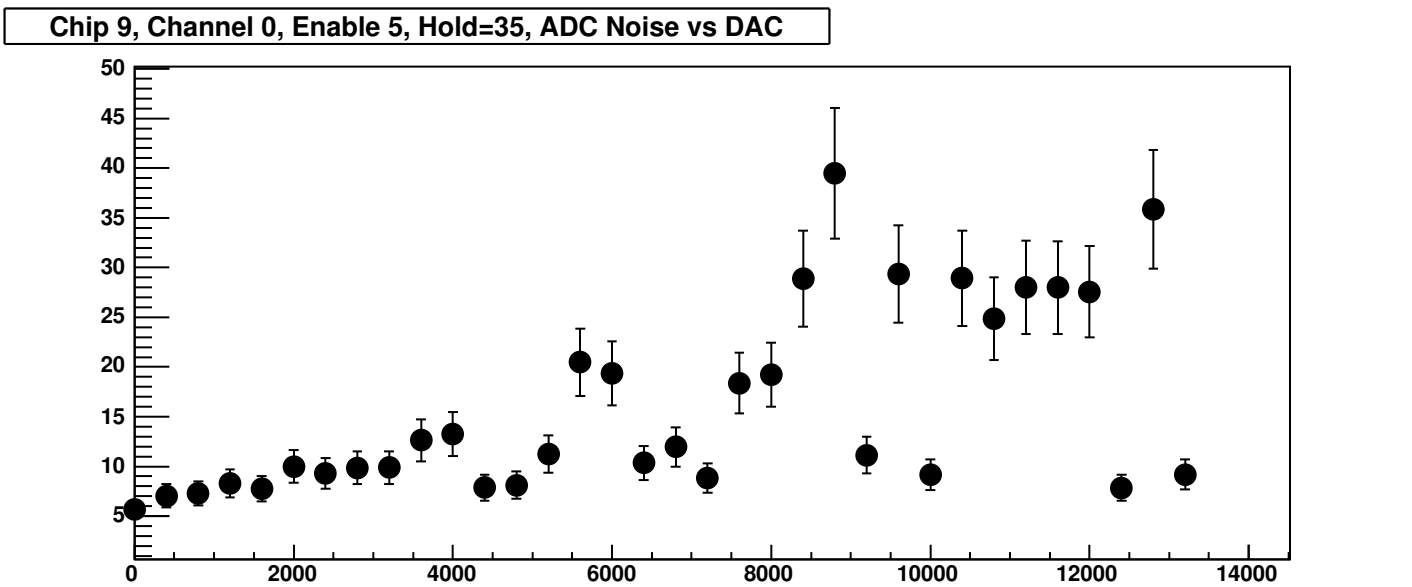

Chip 9, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

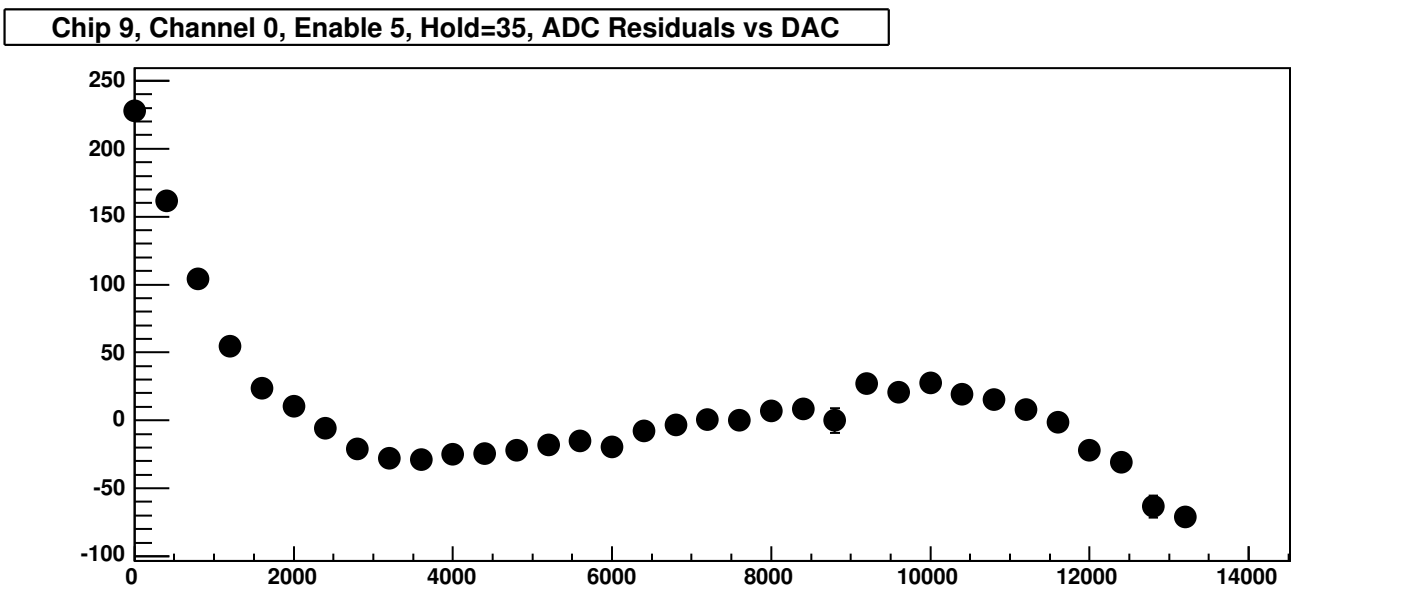

Chip 9, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.47 / 23
p0 -382.9 ± 0.7634
p1 0.0185 ± 0.0001164
-100
-150
-200
-250
-300
-350
0
2000
4000
6000
8000
10000
12000
14000

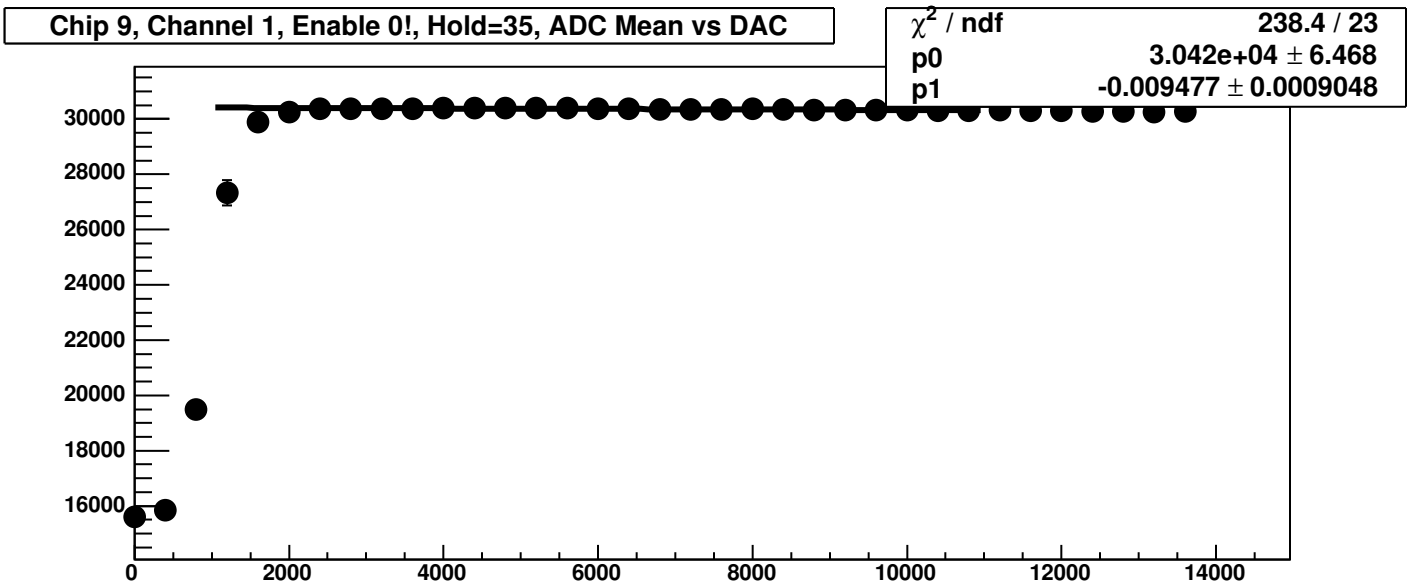

Chip 9, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

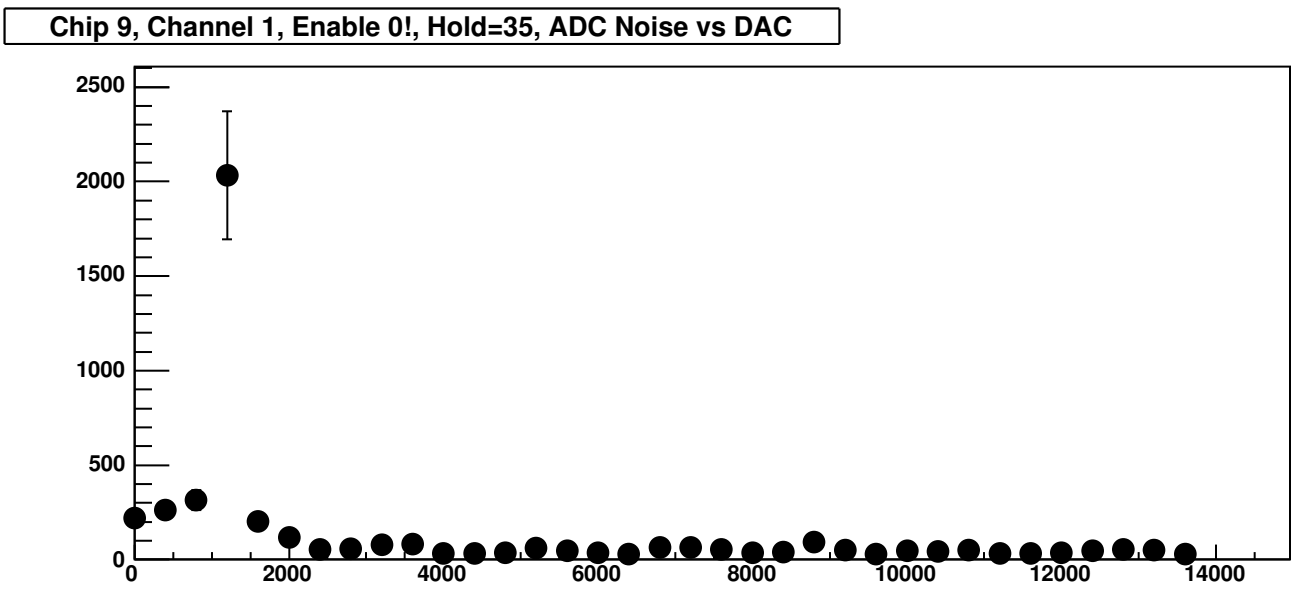

Chip 9, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

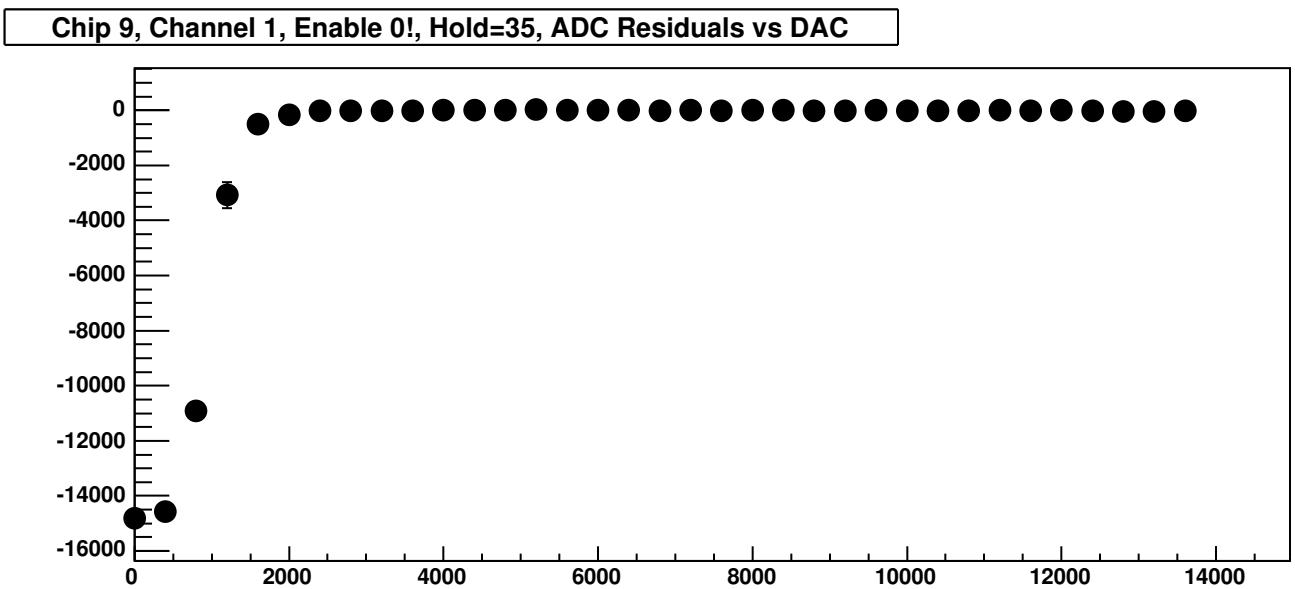

Chip 9, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2150 / 23
p0 -579.7 ± 1.065
p1 0.165 ± 0.0001941
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

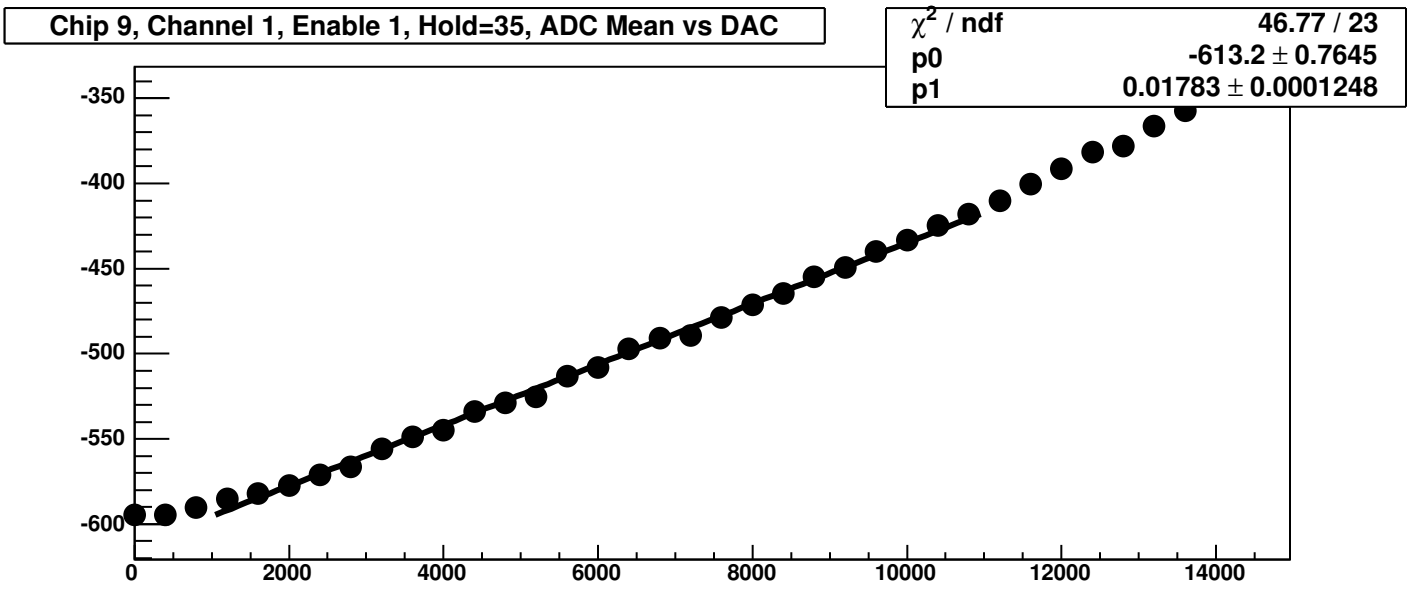

Chip 9, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

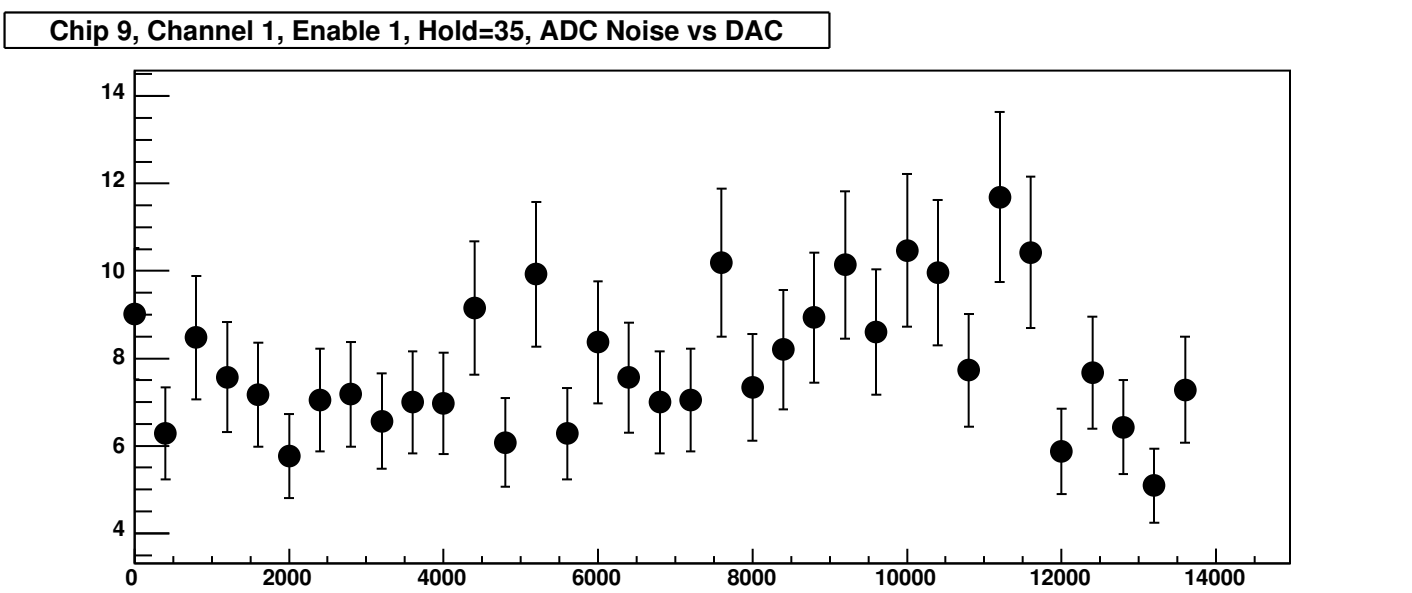

Chip 9, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

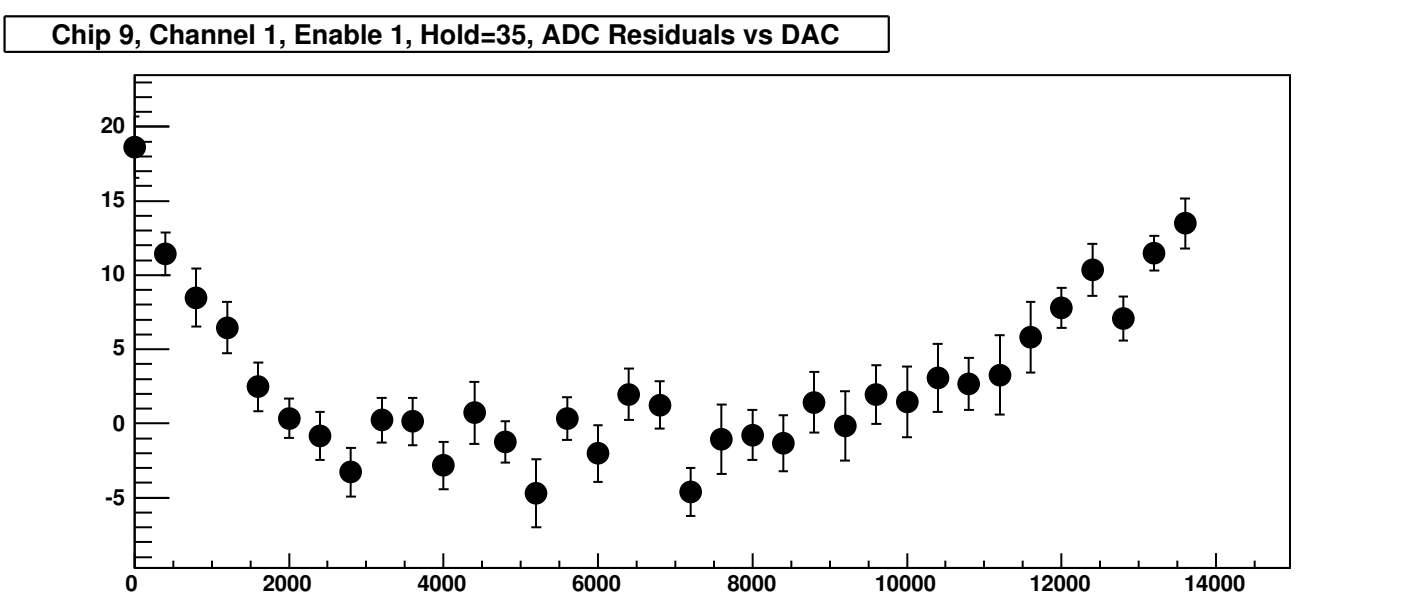

Chip 9, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 238.4 / 23
p0 3.042e+04 ± 6.468
p1 -0.009477 ± 0.0009048
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

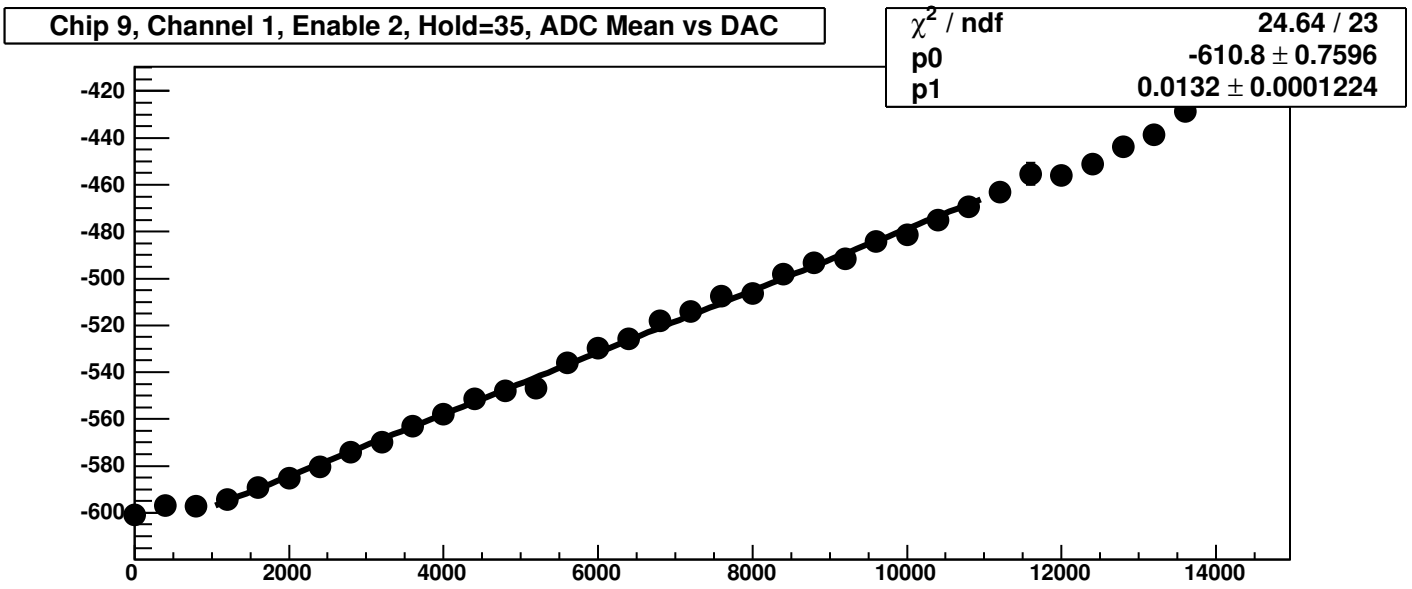

Chip 9, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

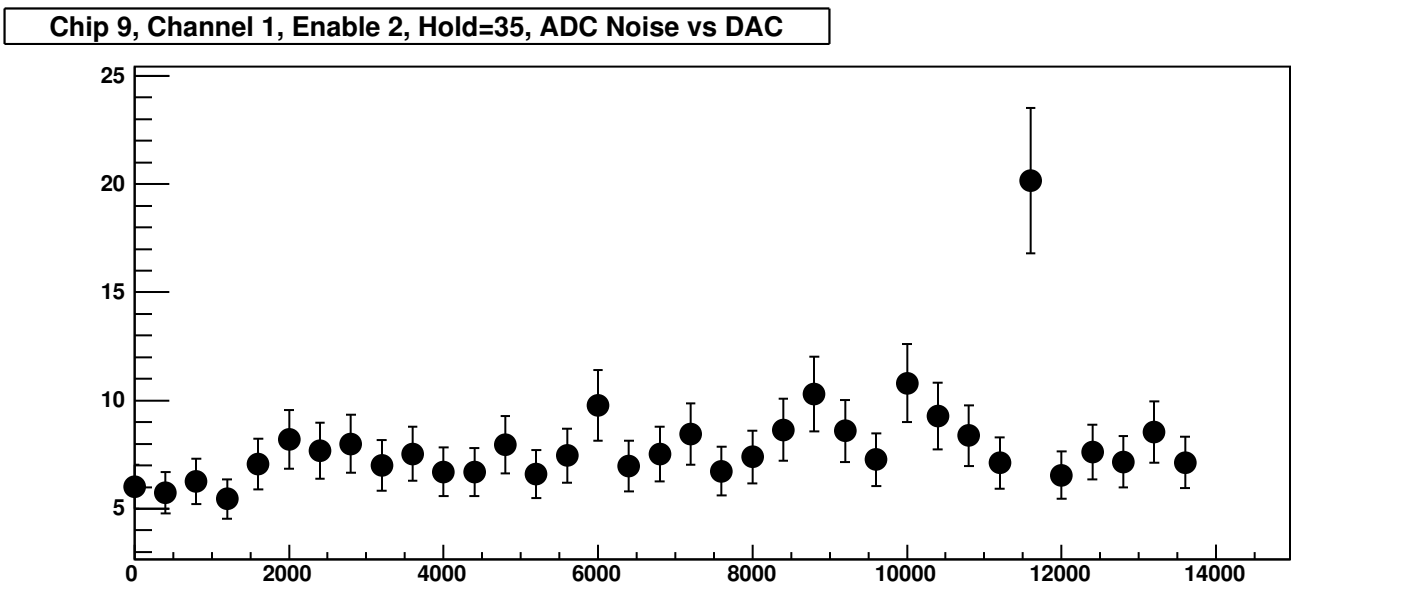

Chip 9, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

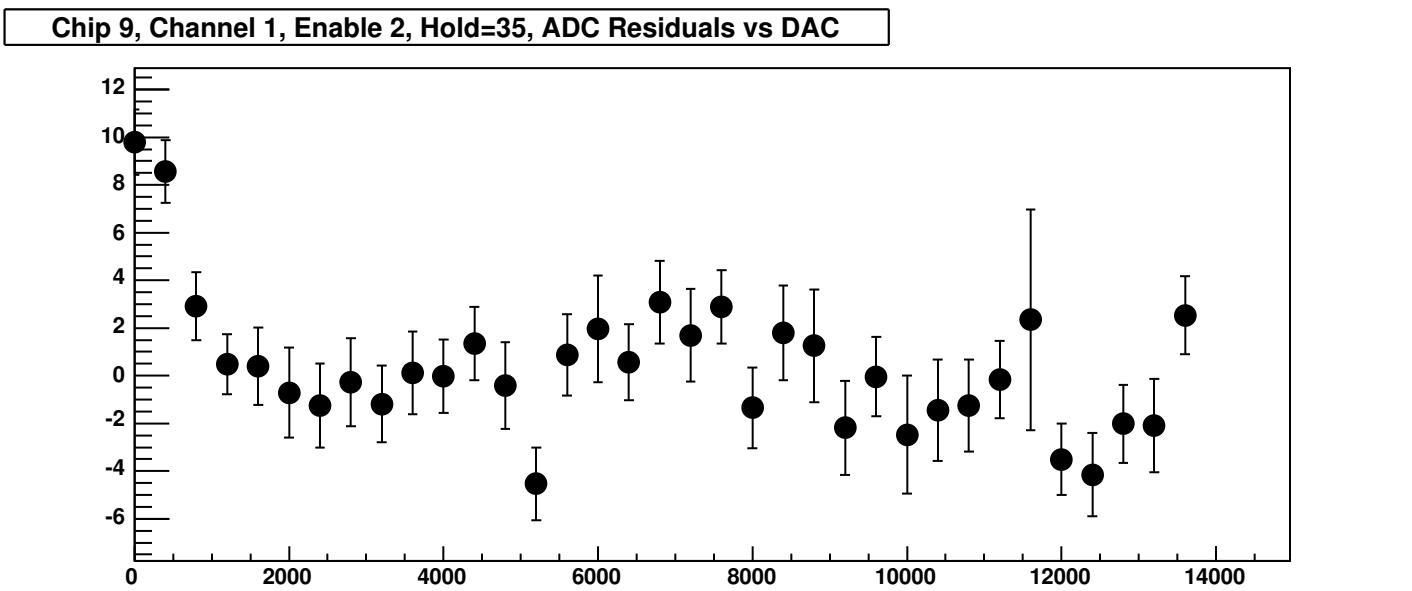

Chip 9, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 46.77 / 23
p0 -613.2 ± 0.7645
p1 0.01783 ± 0.0001248
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

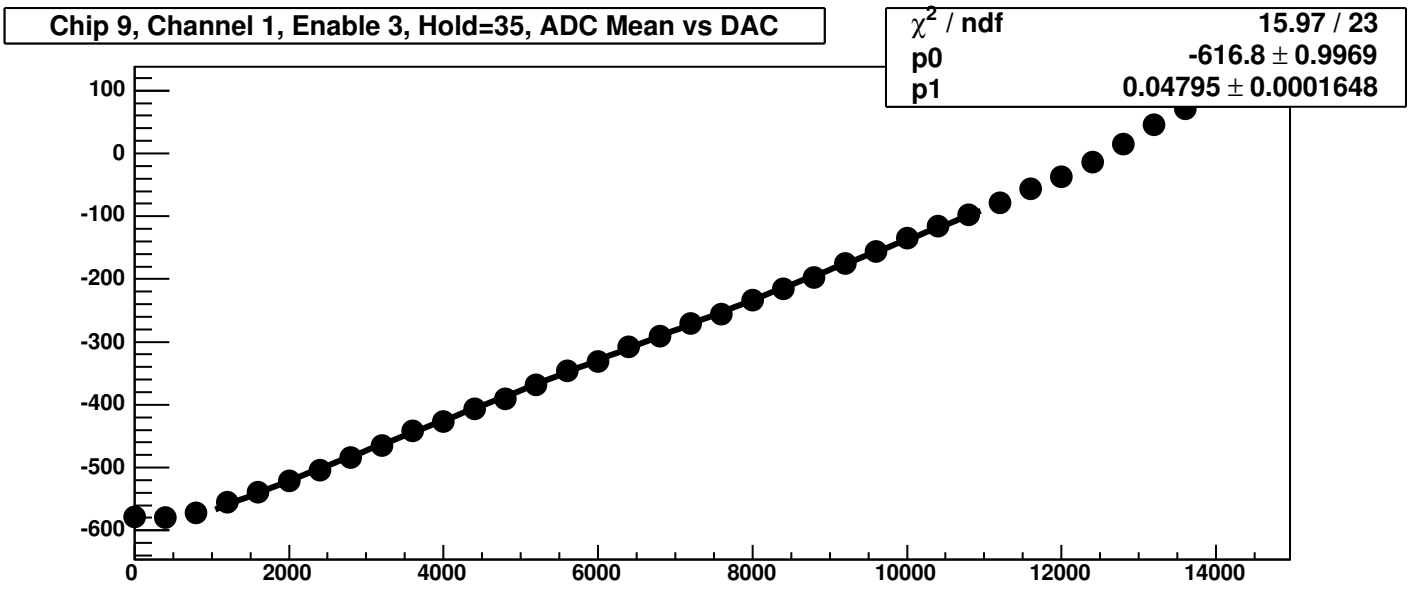

Chip 9, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

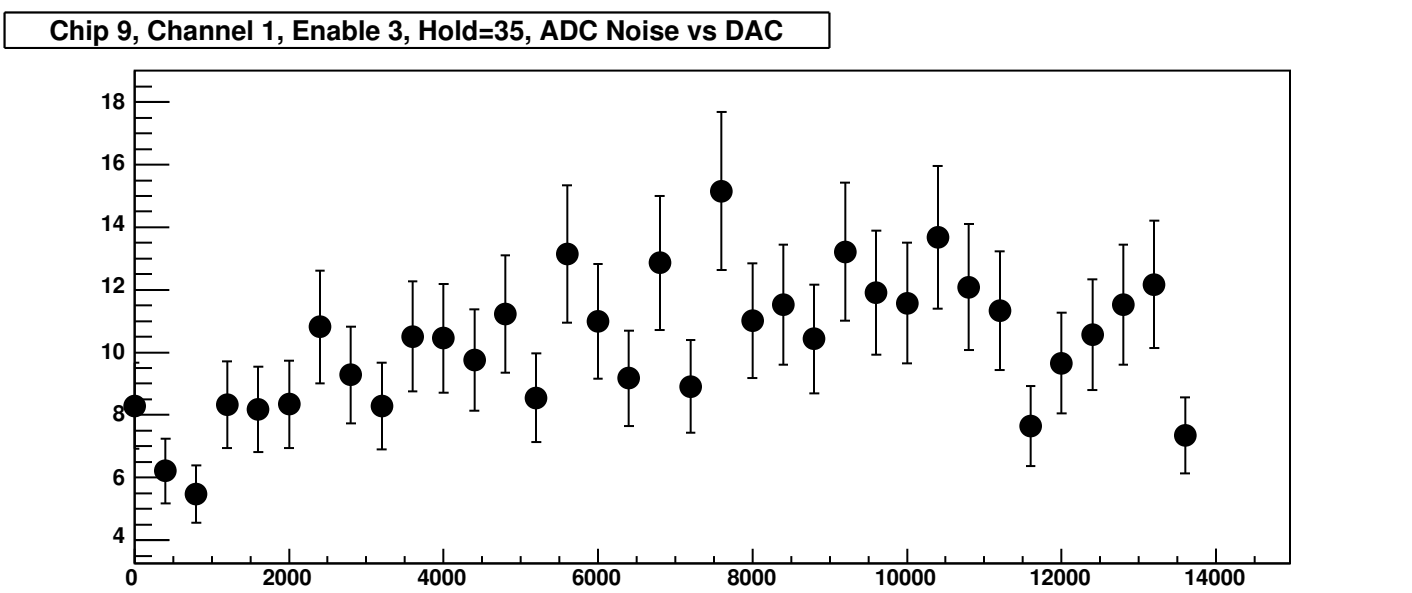

Chip 9, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

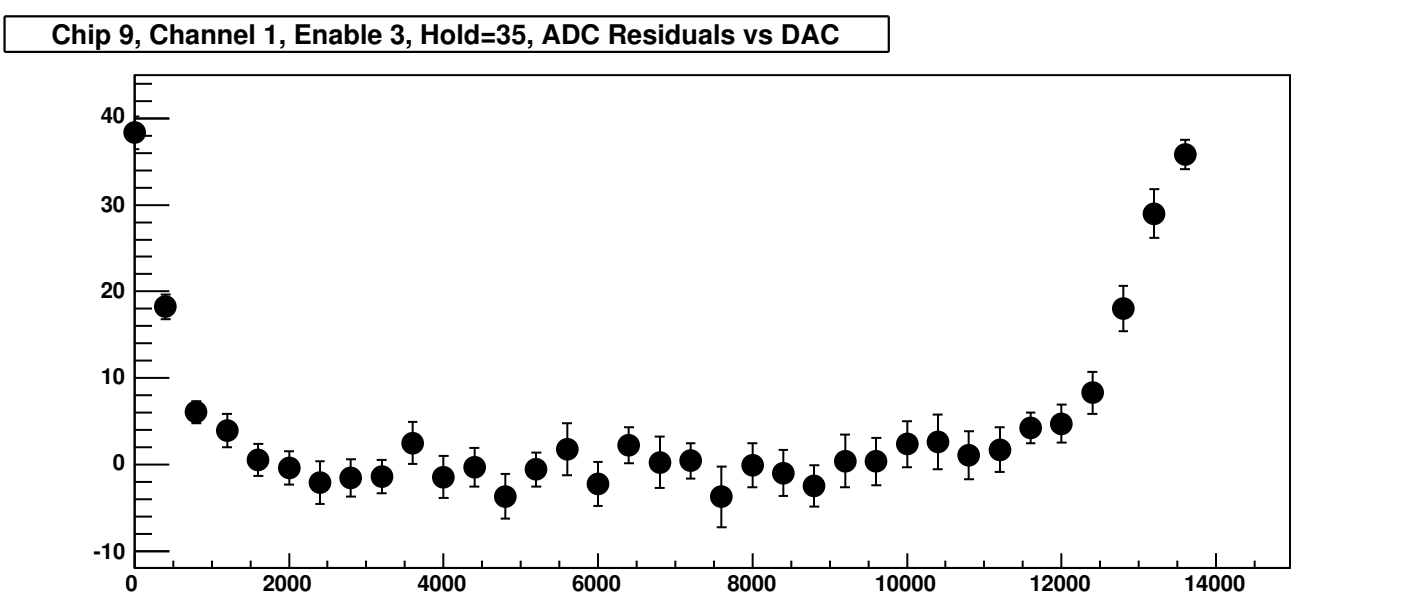

Chip 9, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 24.64 / 23
p0 -610.8 ± 0.7596
p1 0.0132 ± 0.0001224
-420
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

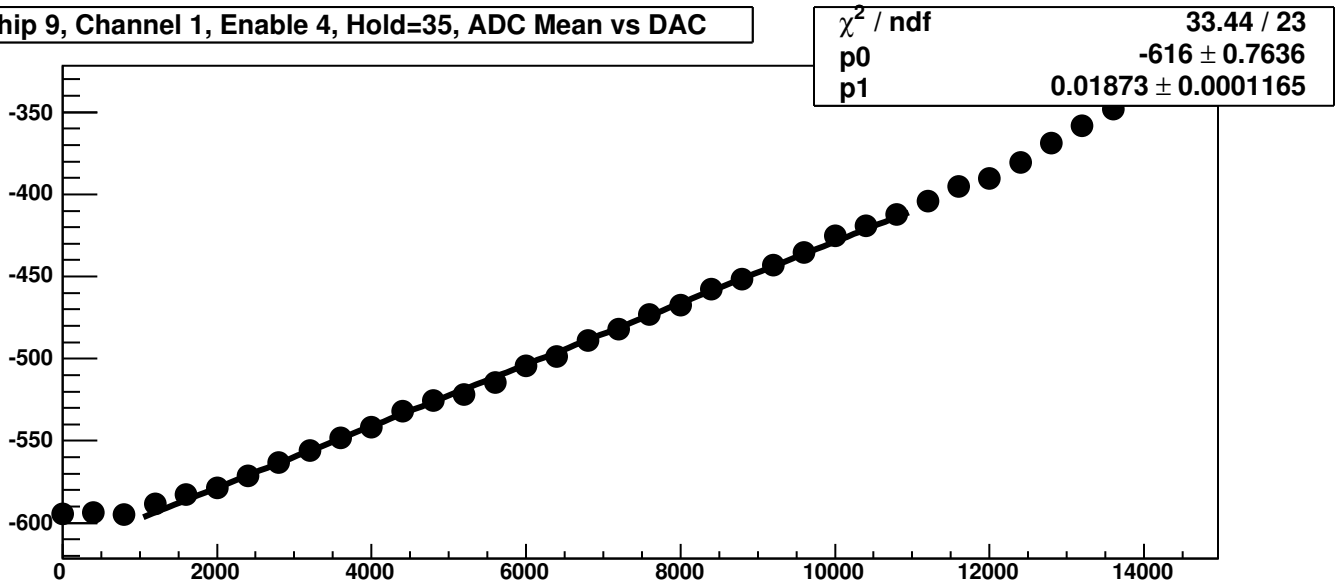

Chip 9, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 15.97 / 23
p0 -616.8 ± 0.9969
p1 0.04795 ± 0.0001648
100
0
-100
-200
-300
-400
-500
-600
0
2000
4000
6000
8000
10000
12000
14000

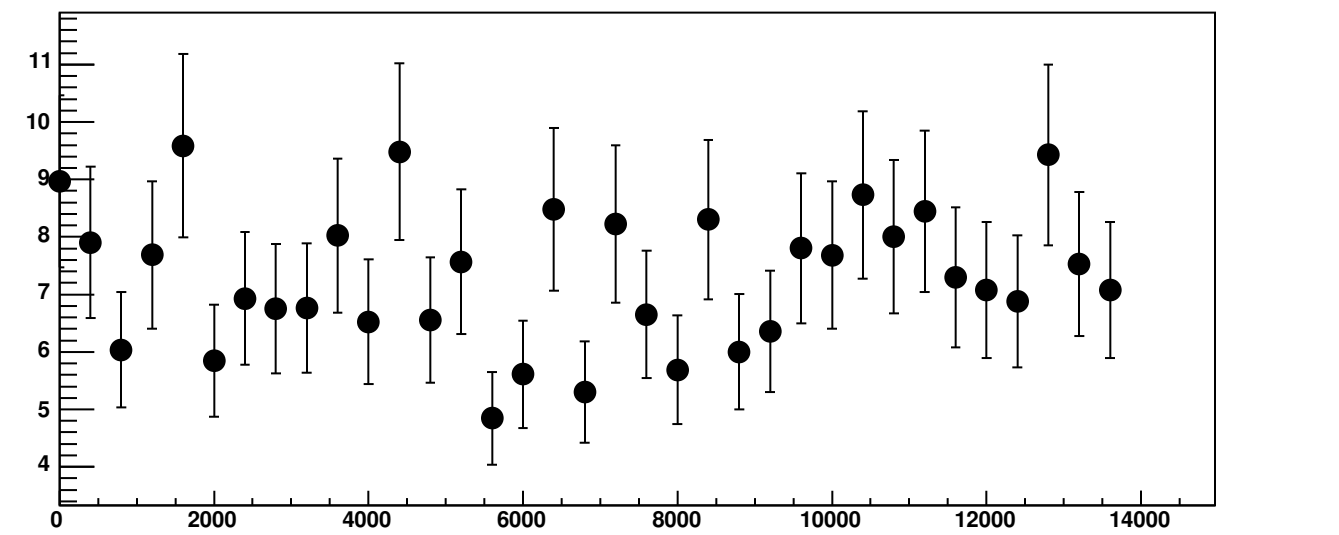

Chip 9, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000

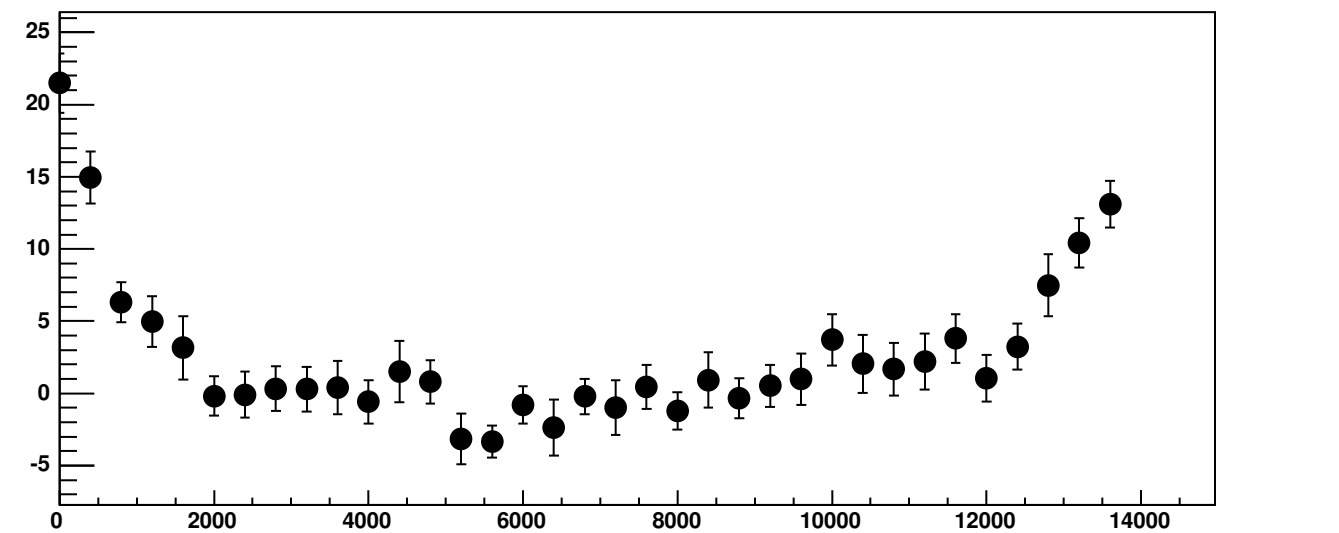
Chip 9, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

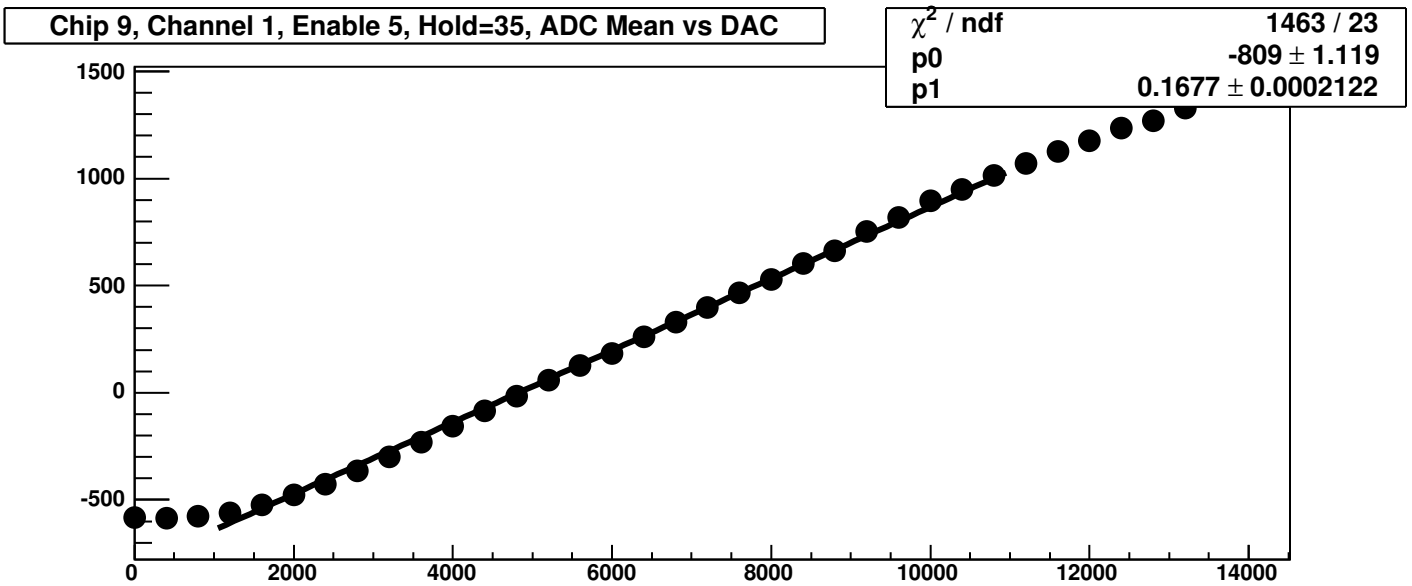


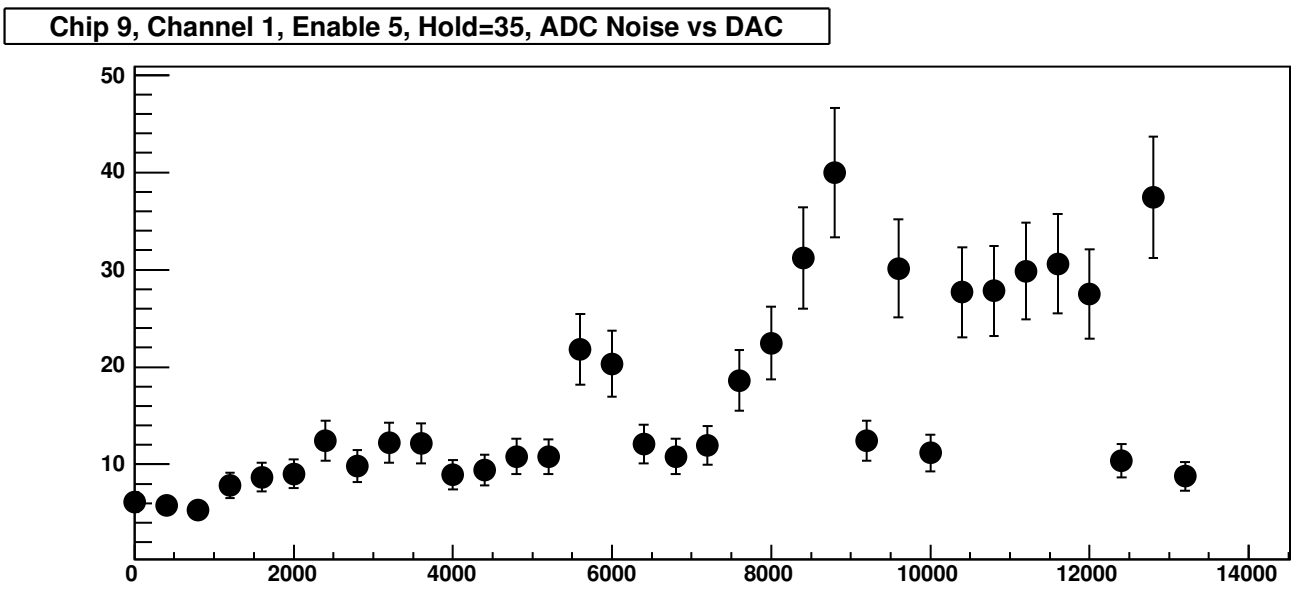
Chip 9, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

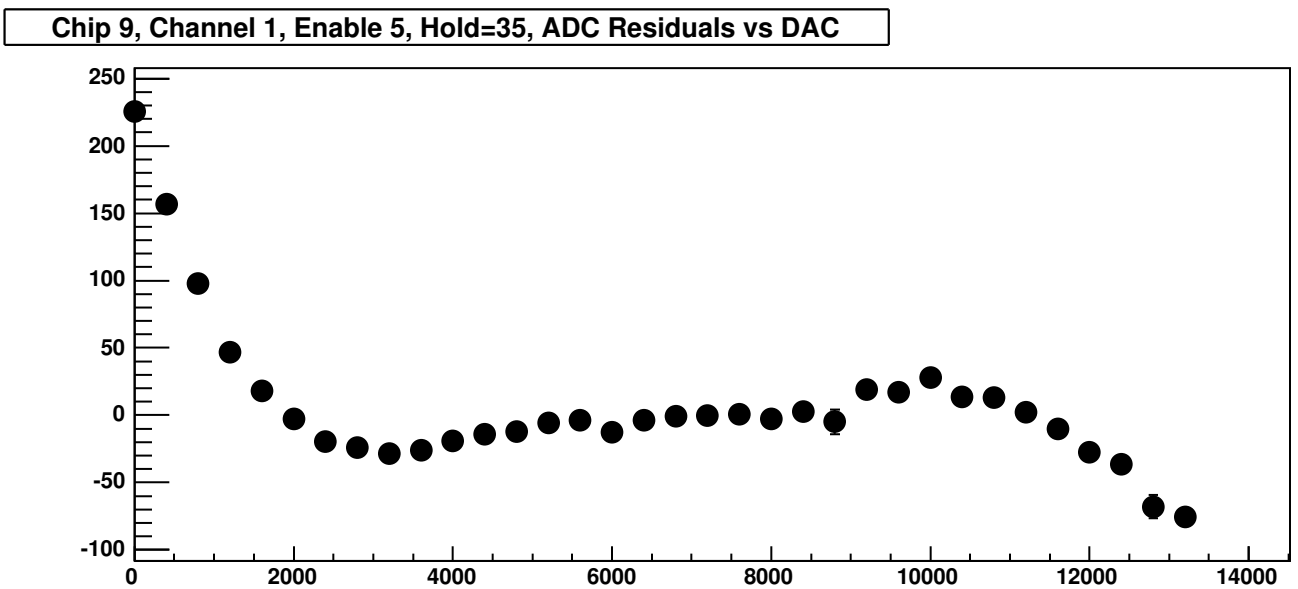


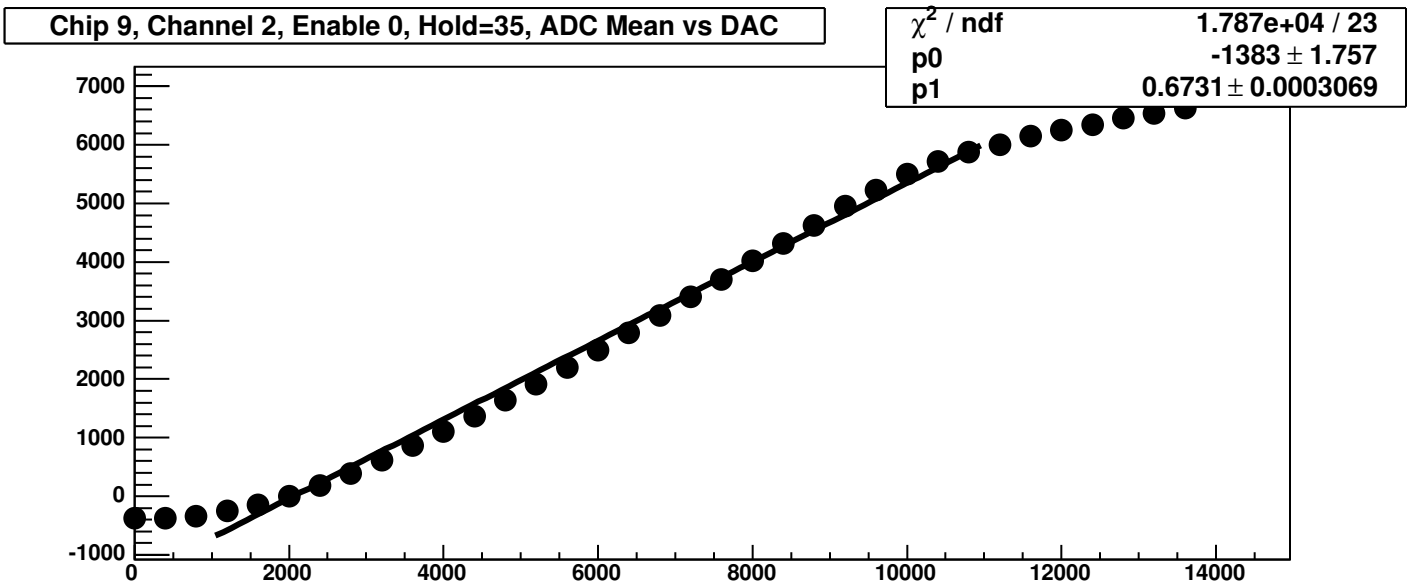
Chip 9, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

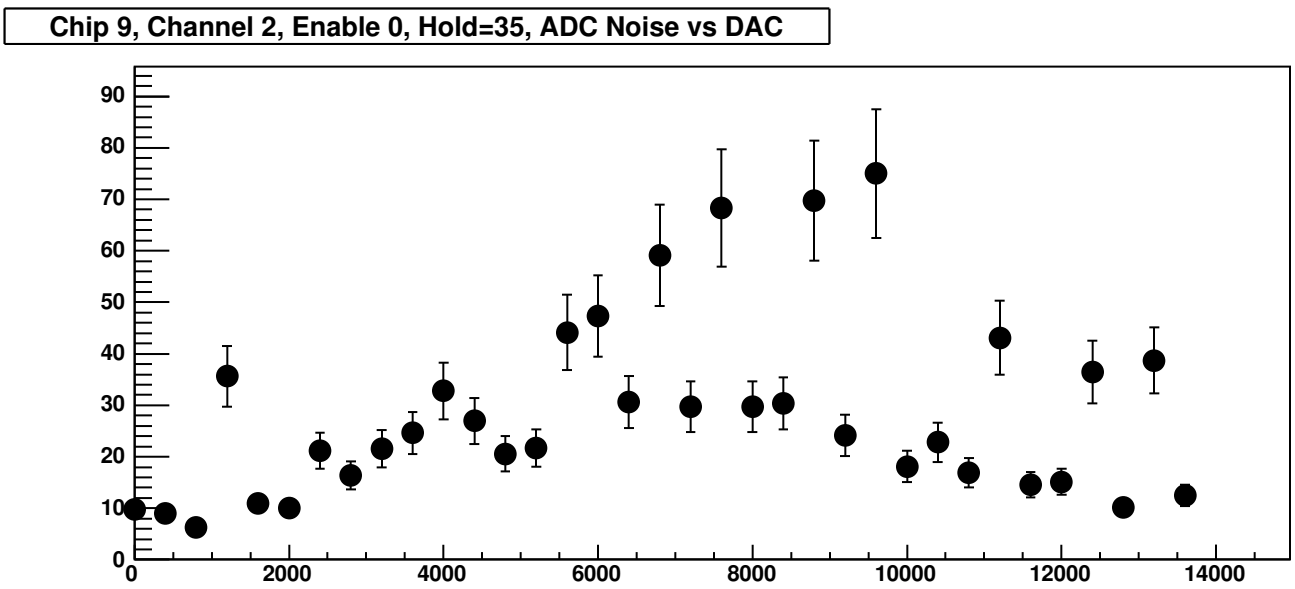


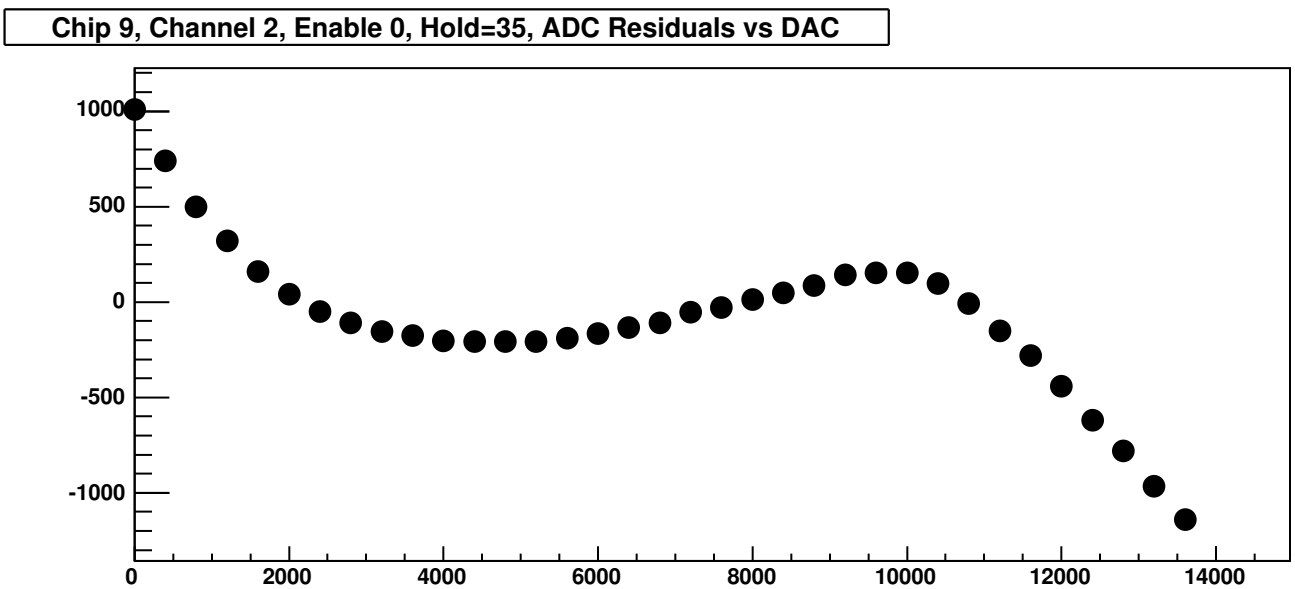

Chip 9, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1463 / 23
p0 -809 ± 1.119
p1 0.1677 ± 0.0002122
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

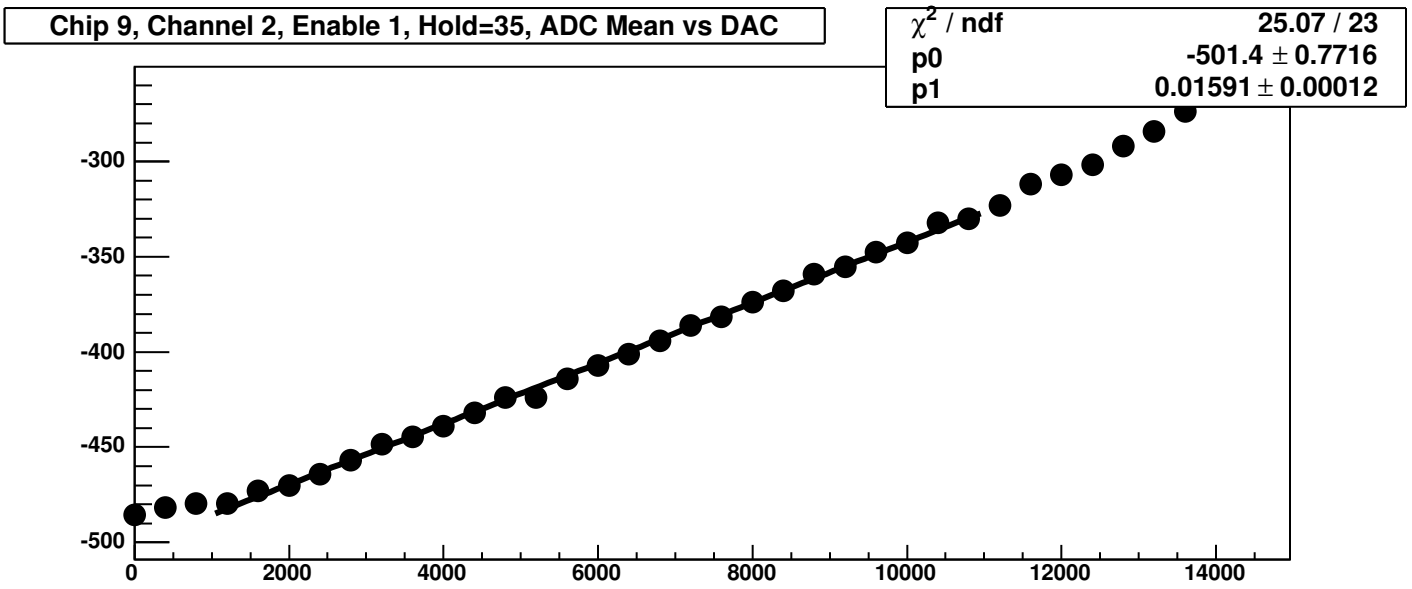

Chip 9, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

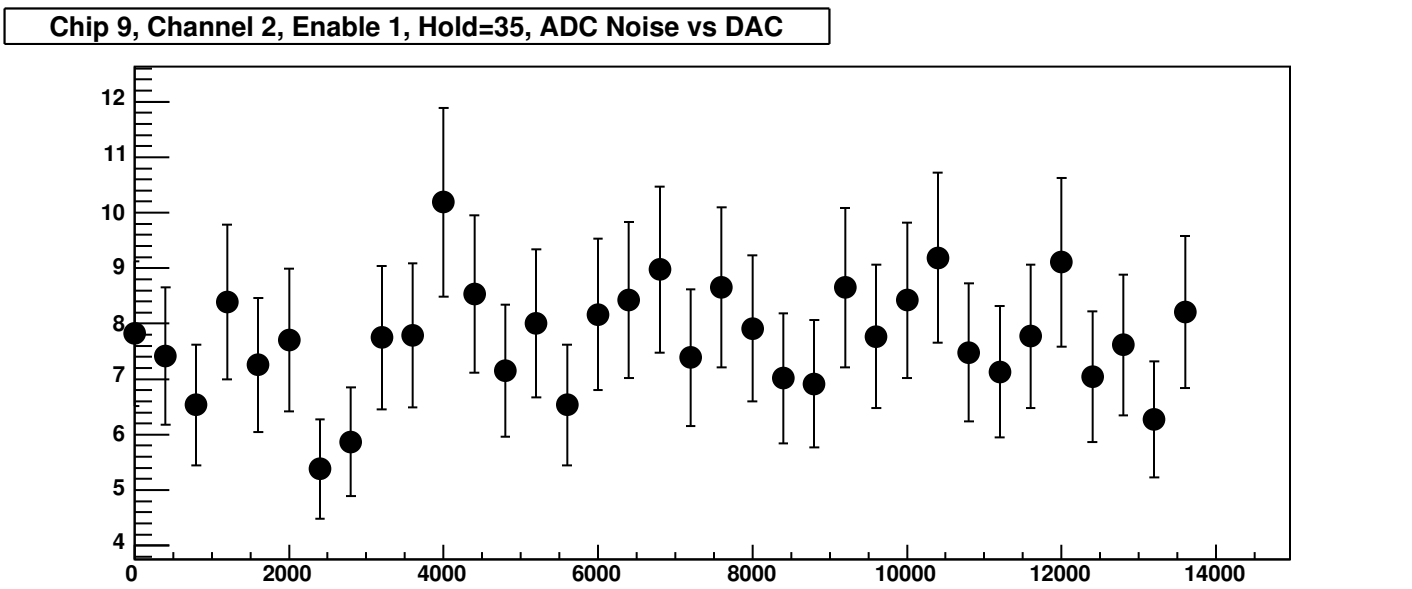

Chip 9, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

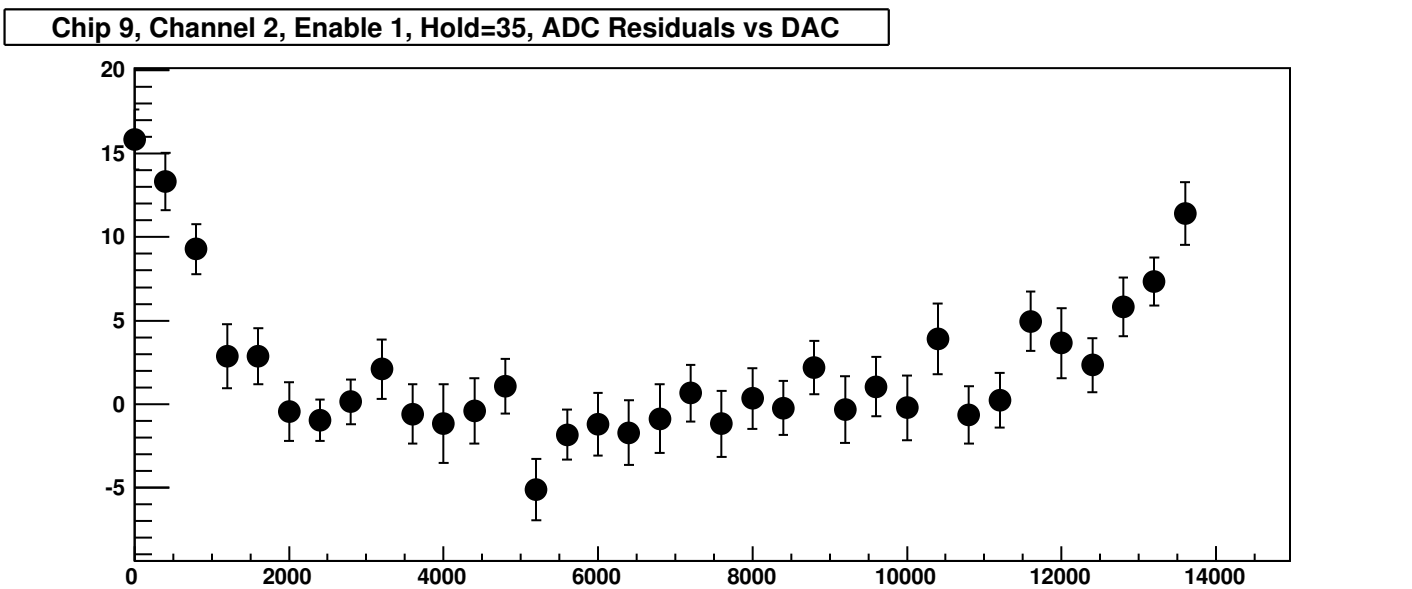

Chip 9, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.787e+04 / 23
p0 -1383 ± 1.757
p1 0.6731 ± 0.0003069
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

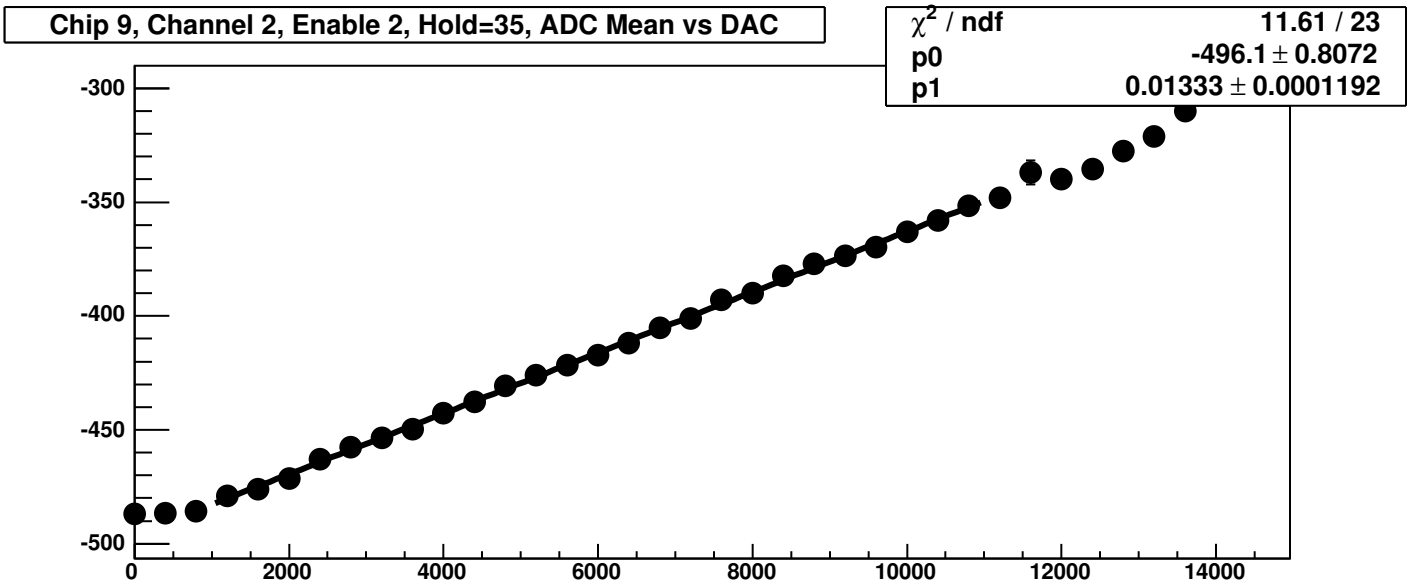

Chip 9, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

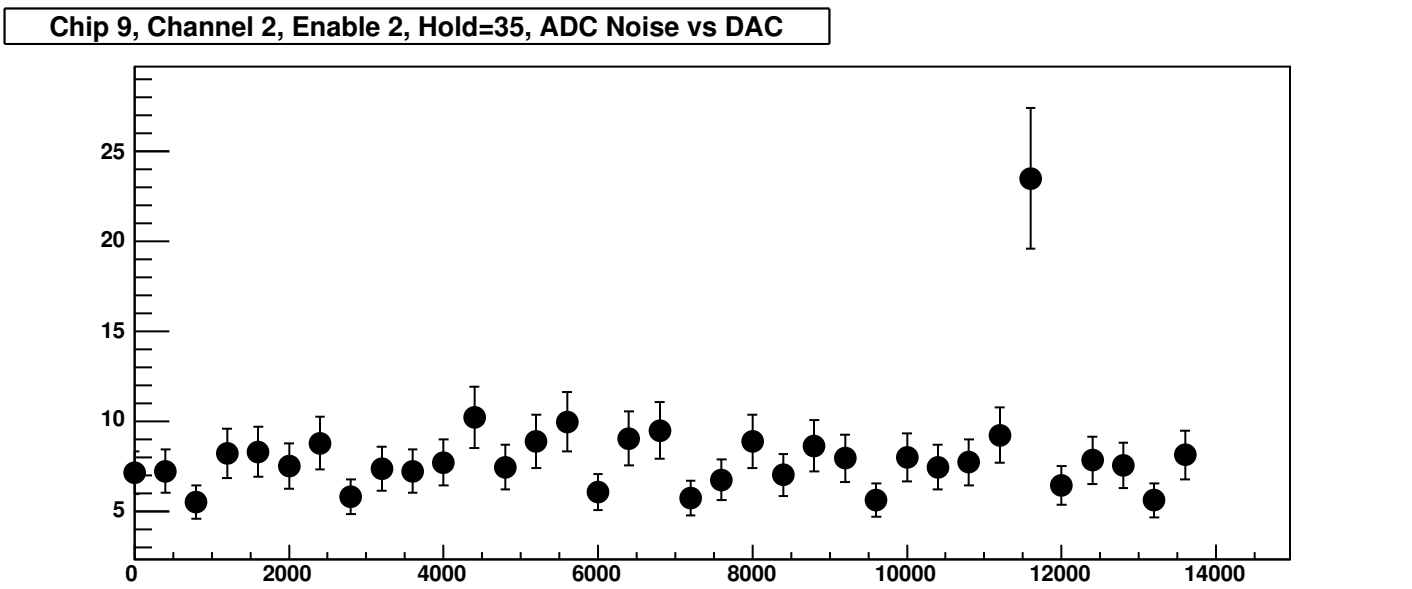

Chip 9, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

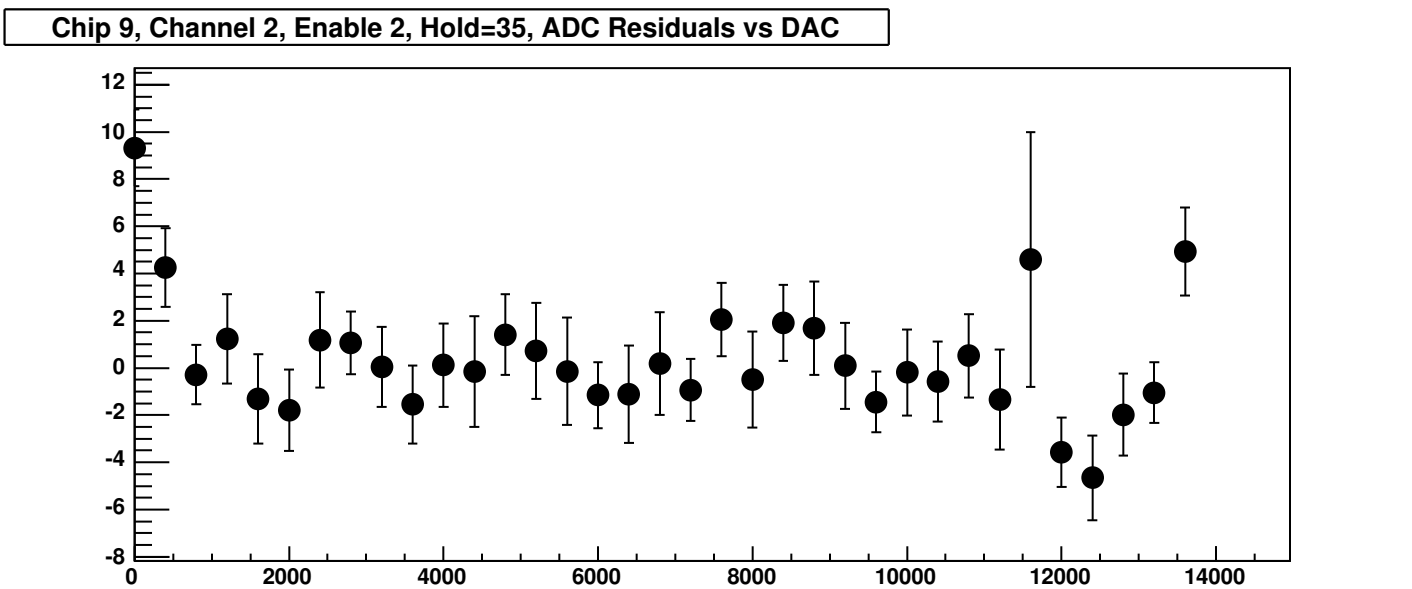

Chip 9, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 25.07 / 23
p0 -501.4 ± 0.7716
p1 0.01591 ± 0.00012
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

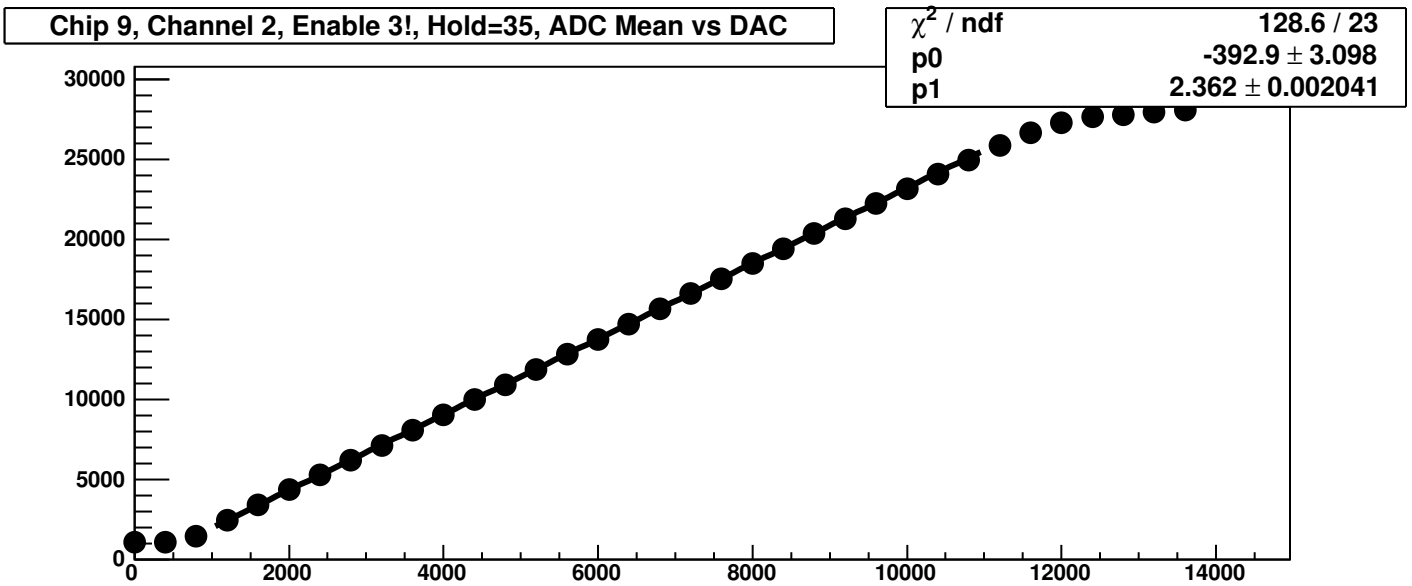

Chip 9, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

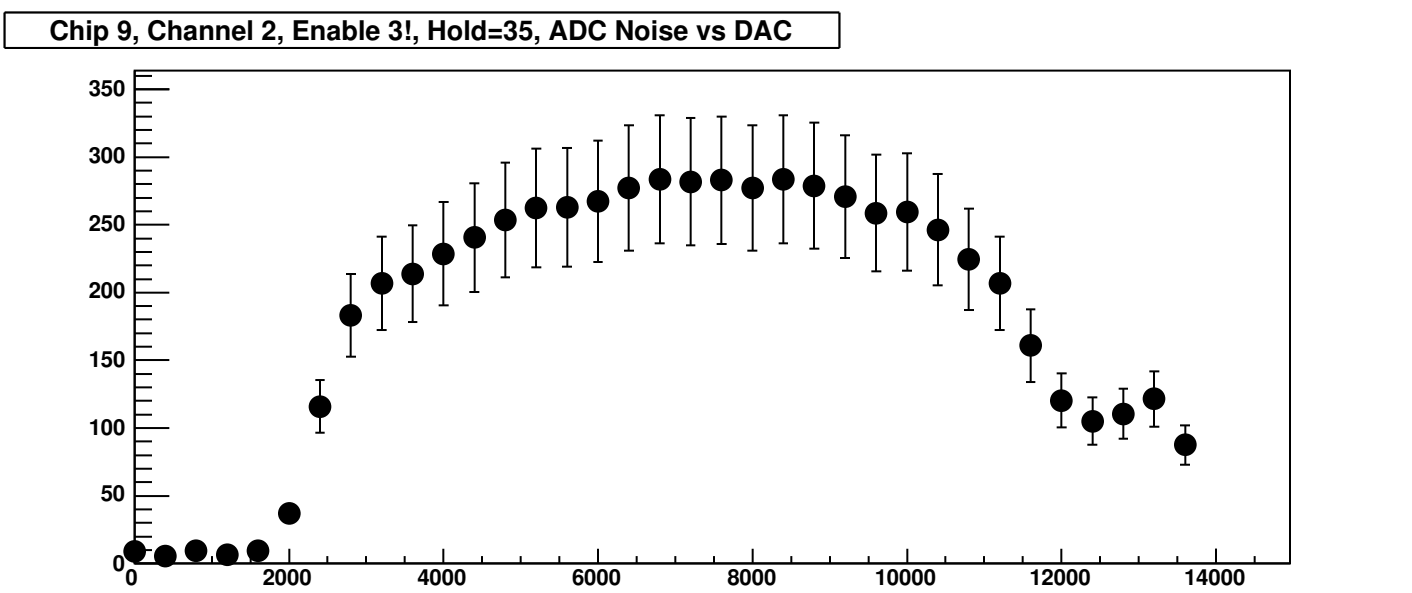

Chip 9, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

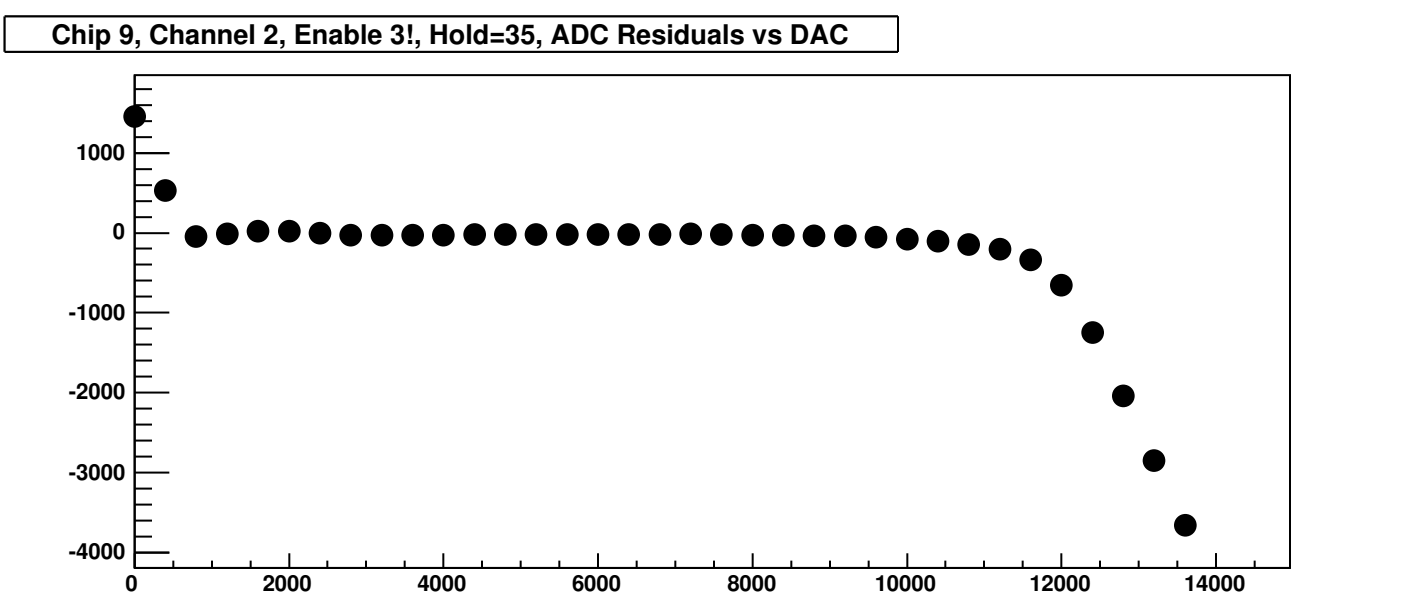

Chip 9, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 11.61 / 23
p0 -496.1 ± 0.8072
p1 0.01333 ± 0.0001192
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

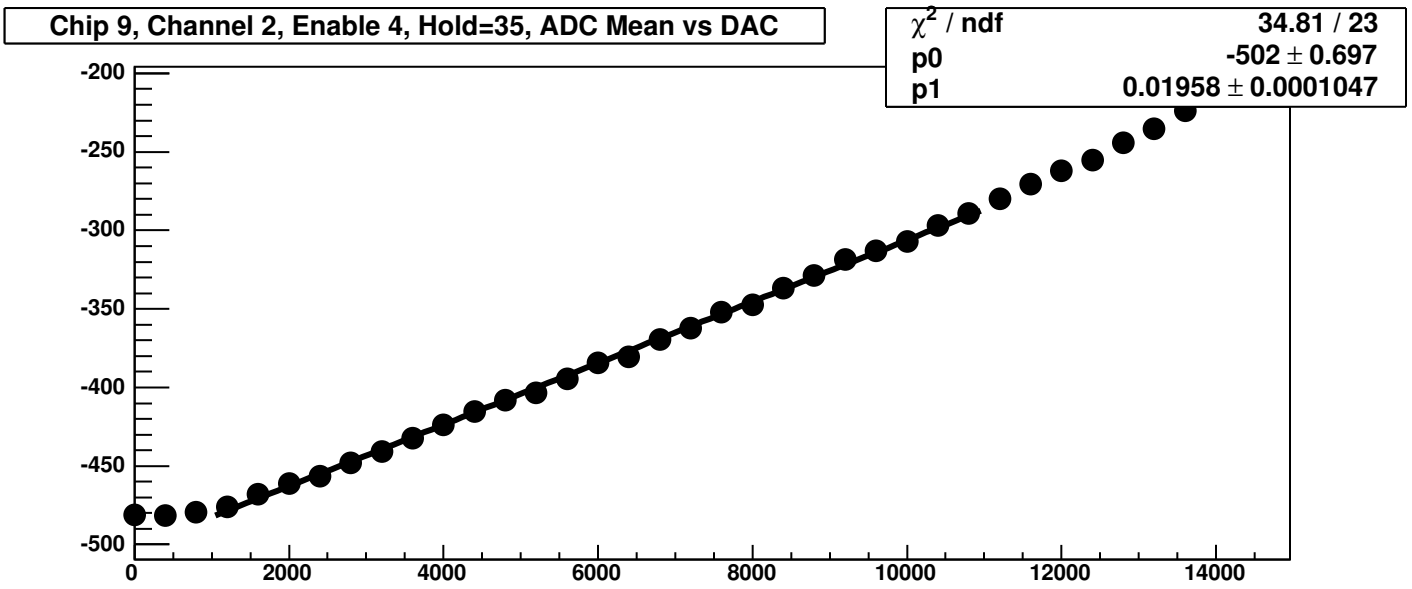

Chip 9, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

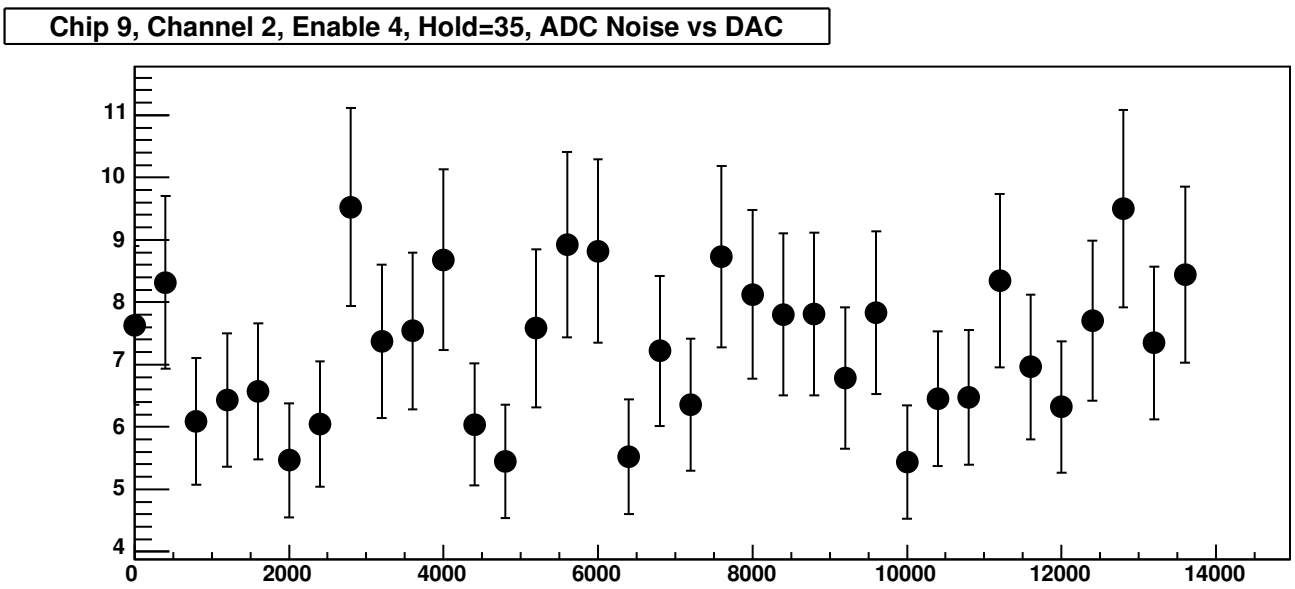

Chip 9, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

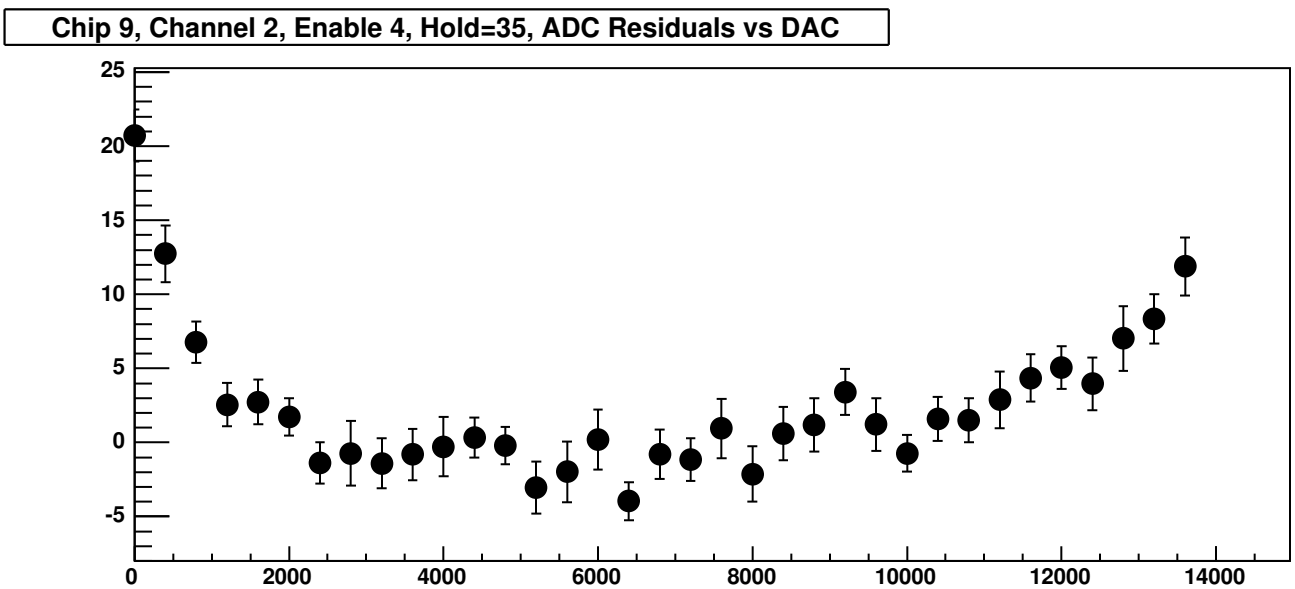

Chip 9, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 128.6 / 23
p0 -392.9 ± 3.098
p1 2.362 ± 0.002041
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

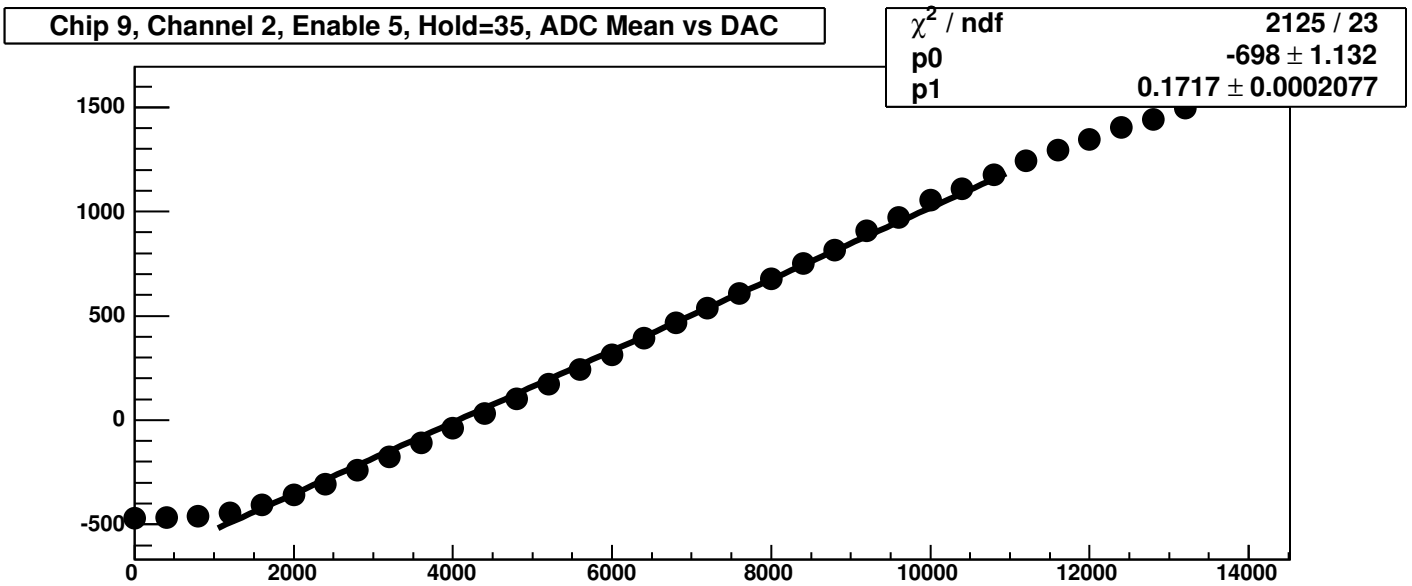

Chip 9, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

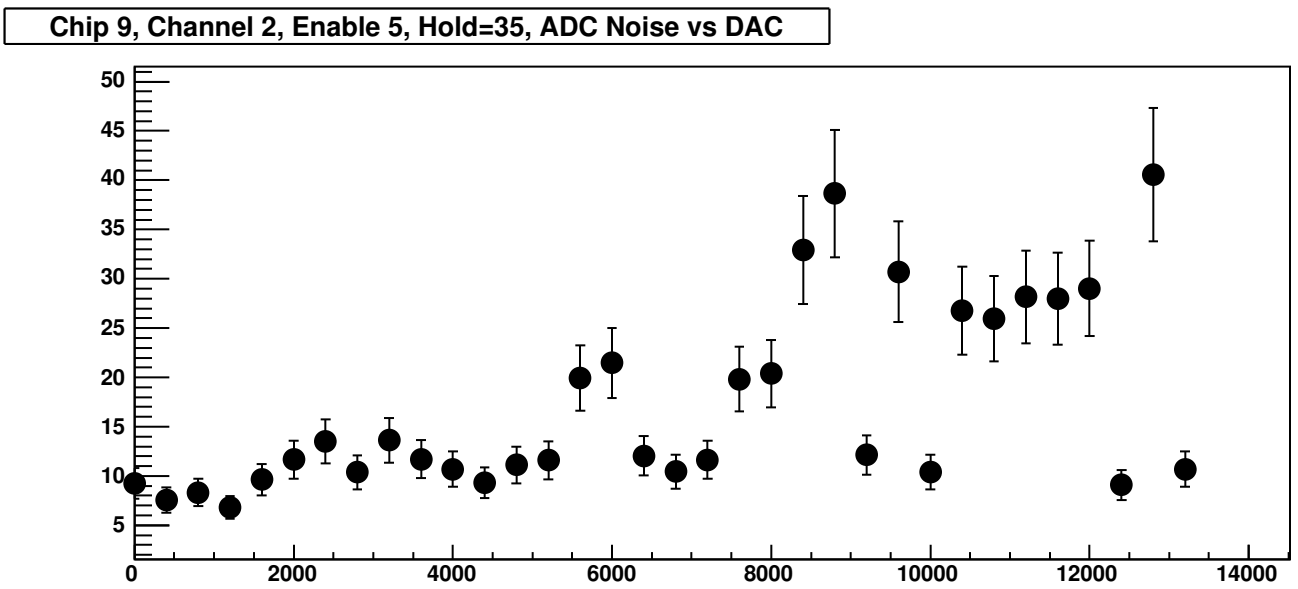

Chip 9, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

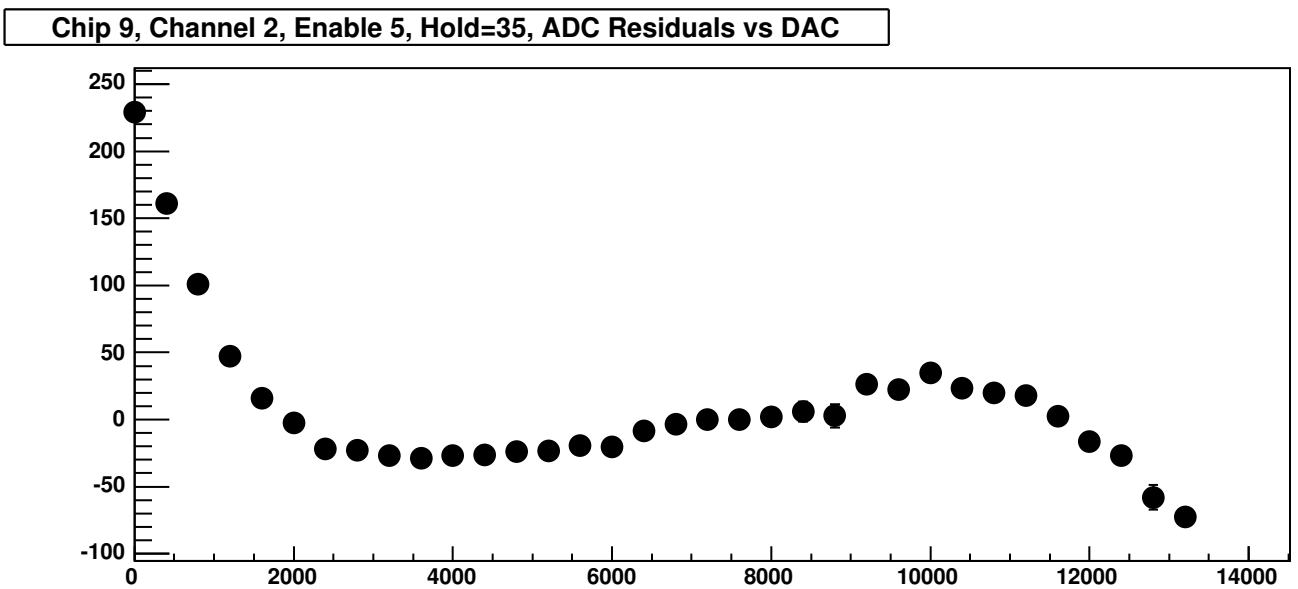

Chip 9, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 34.81 / 23
p0 -502 ± 0.697
p1 0.01958 ± 0.0001047
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

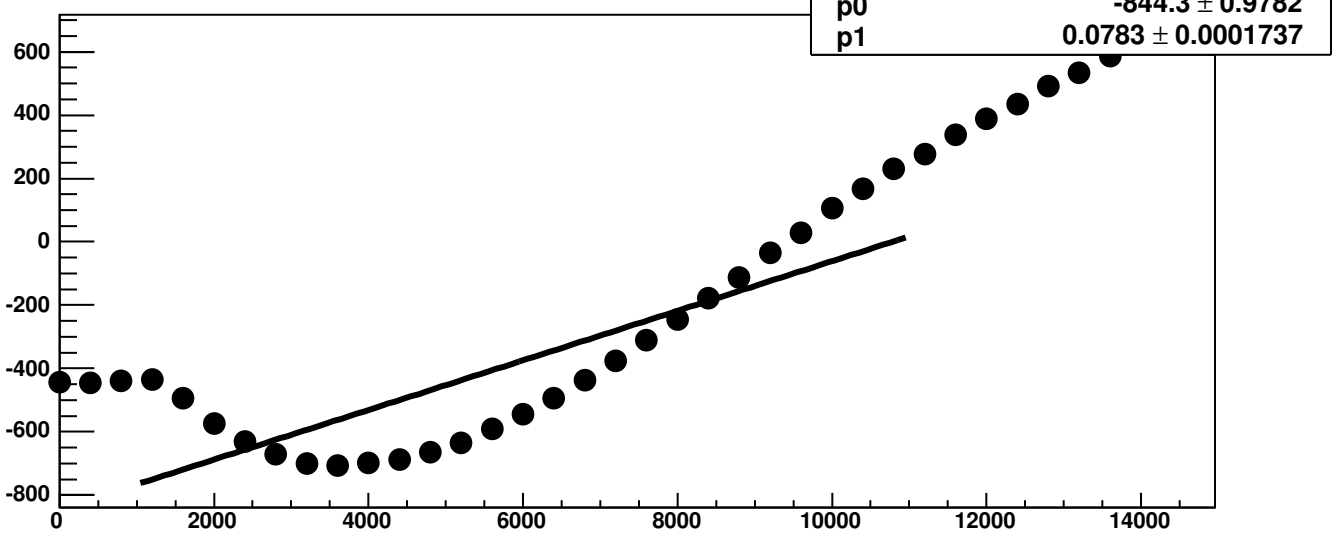

Chip 9, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2125 / 23
p0 -698 ± 1.132
p1 0.1717 ± 0.0002077
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

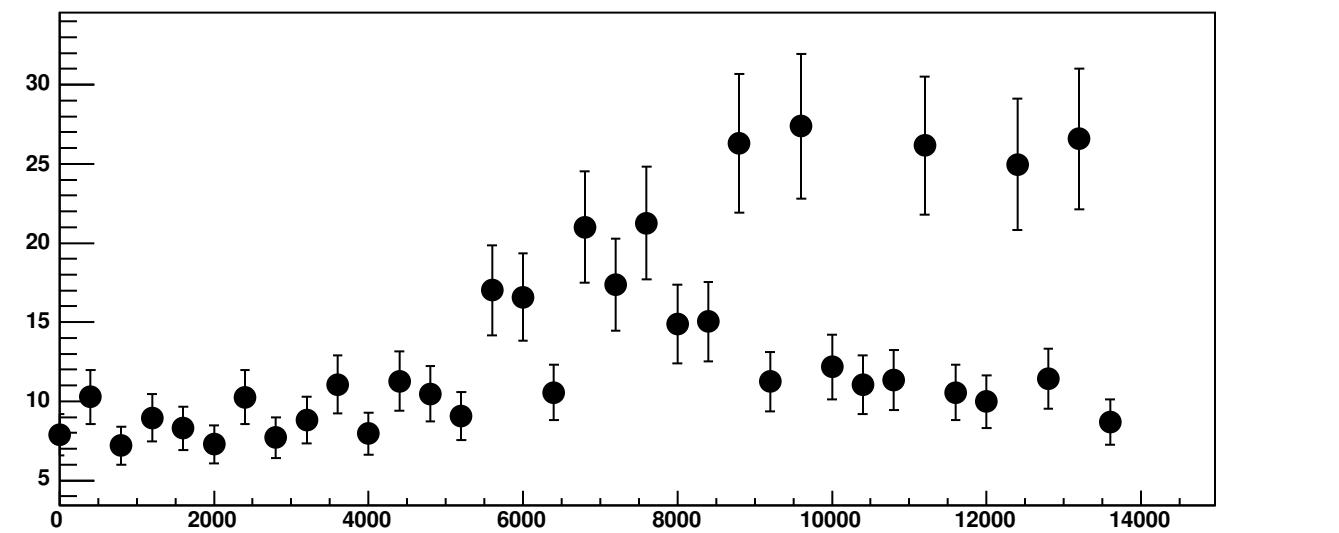

Chip 9, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

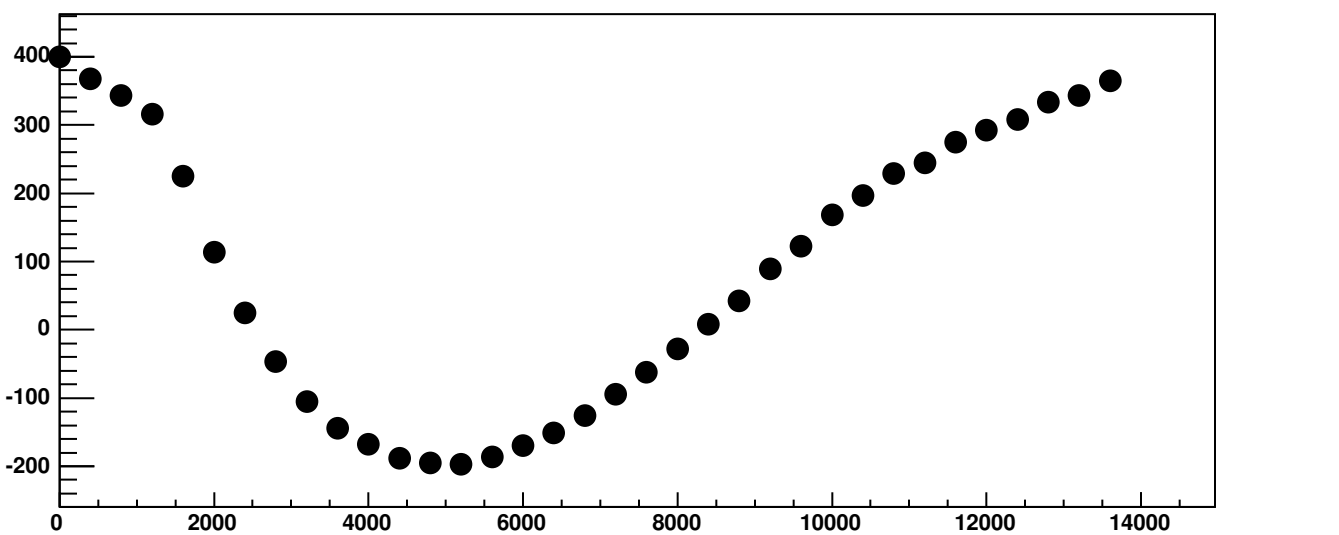
Chip 9, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC



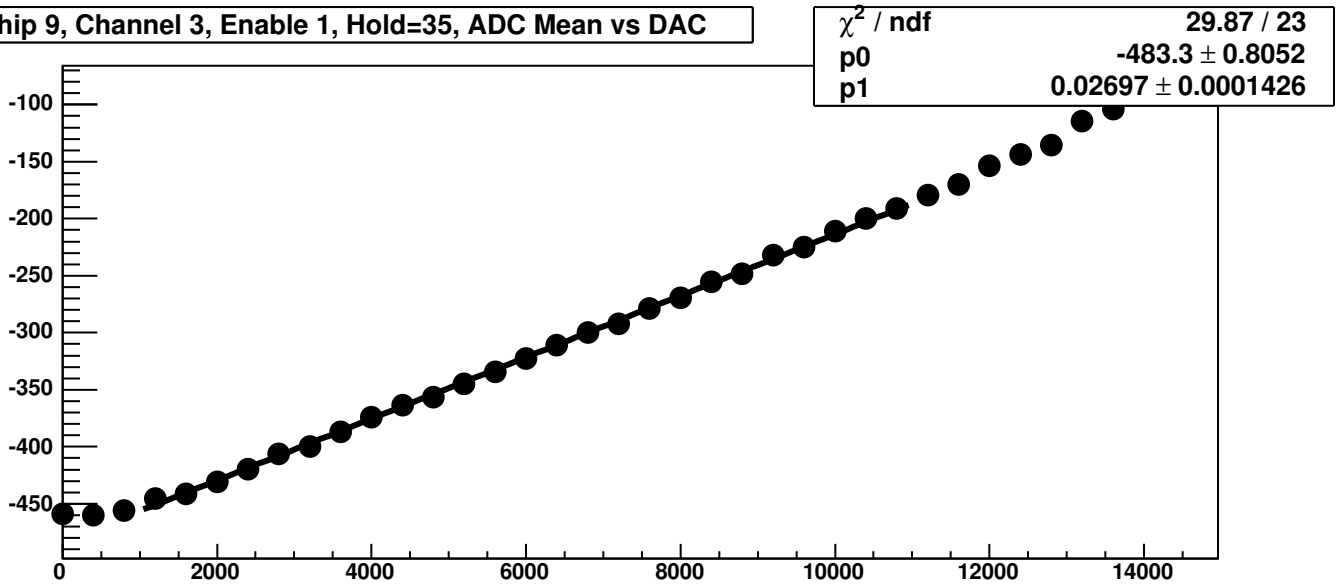
Chip 9, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC



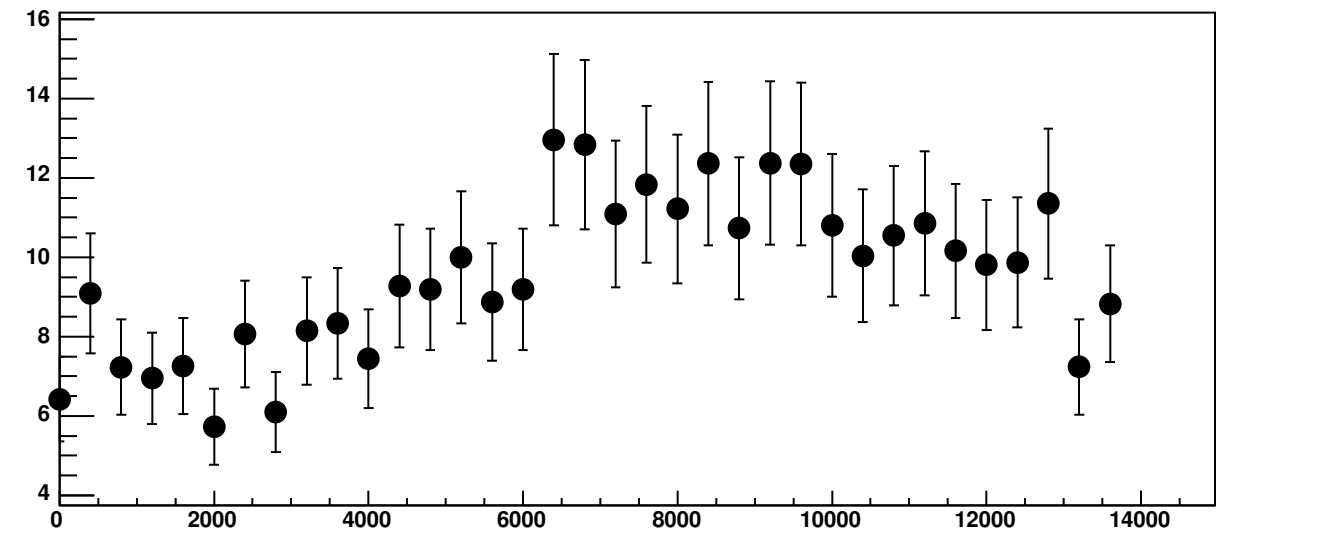
Chip 9, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC



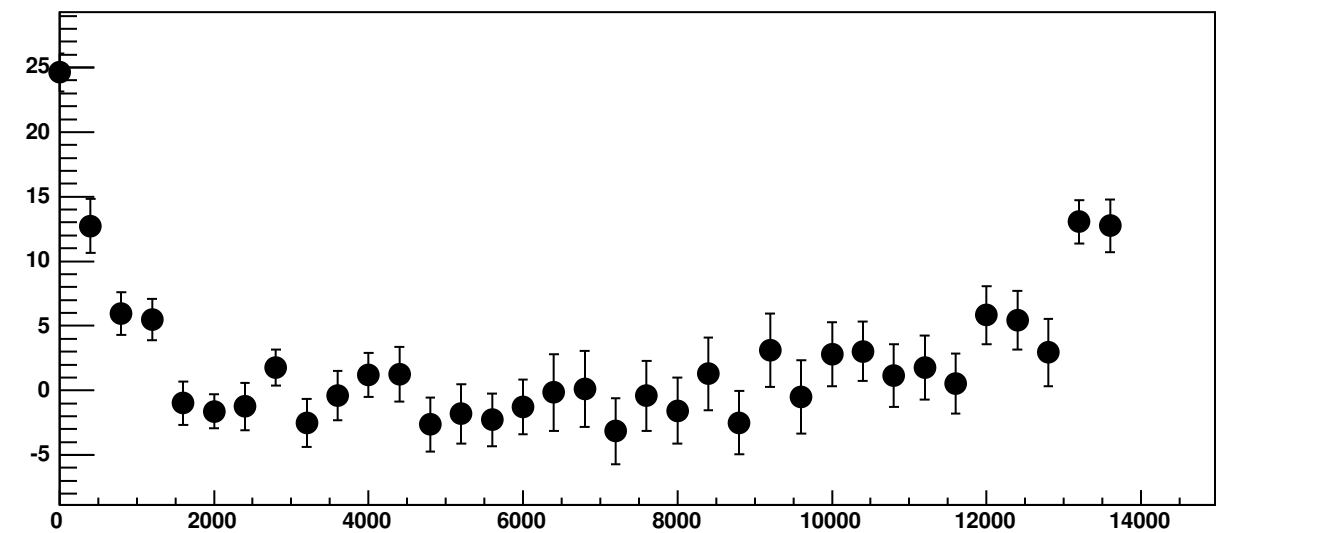
Chip 9, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC

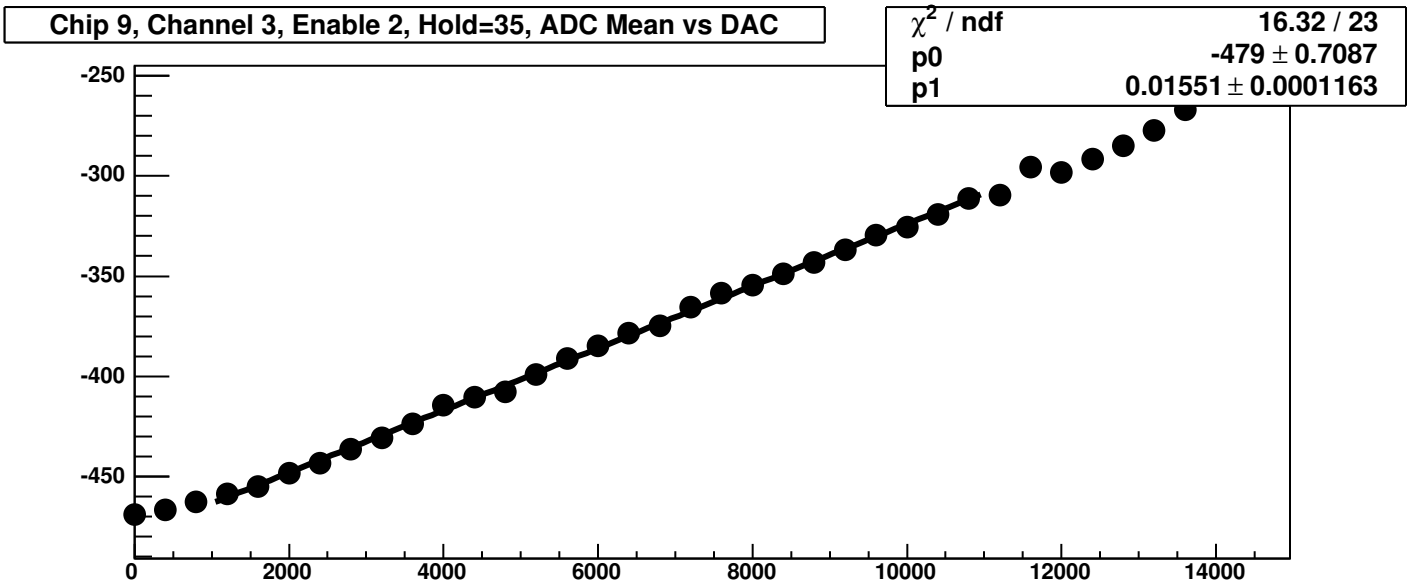


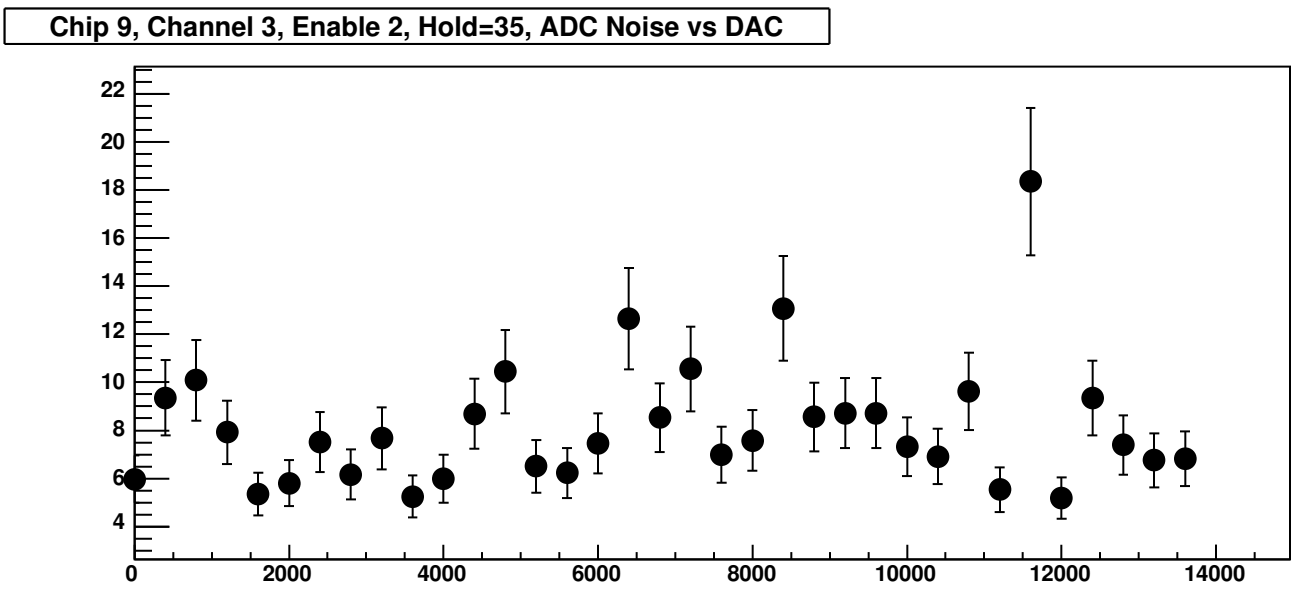
Chip 9, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC

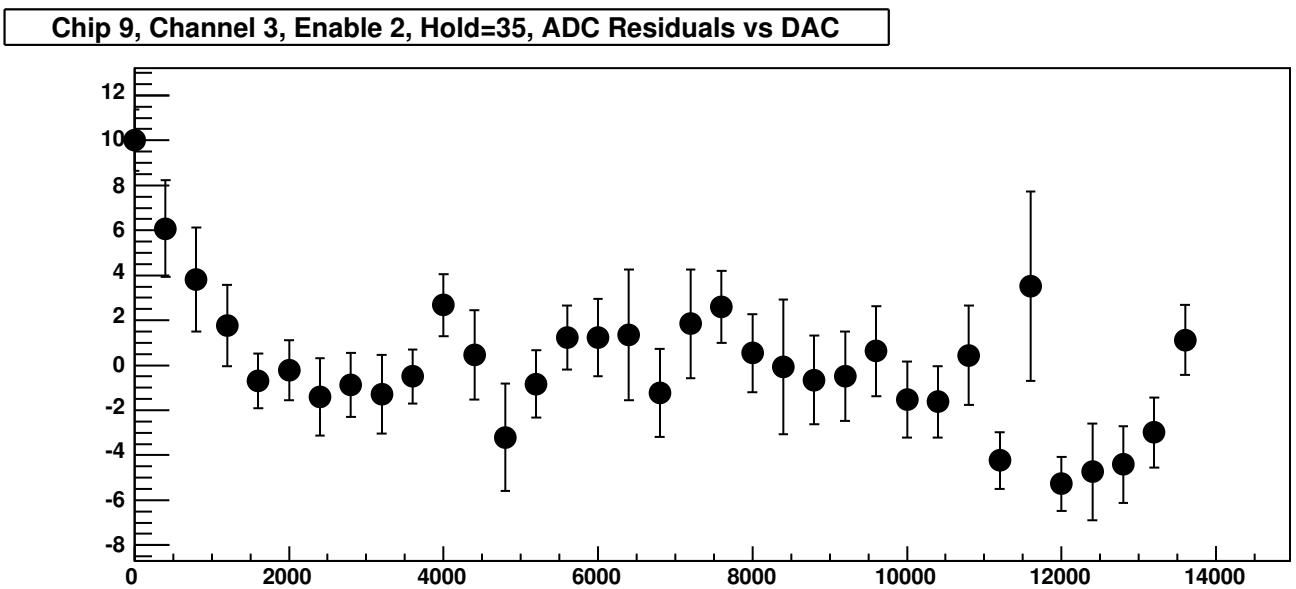


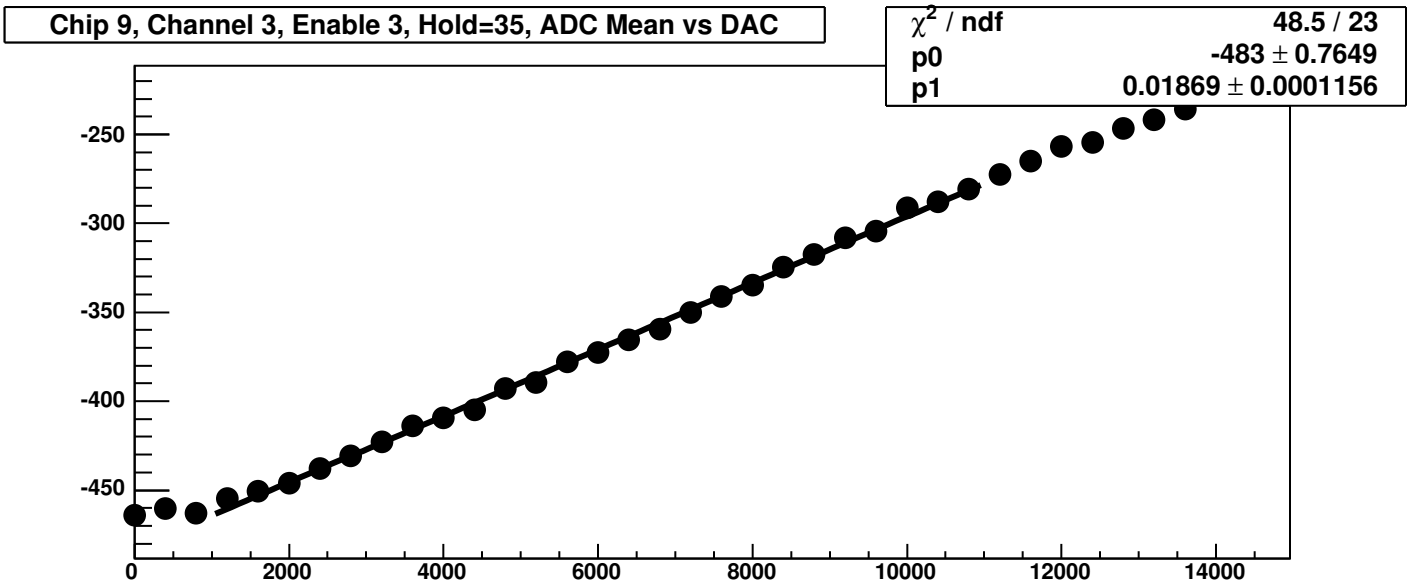
Chip 9, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC

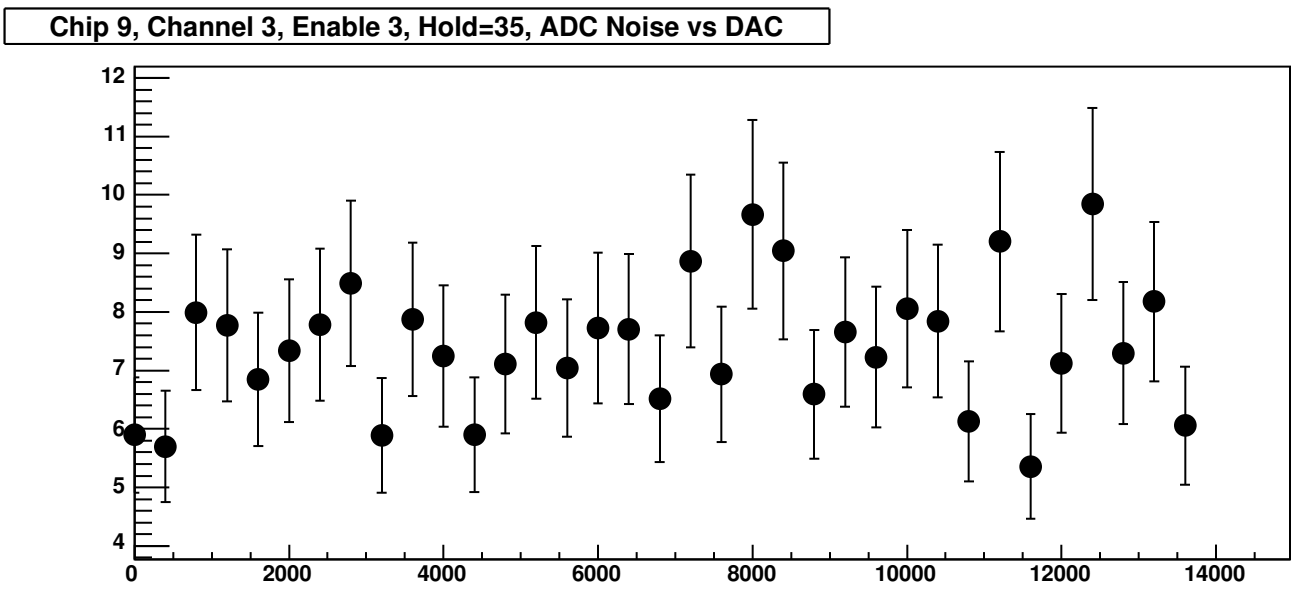


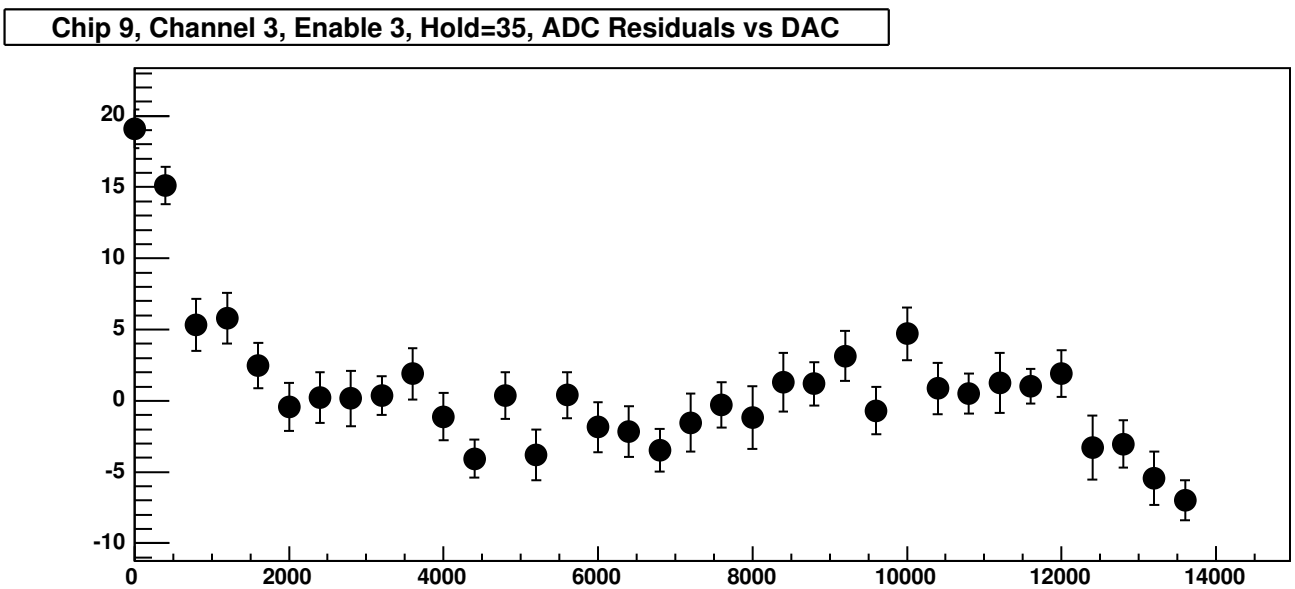

Chip 9, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 16.32 / 23
p0 -479 ± 0.7087
p1 0.01551 ± 0.0001163
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

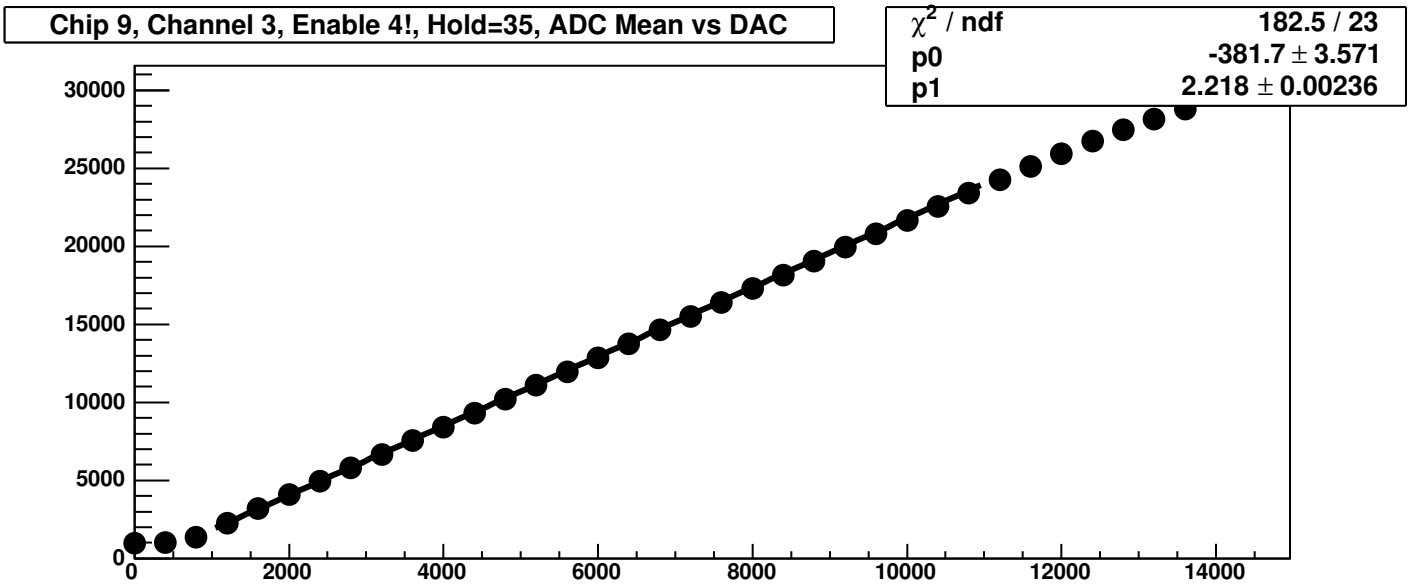

Chip 9, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

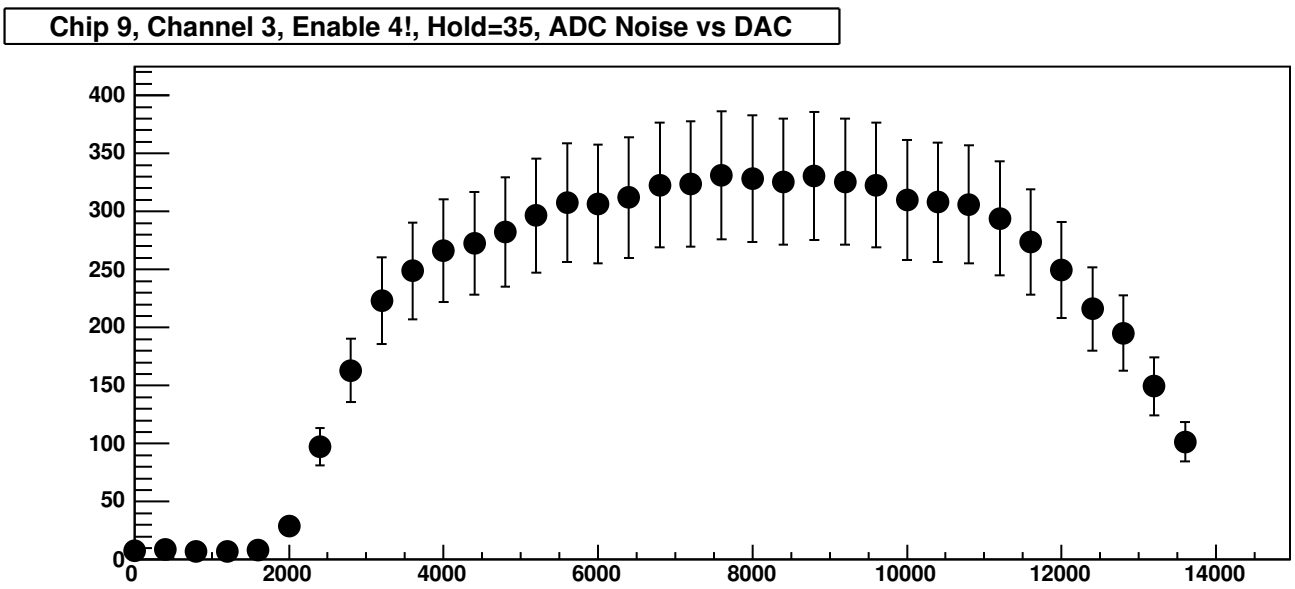

Chip 9, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

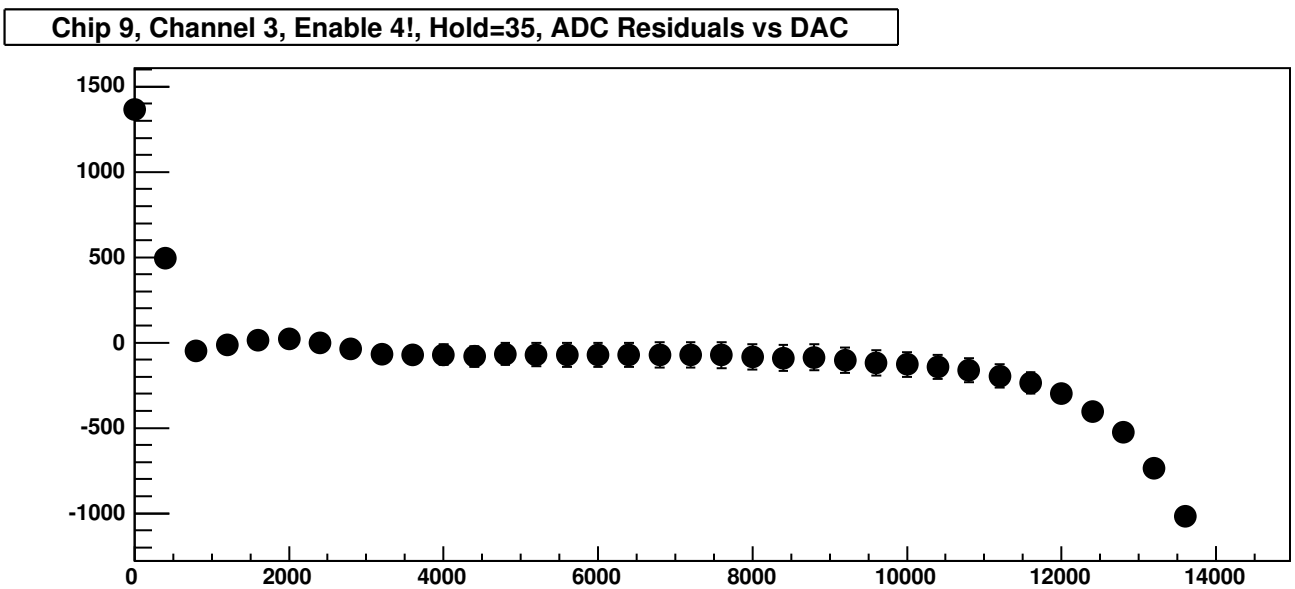

Chip 9, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 48.5 / 23
p0 -483 ± 0.7649
p1 0.01869 ± 0.0001156
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

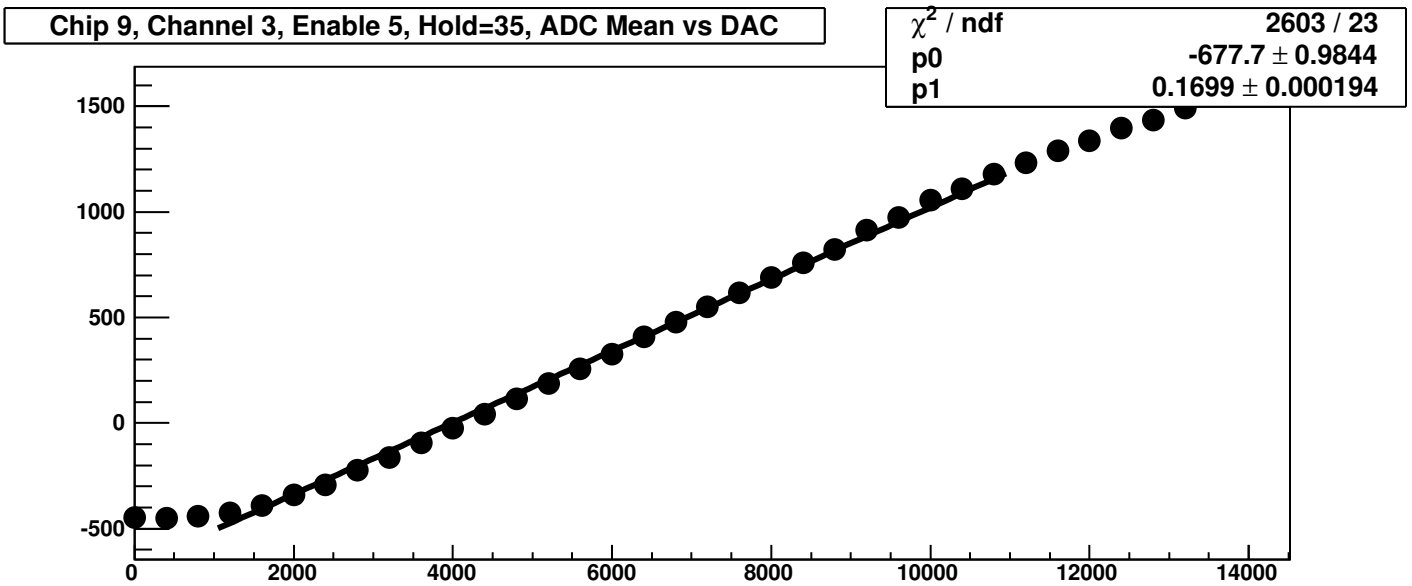

Chip 9, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

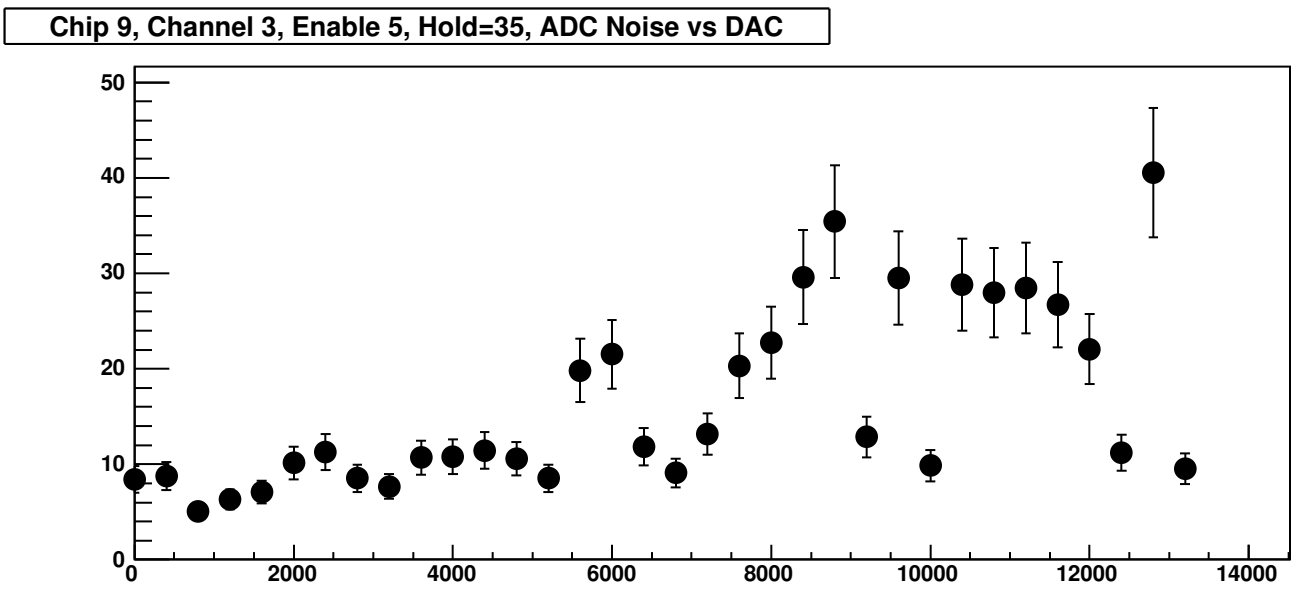

Chip 9, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

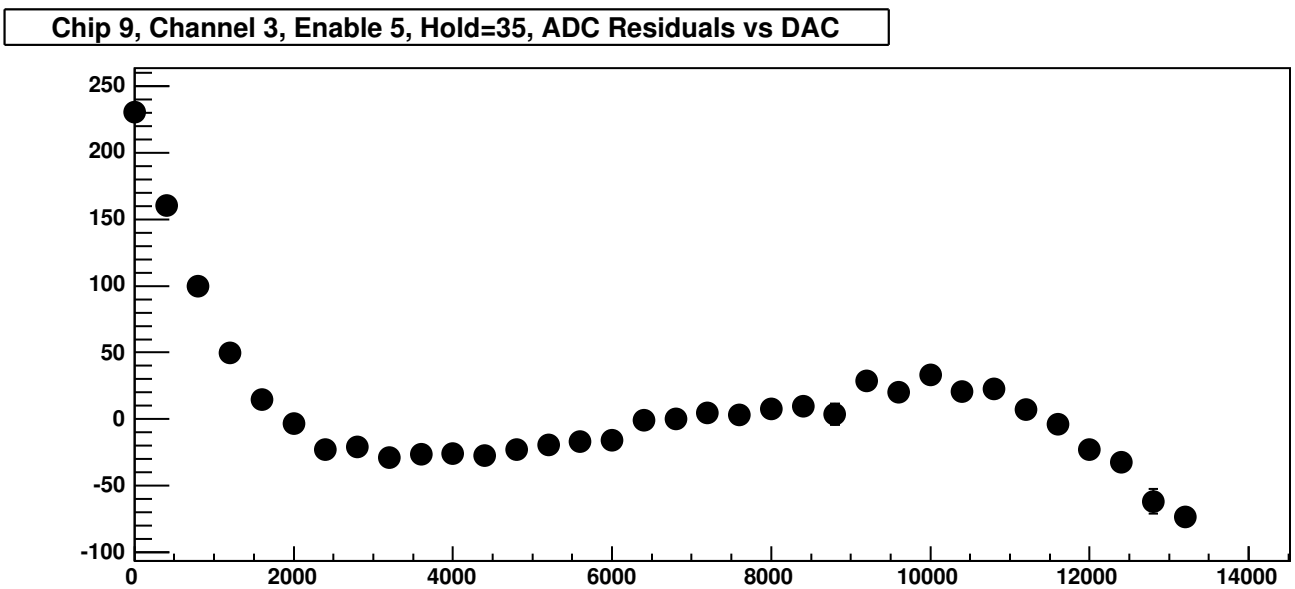

Chip 9, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 182.5 / 23
p0 -381.7 ± 3.571
p1 2.218 ± 0.00236
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

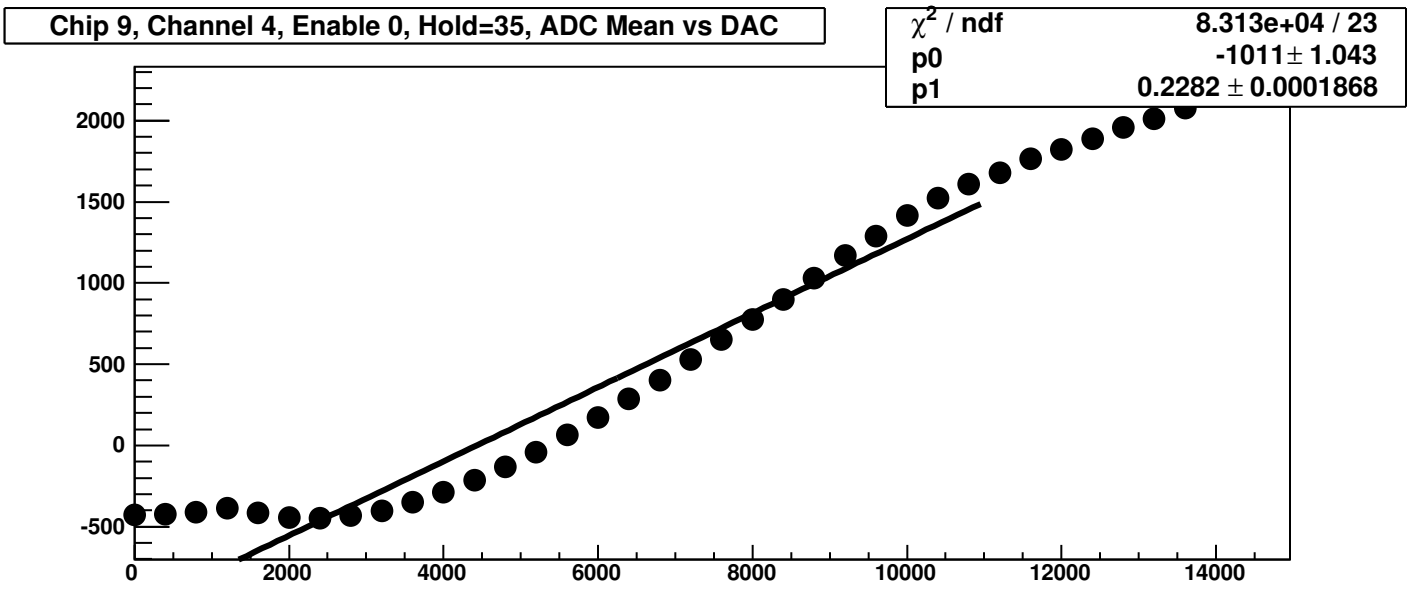

Chip 9, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

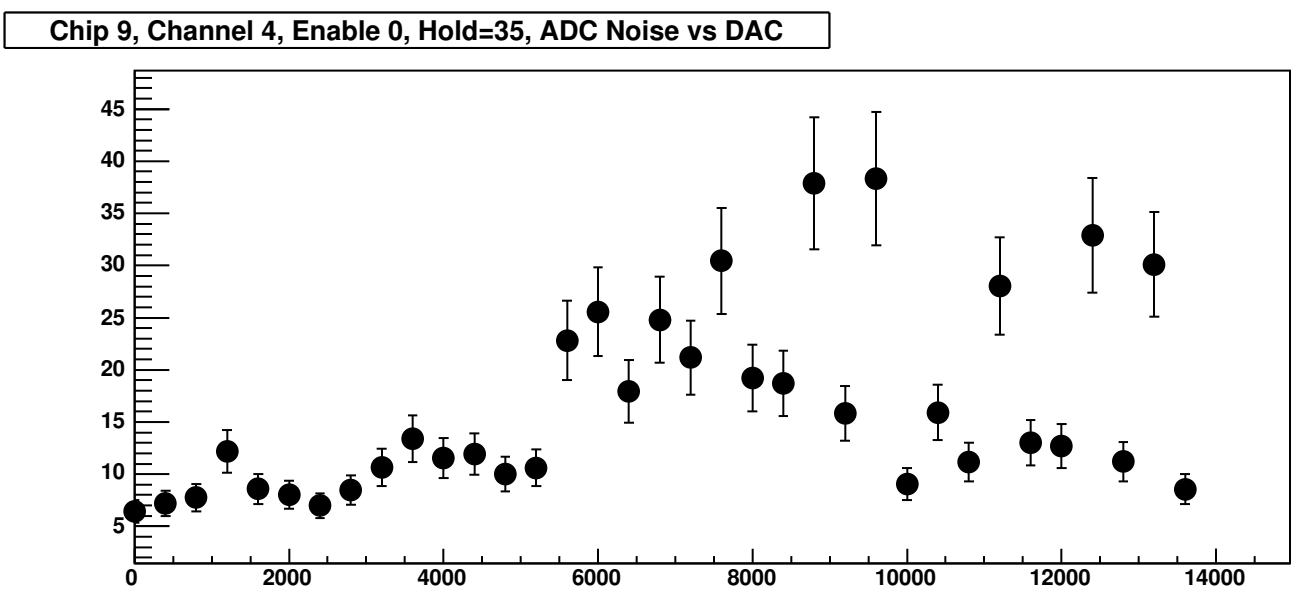

Chip 9, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

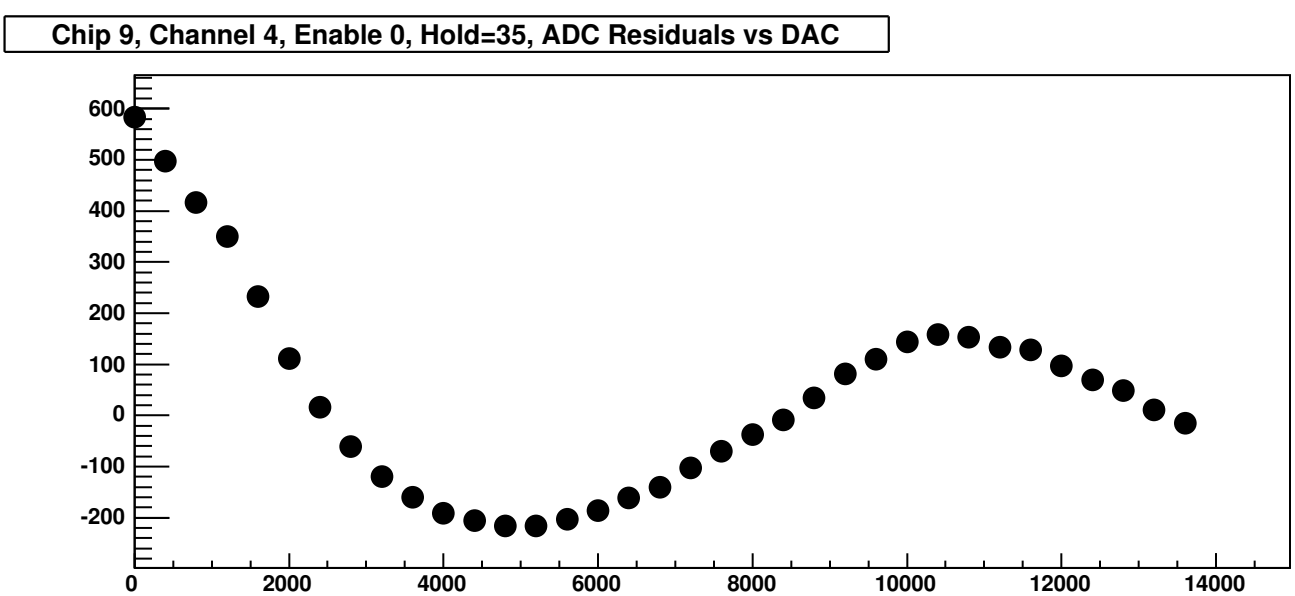

Chip 9, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2603 / 23
p0 -677.7 ± 0.9844
p1 0.1699 ± 0.000194
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

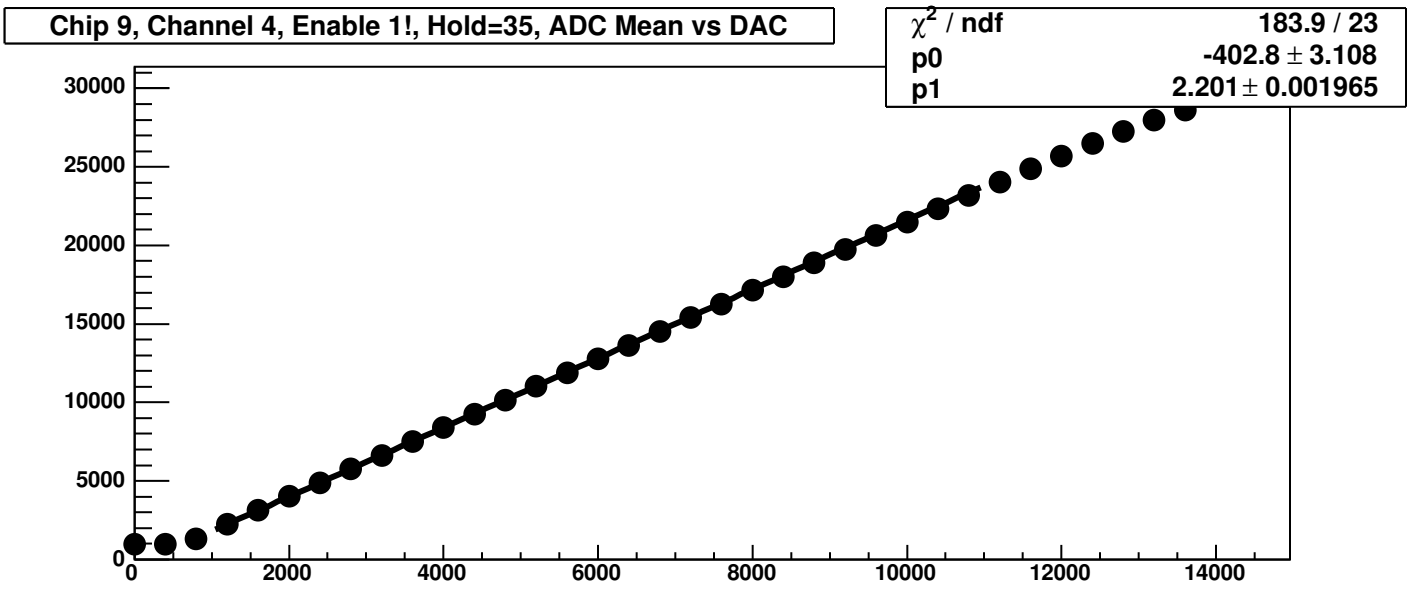

Chip 9, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

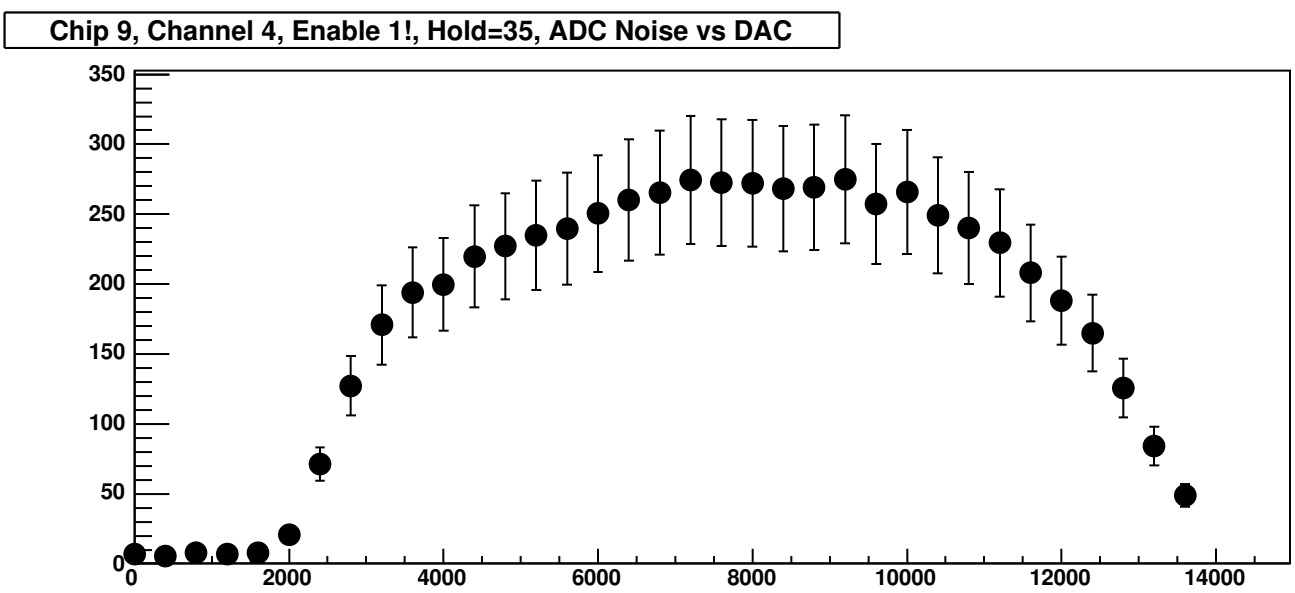

Chip 9, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

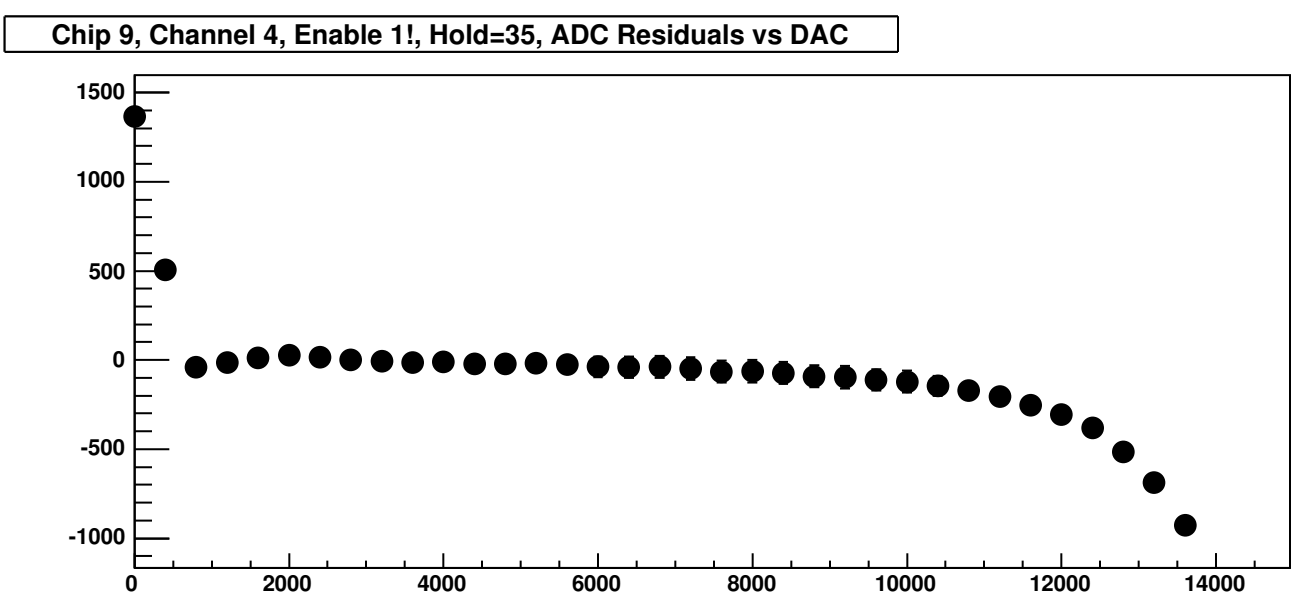

Chip 9, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 8.313e+04 / 23
p0 -1011 ± 1.043
p1 0.2282 ± 0.0001868
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

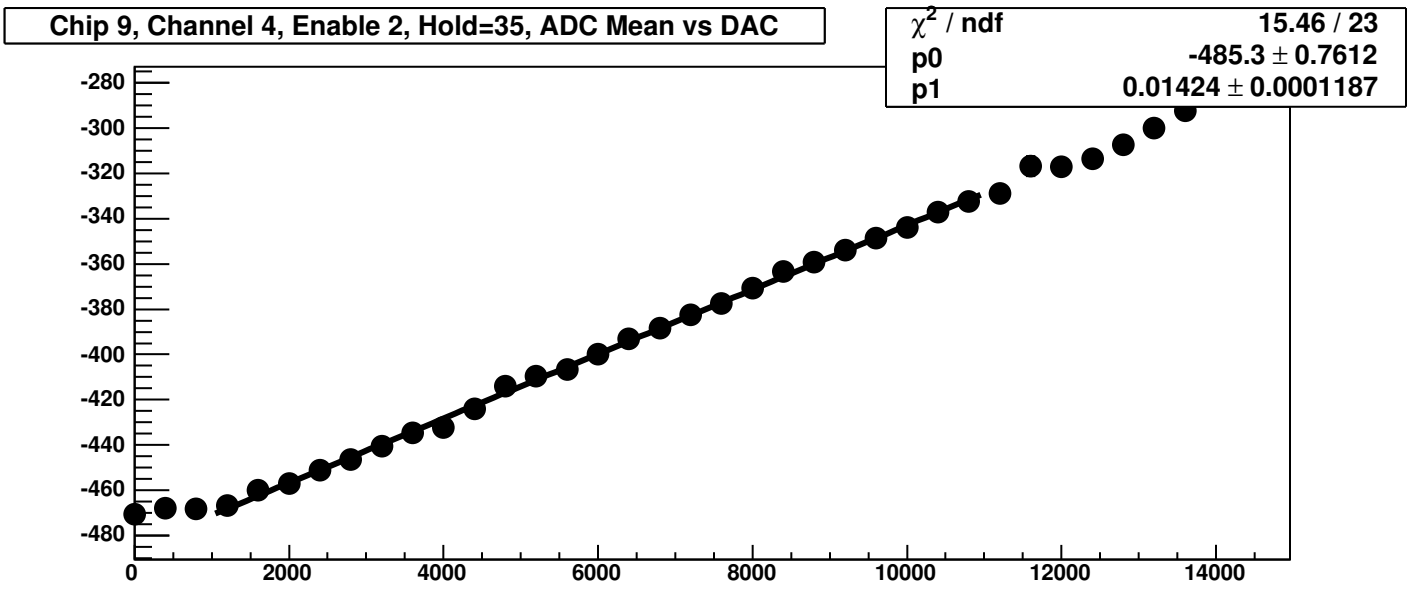

Chip 9, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

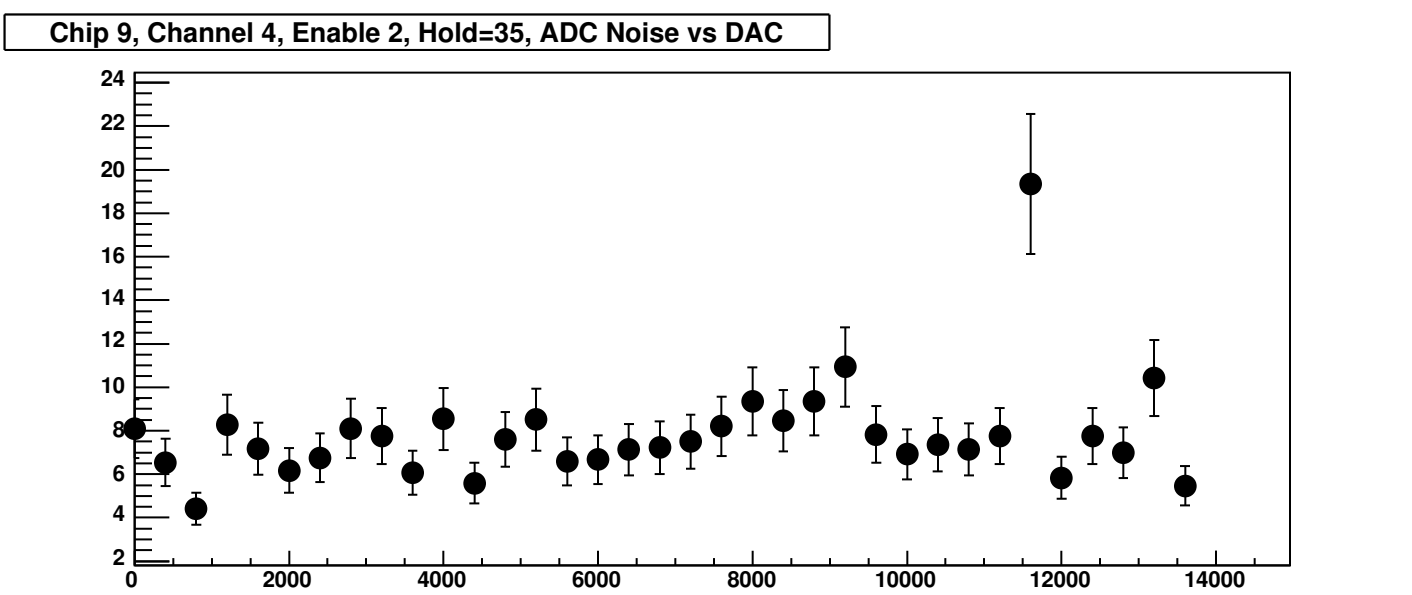

Chip 9, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

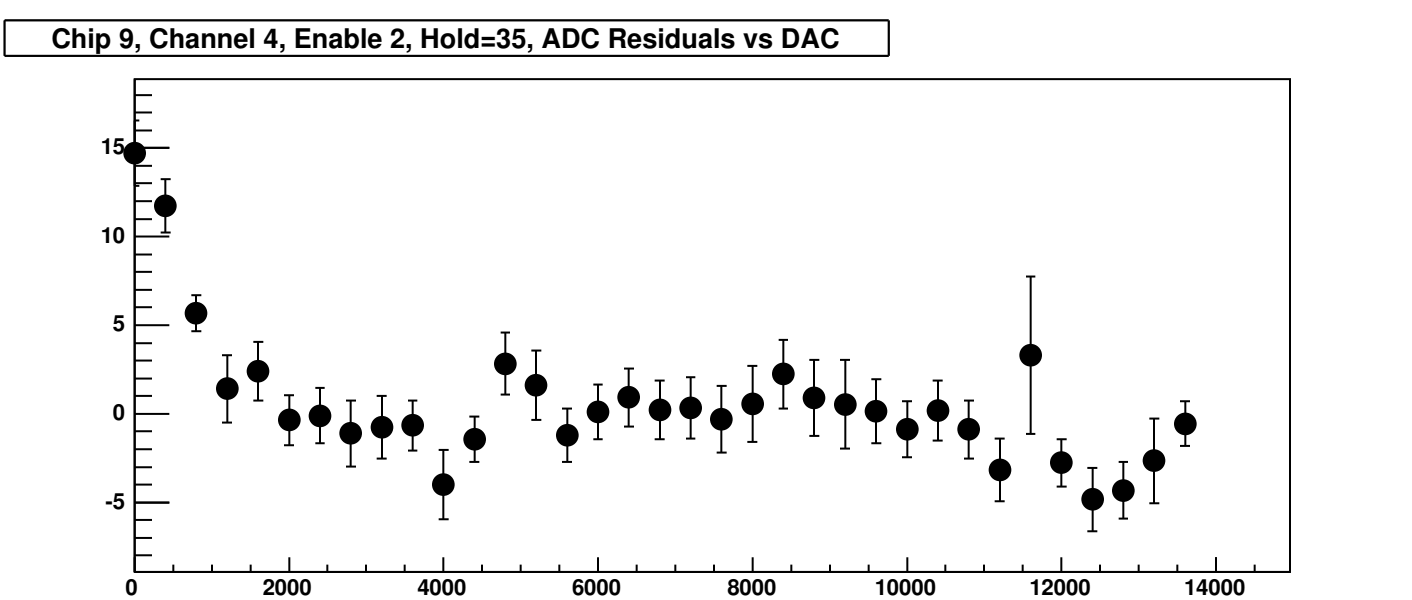

Chip 9, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 183.9 / 23
p0 -402.8 ± 3.108
p1 2.201 ± 0.001965
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

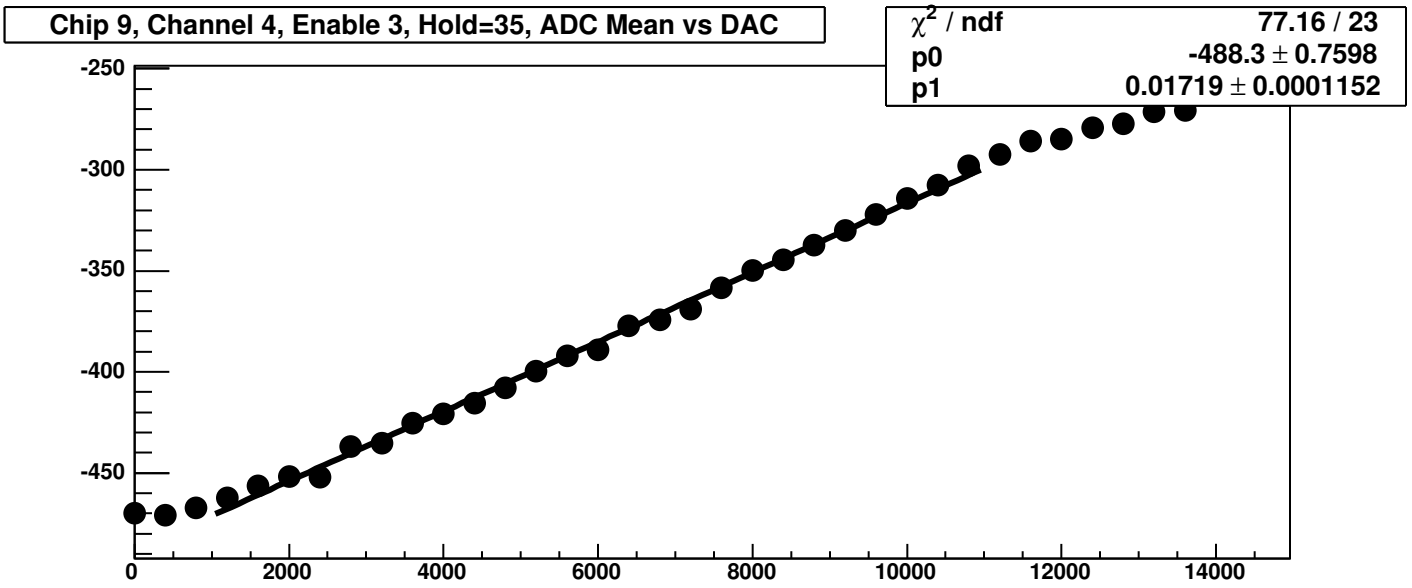

Chip 9, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

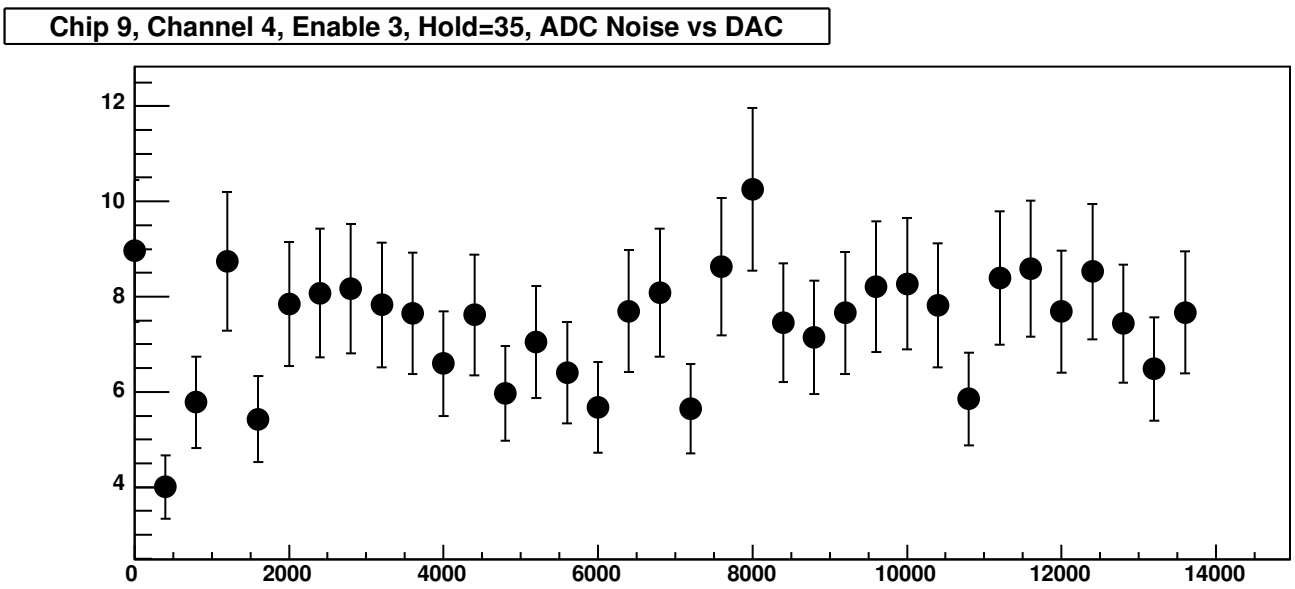

Chip 9, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

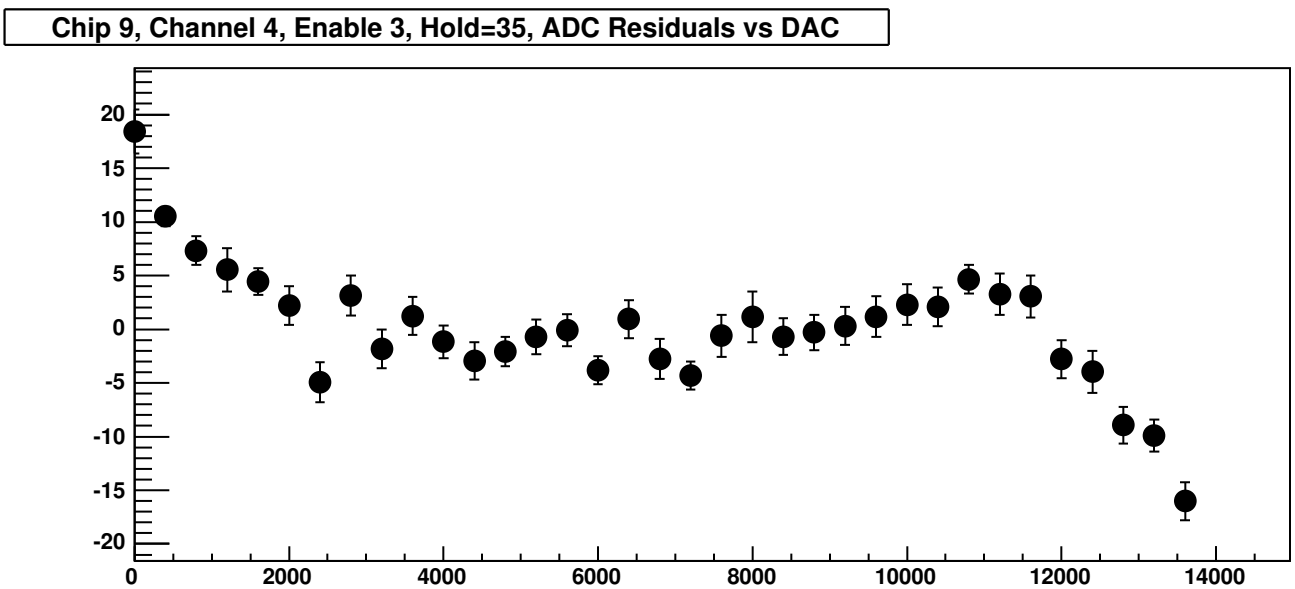

Chip 9, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 15.46 / 23
p0 -485.3 ± 0.7612
p1 0.01424 ± 0.0001187
-280
-300
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
2000
4000
6000
8000
10000
12000
14000

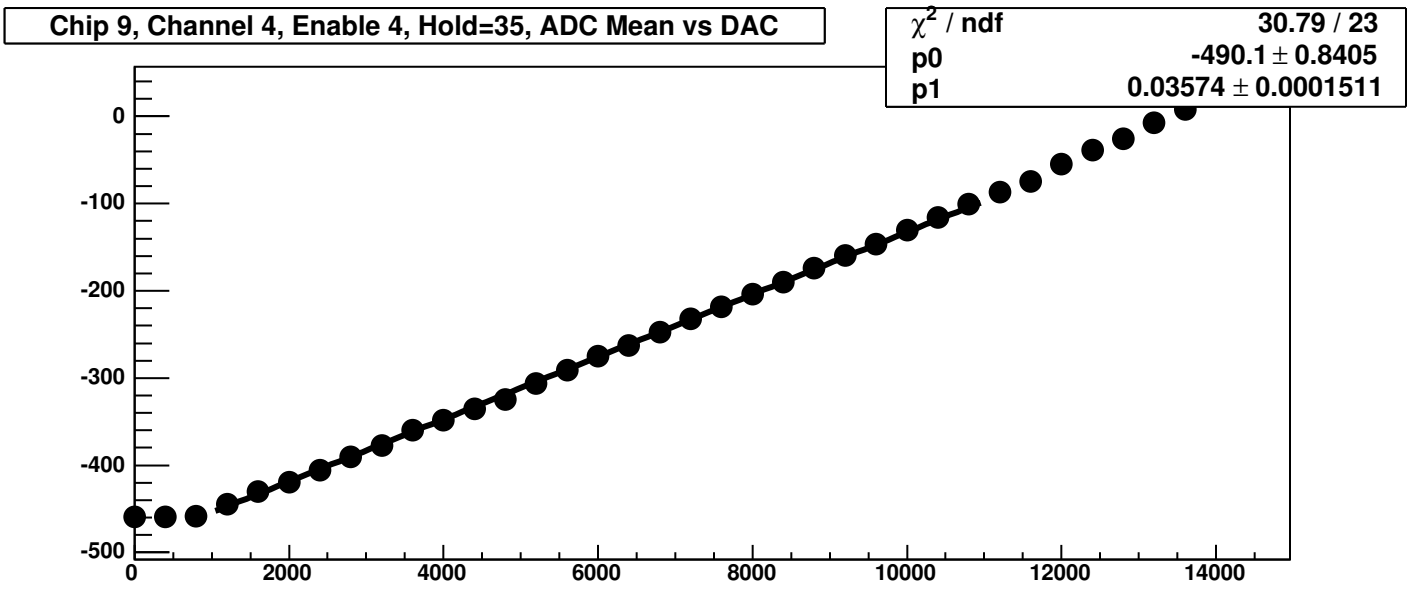

Chip 9, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
24
22
20
18
16
14
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

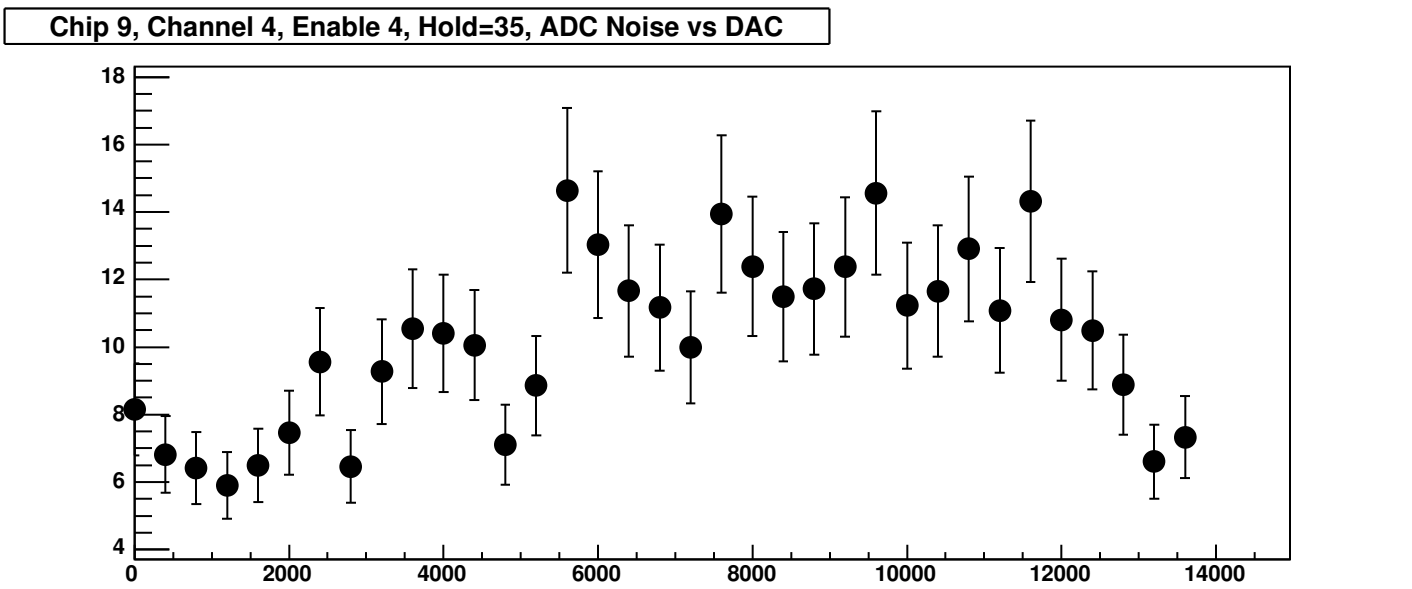

Chip 9, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

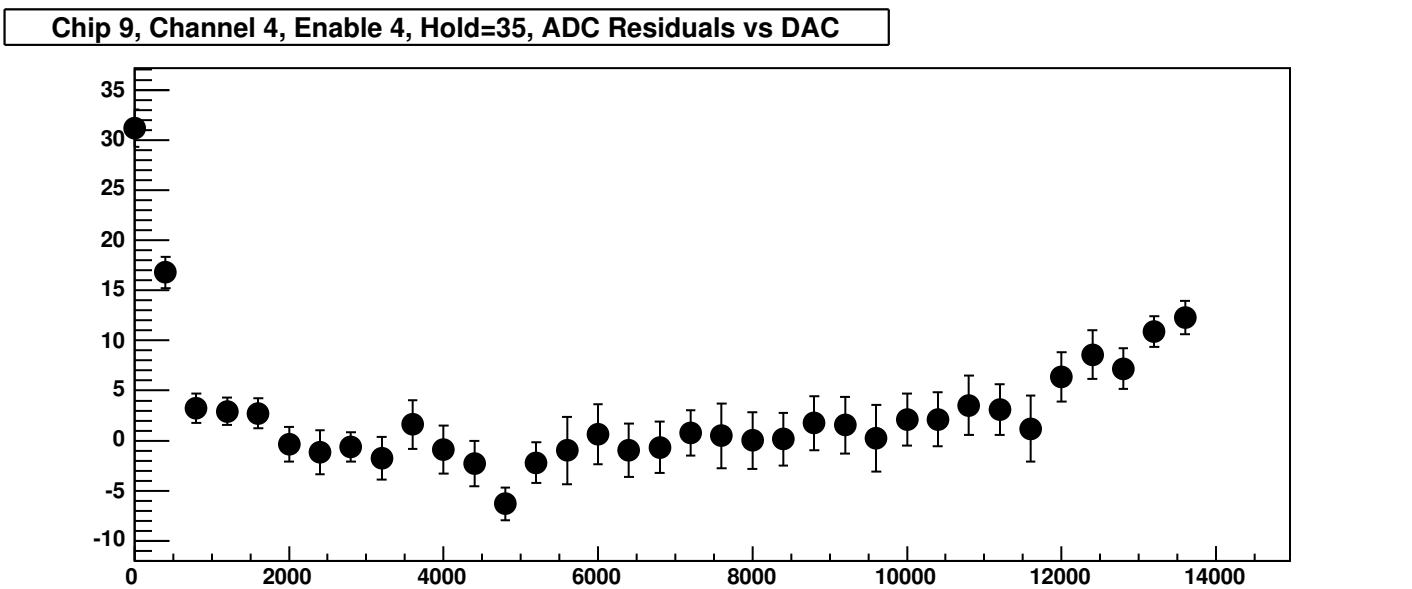

Chip 9, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 77.16 / 23
p0 -488.3 ± 0.7598
p1 0.01719 ± 0.0001152
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

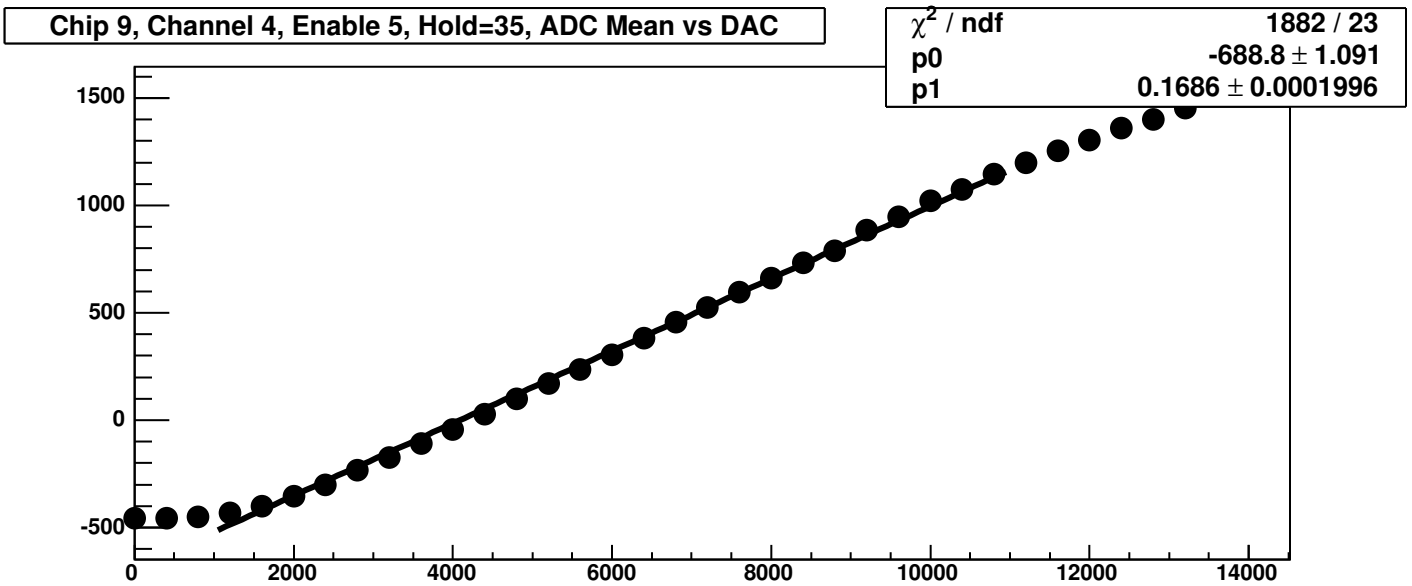

Chip 9, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

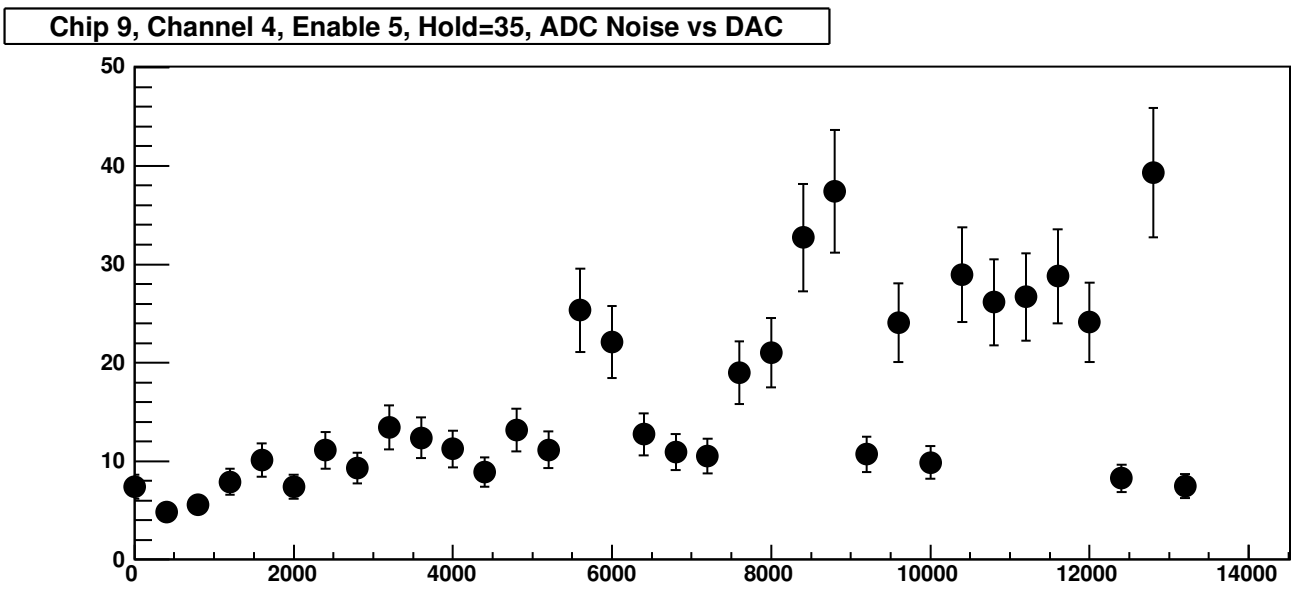

Chip 9, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

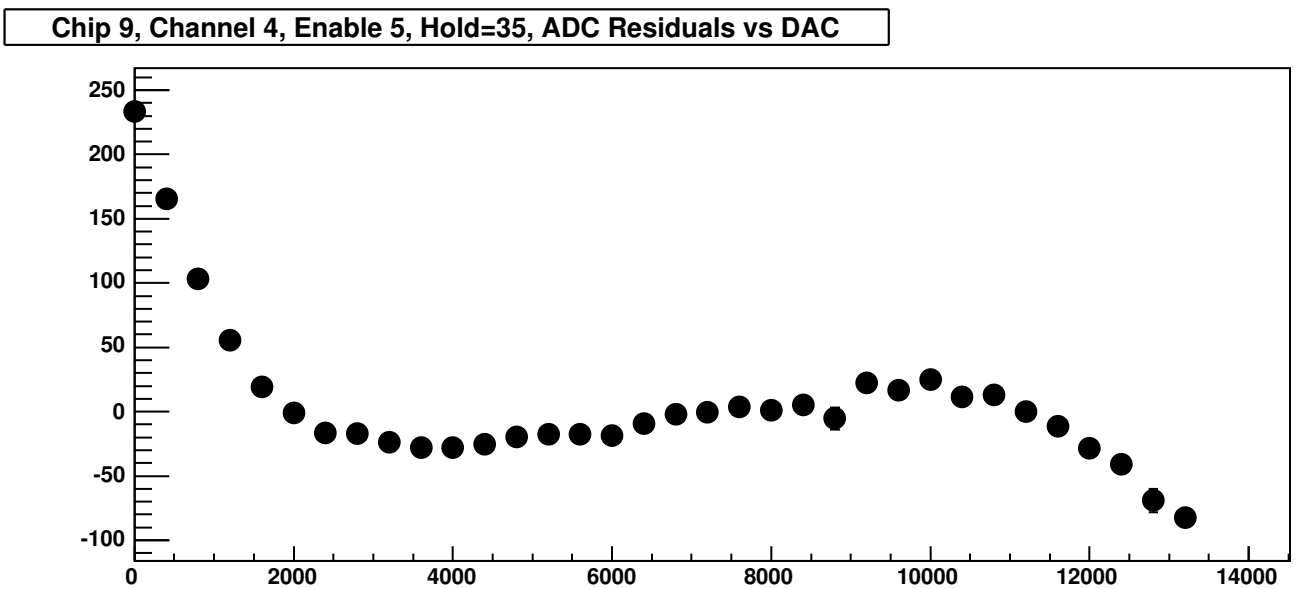

Chip 9, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.79 / 23
p0 -490.1 ± 0.8405
p1 0.03574 ± 0.0001511
0
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

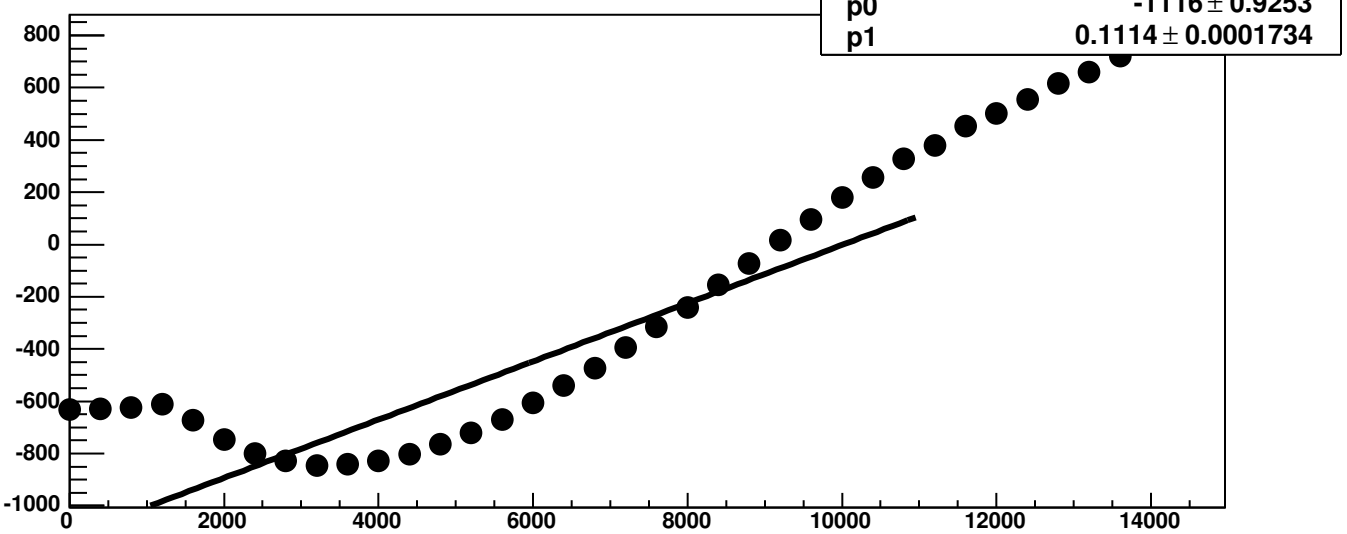

Chip 9, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1882 / 23
p0 -688.8 ± 1.091
p1 0.1686 ± 0.0001996
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

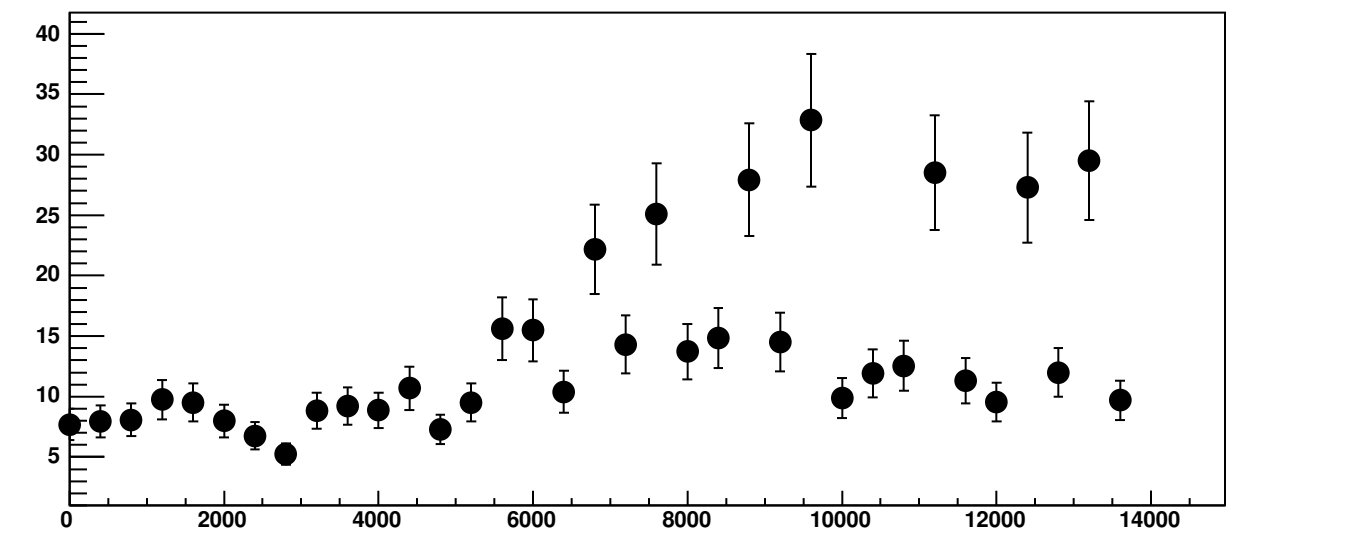

Chip 9, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

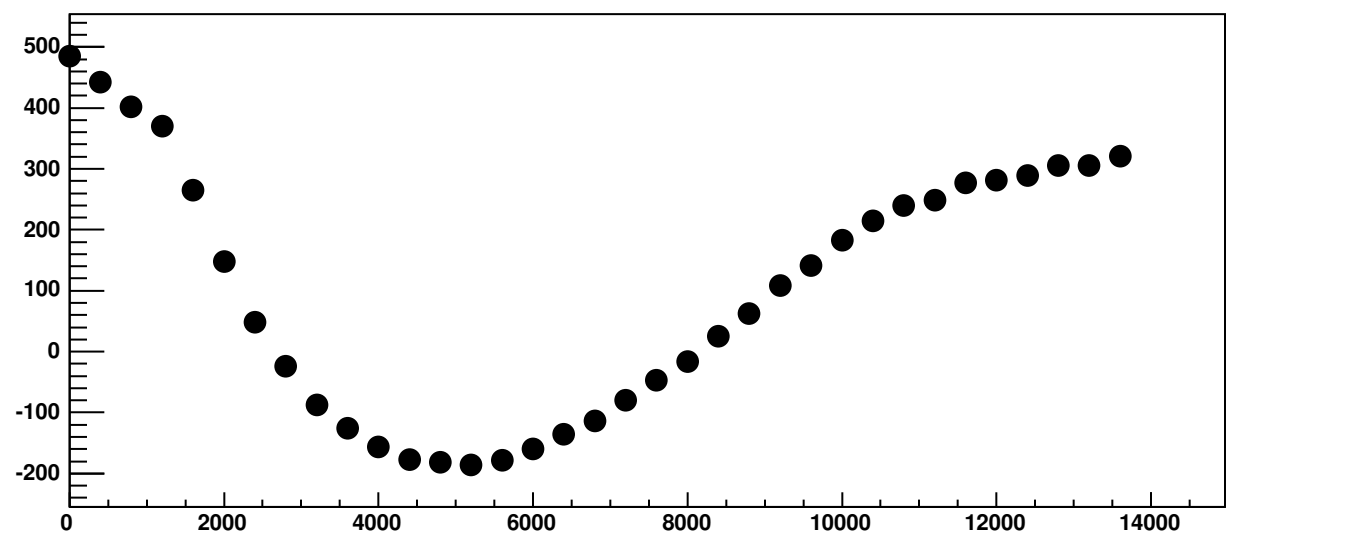
Chip 9, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

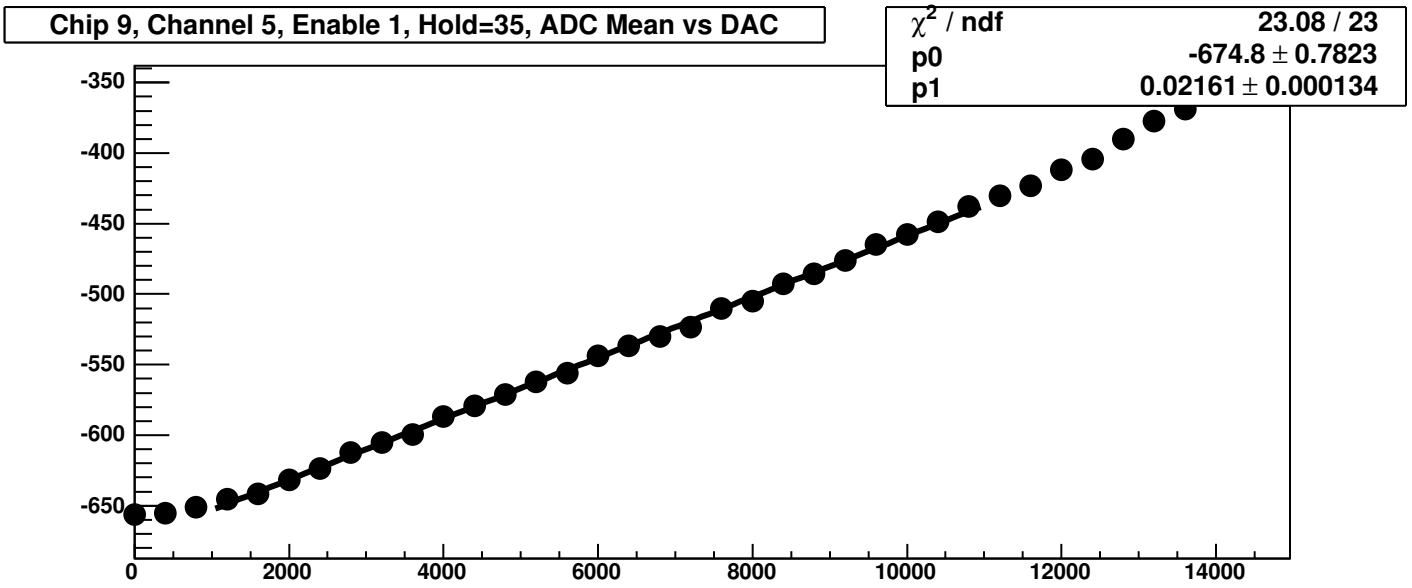


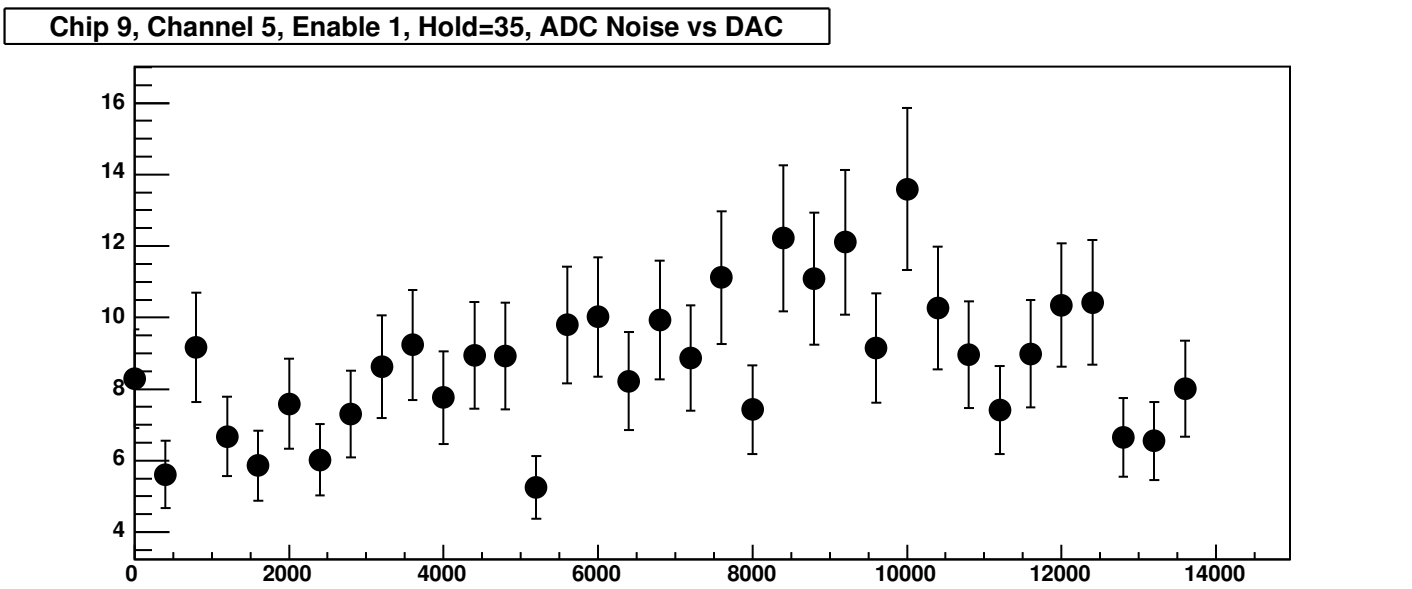
Chip 9, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC

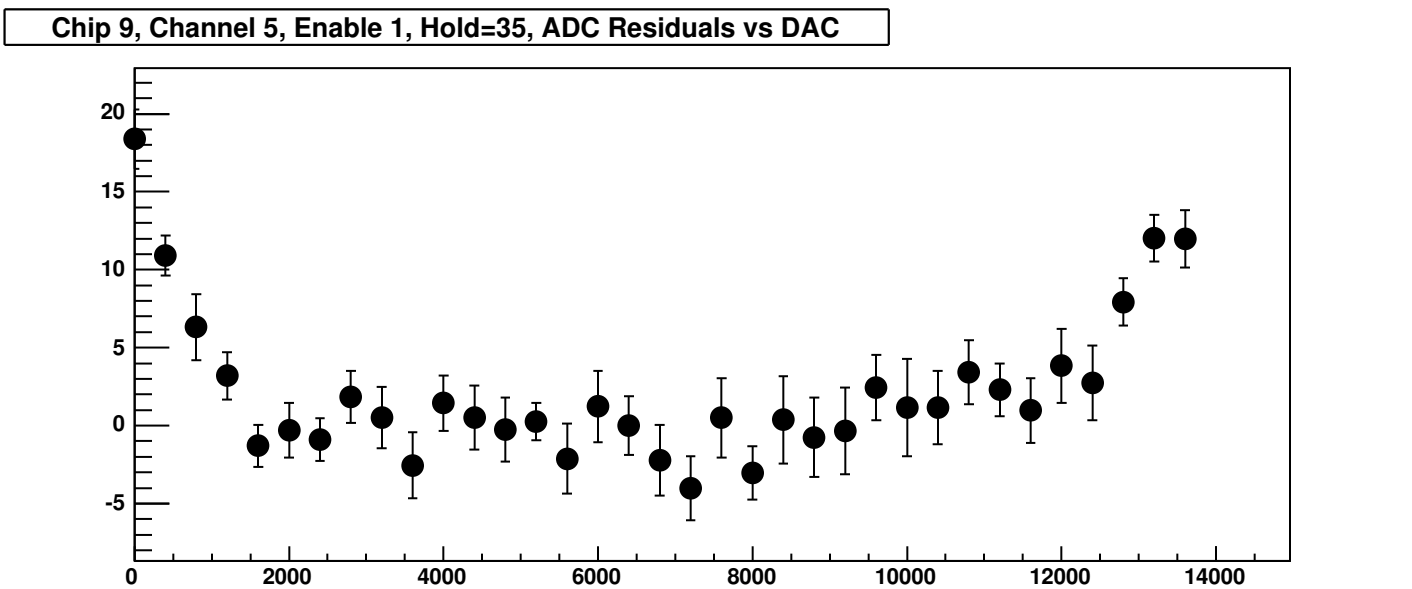


Chip 9, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

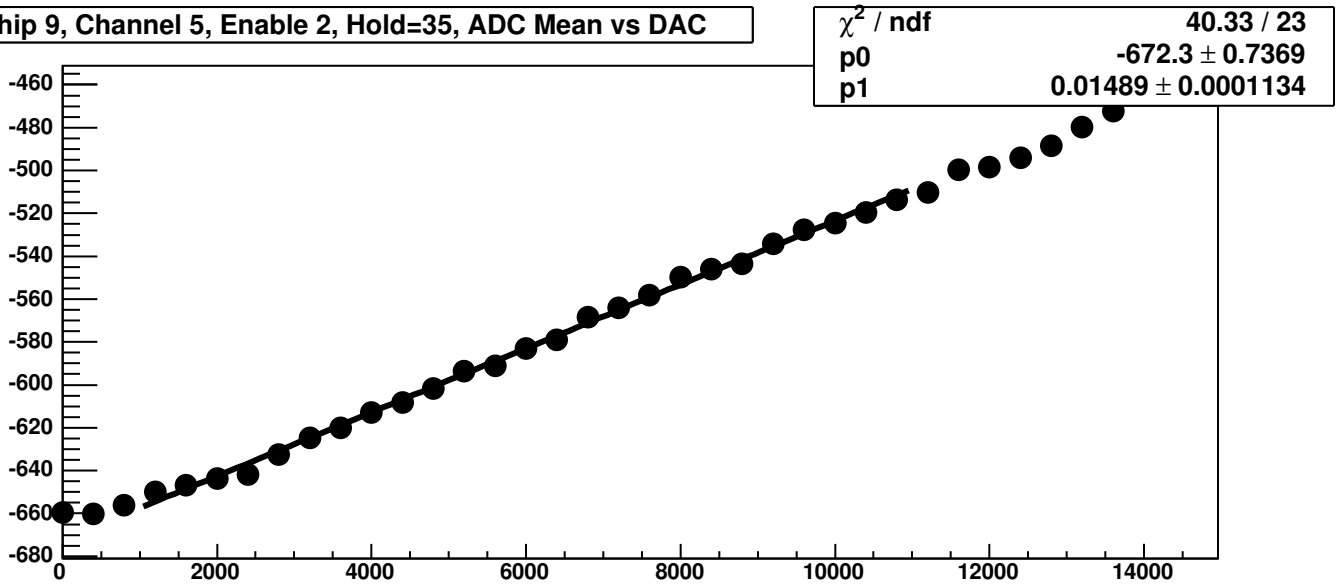



Chip 9, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 23.08 / 23
p0 -674.8 ± 0.7823
p1 0.02161 ± 0.000134
-350
-400
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

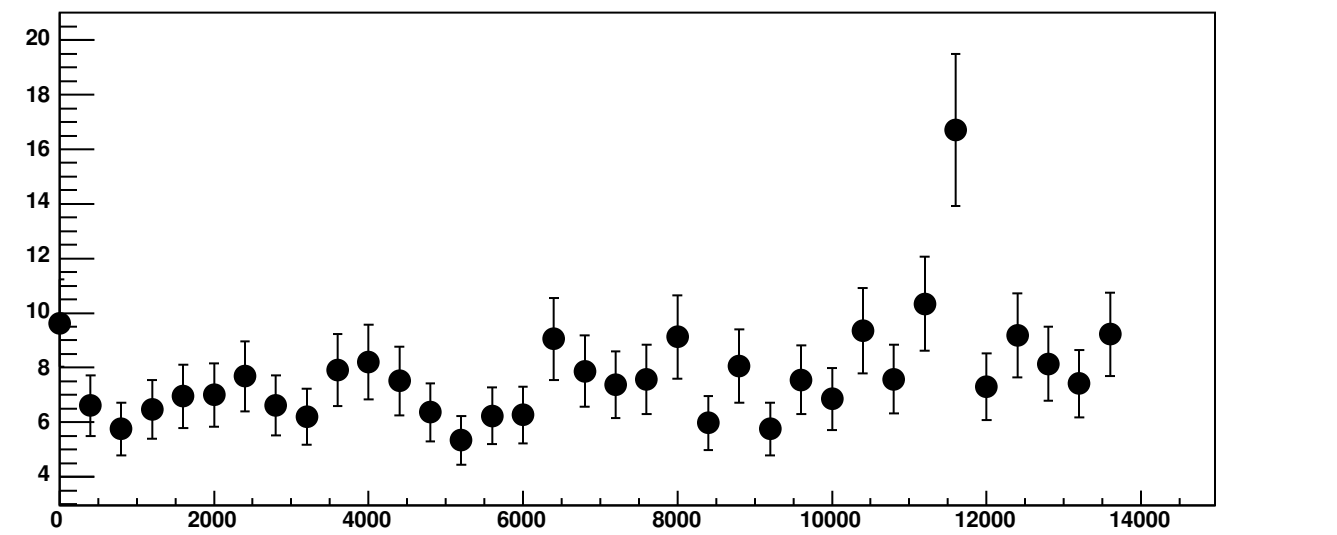

Chip 9, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

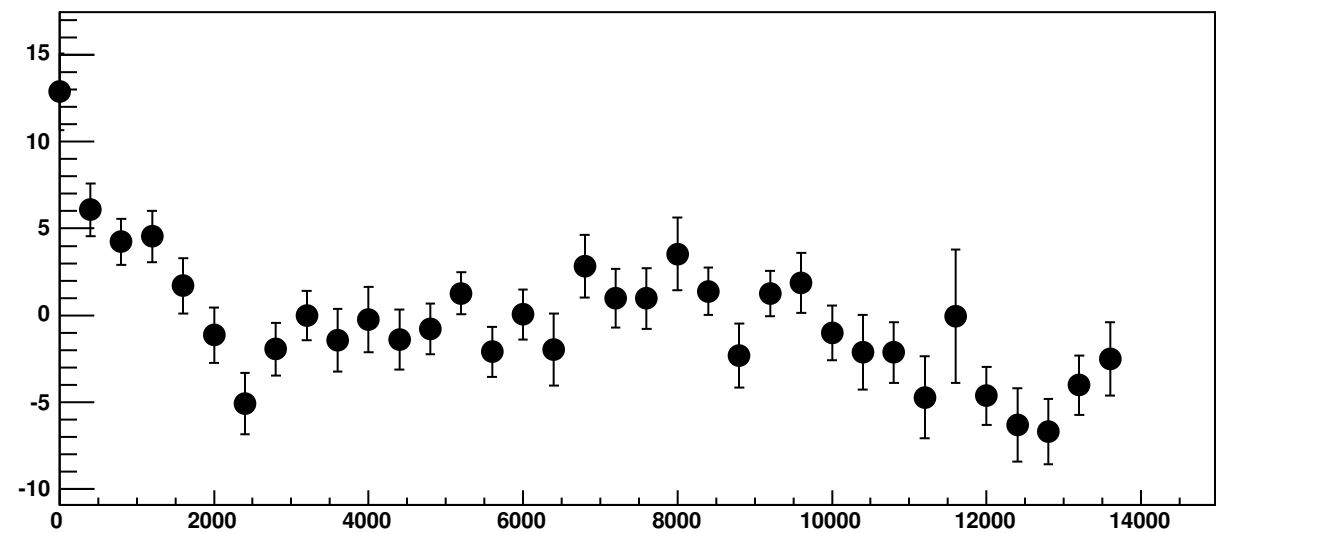
Chip 9, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

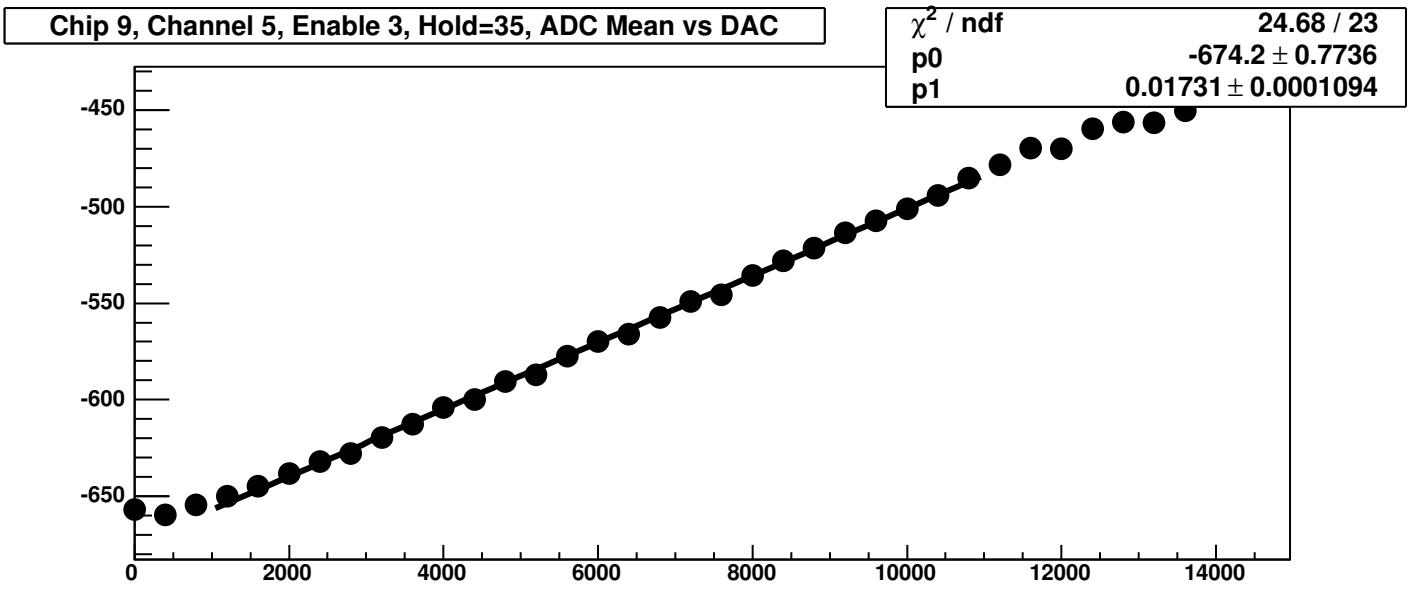


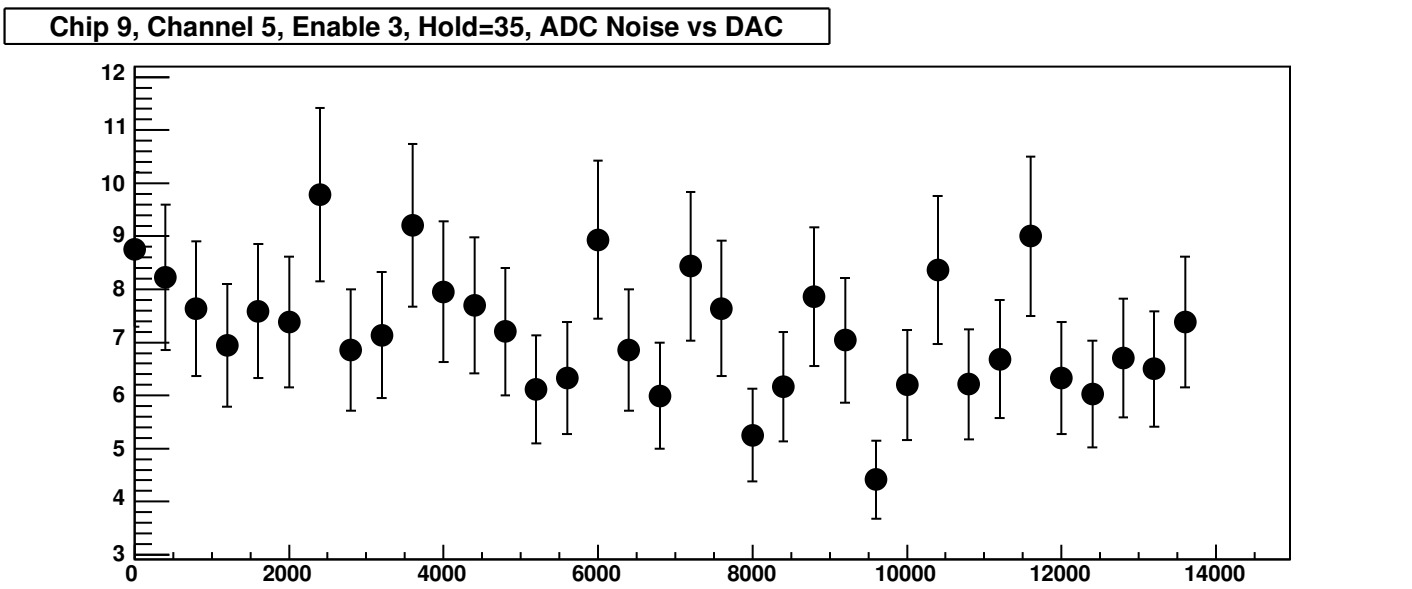
Chip 9, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC

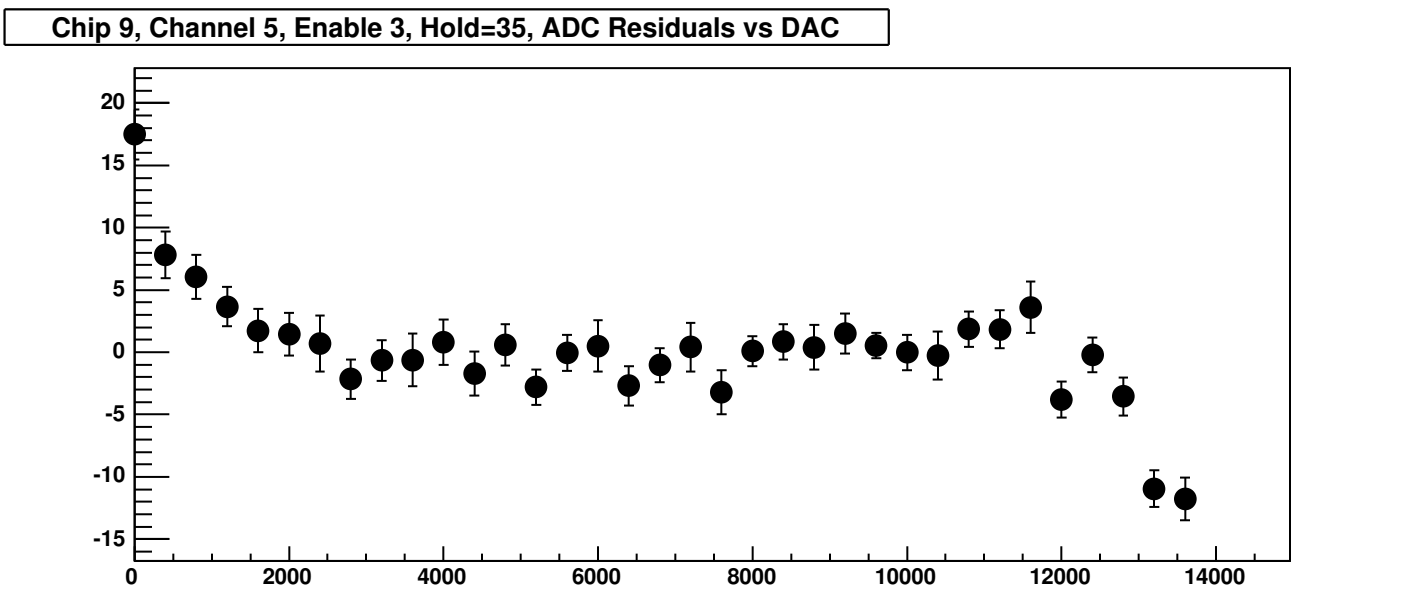


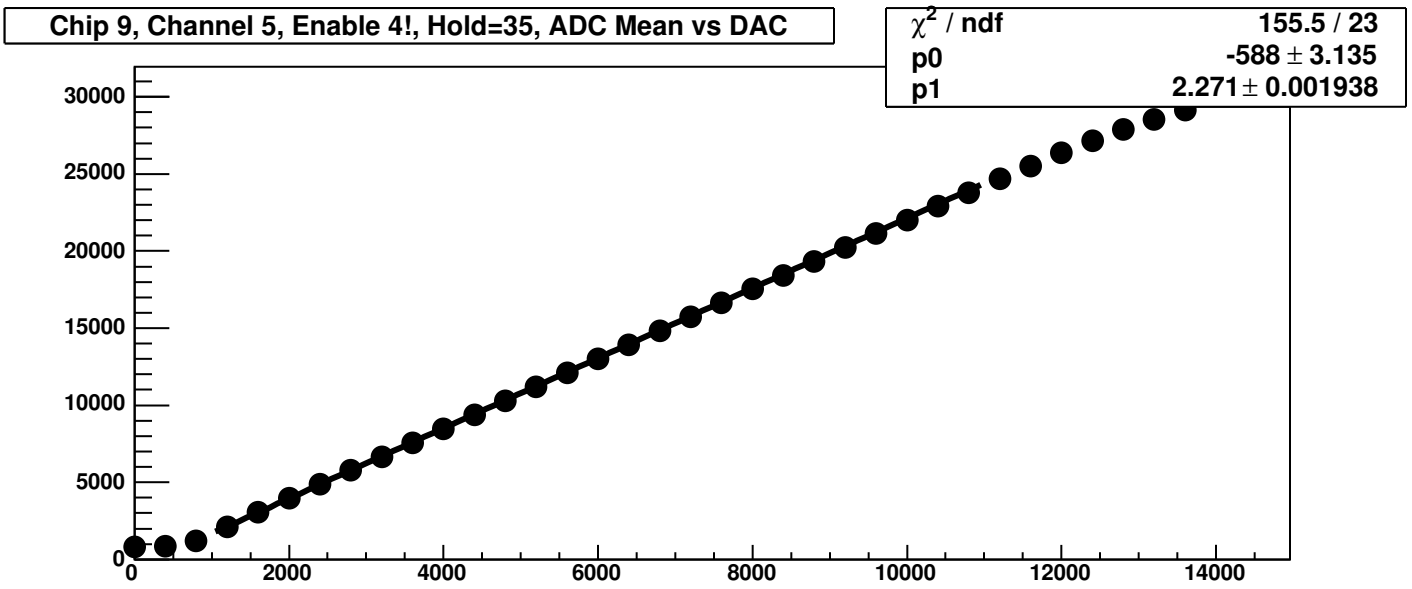
Chip 9, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

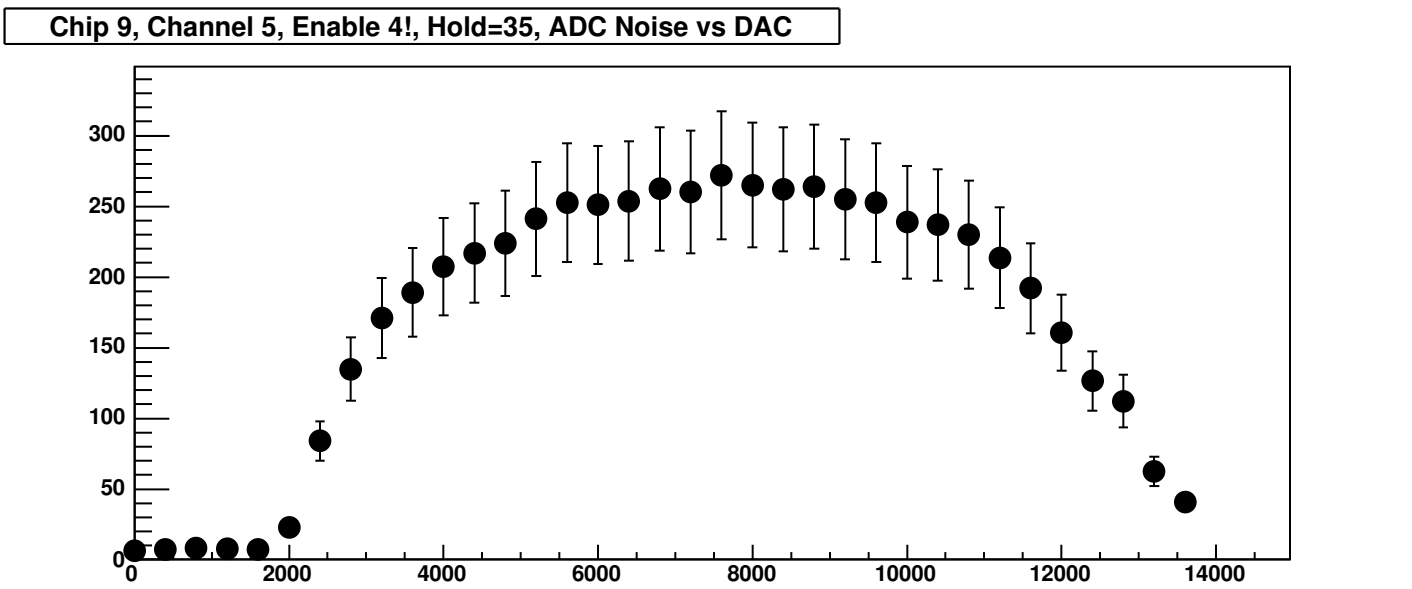


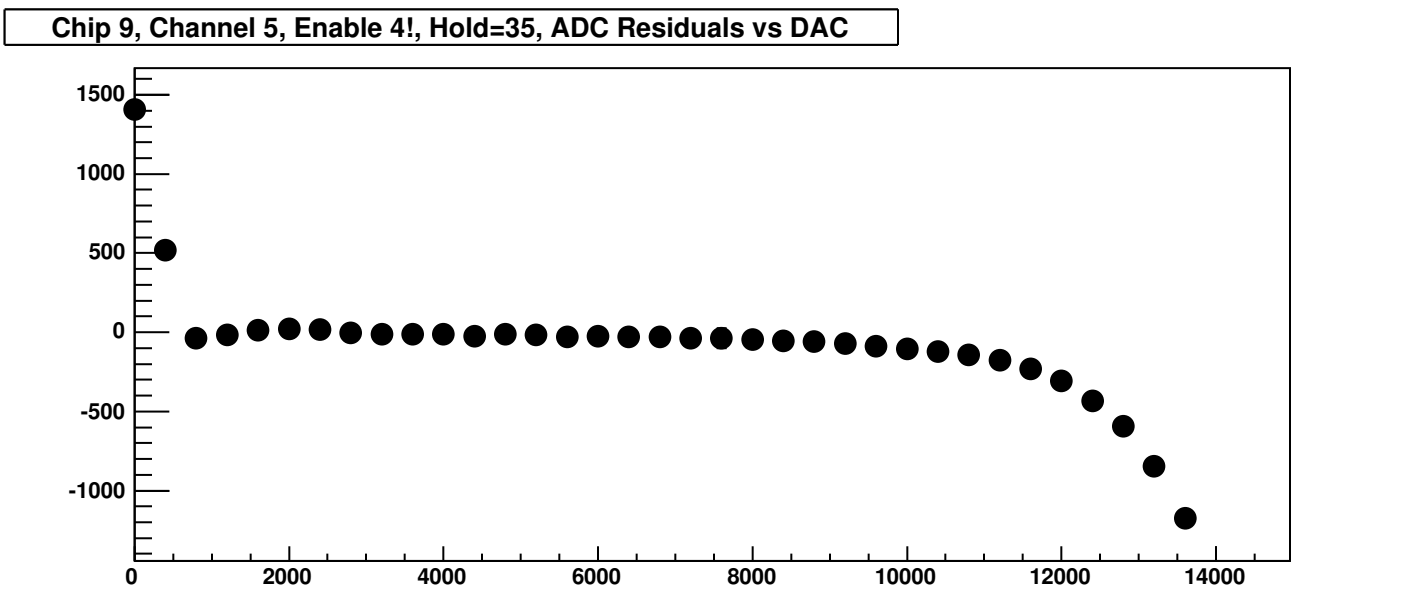

Chip 9, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.68 / 23
p0 -674.2 ± 0.7736
p1 0.01731 ± 0.0001094
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

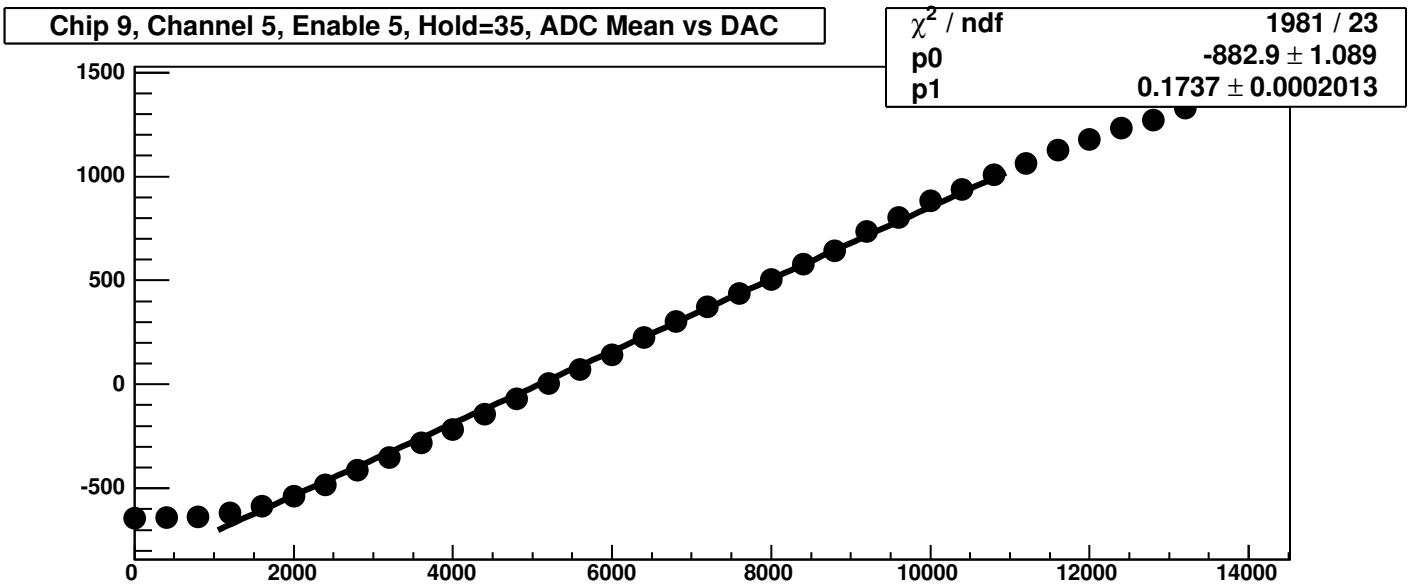

Chip 9, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

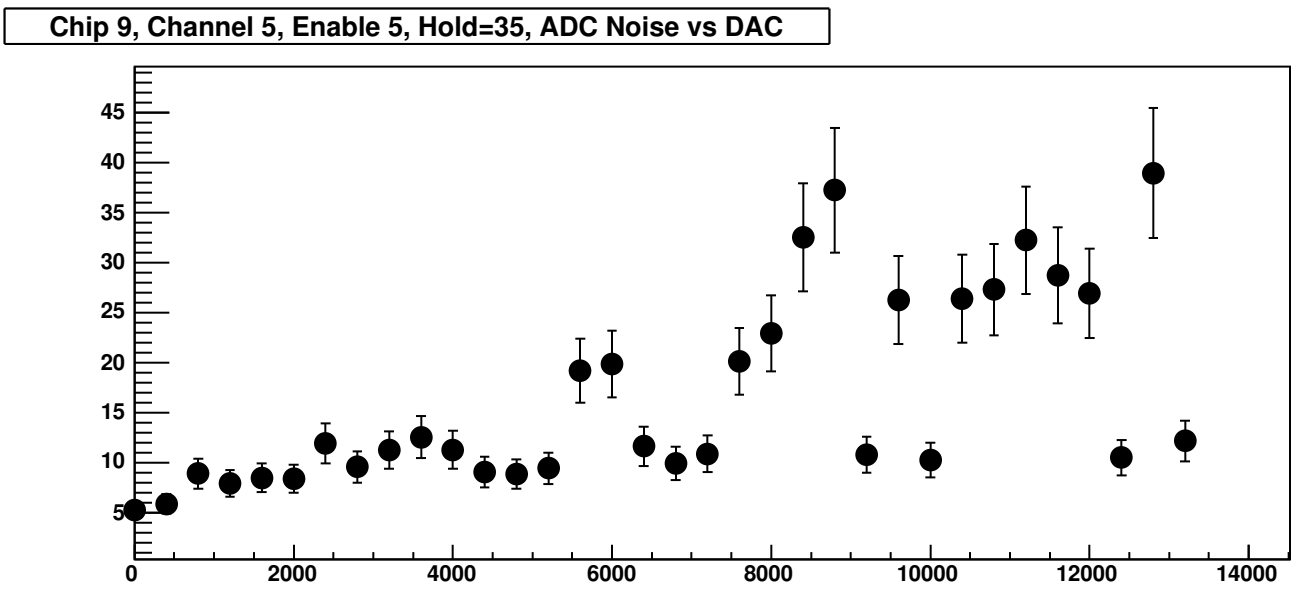

Chip 9, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

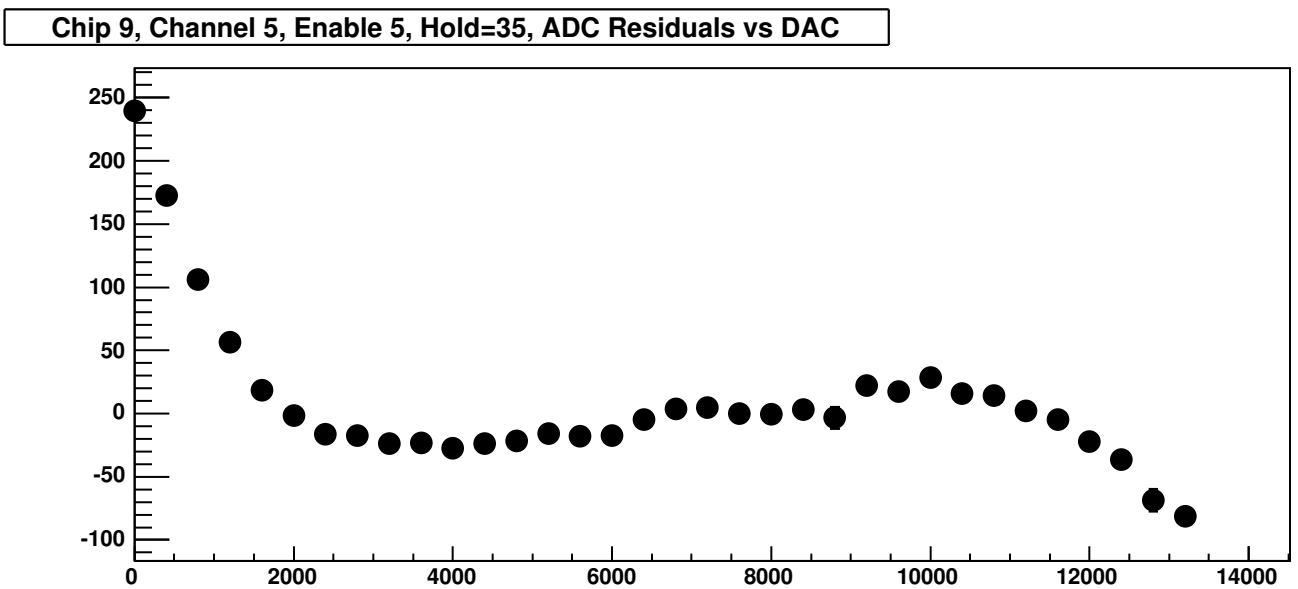

Chip 9, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 155.5 / 23
p0 -588 ± 3.135
p1 2.271 ± 0.001938
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

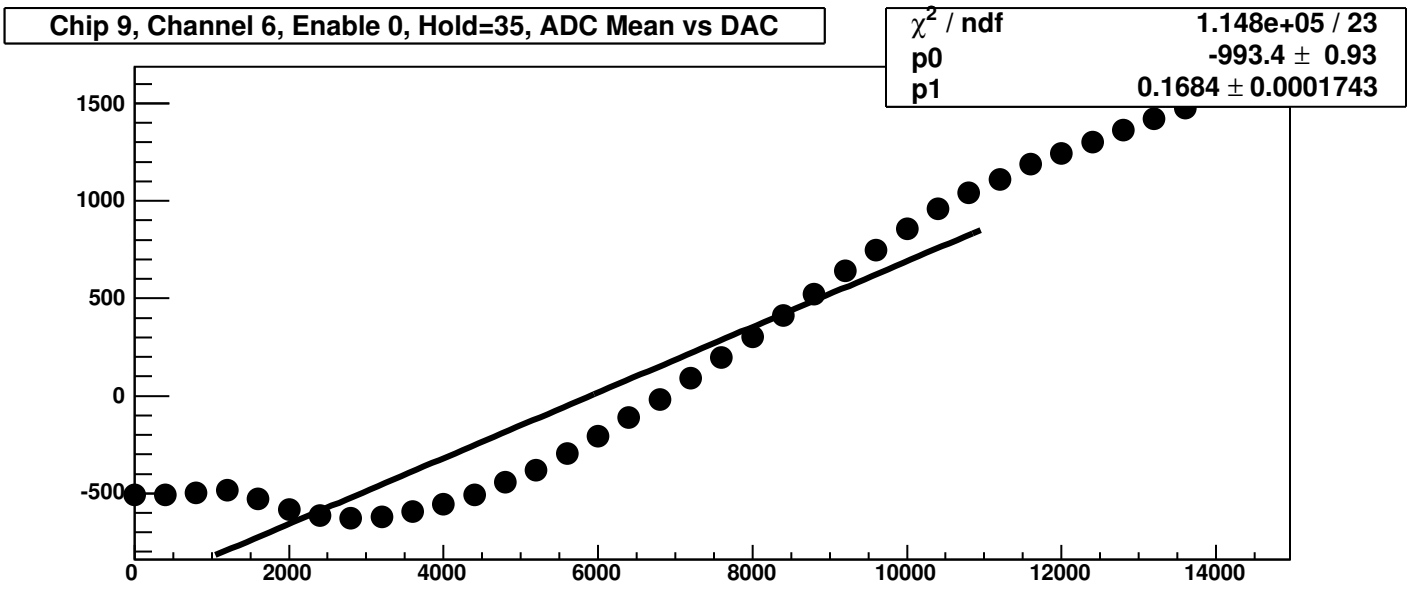

Chip 9, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

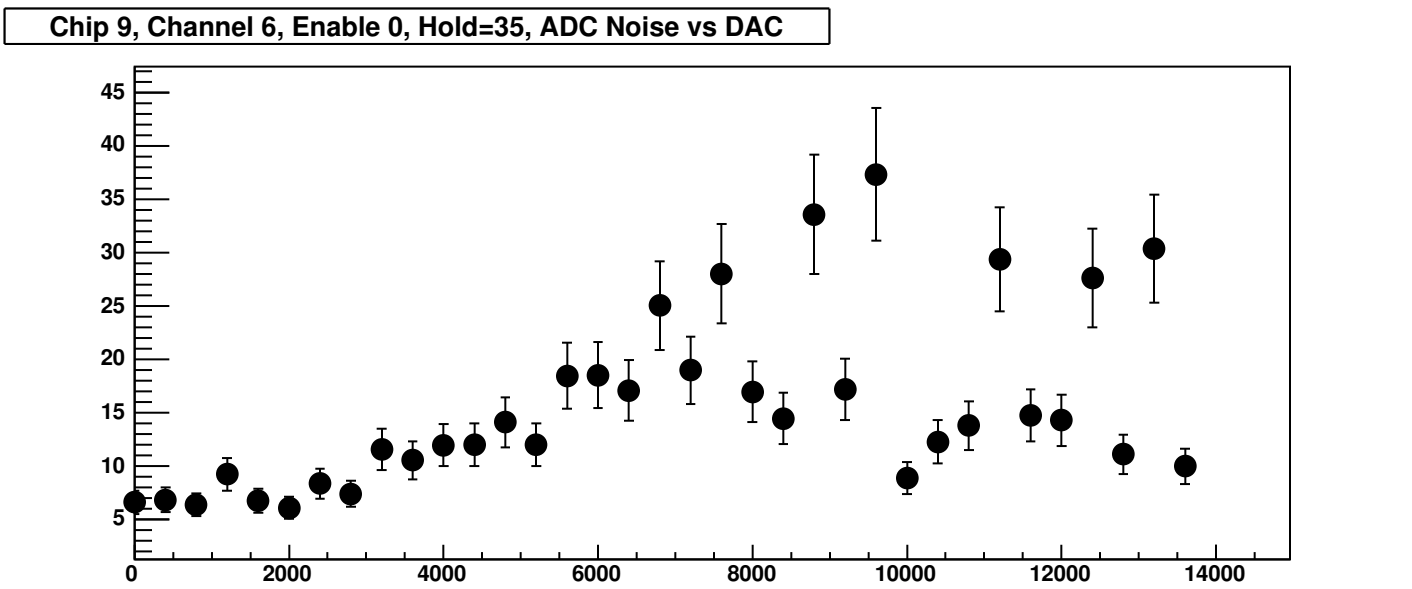

Chip 9, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

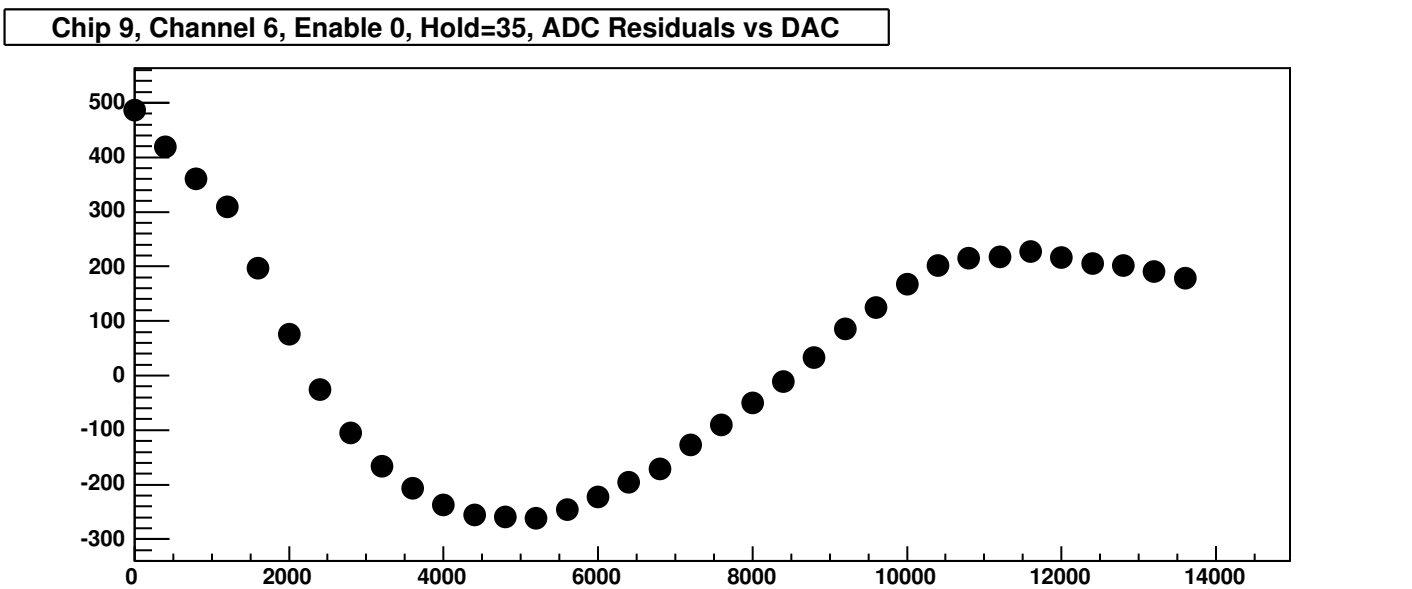

Chip 9, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1981 / 23
p0 -882.9 ± 1.089
p1 0.1737 ± 0.0002013
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

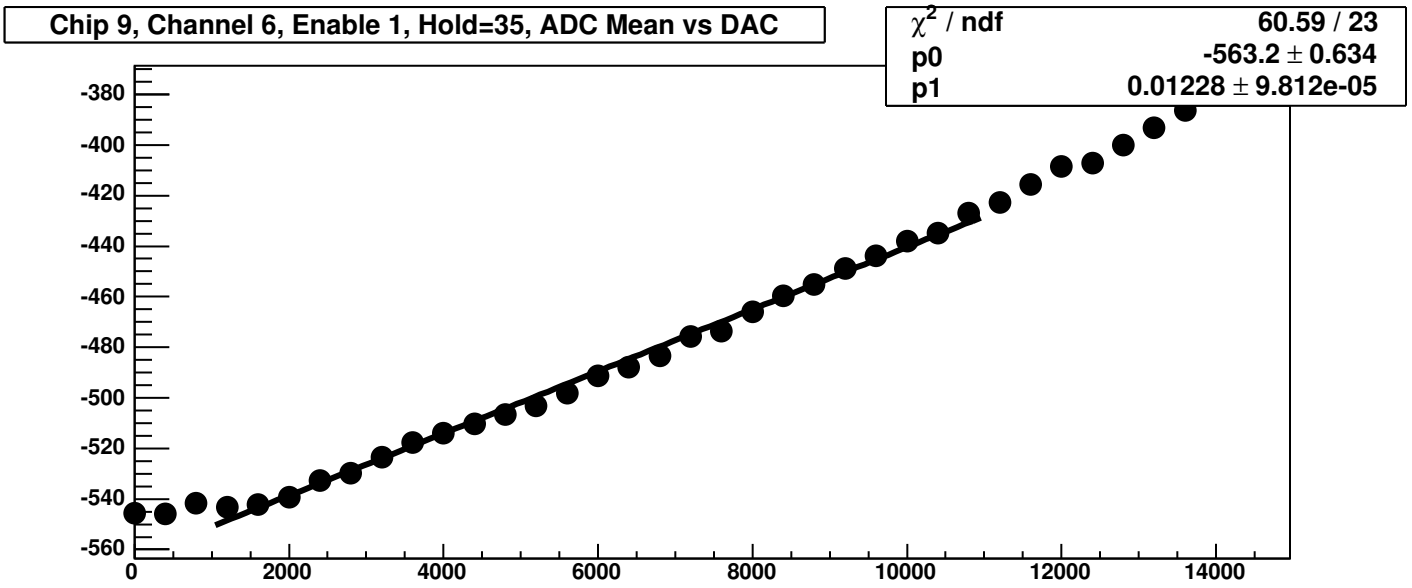

Chip 9, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

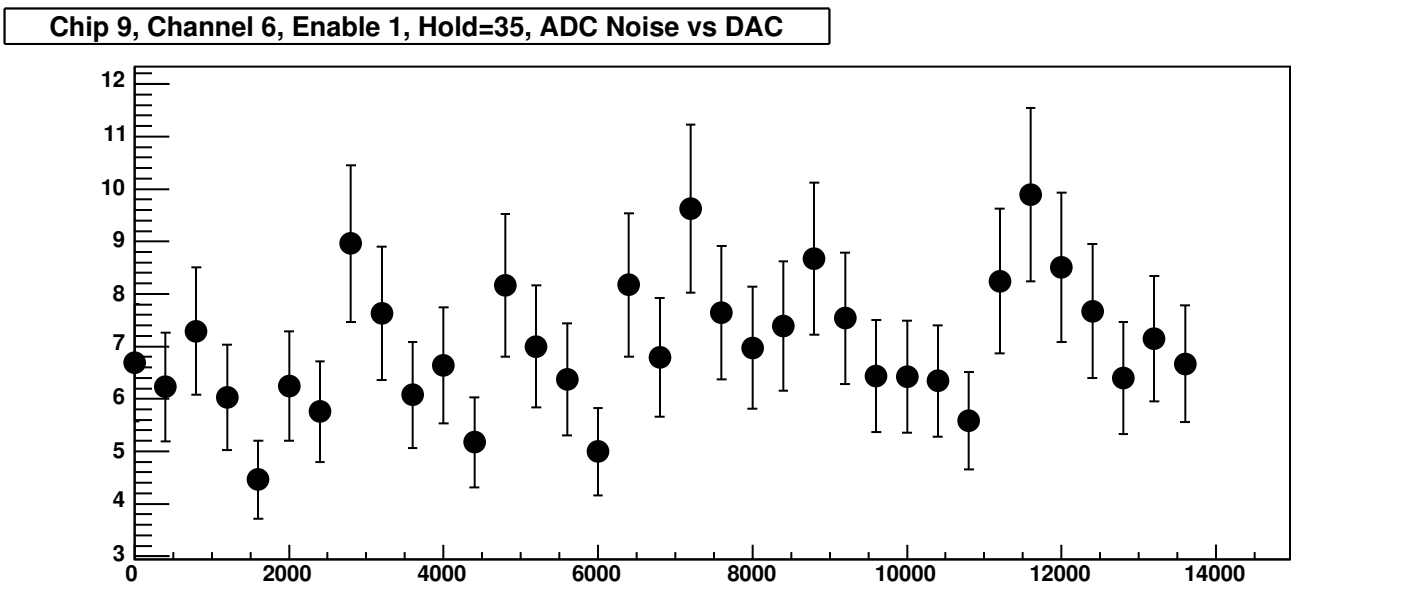

Chip 9, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

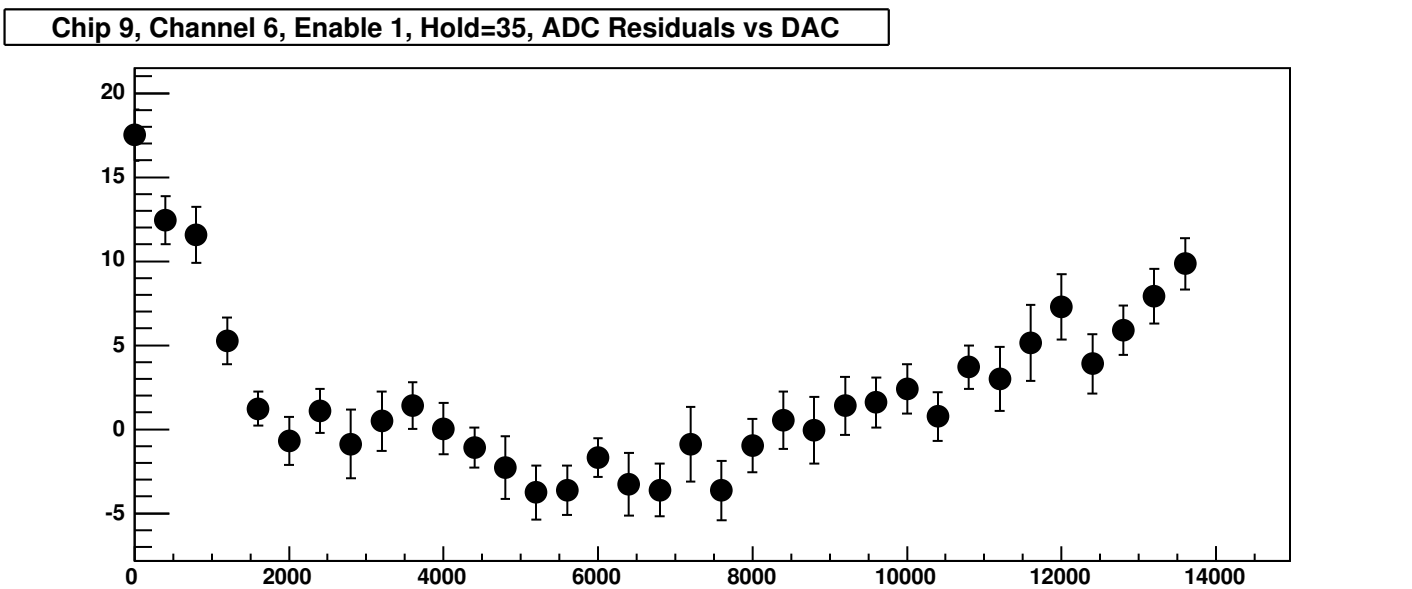

Chip 9, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.148e+05 / 23
p0 -993.4 ± 0.93
p1 0.1684 ± 0.0001743
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

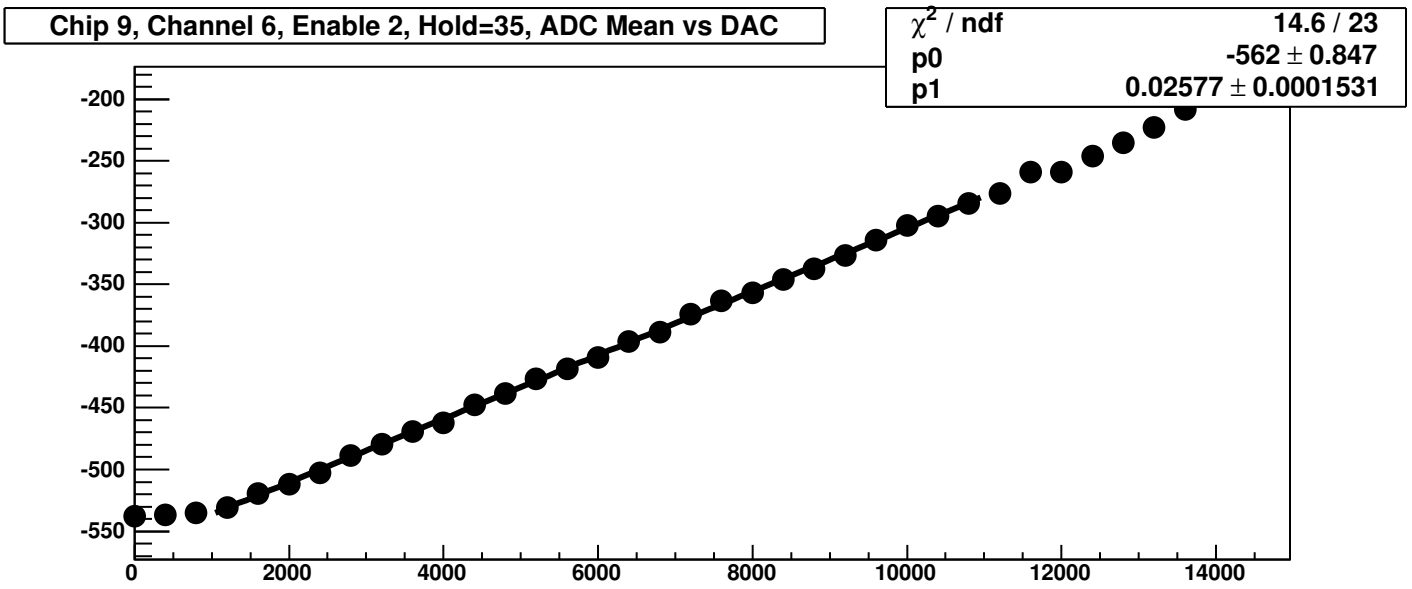

Chip 9, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

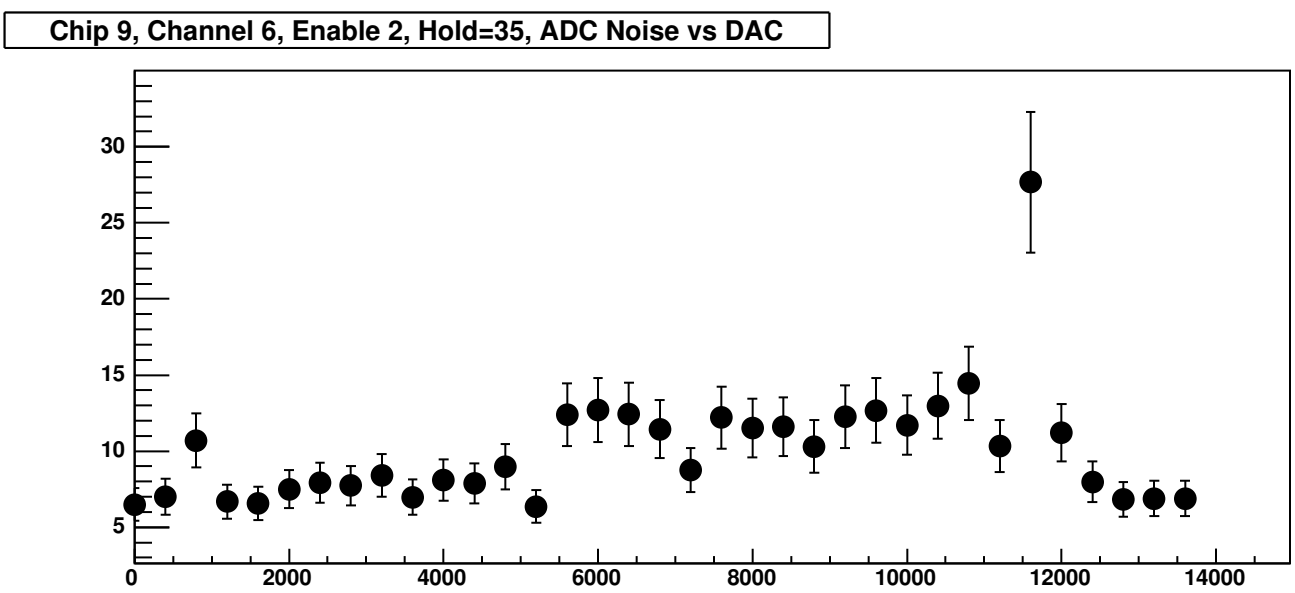

Chip 9, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

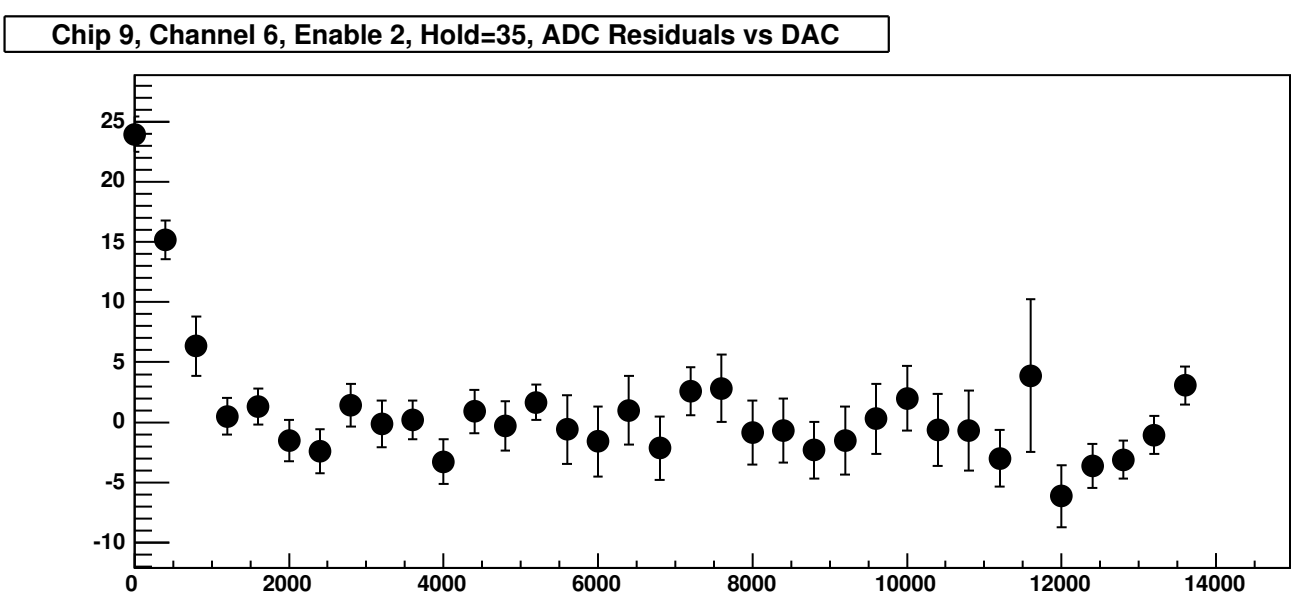

Chip 9, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 60.59 / 23
p0 -563.2 ± 0.634
p1 0.01228 ± 9.812e-05
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

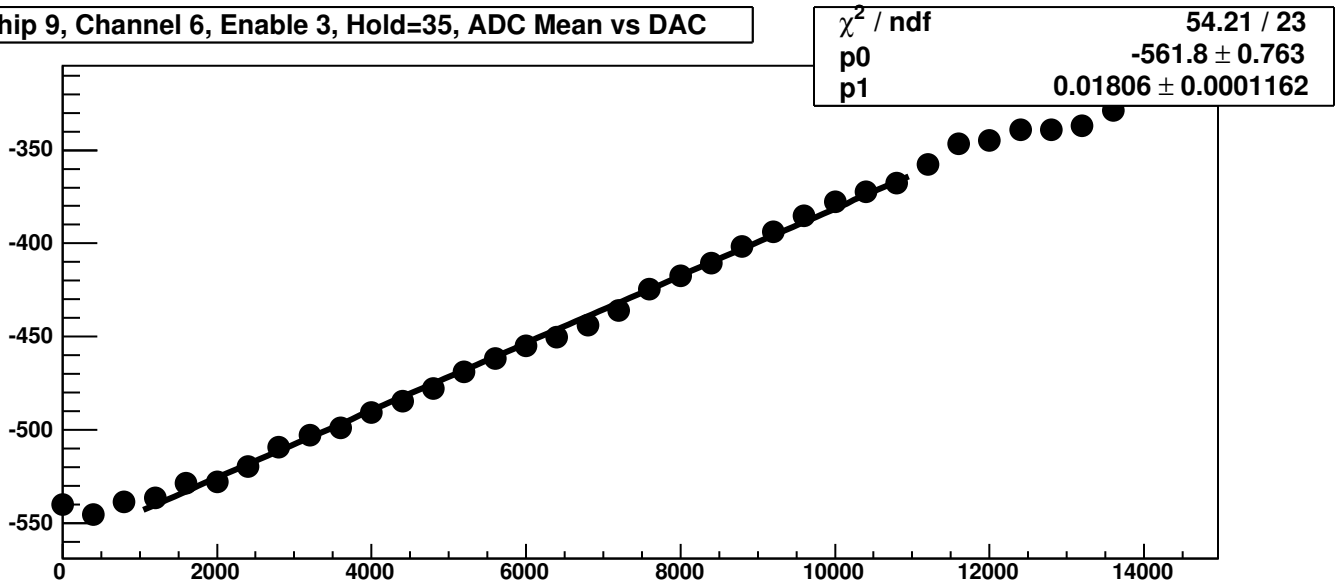

Chip 9, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 14.6 / 23
p0 -562 ± 0.847
p1 0.02577 ± 0.0001531
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

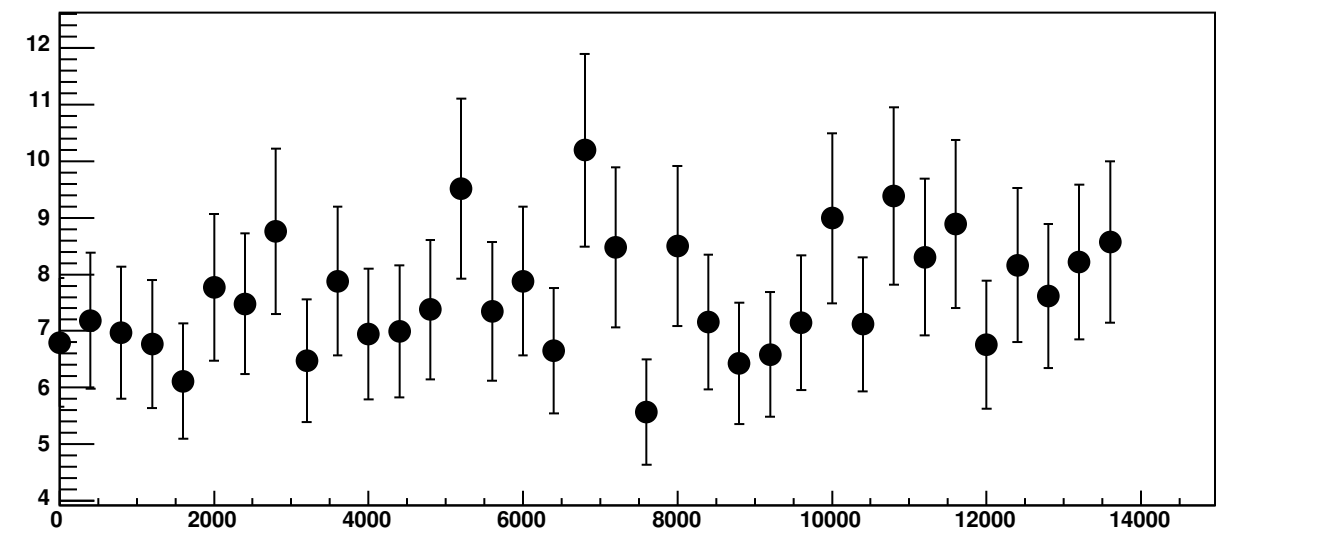

Chip 9, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

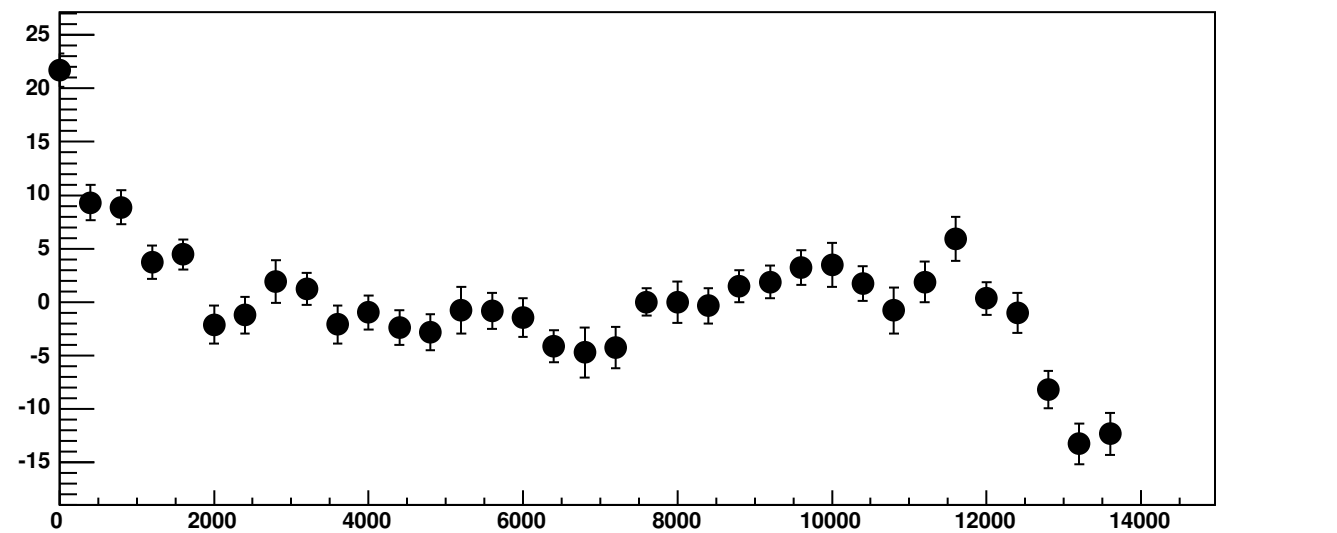
Chip 9, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC



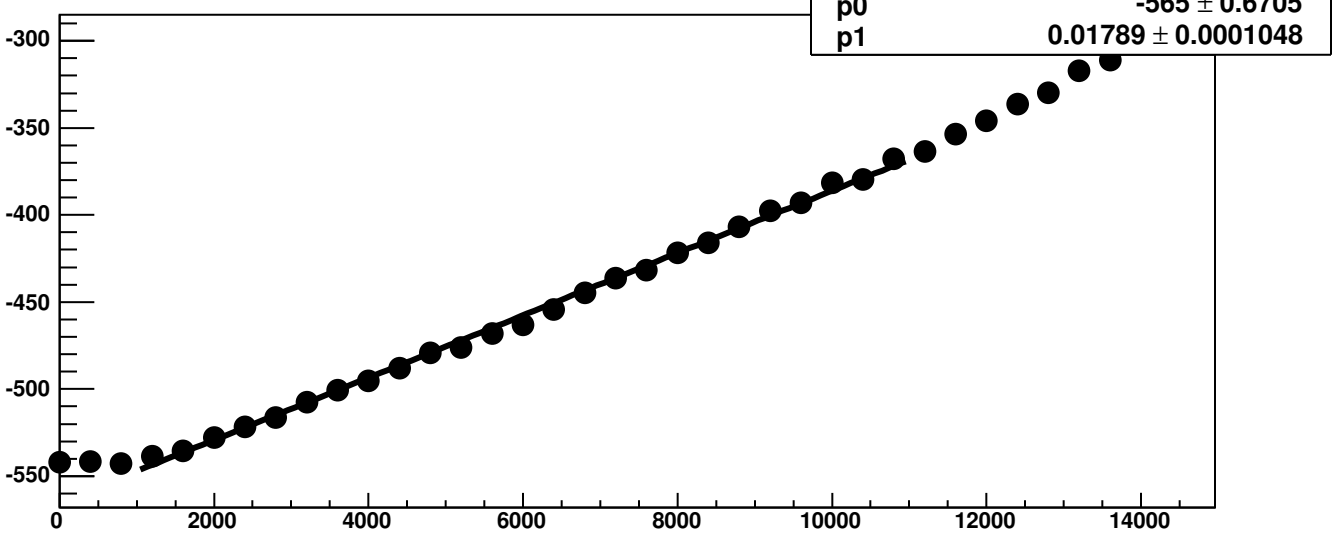
Chip 9, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC



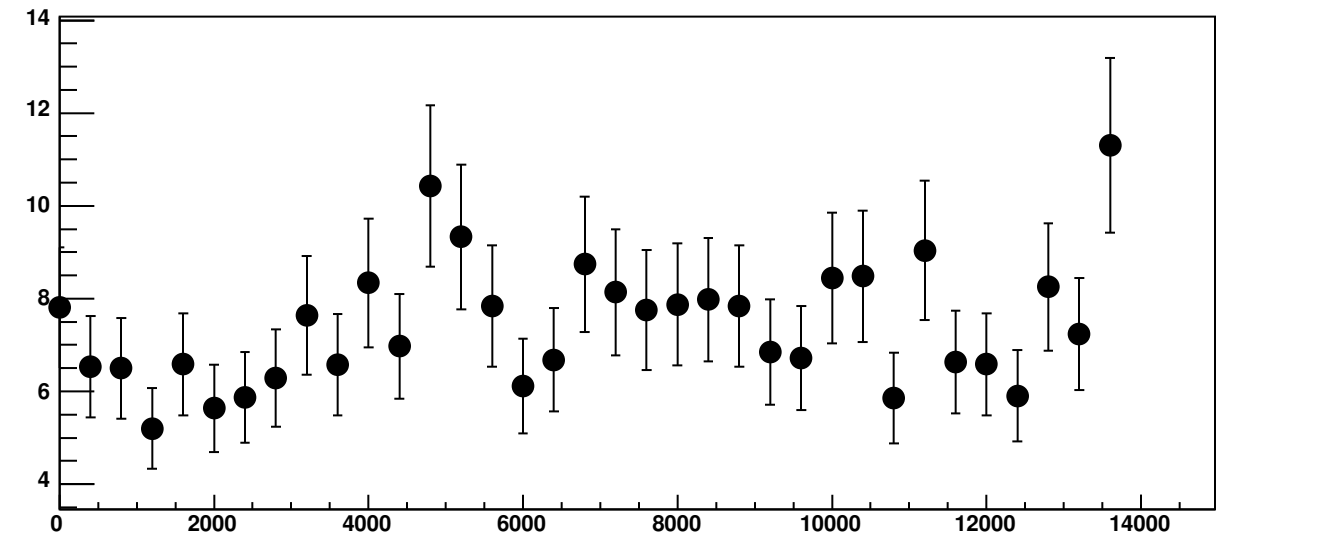
Chip 9, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC



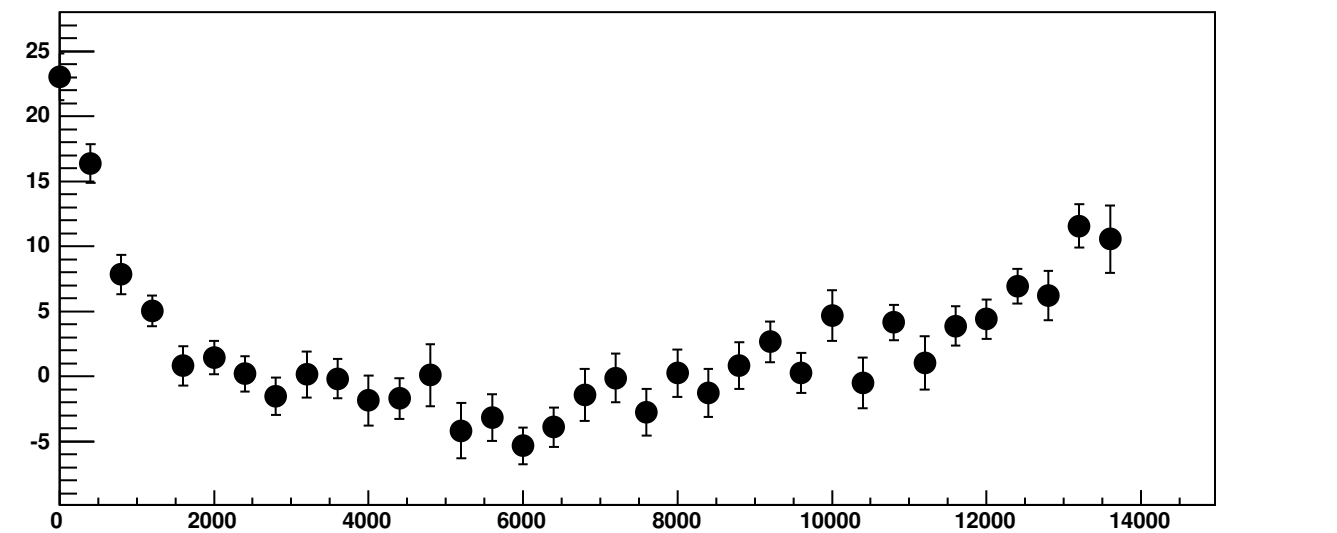
Chip 9, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

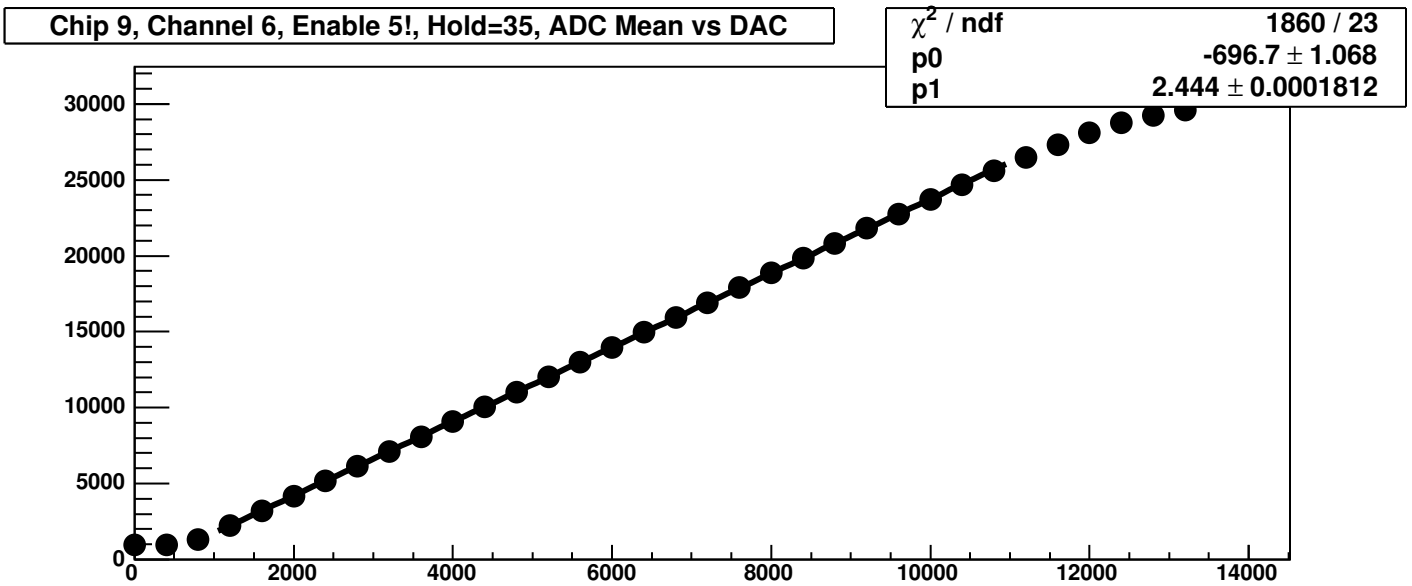


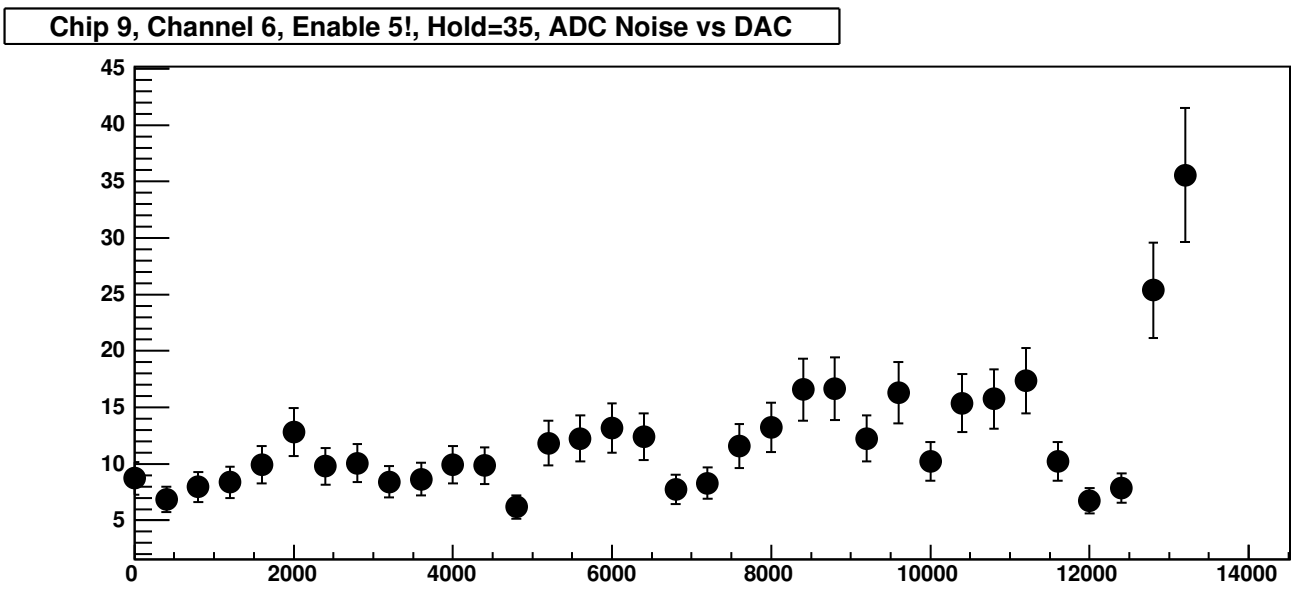
Chip 9, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

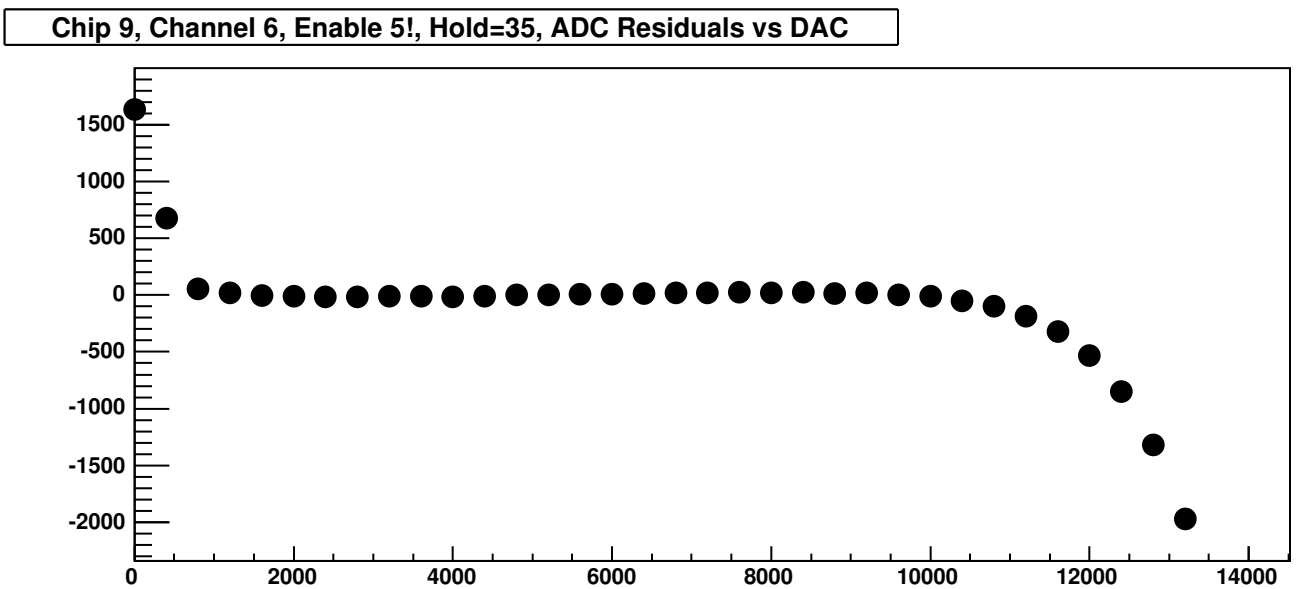


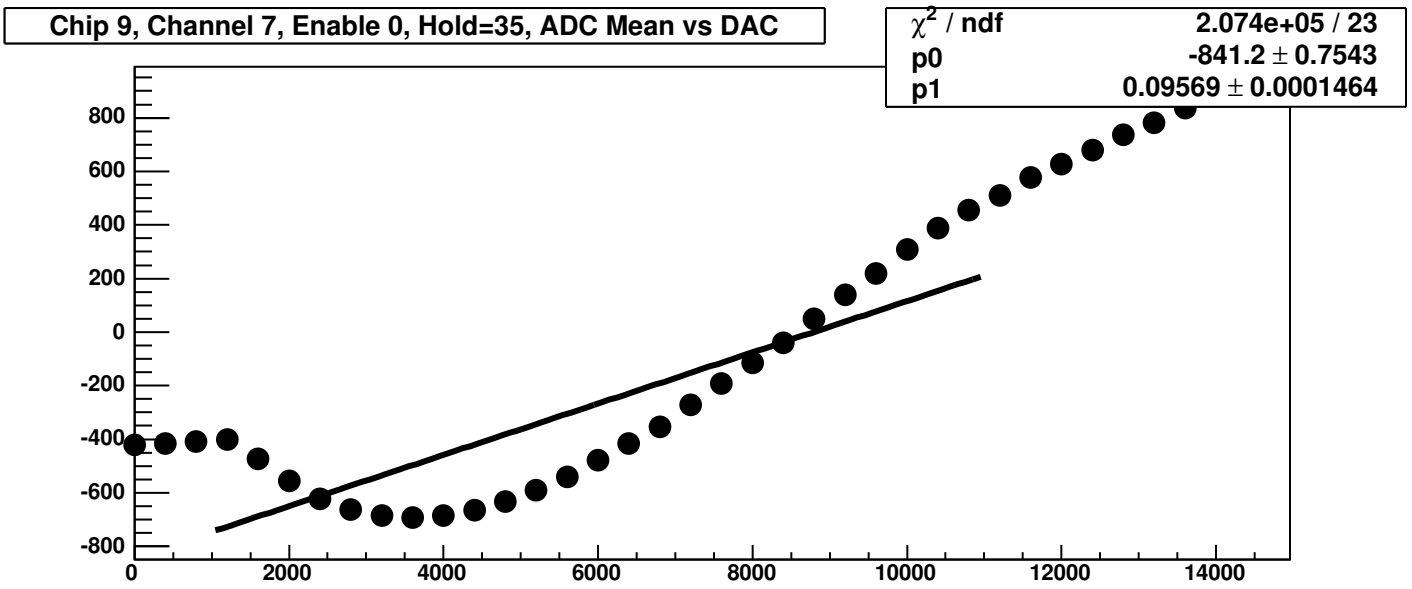
Chip 9, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

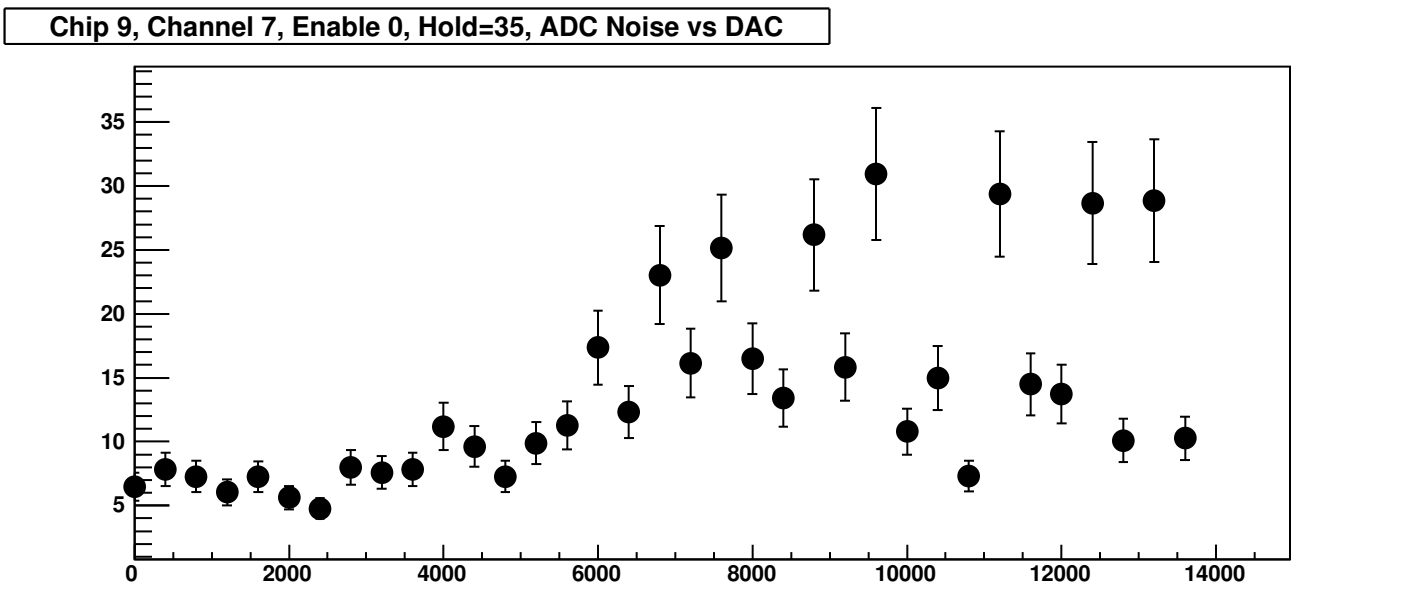


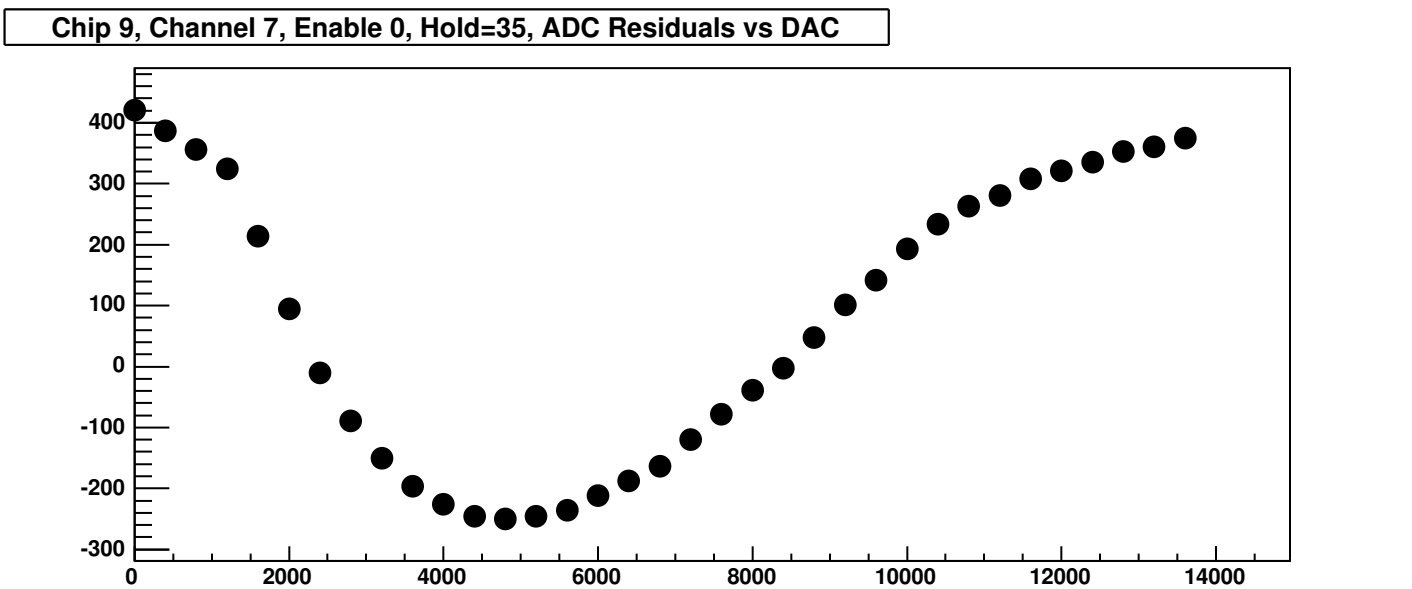

Chip 9, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1860 / 23
p0 -696.7 ± 1.068
p1 2.444 ± 0.0001812
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

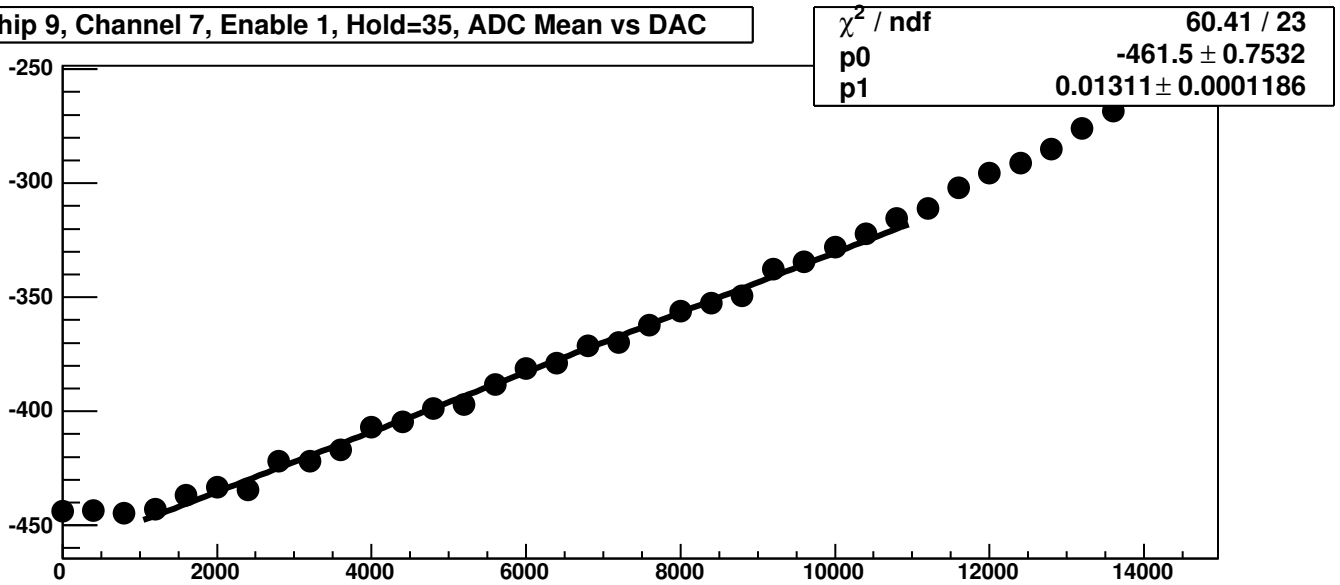

Chip 9, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.074e+05 / 23
p0 -841.2 ± 0.7543
p1 0.09569 ± 0.0001464
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

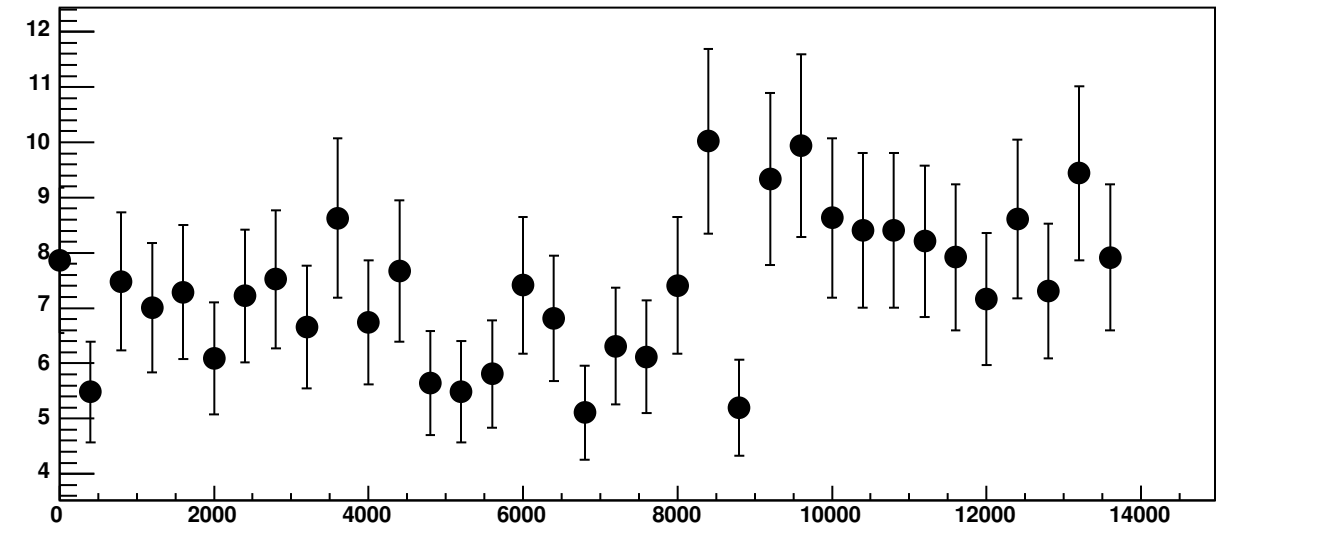

Chip 9, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

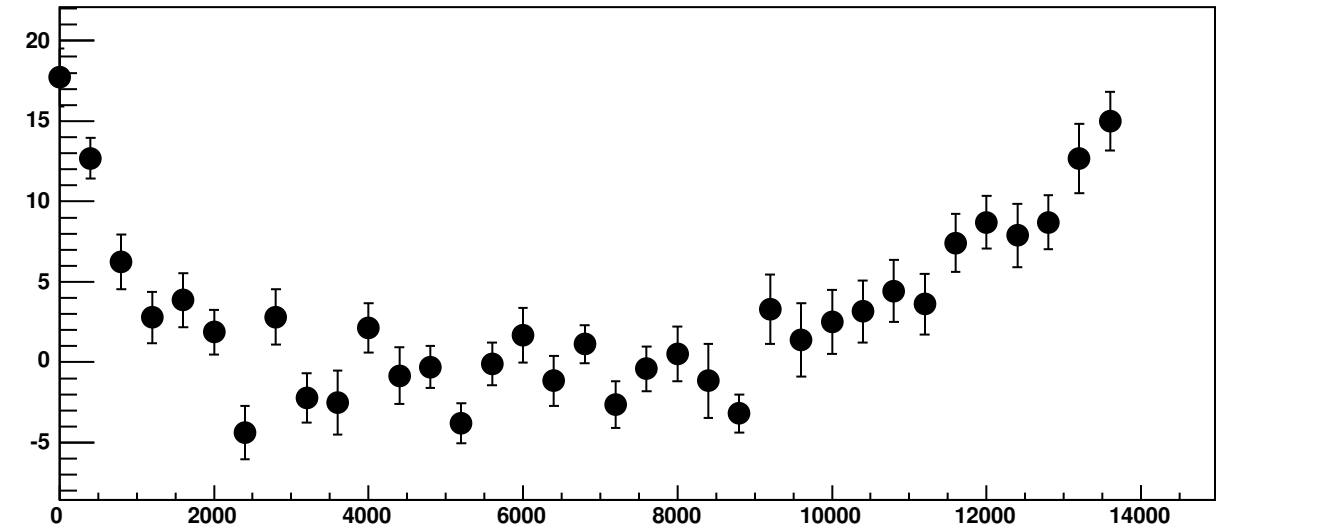
Chip 9, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

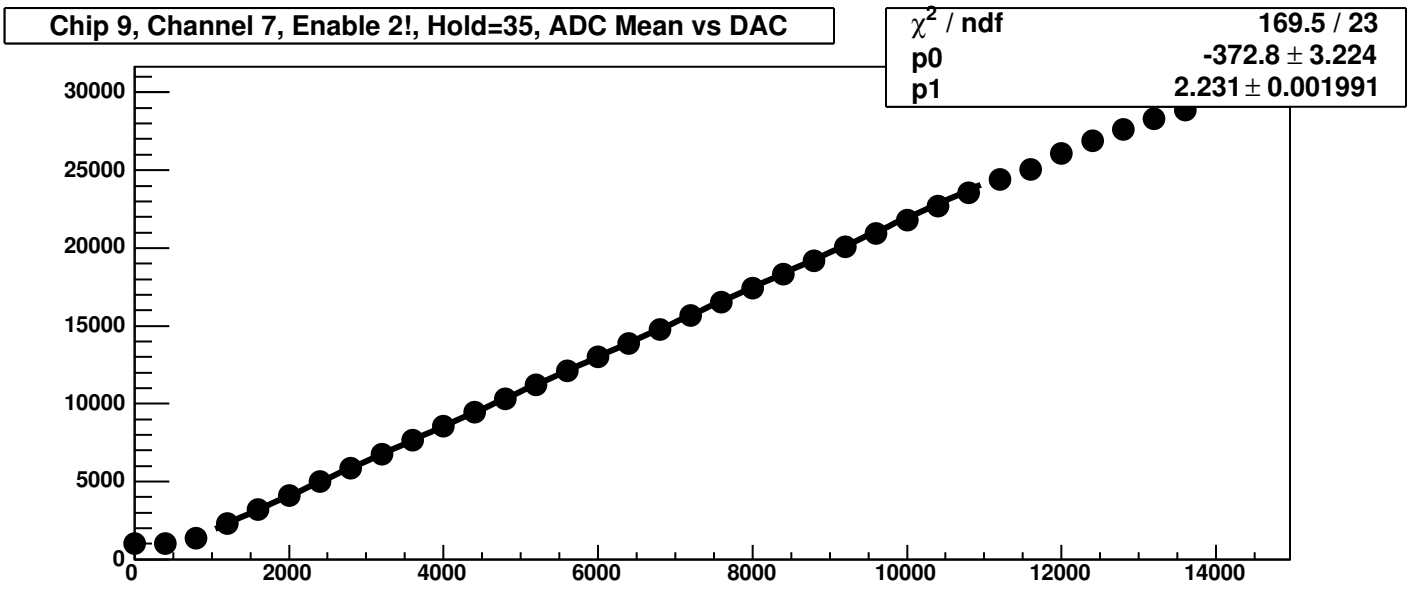


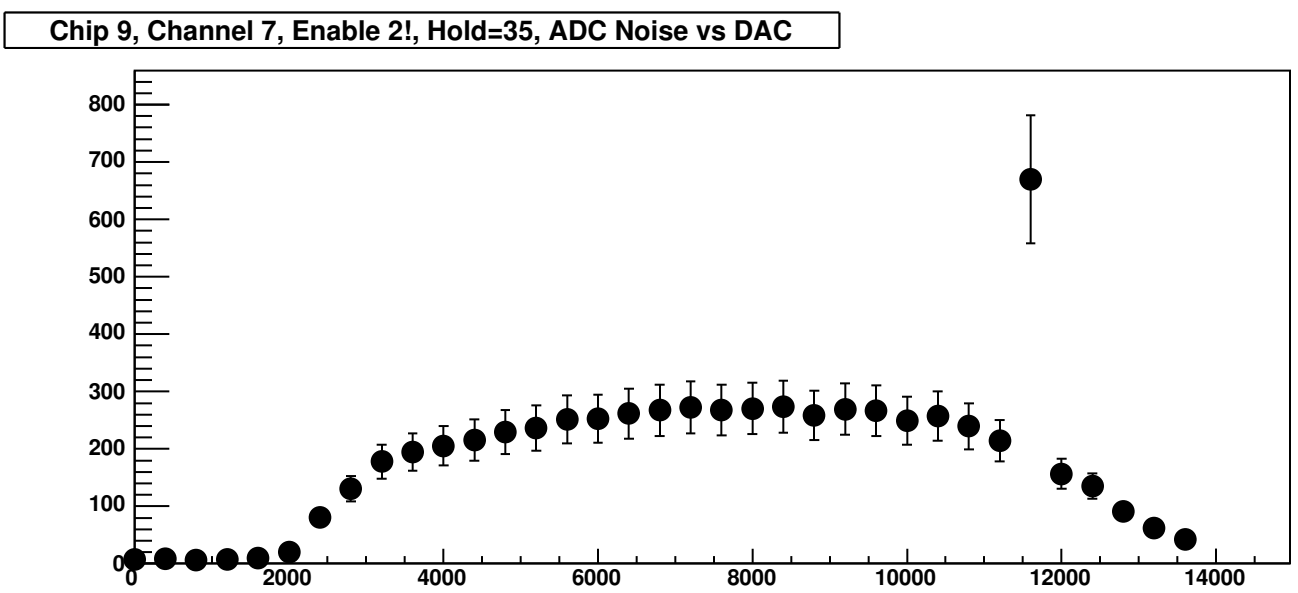
Chip 9, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC

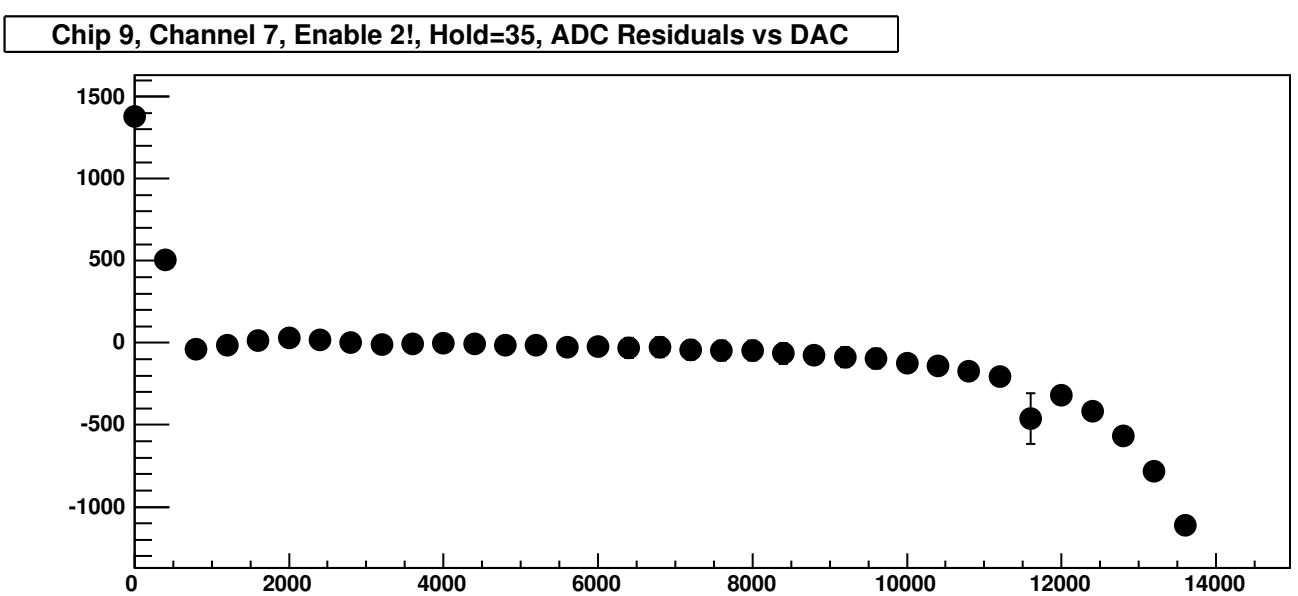


Chip 9, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

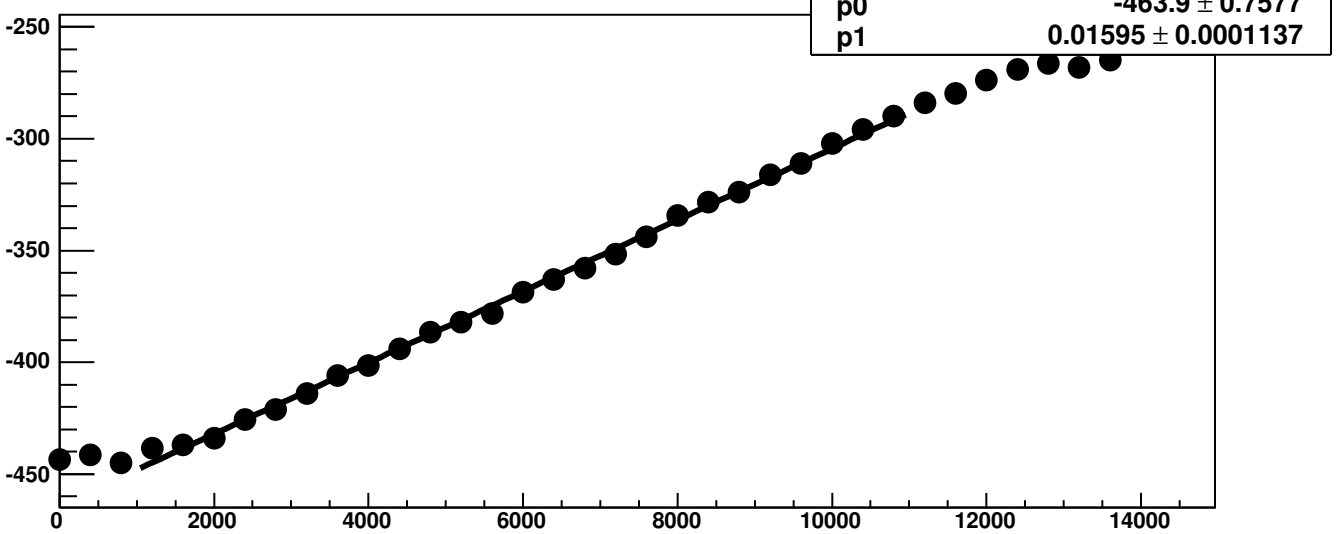



Chip 9, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 169.5 / 23
p0 -372.8 ± 3.224
p1 2.231 ± 0.001991
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

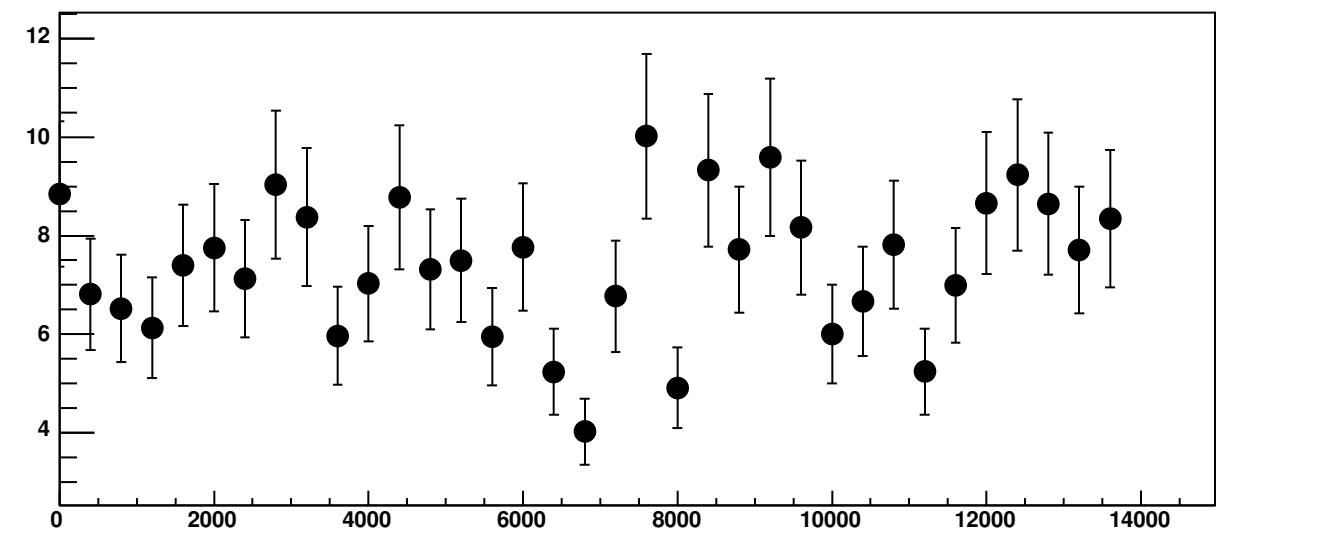

Chip 9, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
800
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

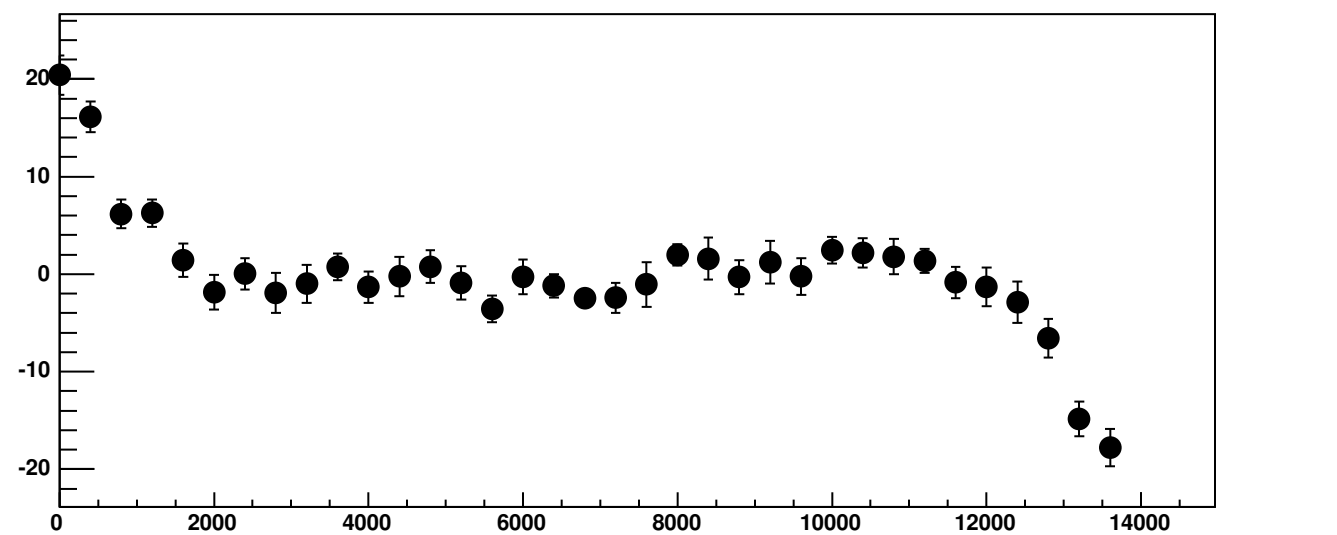
Chip 9, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC

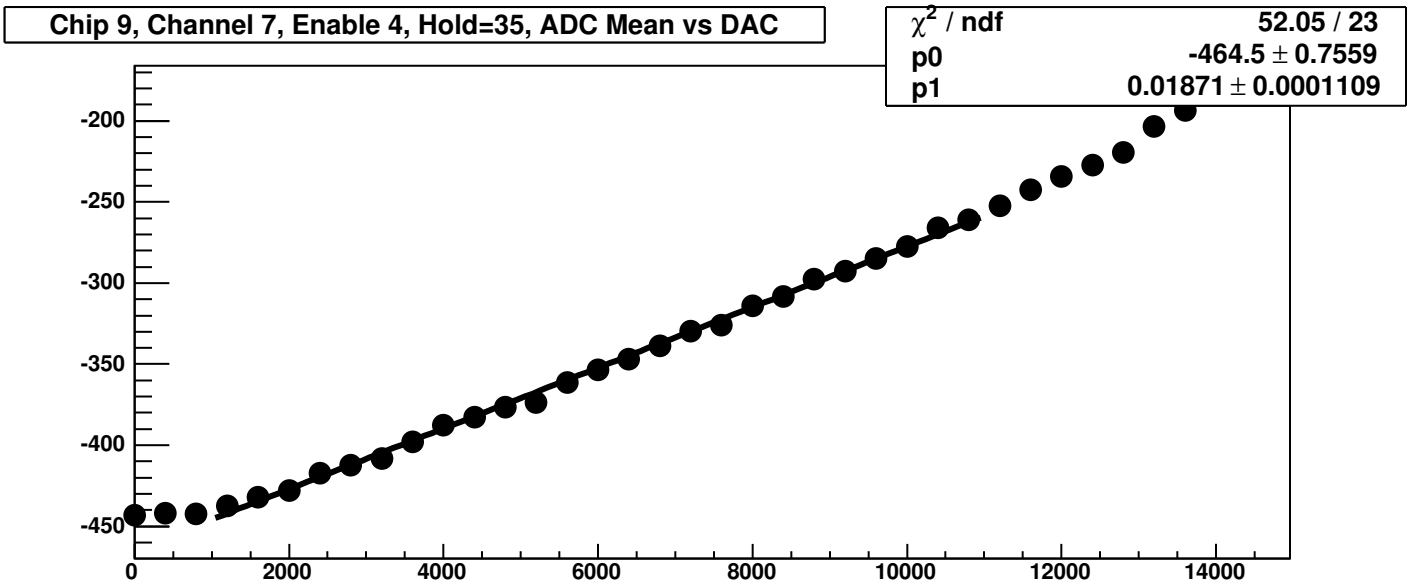


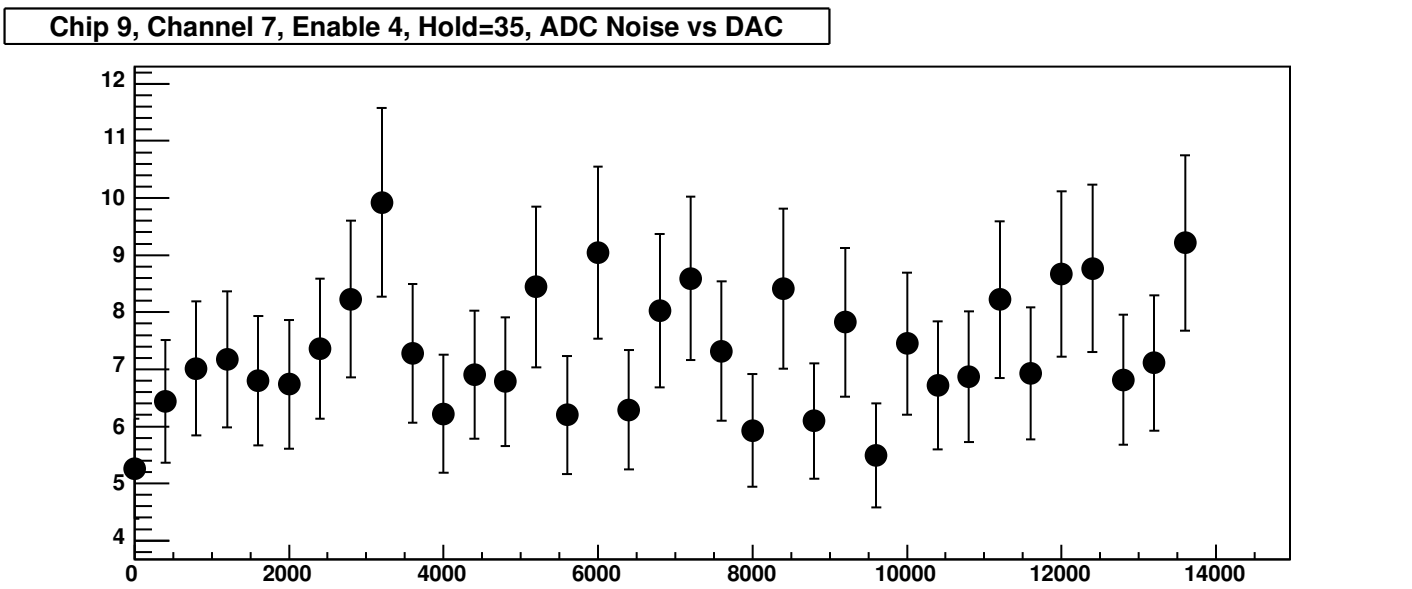
Chip 9, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC

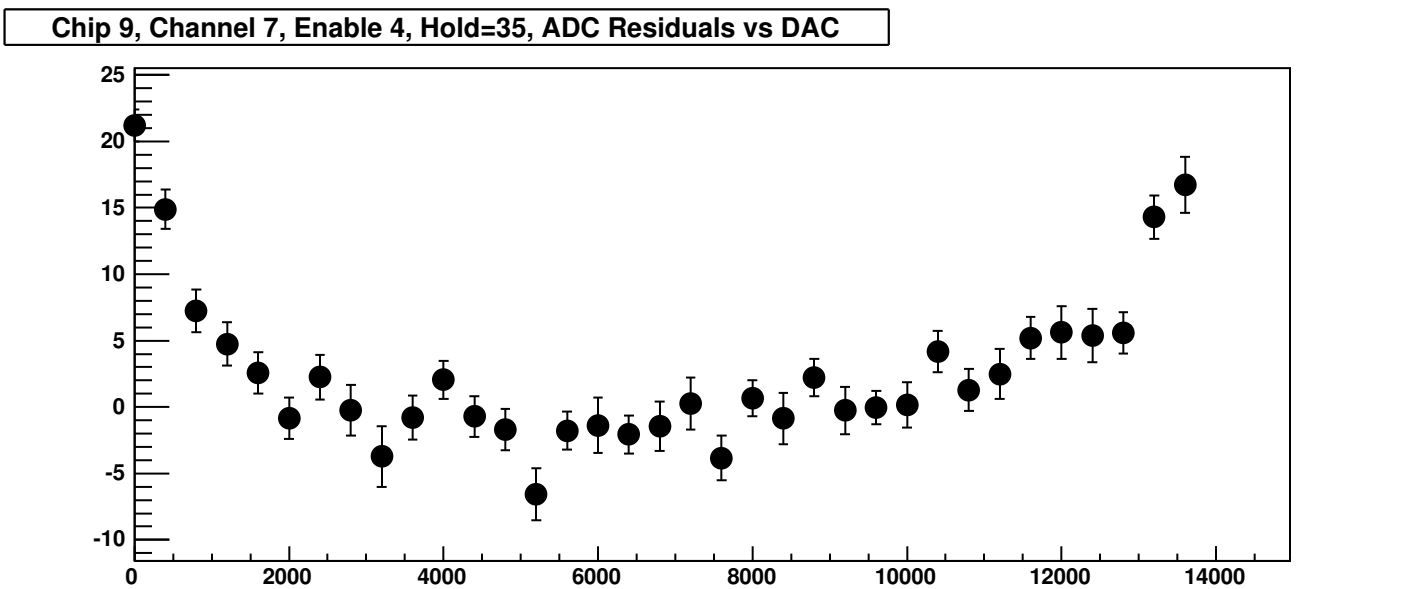


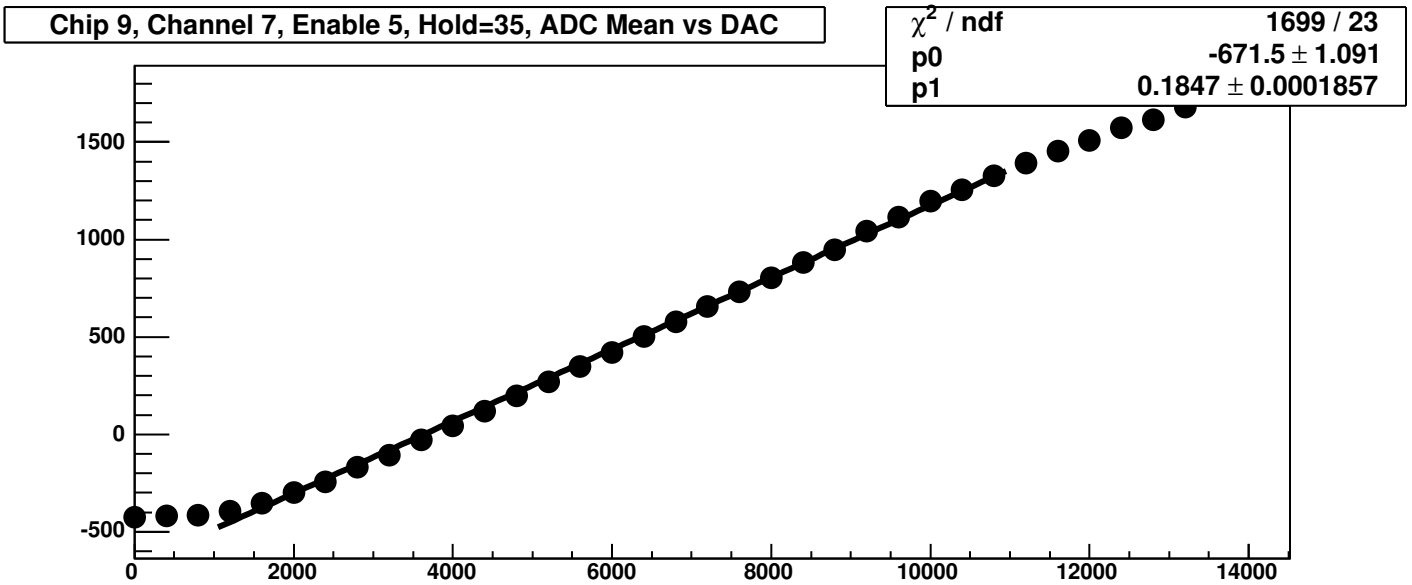
Chip 9, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC

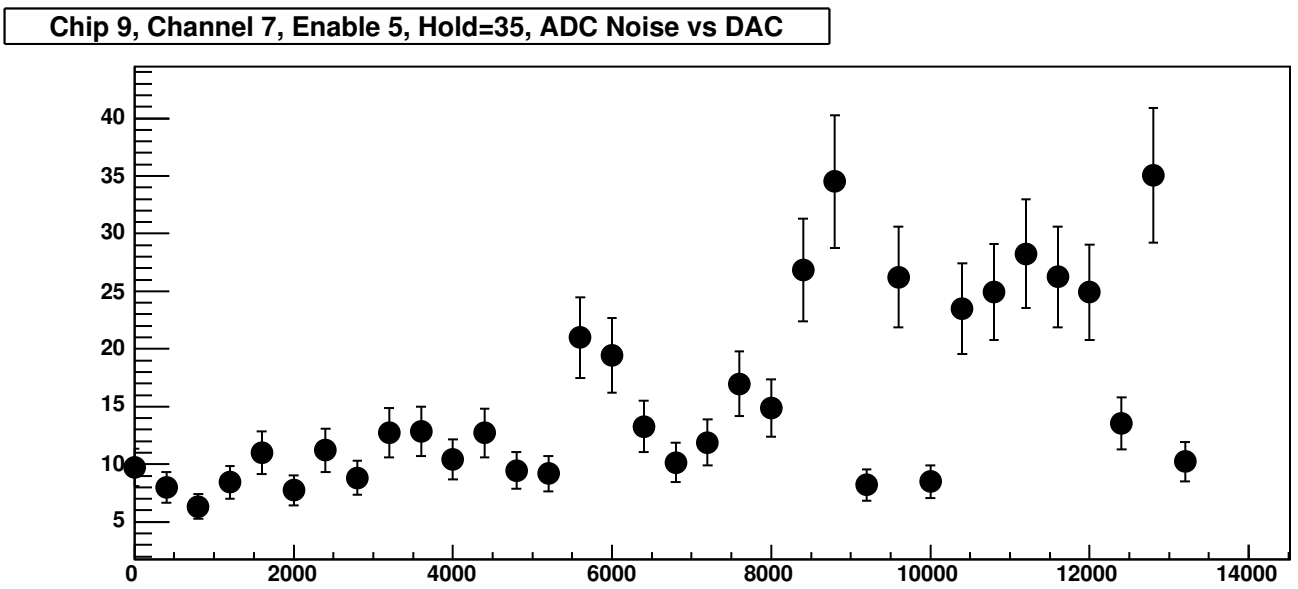


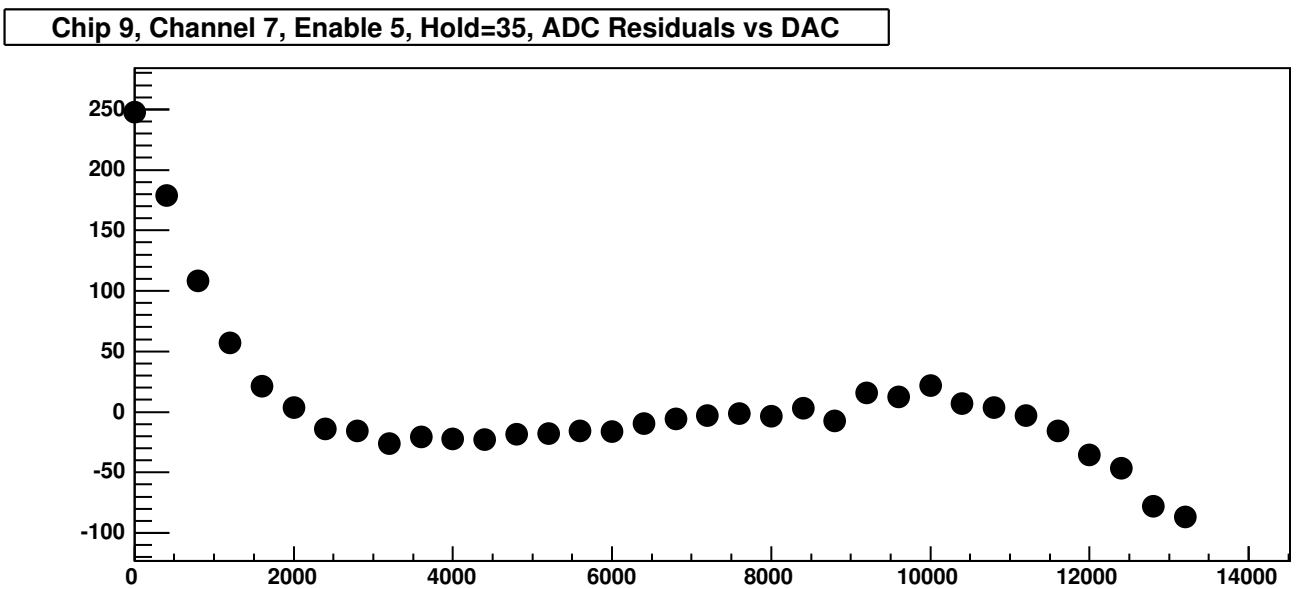

Chip 9, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 52.05 / 23
p0 -464.5 ± 0.7559
p1 0.01871 ± 0.0001109
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

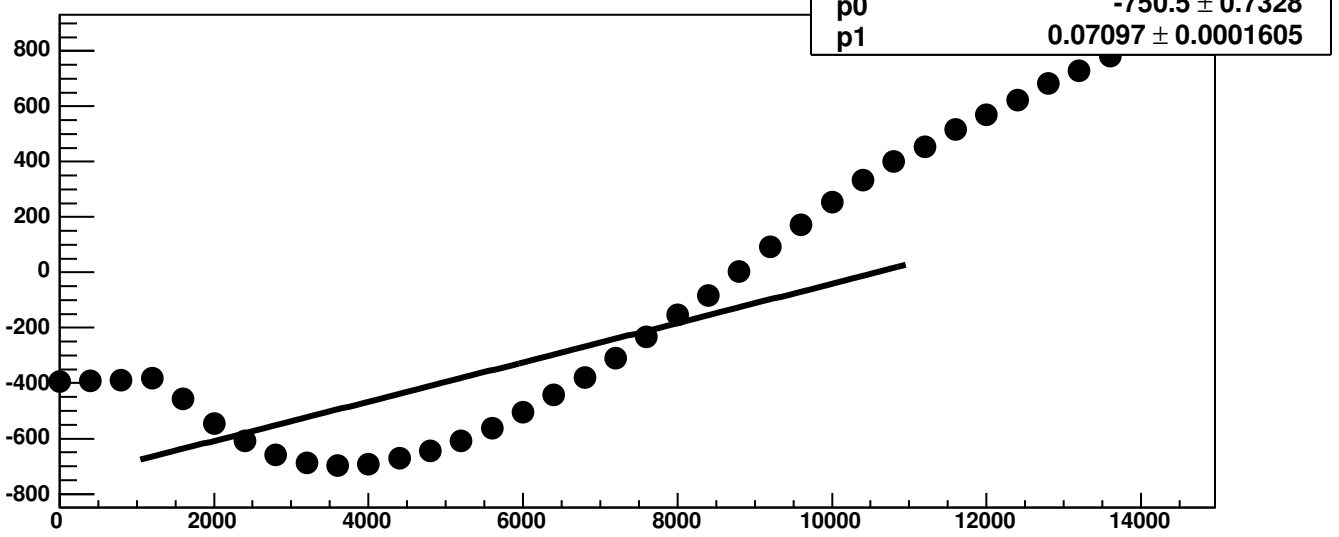

Chip 9, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1699 / 23
p0 -671.5 ± 1.091
p1 0.1847 ± 0.0001857
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

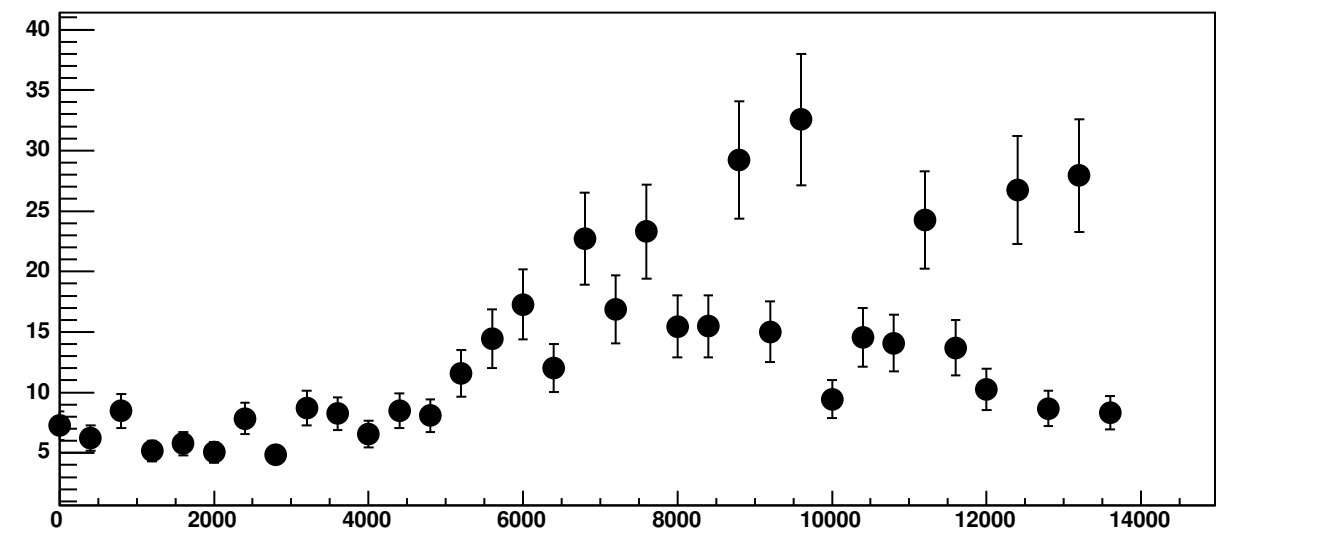

Chip 9, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

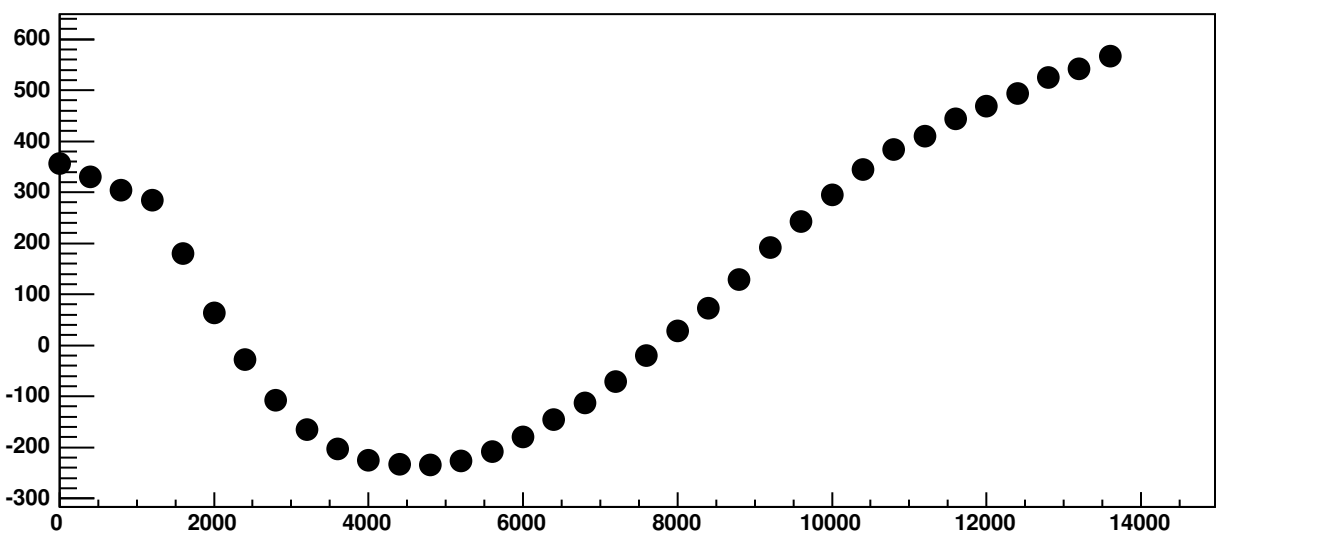
Chip 9, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC

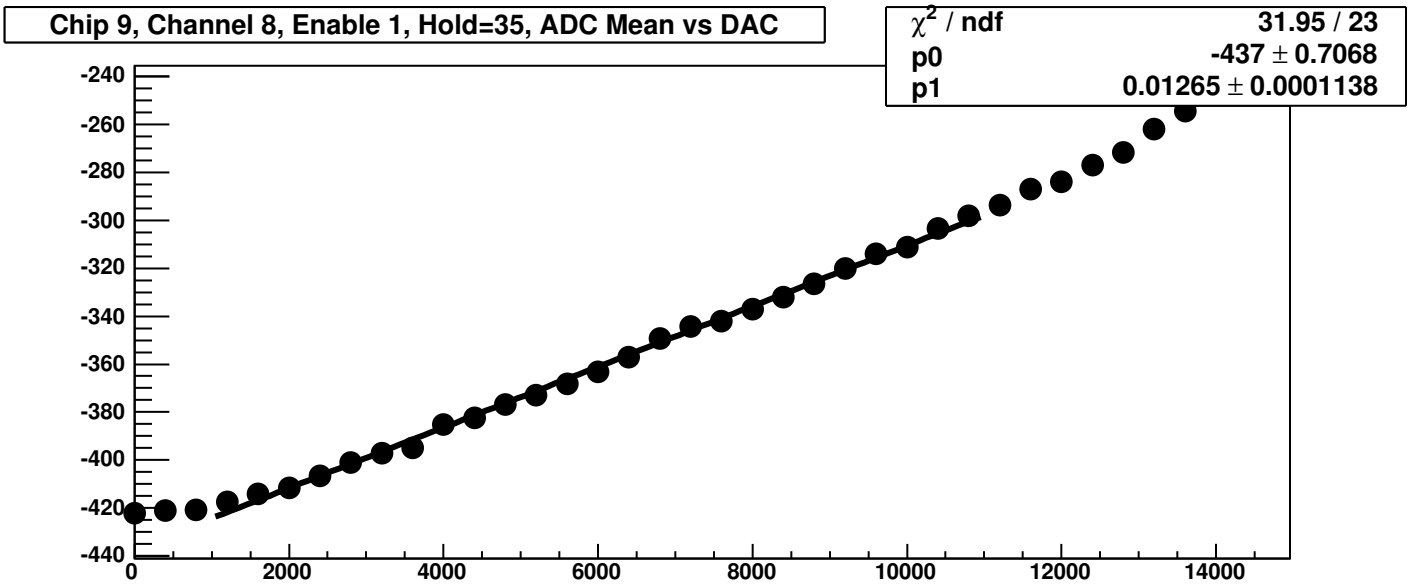


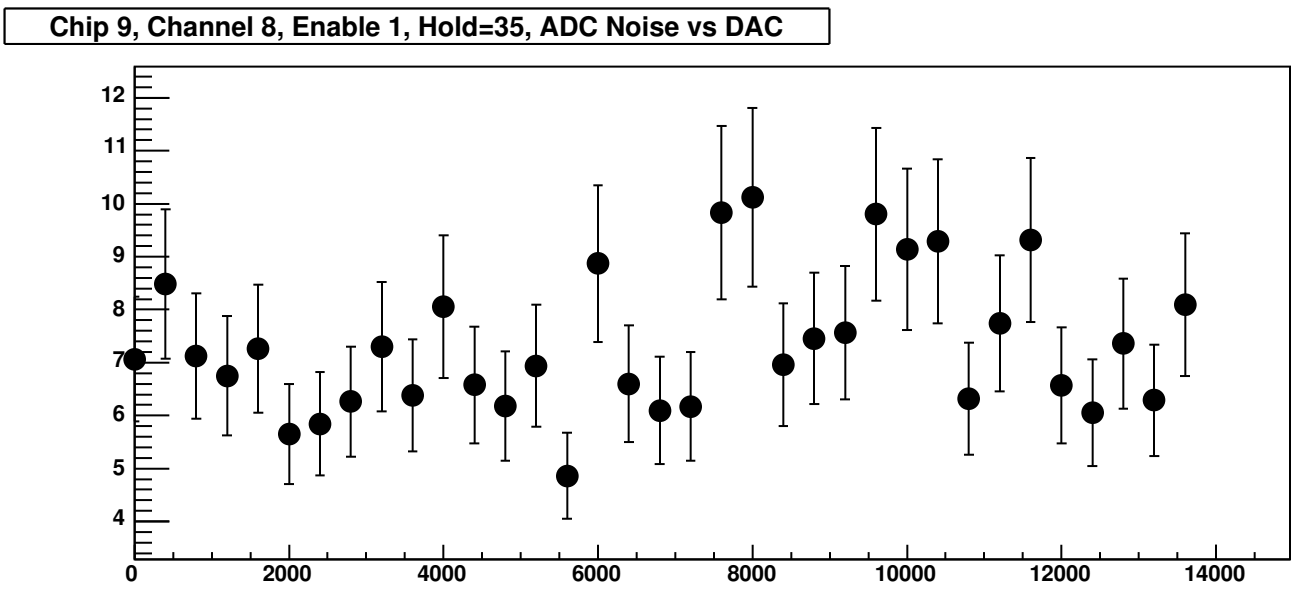
Chip 9, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC

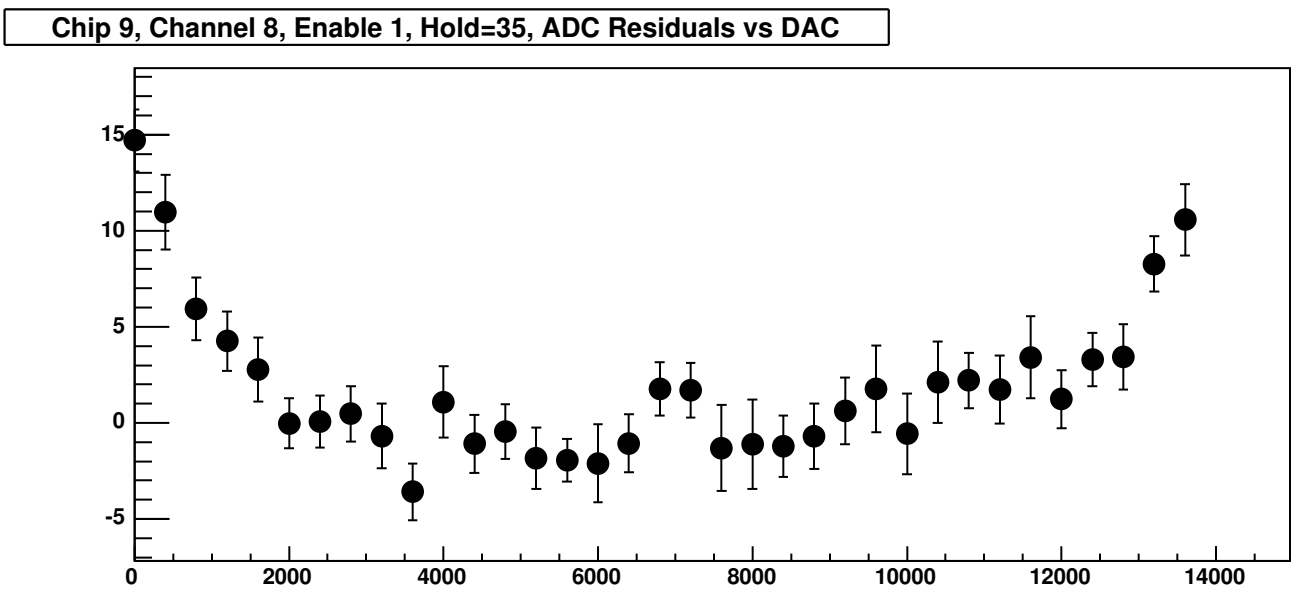


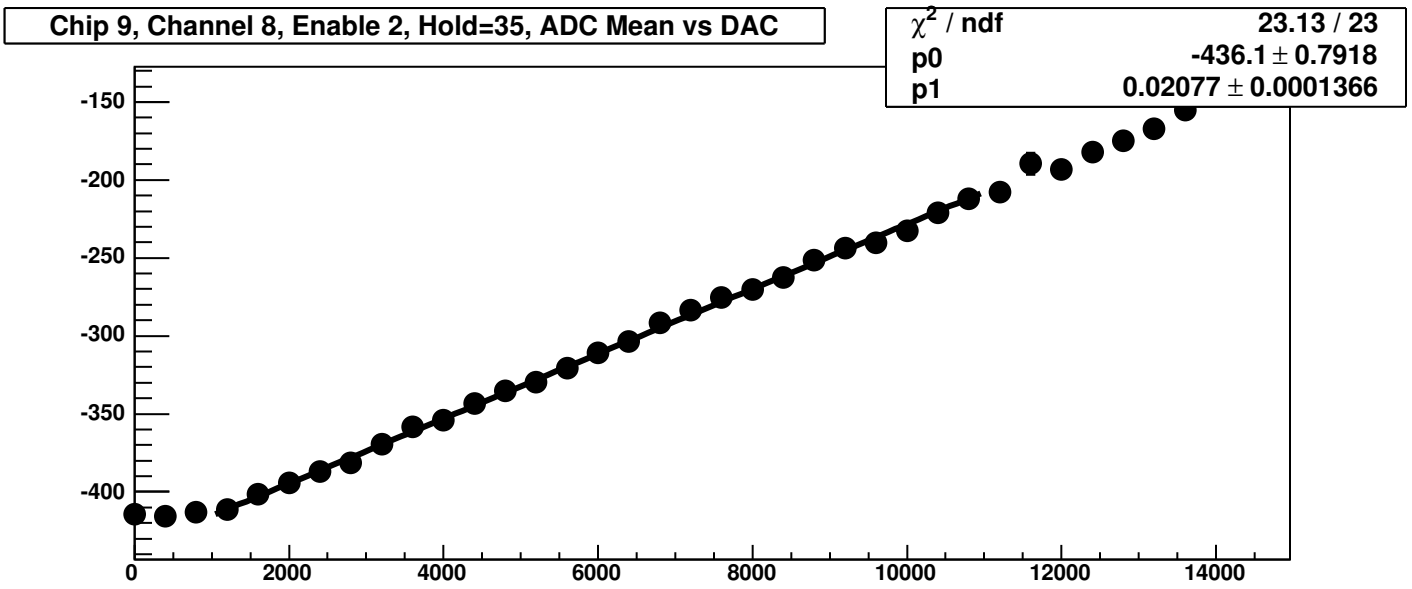
Chip 9, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC

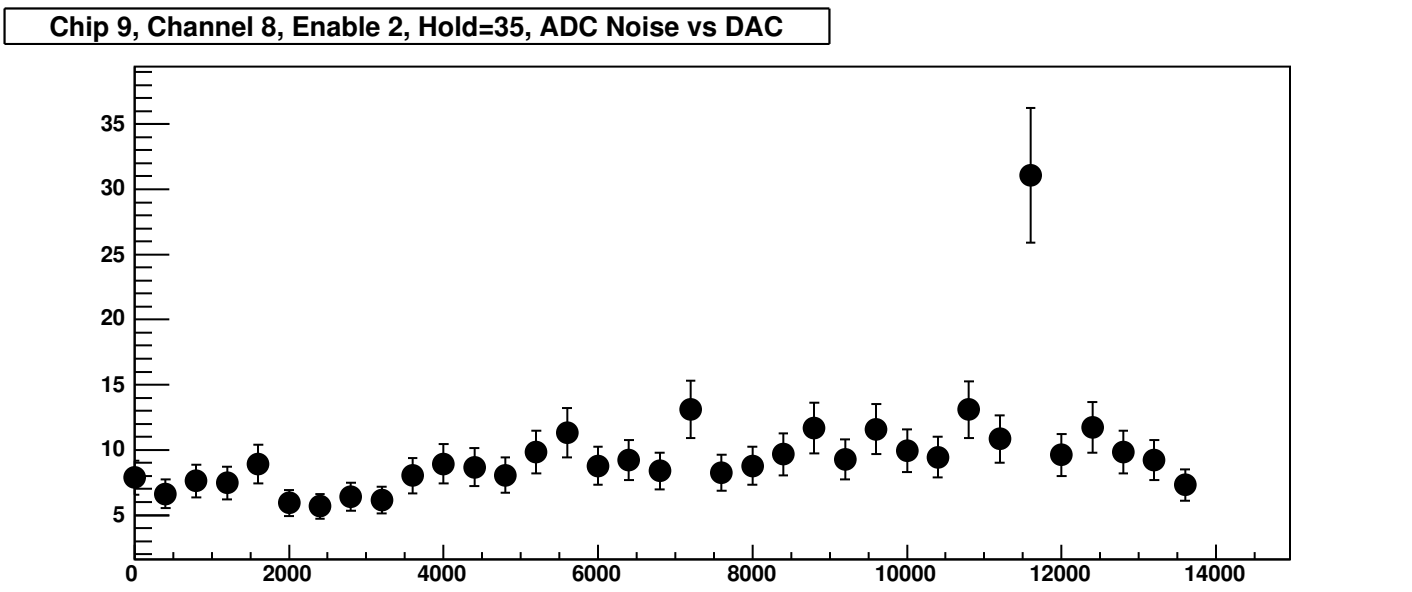


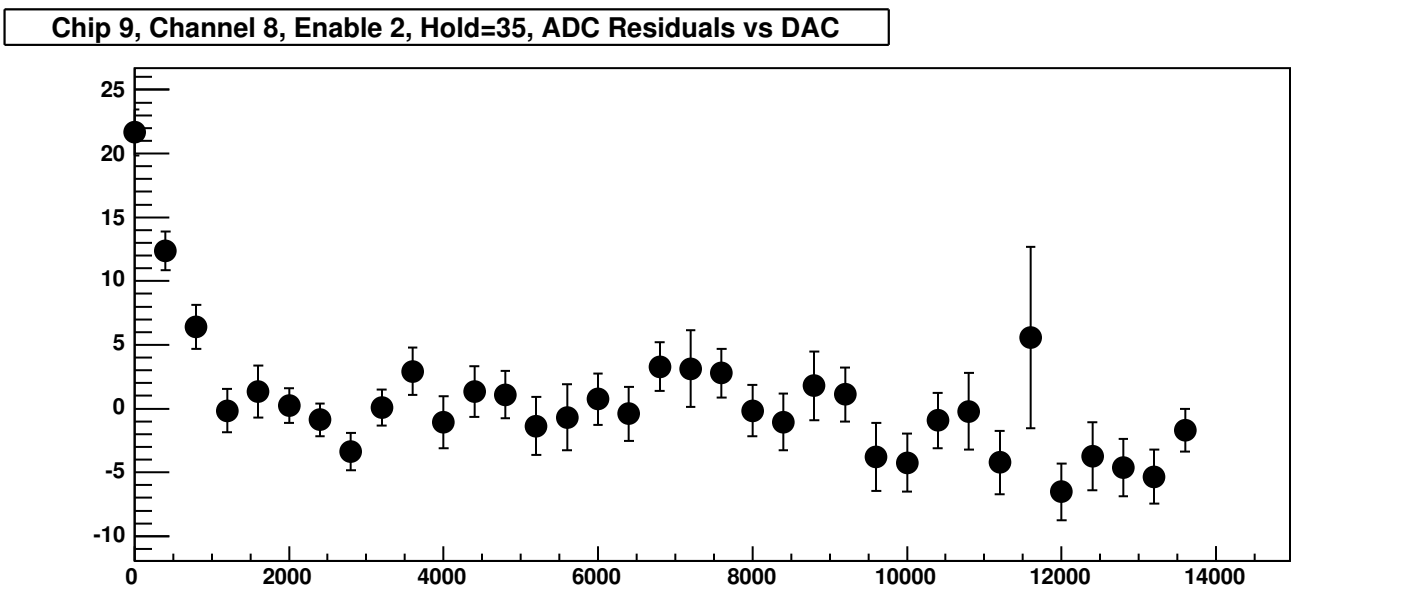

Chip 9, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.95 / 23
p0 -437 ± 0.7068
p1 0.01265 ± 0.0001138
-240
-260
-280
-300
-320
-340
-360
-380
-400
-420
-440
0
2000
4000
6000
8000
10000
12000
14000

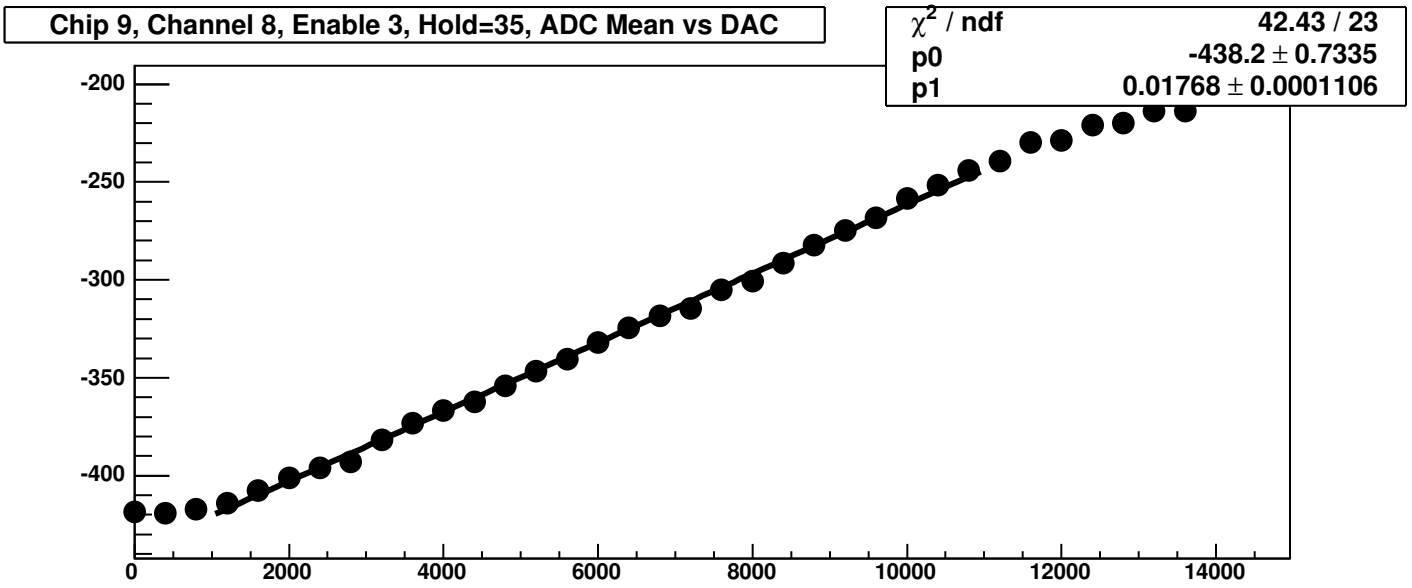

Chip 9, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

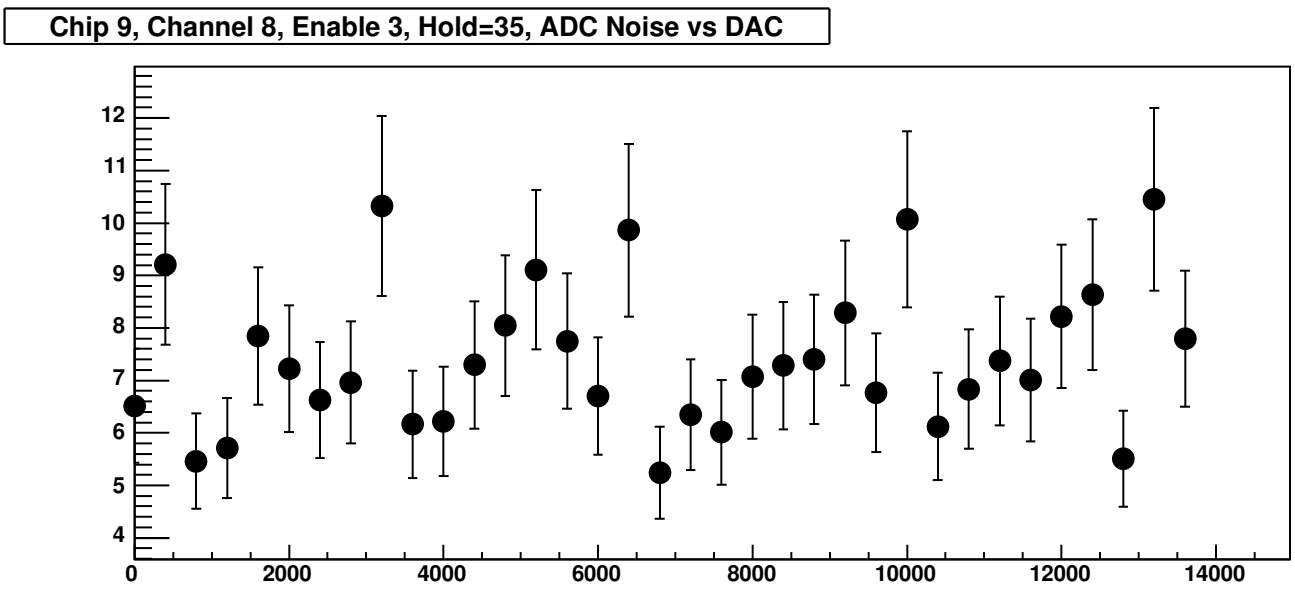

Chip 9, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

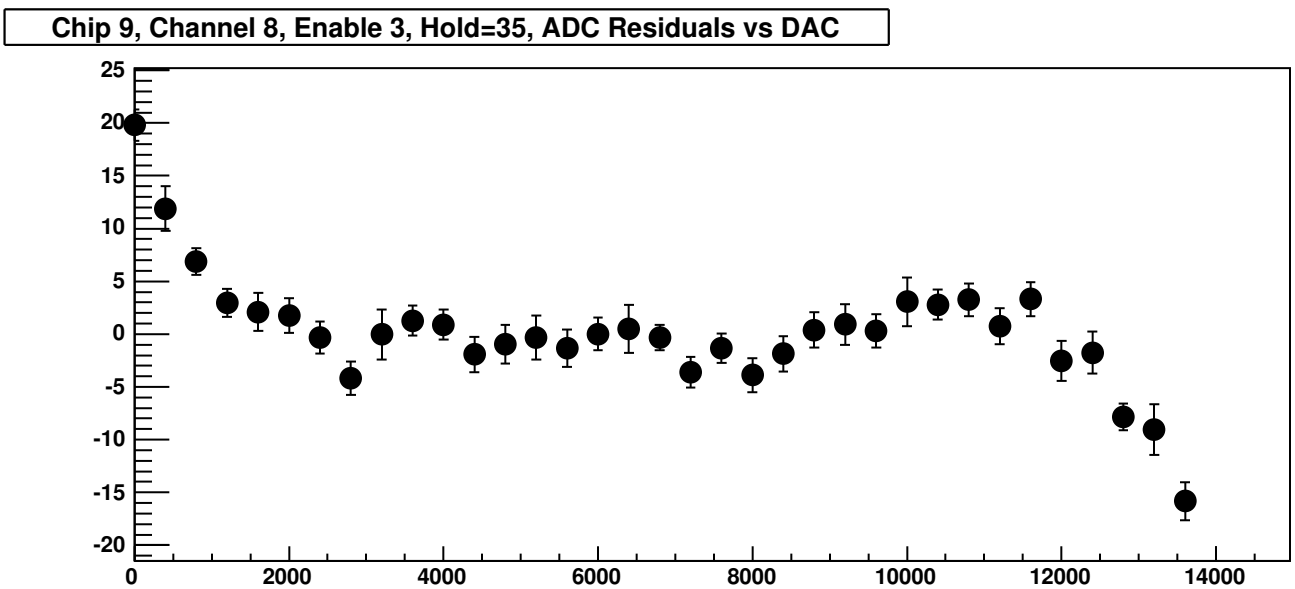

Chip 9, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 23.13 / 23
p0 -436.1 ± 0.7918
p1 0.02077 ± 0.0001366
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

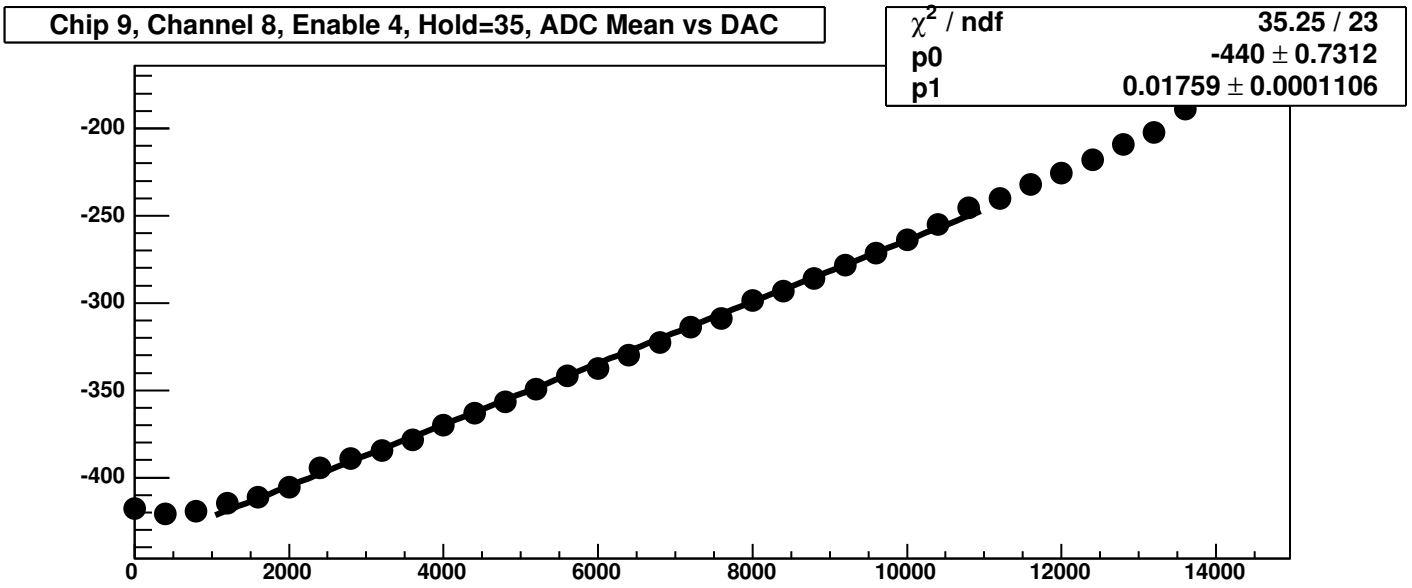

Chip 9, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

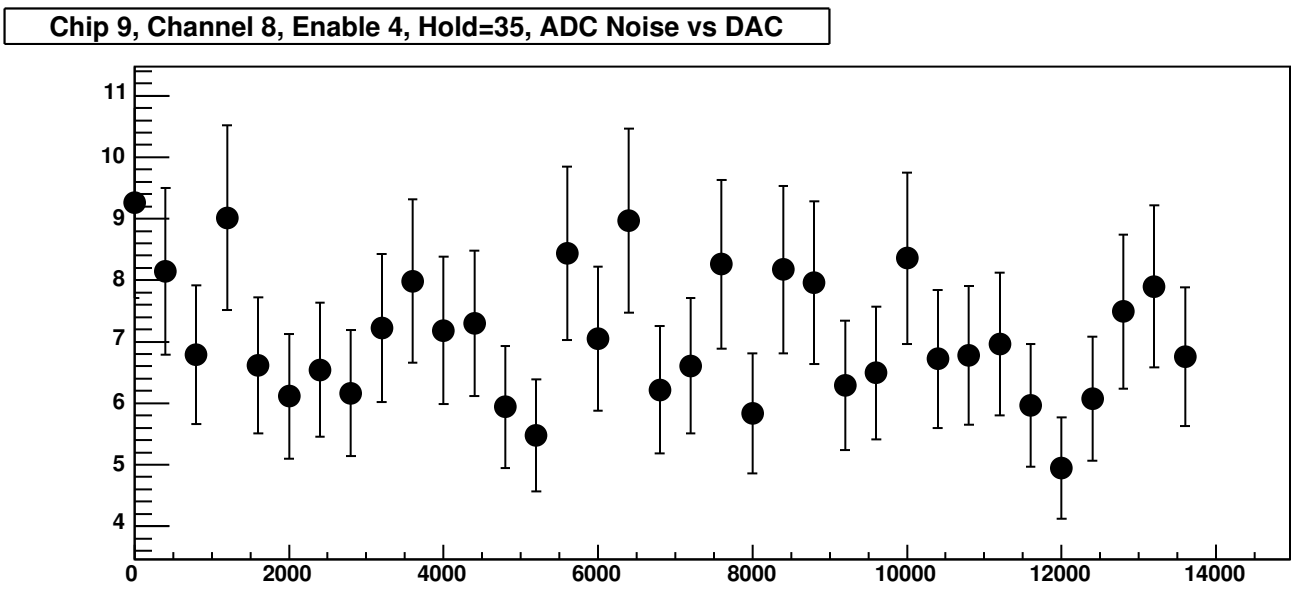

Chip 9, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

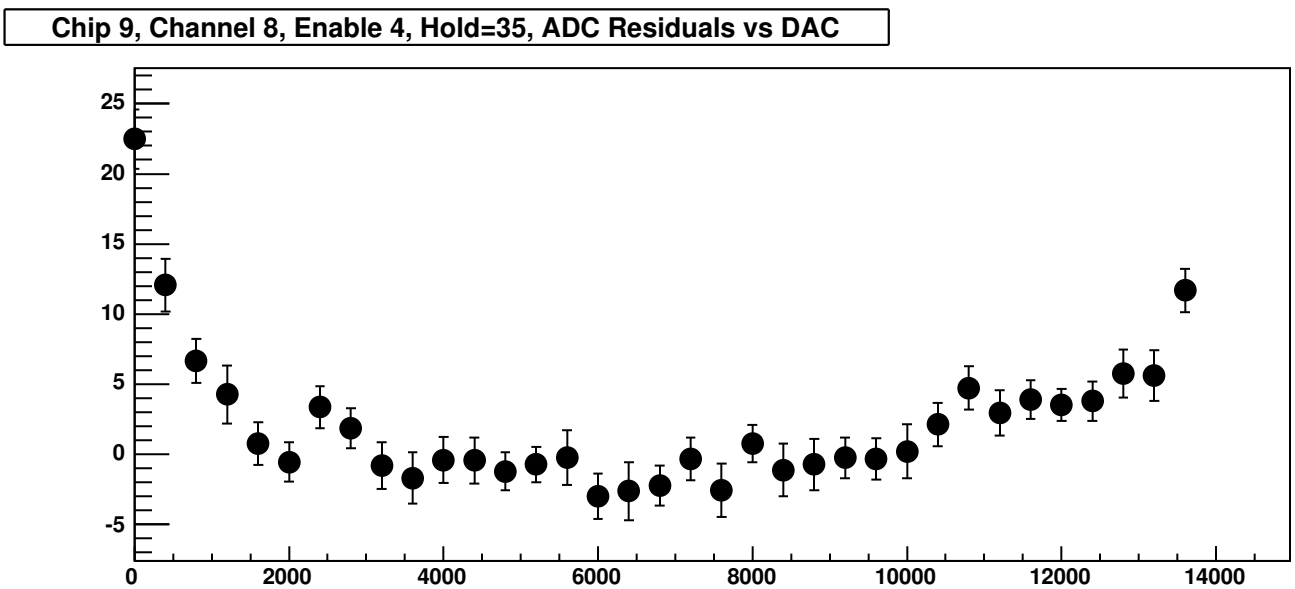

Chip 9, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 42.43 / 23
p0 -438.2 ± 0.7335
p1 0.01768 ± 0.0001106
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

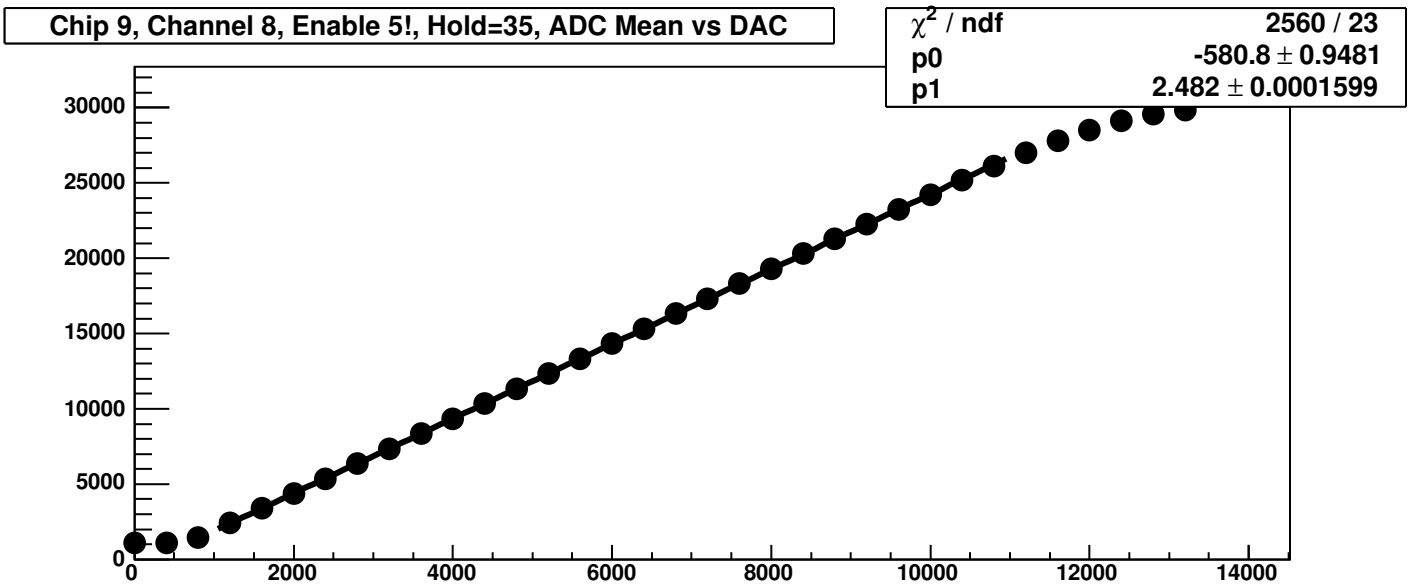

Chip 9, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

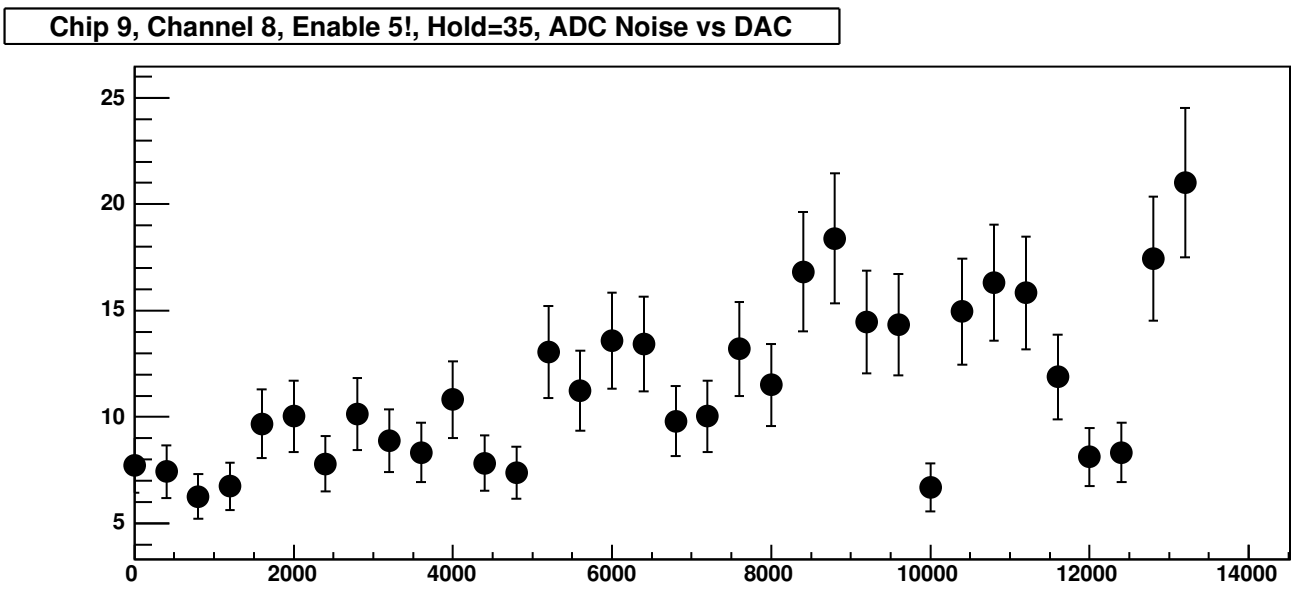

Chip 9, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

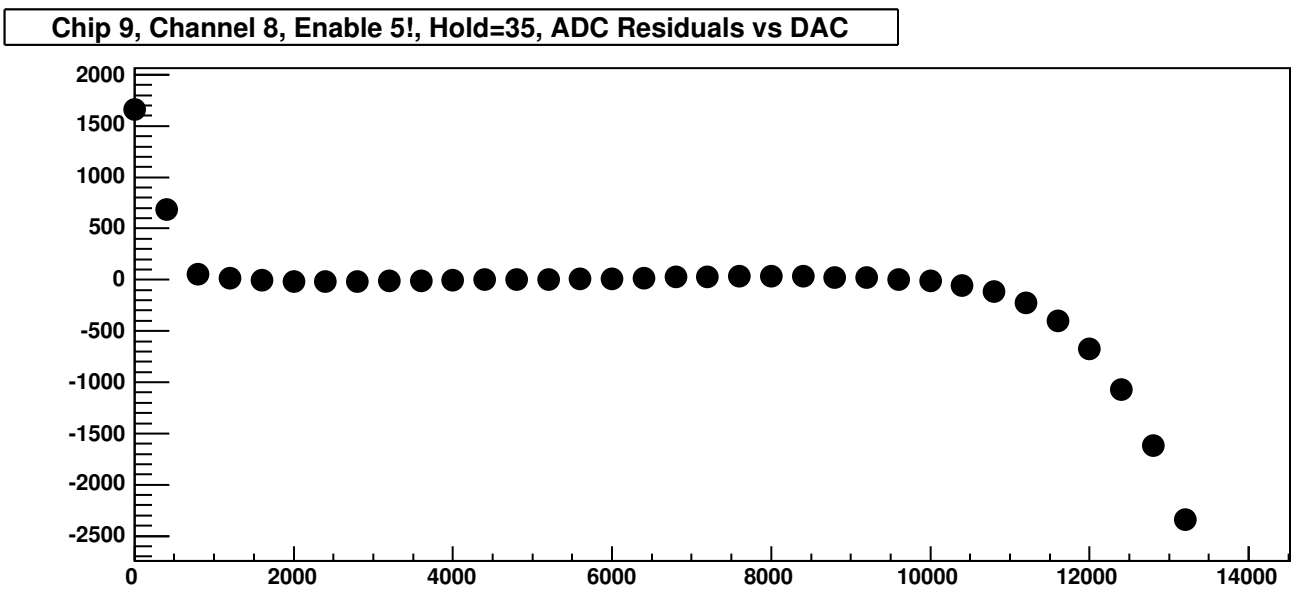

Chip 9, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 35.25 / 23
p0 -440 ± 0.7312
p1 0.01759 ± 0.0001106
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

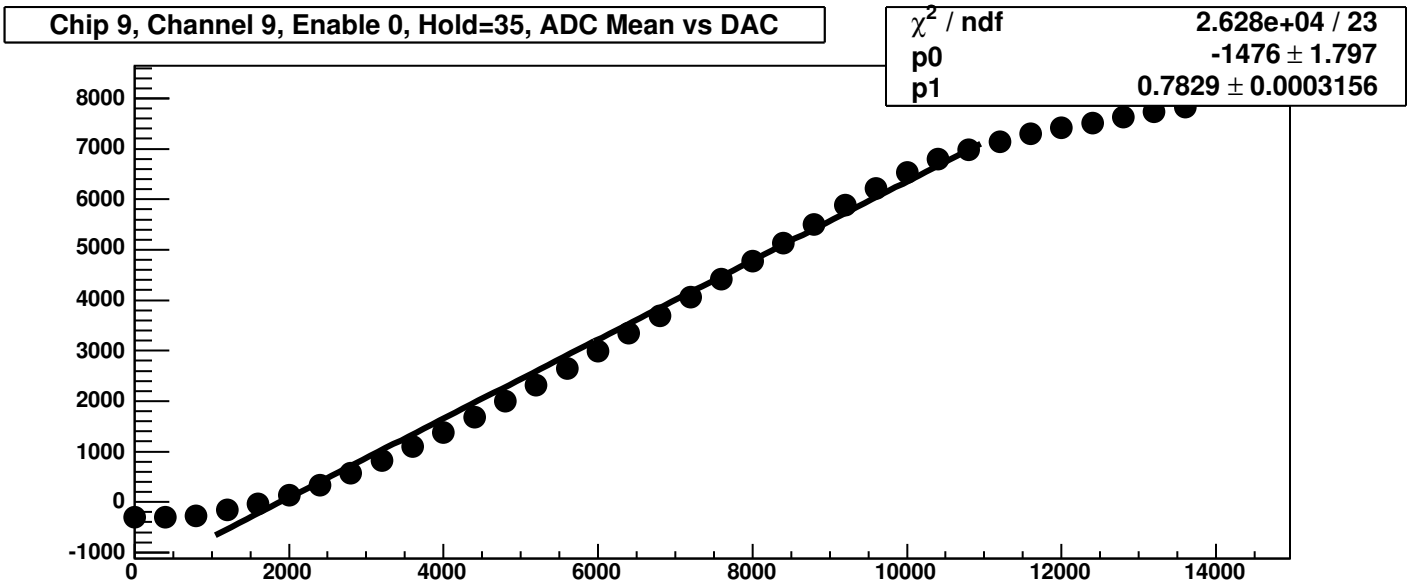

Chip 9, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

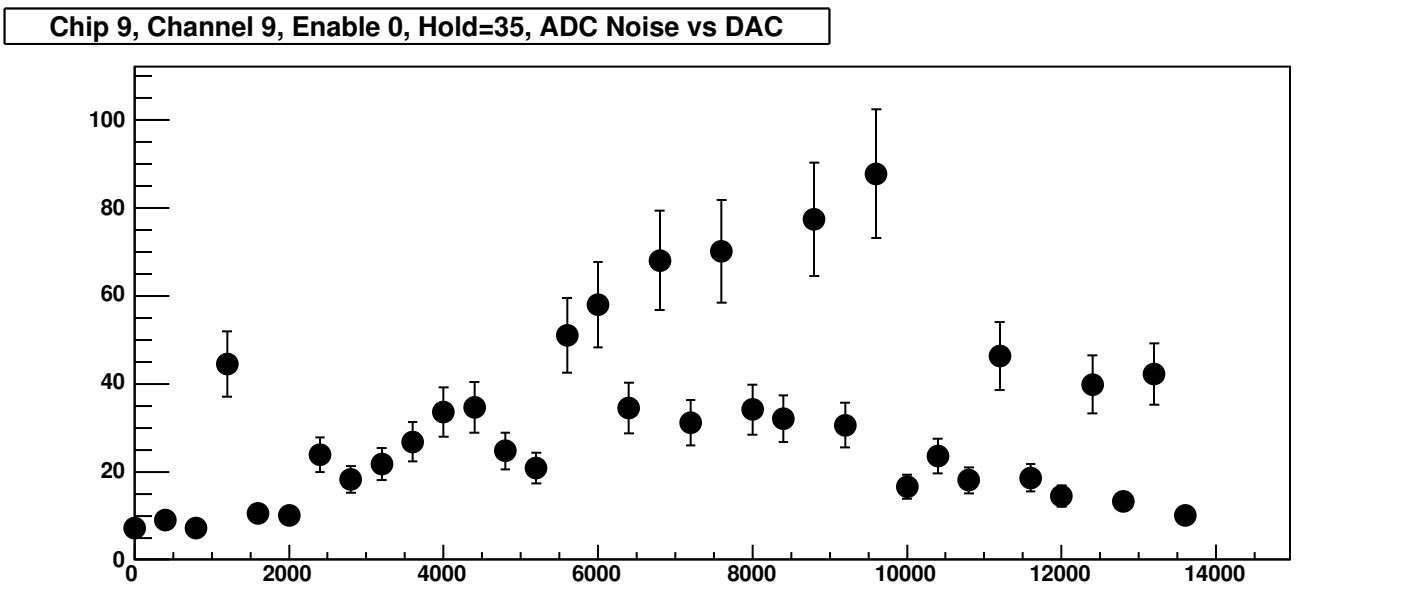

Chip 9, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

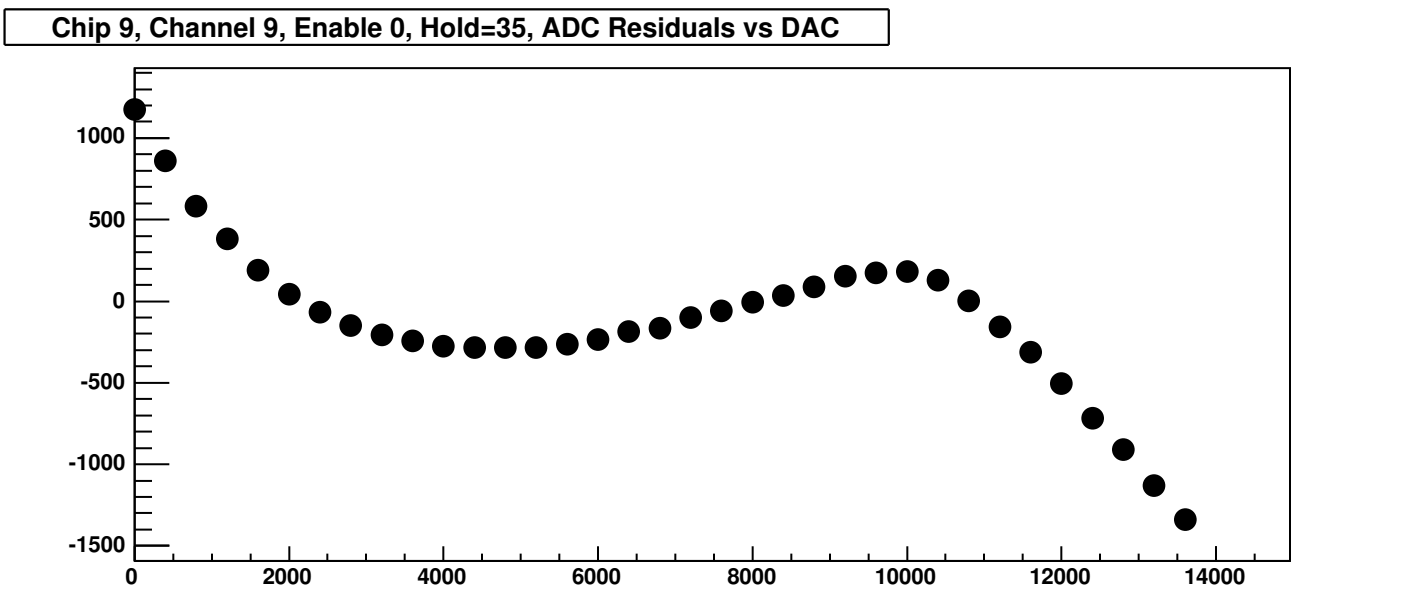

Chip 9, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2560 / 23
p0 -580.8 ± 0.9481
p1 2.482 ± 0.0001599
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

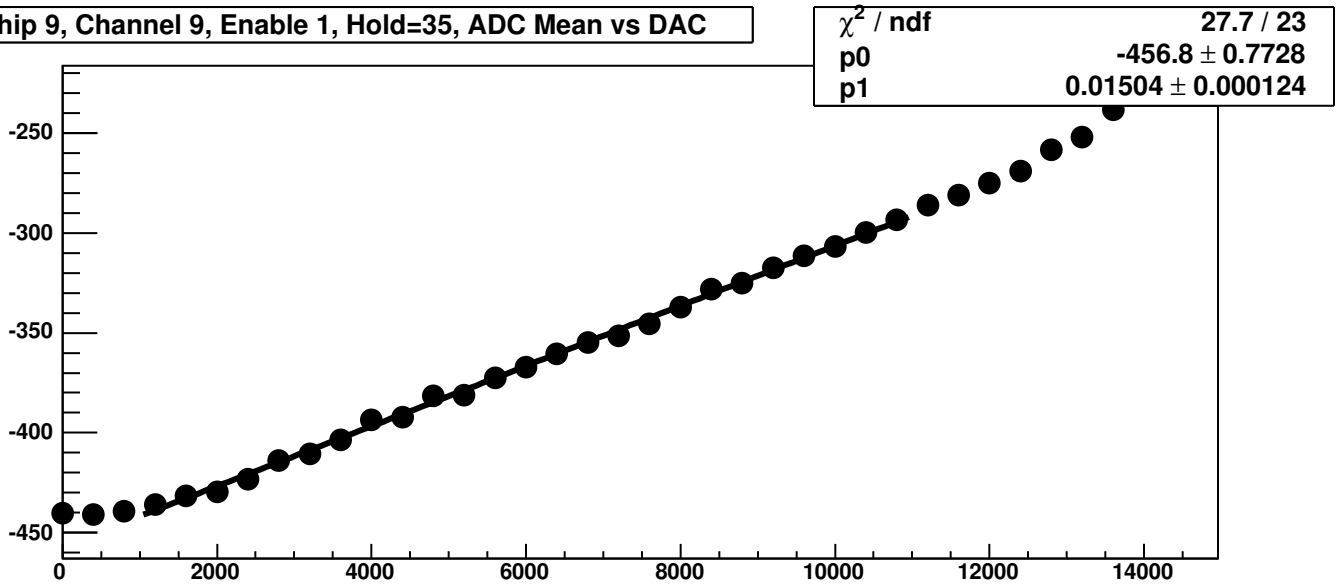

Chip 9, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.628e+04 / 23
p0 -1476 ± 1.797
p1 0.7829 ± 0.0003156
8000
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

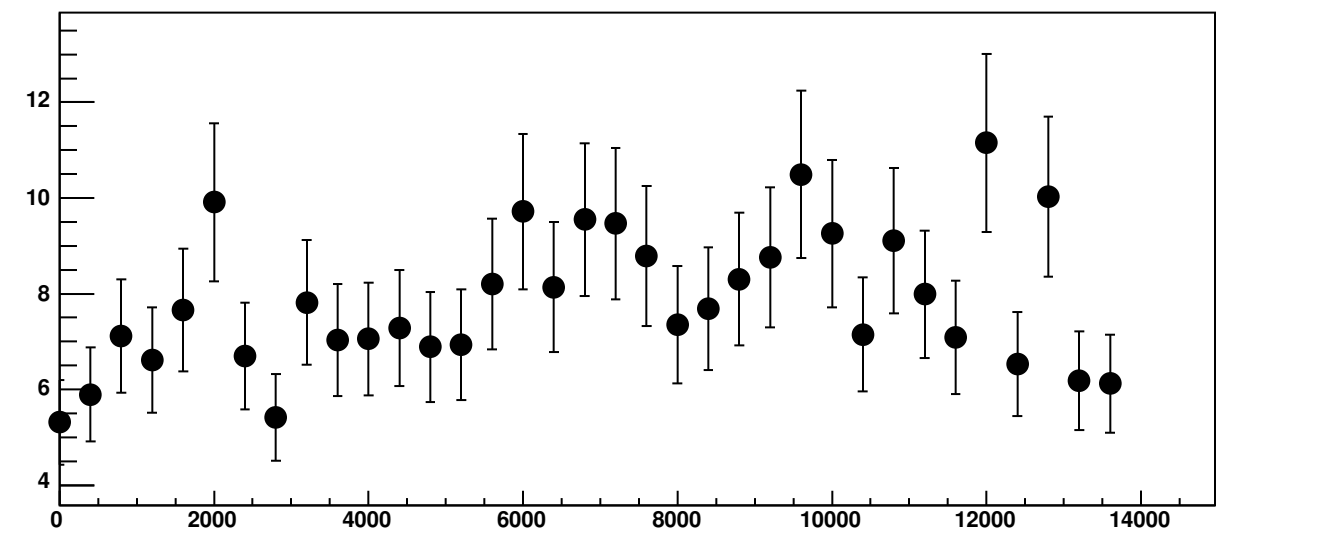

Chip 9, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

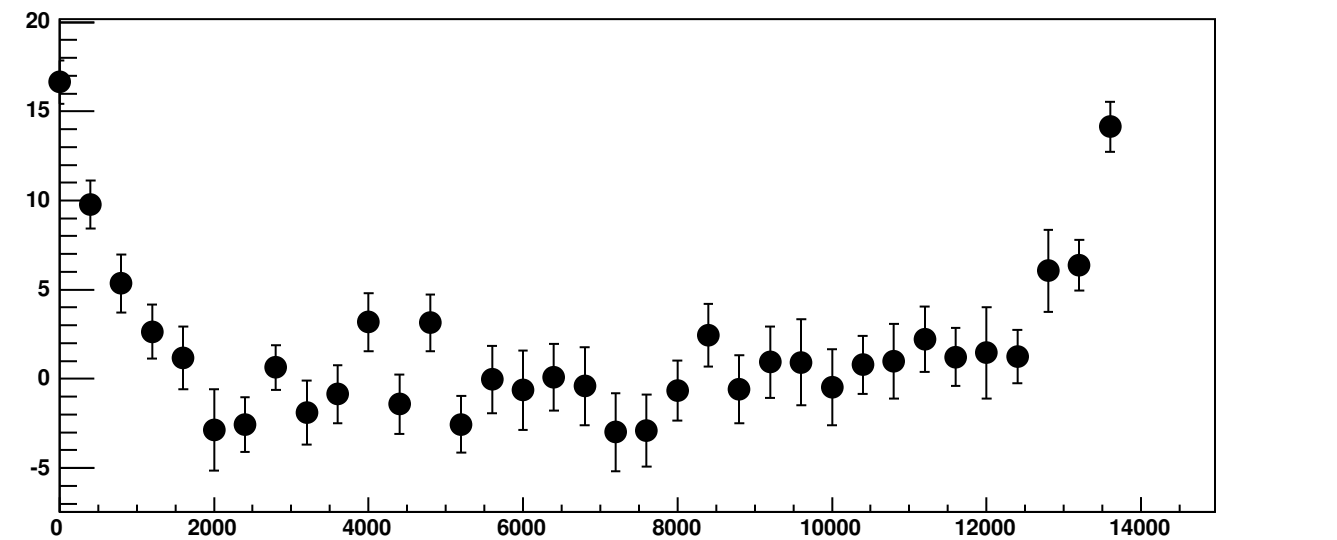
Chip 9, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

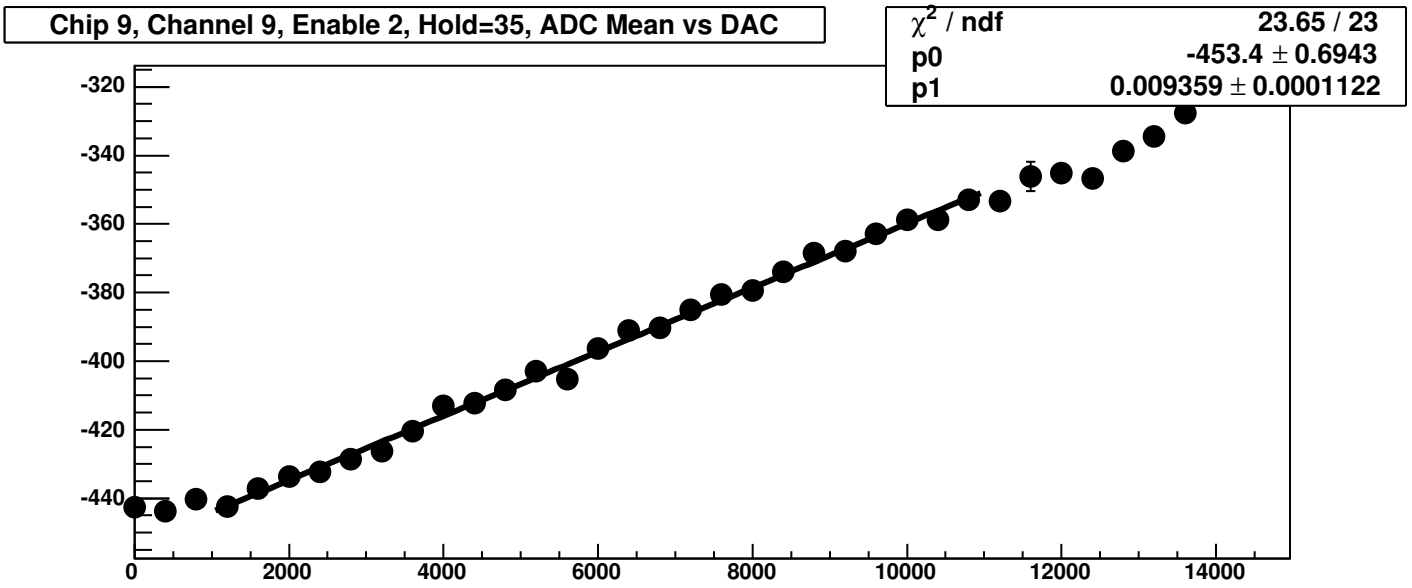


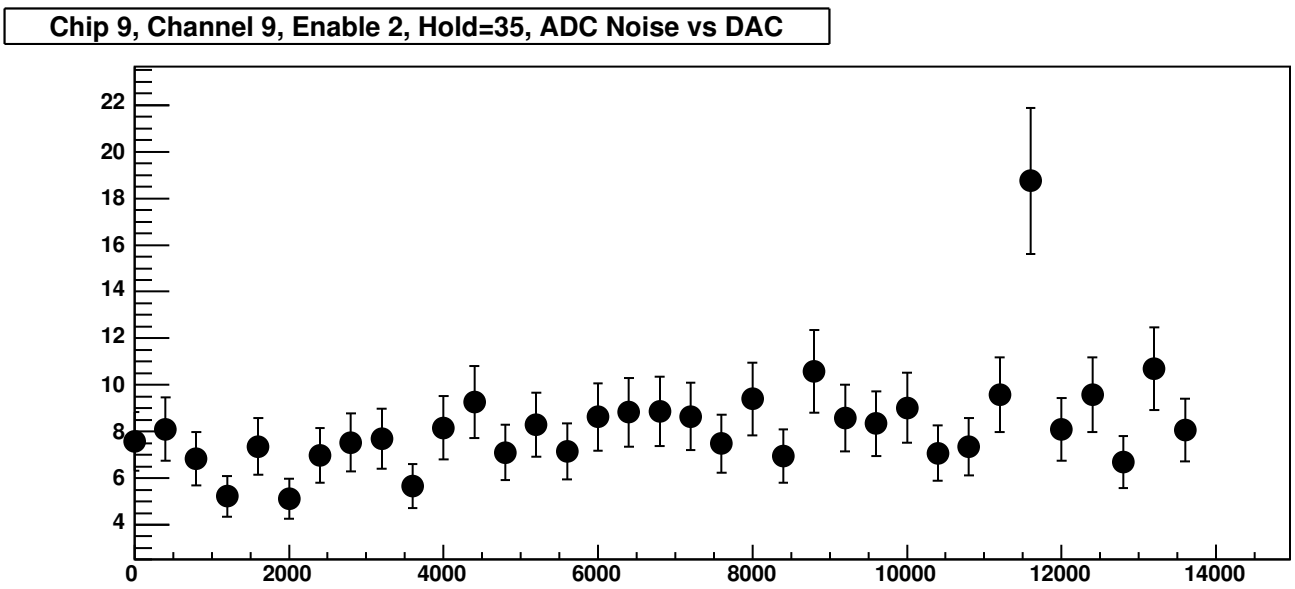
Chip 9, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC

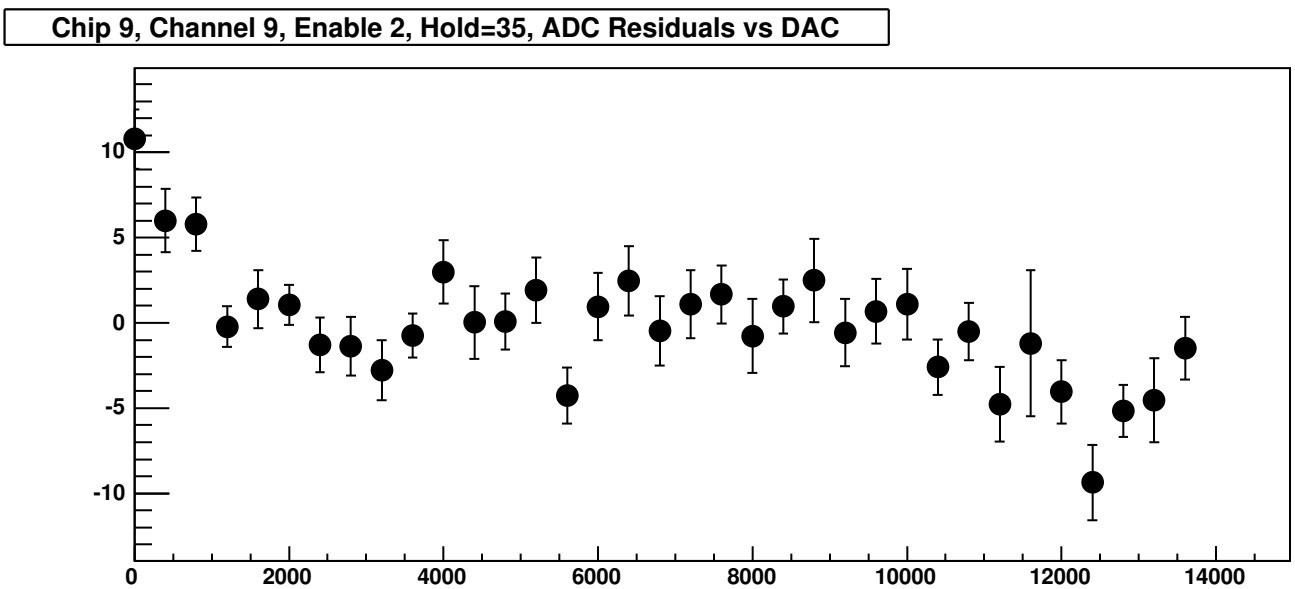


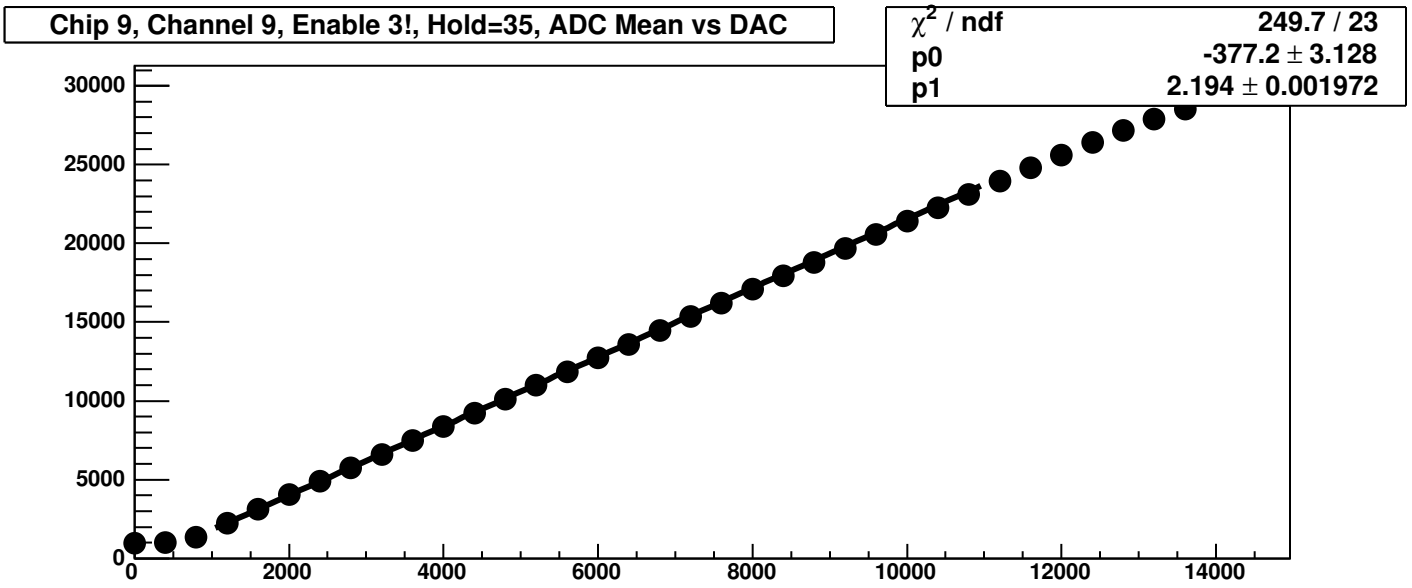
Chip 9, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

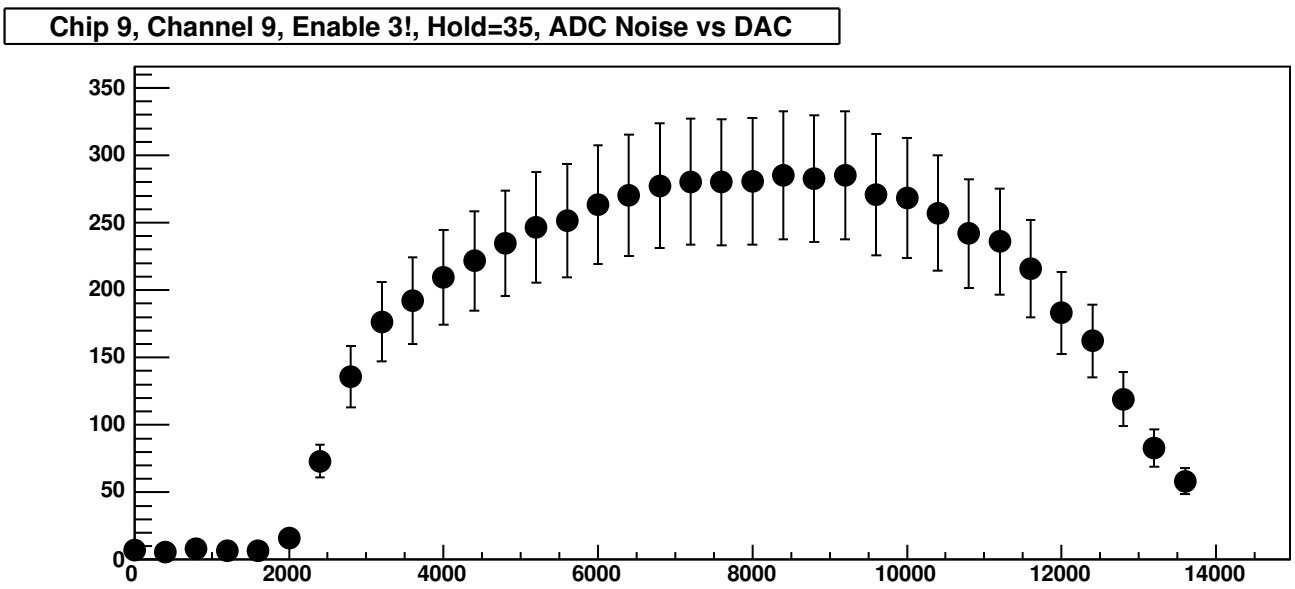


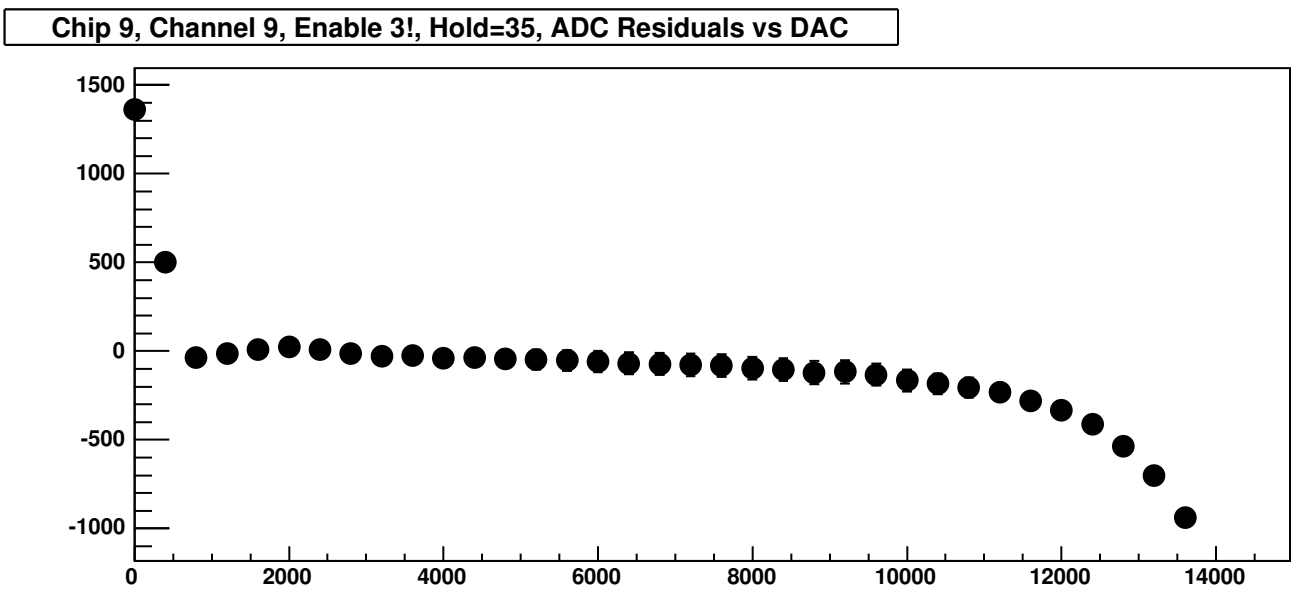

Chip 9, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 23.65 / 23
p0 -453.4 ± 0.6943
p1 0.009359 ± 0.0001122
-320
-340
-360
-380
-400
-420
-440
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

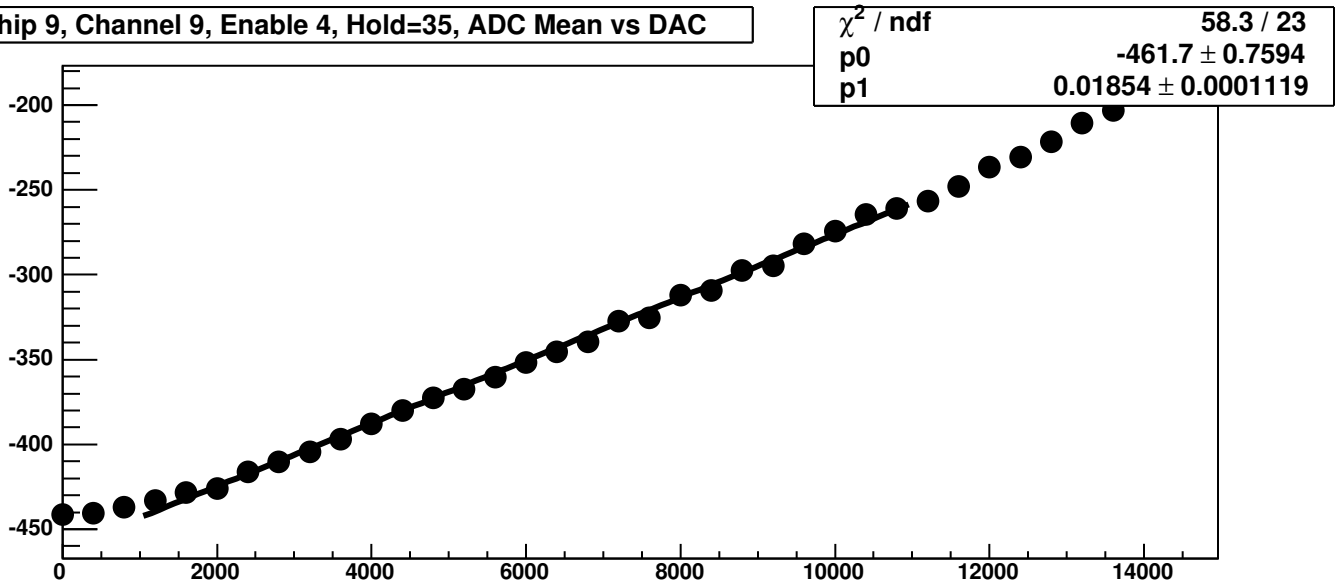

Chip 9, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 249.7 / 23
p0 -377.2 ± 3.128
p1 2.194 ± 0.001972
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

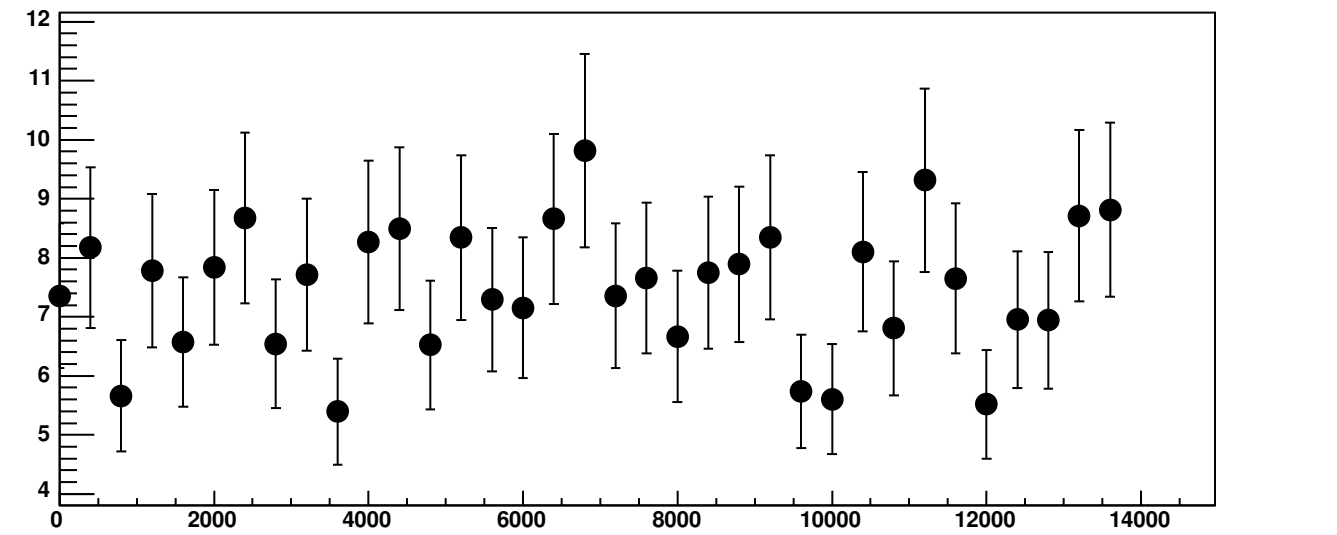

Chip 9, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

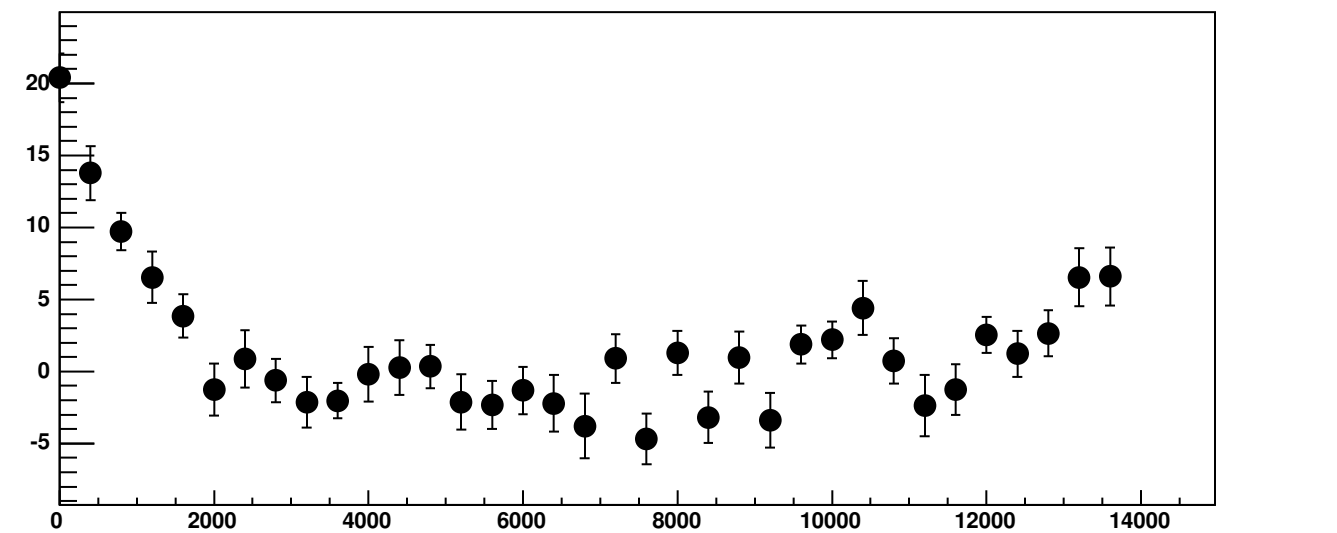
Chip 9, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

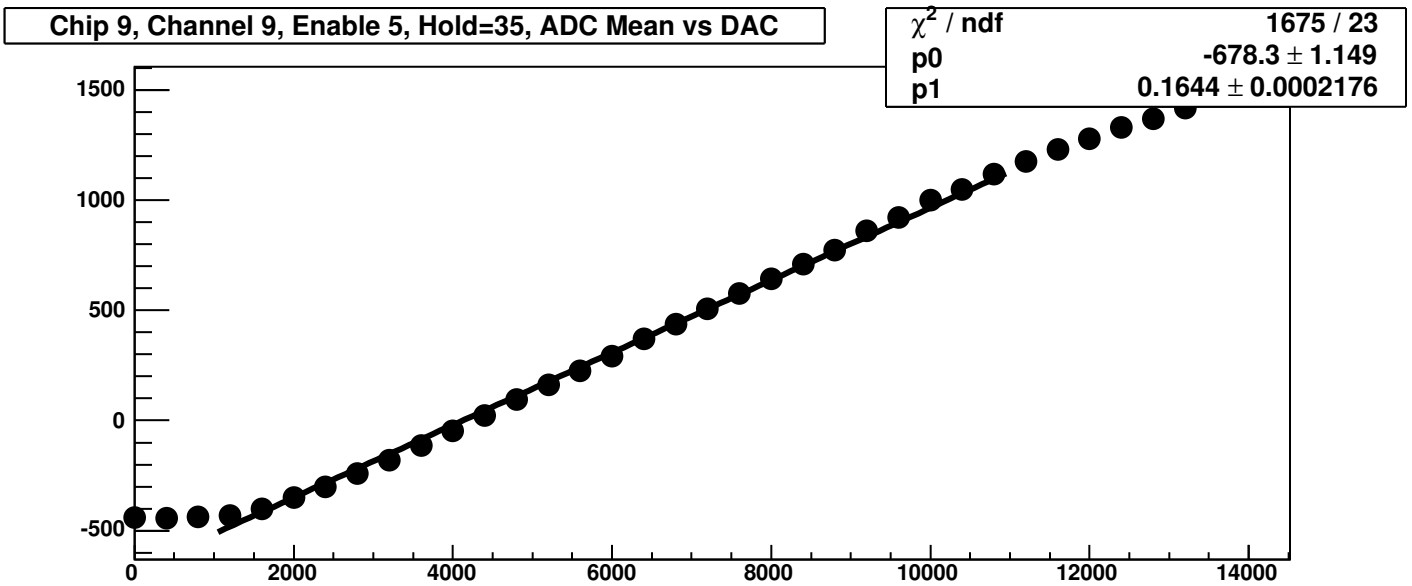


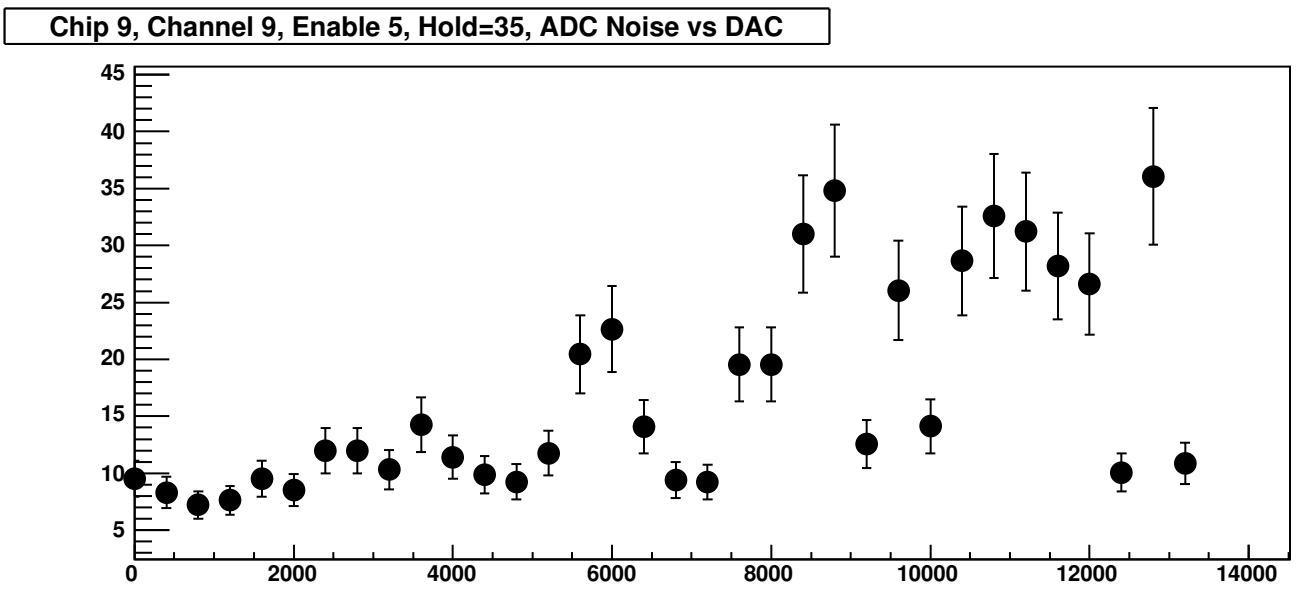
Chip 9, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC

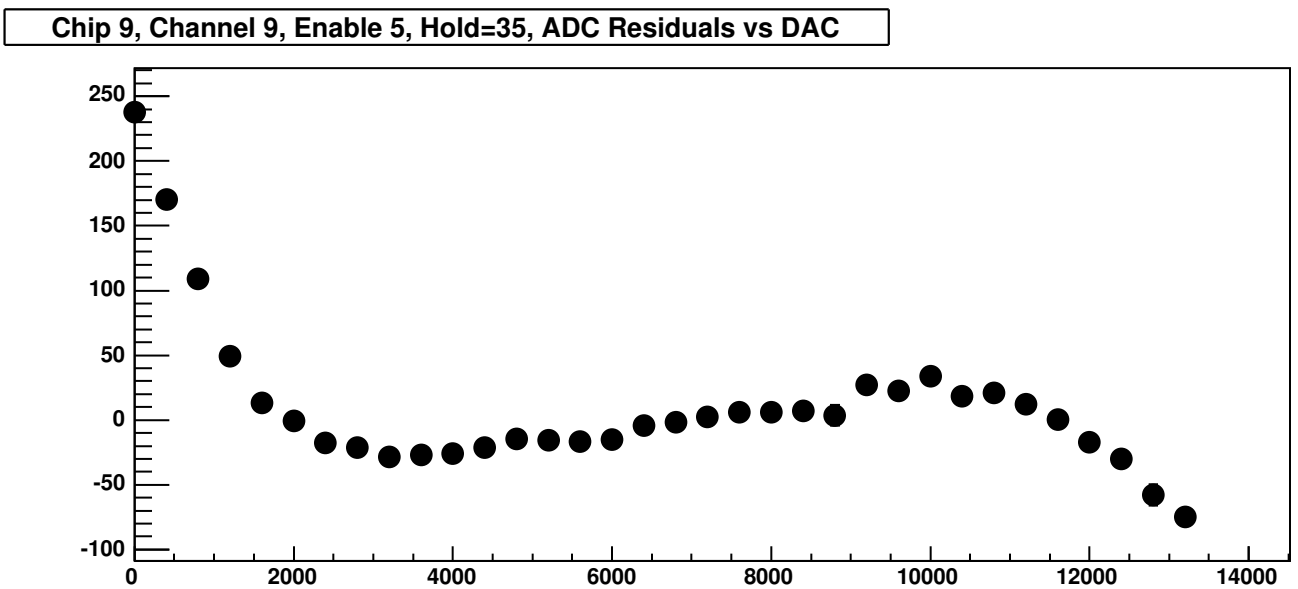


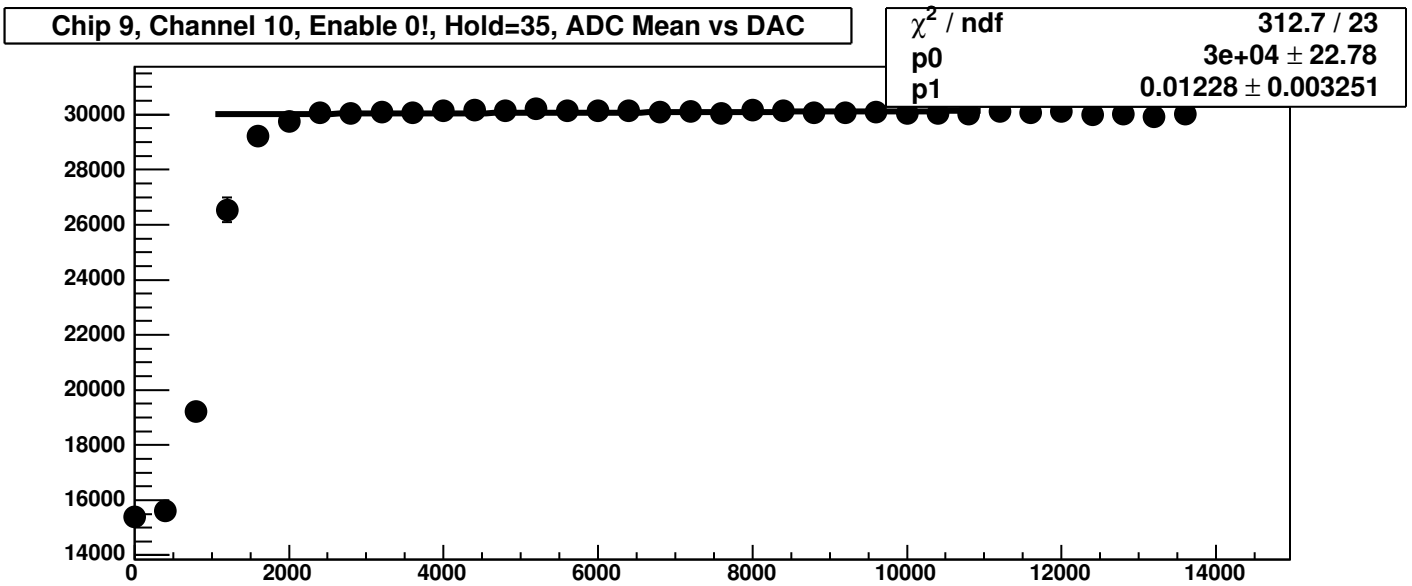
Chip 9, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

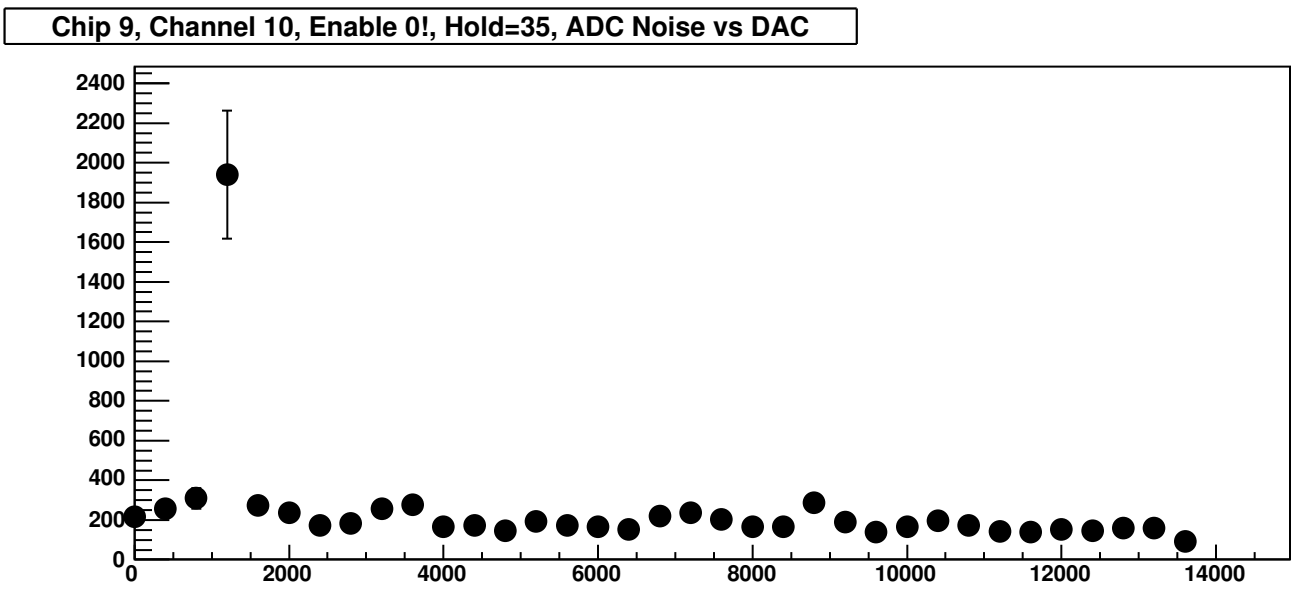


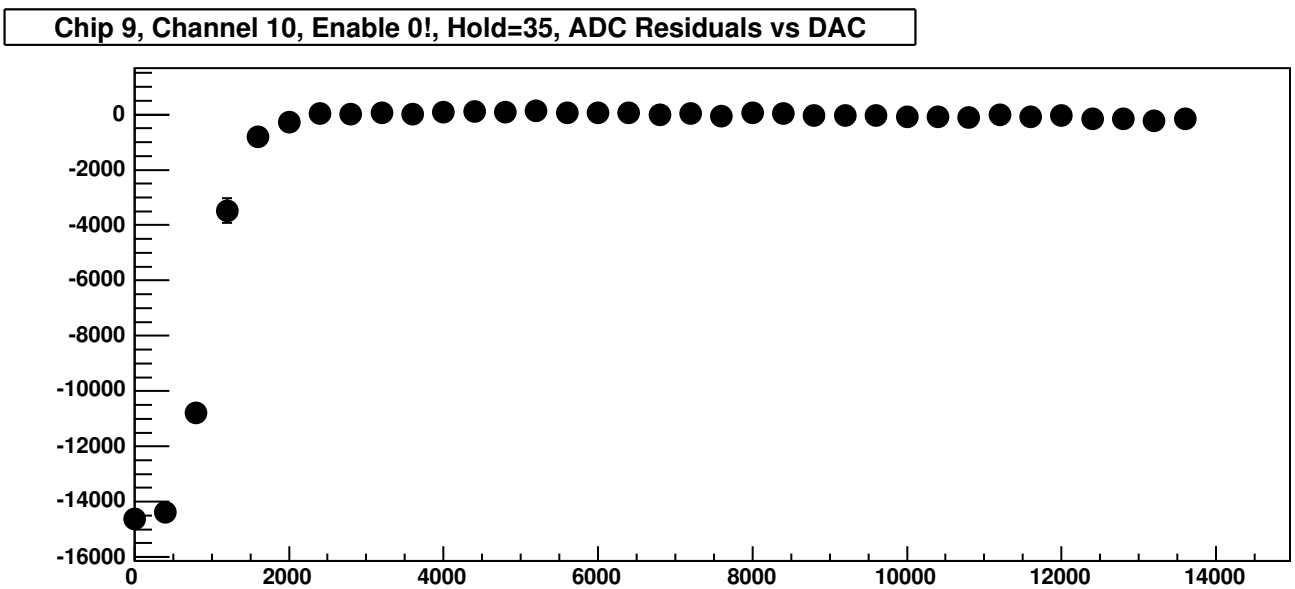

Chip 9, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1675 / 23
p0 -678.3 ± 1.149
p1 0.1644 ± 0.0002176
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

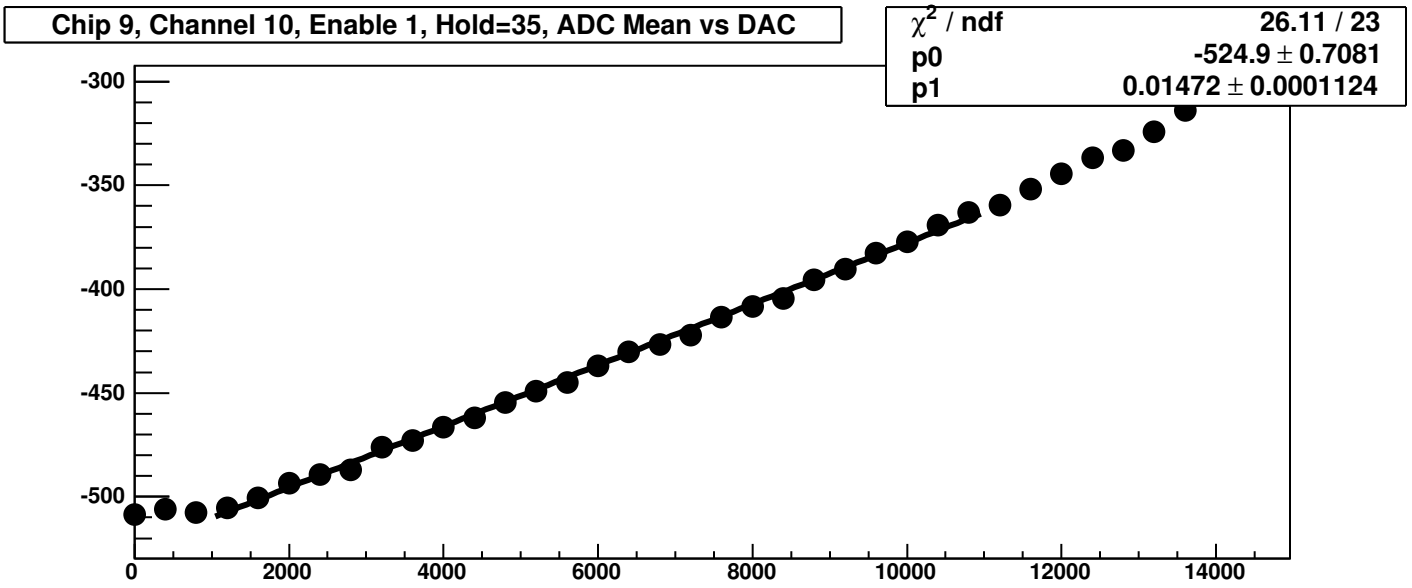

Chip 9, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

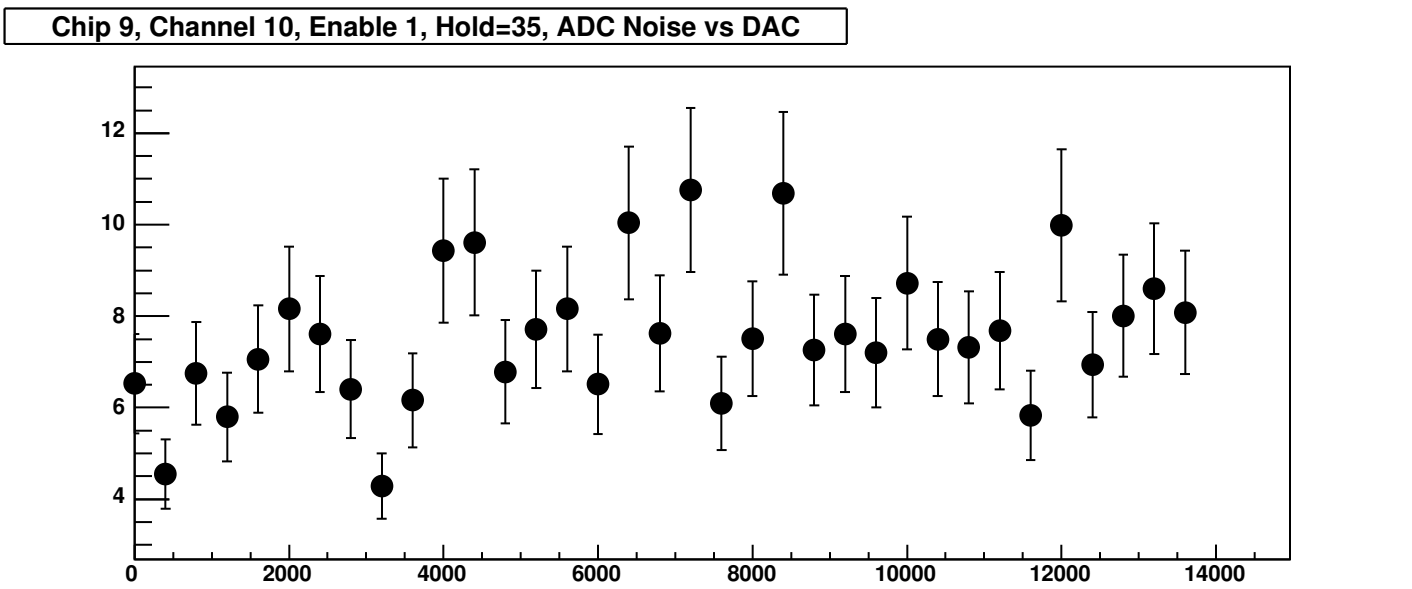

Chip 9, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

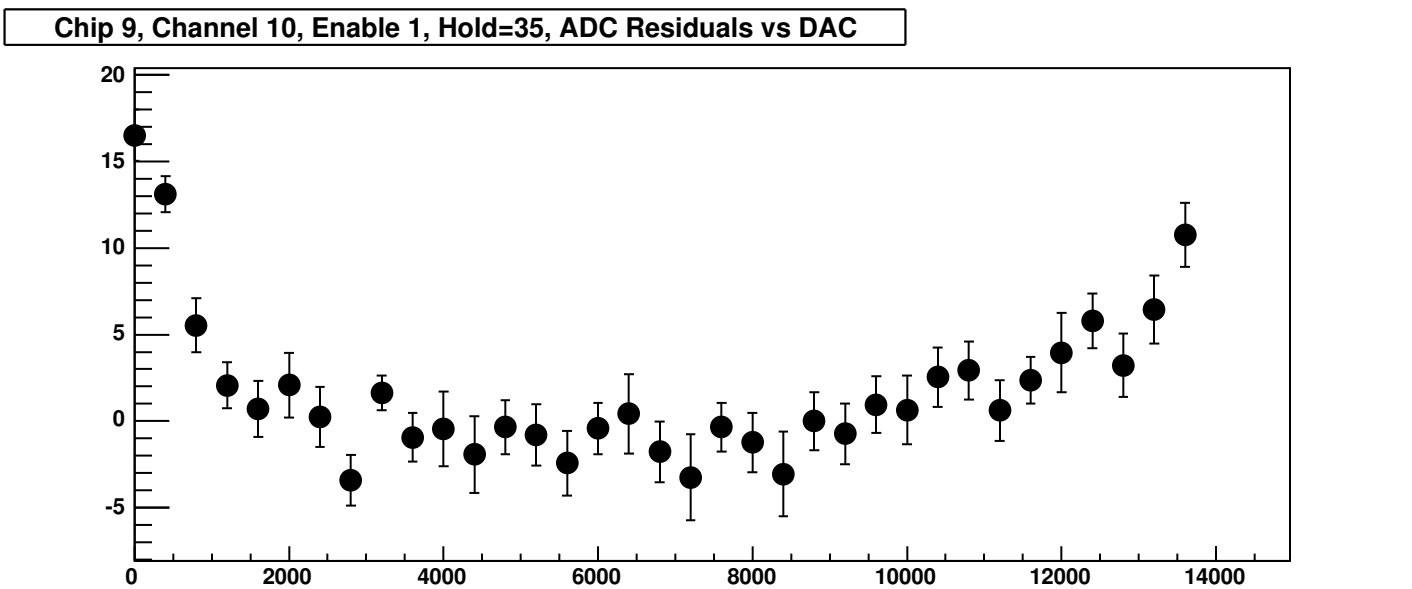

Chip 9, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 312.7 / 23
p0 3e+04 ± 22.78
p1 0.01228 ± 0.003251
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

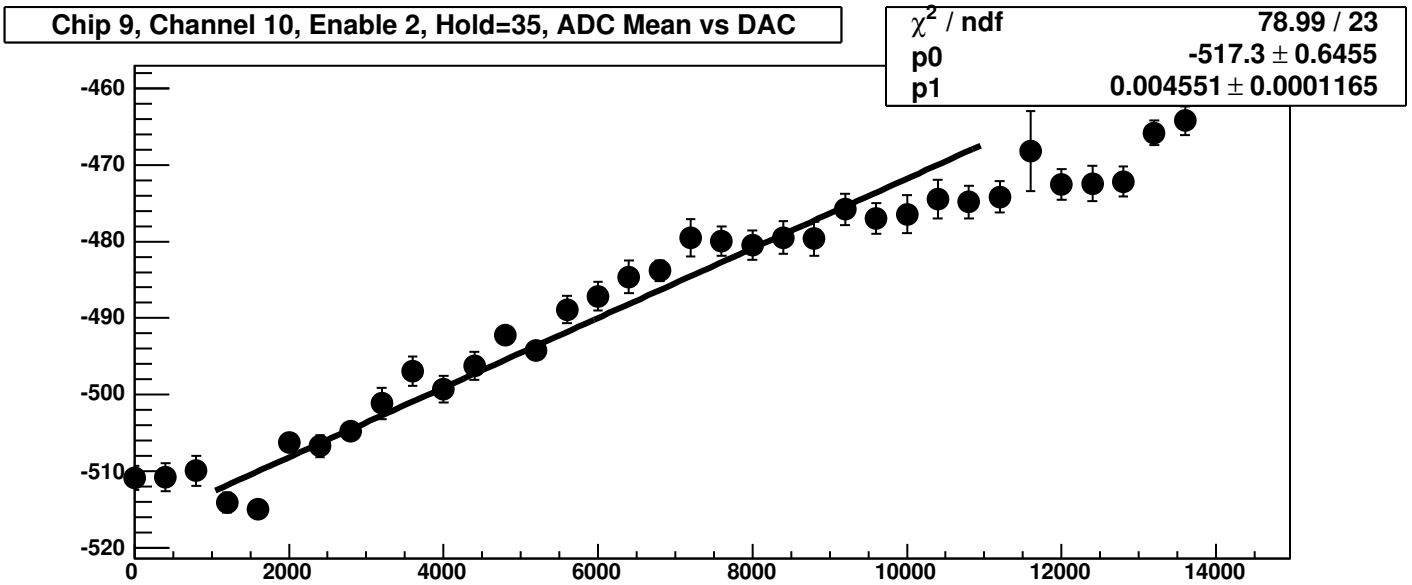

Chip 9, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

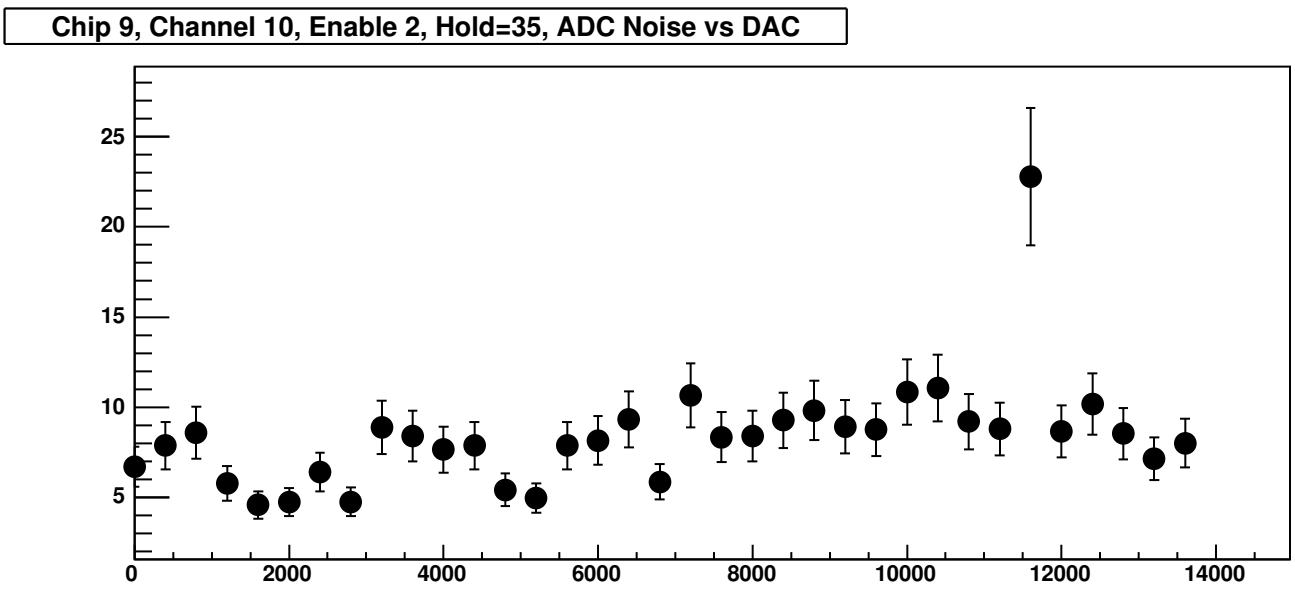

Chip 9, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
-16000
0
2000
4000
6000
8000
10000
12000
14000

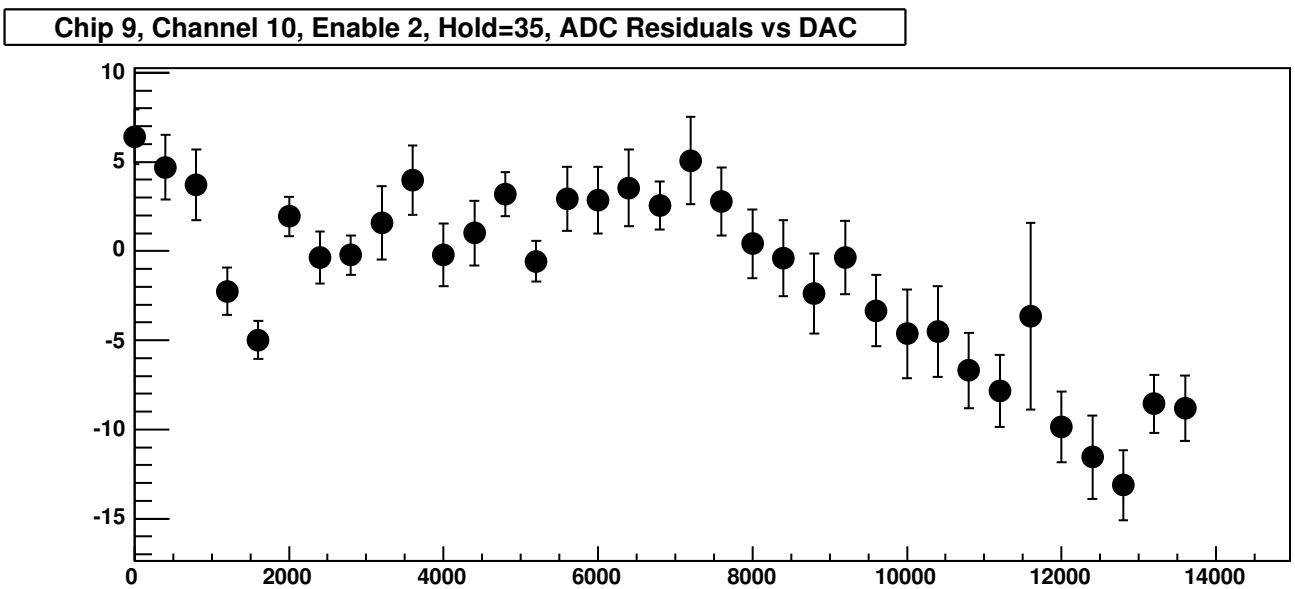

Chip 9, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 26.11 / 23
p0 -524.9 ± 0.7081
p1 0.01472 ± 0.0001124
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

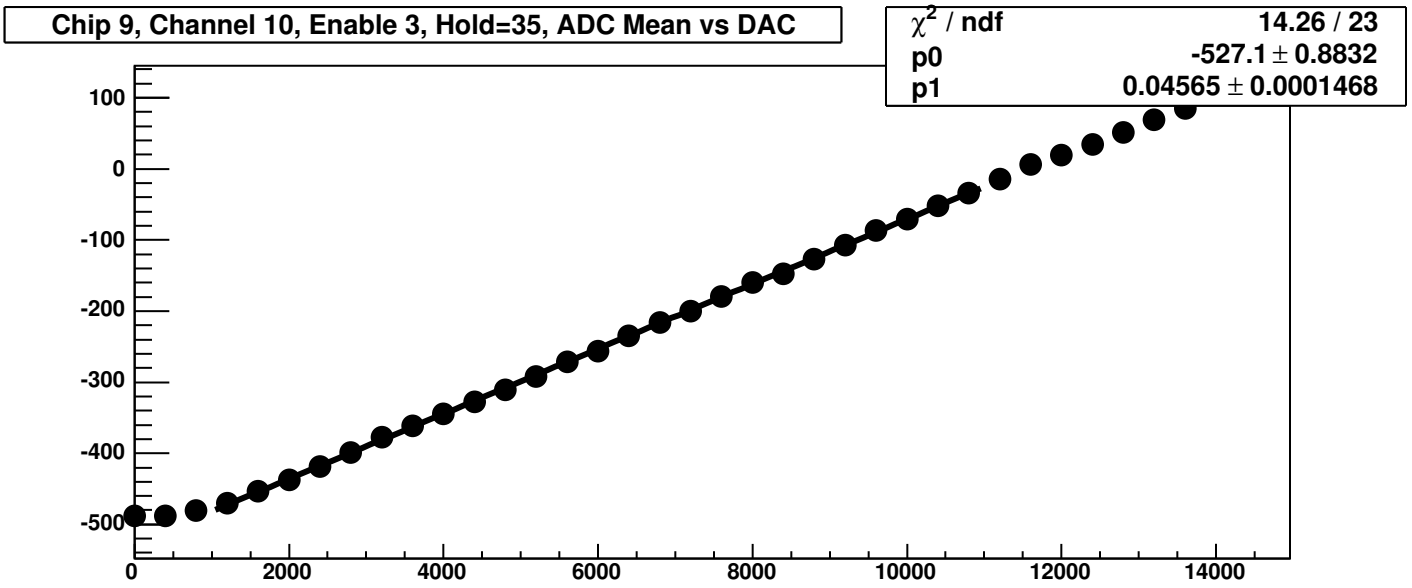

Chip 9, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

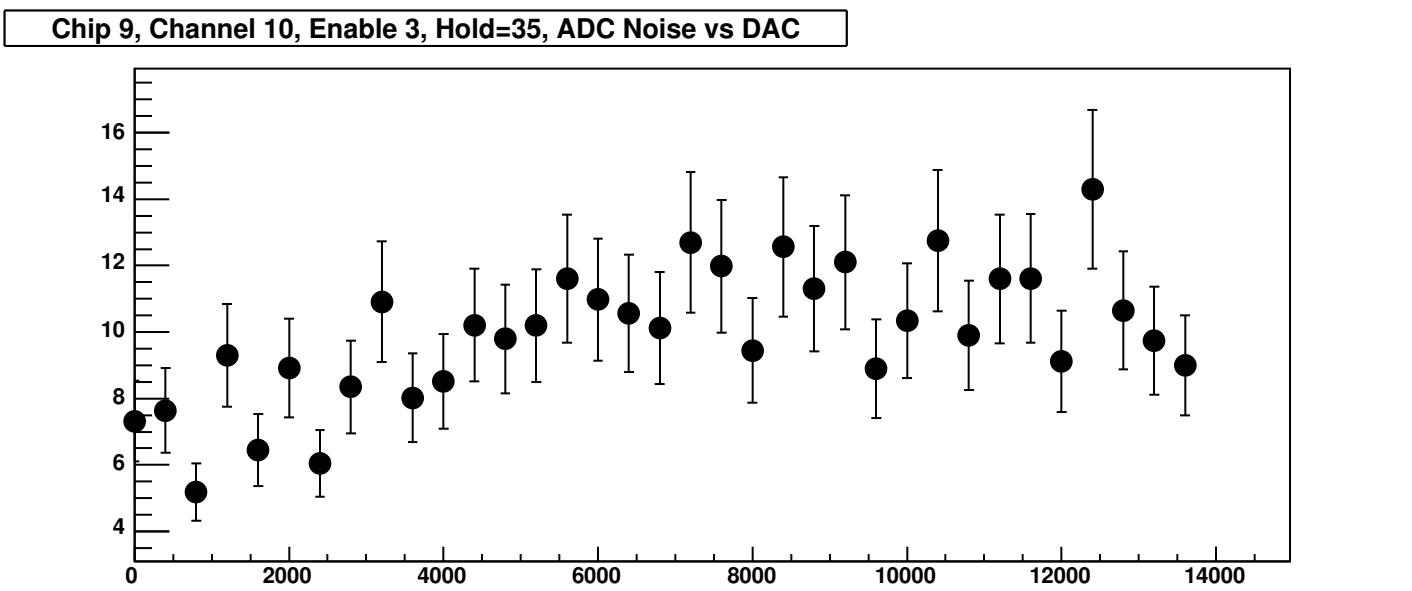

Chip 9, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

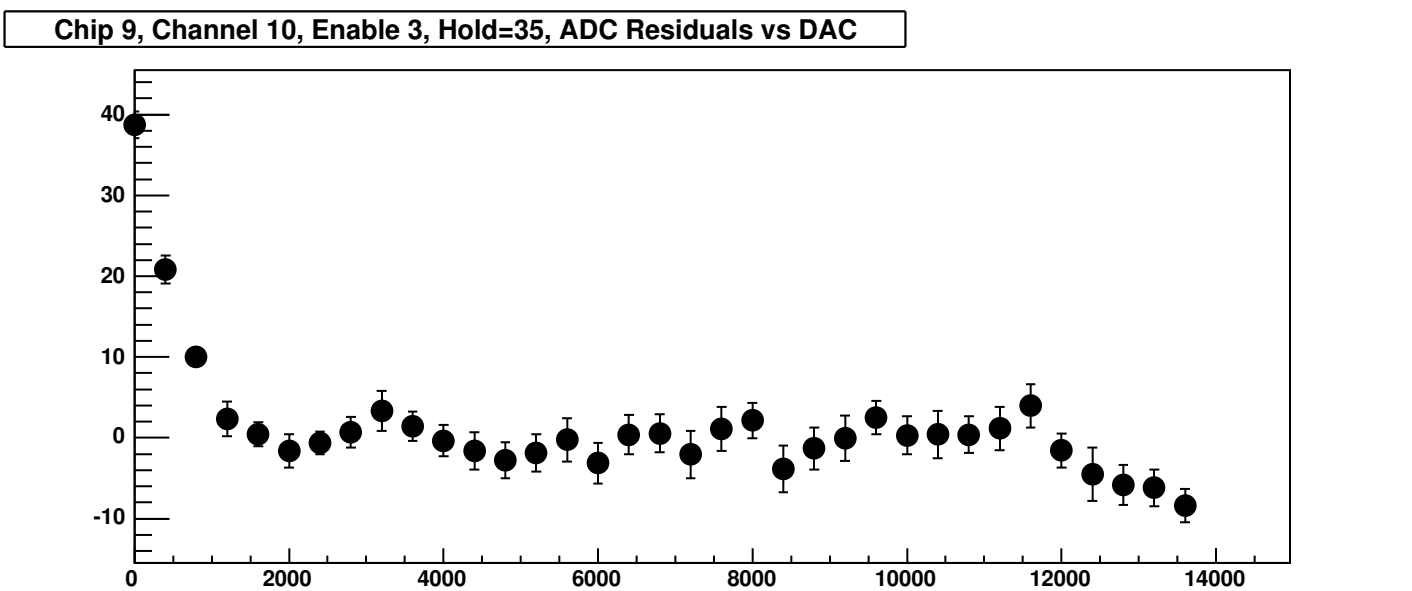

Chip 9, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 78.99 / 23
p0 -517.3 ± 0.6455
p1 0.004551 ± 0.0001165
-460
-470
-480
-490
-500
-510
-520
0
2000
4000
6000
8000
10000
12000
14000

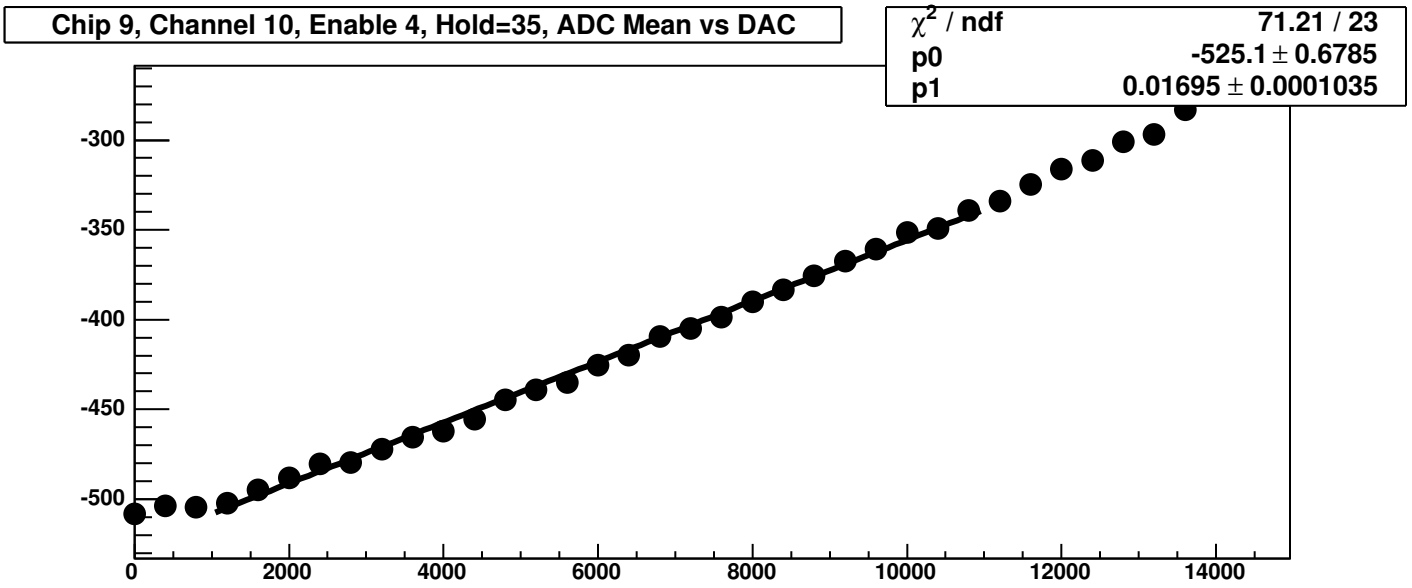

Chip 9, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

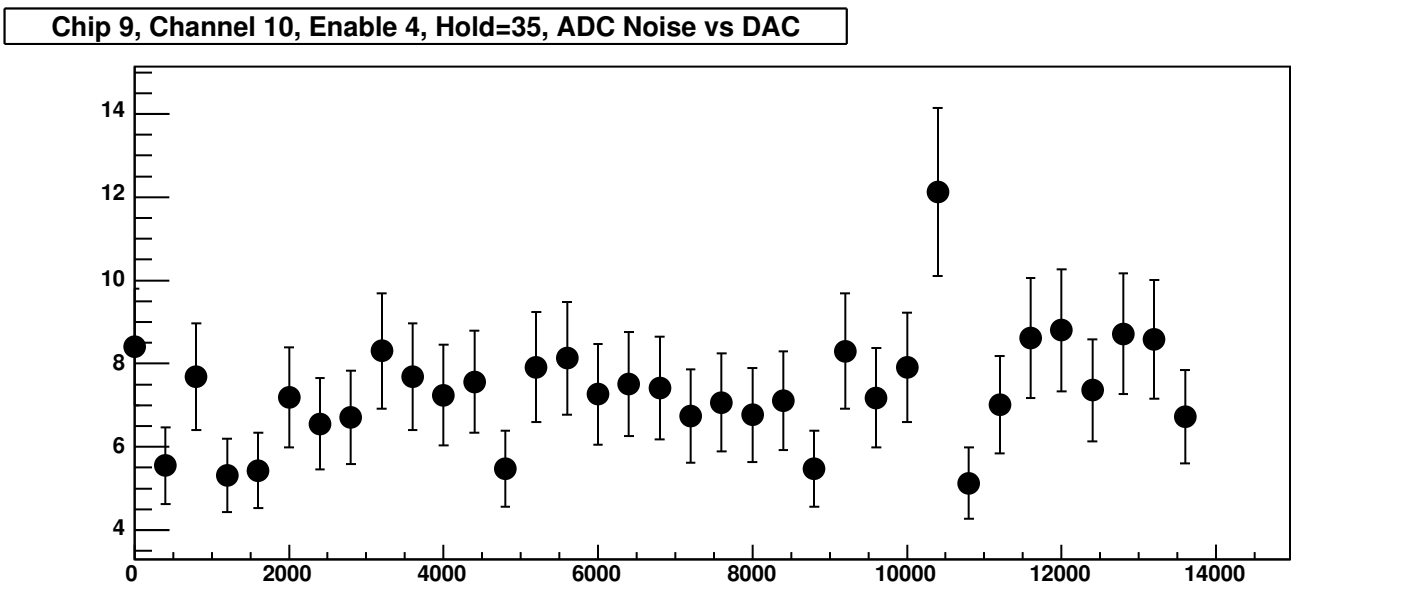

Chip 9, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

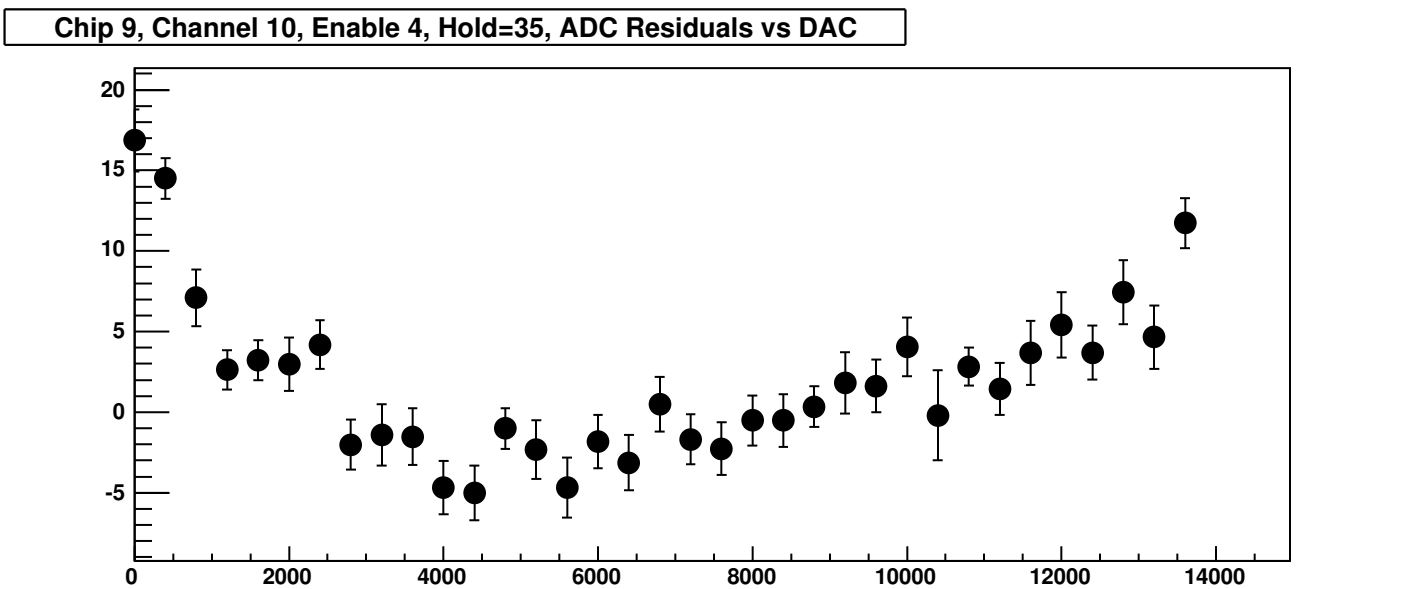

Chip 9, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 14.26 / 23
p0 -527.1 ± 0.8832
p1 0.04565 ± 0.0001468
100
0
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000

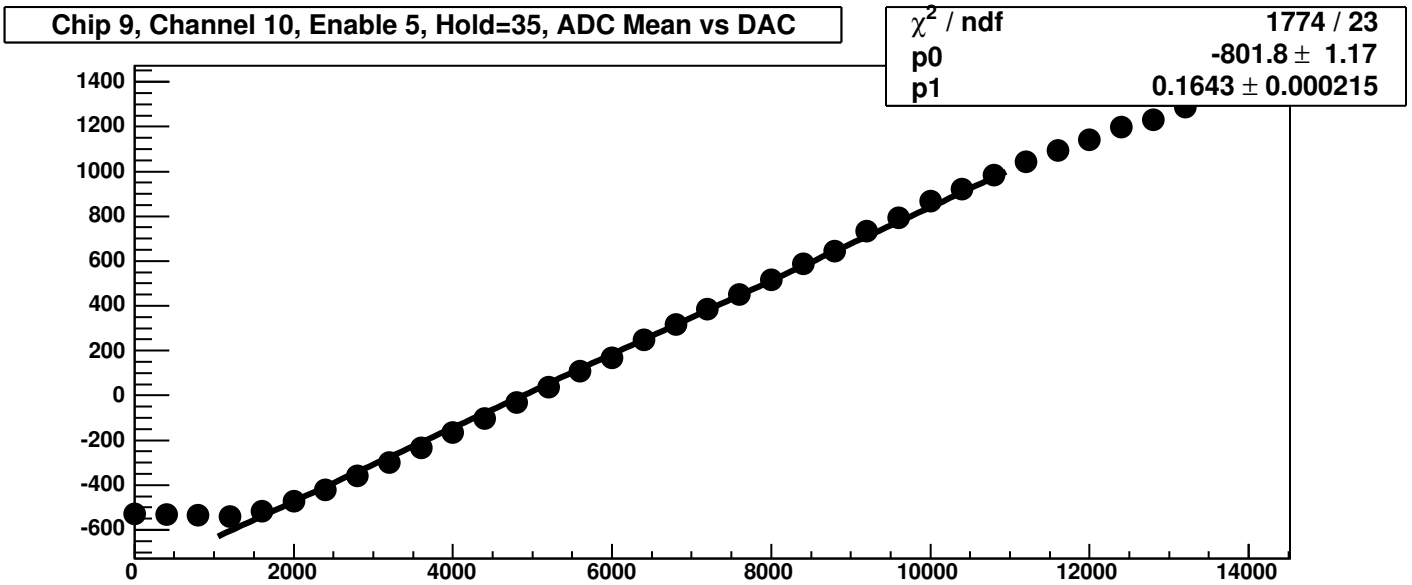

Chip 9, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

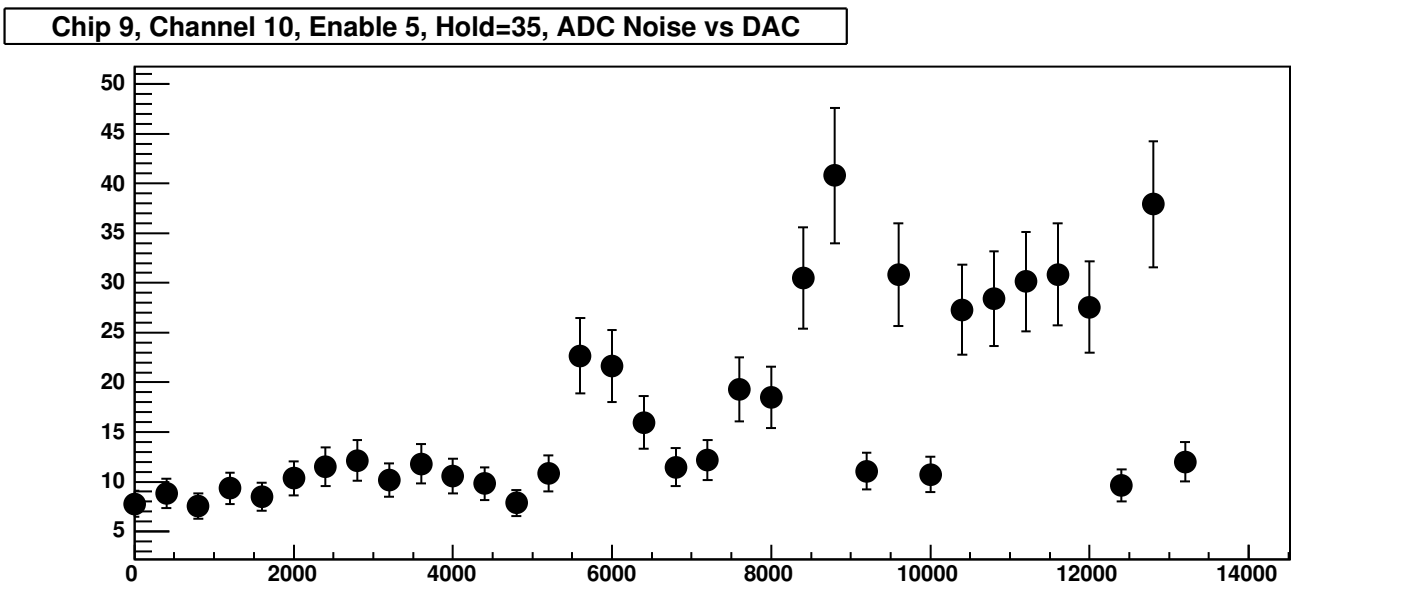

Chip 9, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
40
30
20
10
0
-10
0
2000
4000
6000
8000
10000
12000
14000

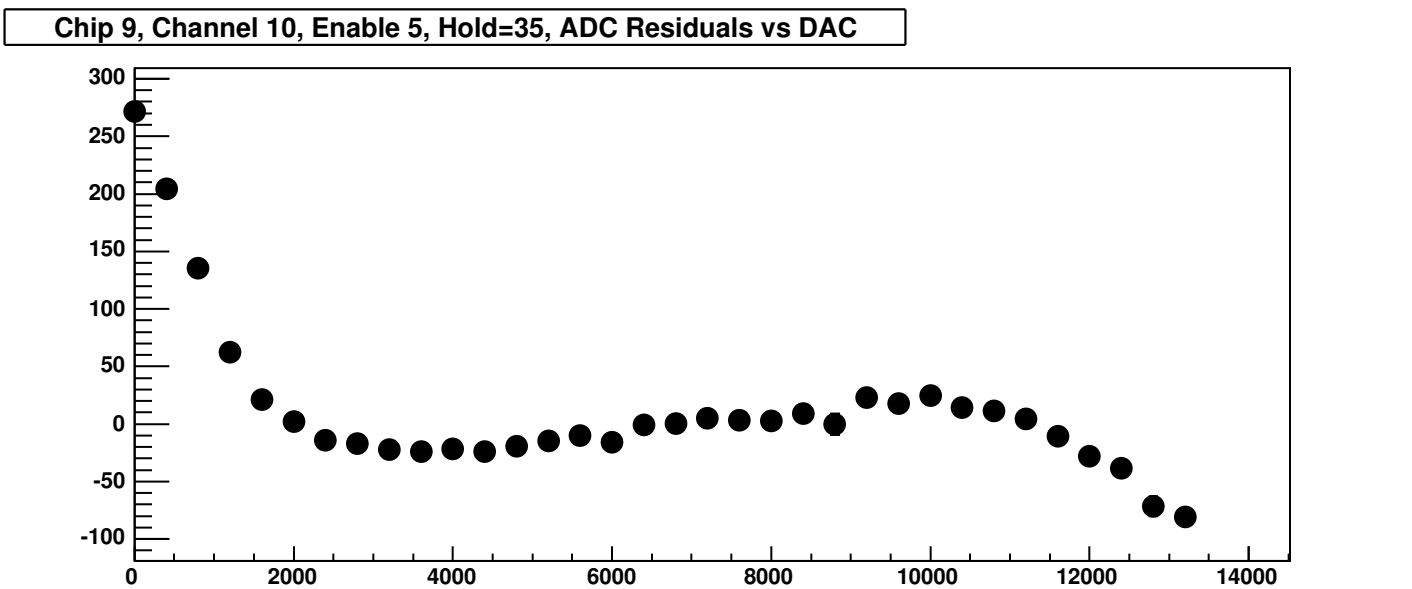

Chip 9, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 71.21 / 23
p0 -525.1 ± 0.6785
p1 0.01695 ± 0.0001035
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

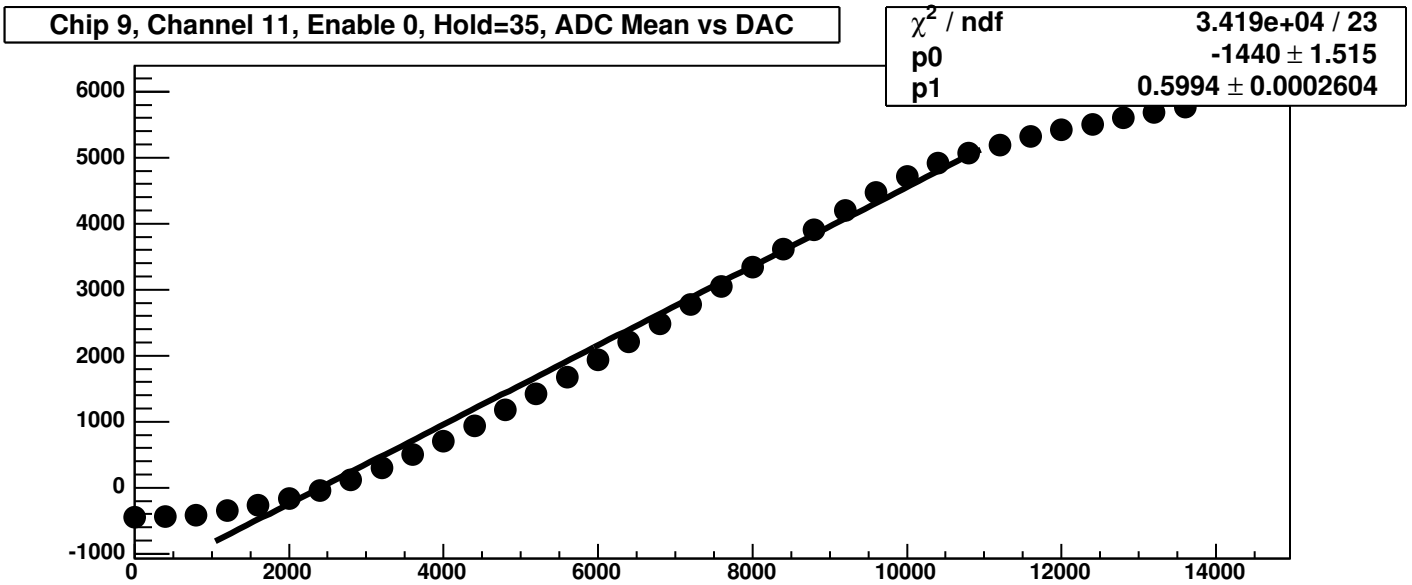

Chip 9, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

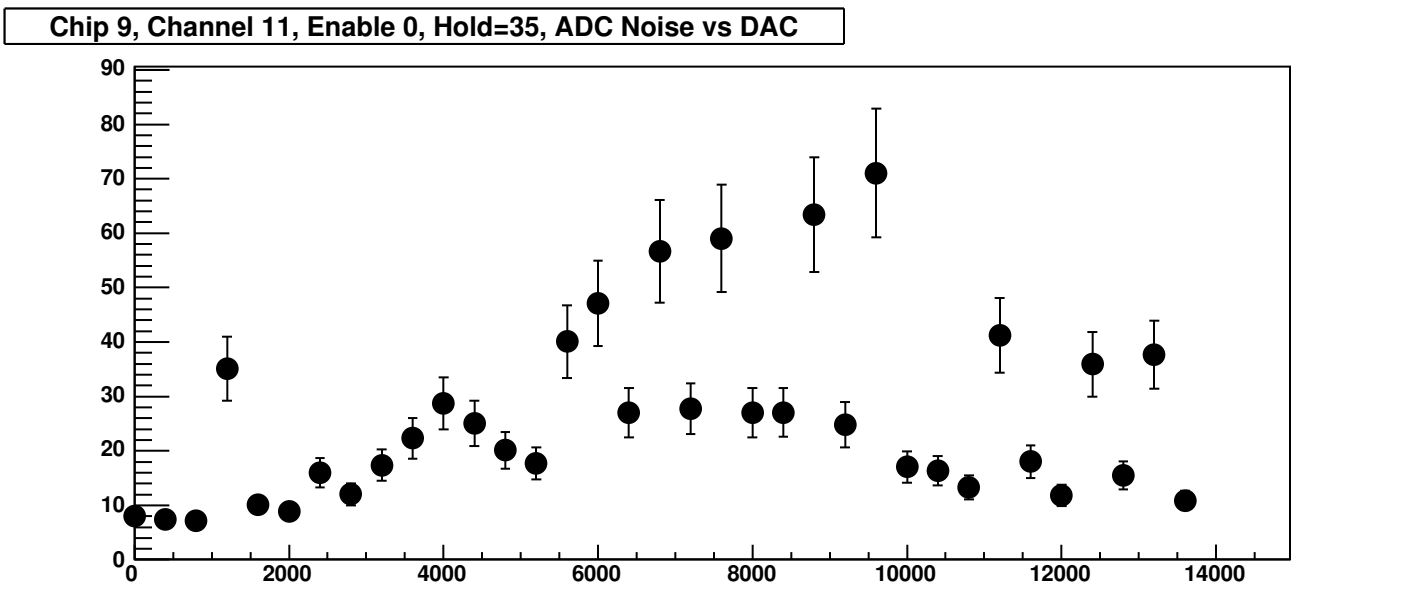

Chip 9, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

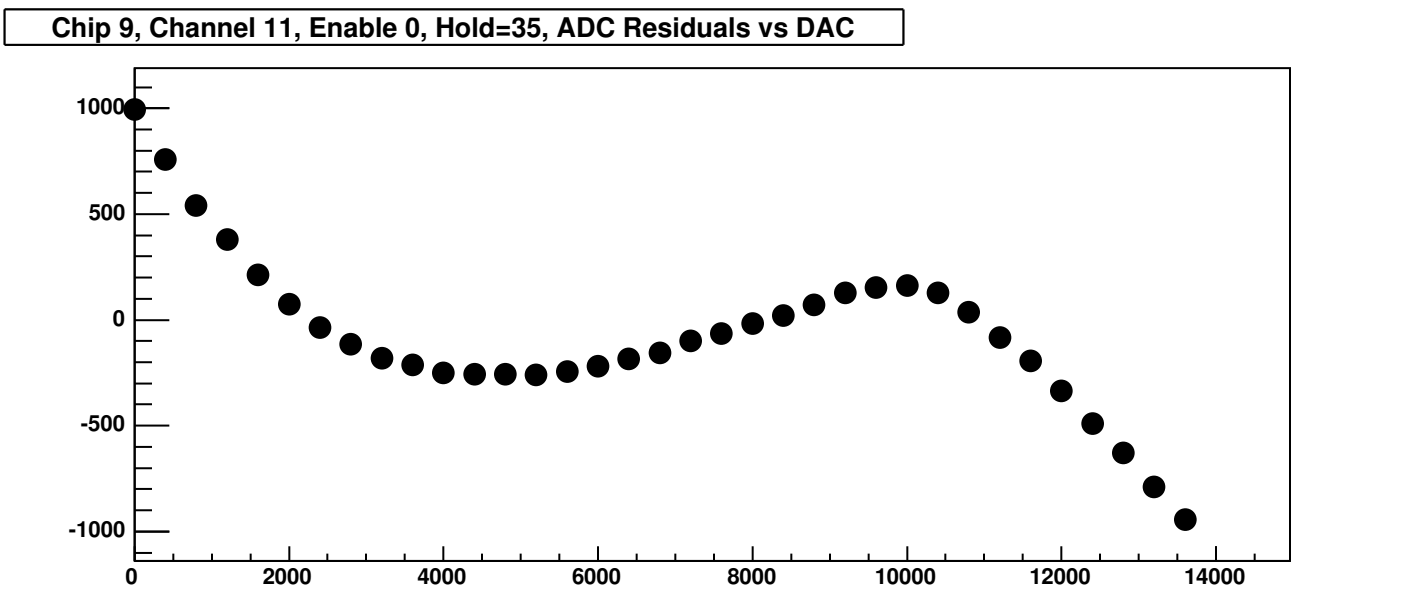

Chip 9, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1774 / 23
p0 -801.8 ± 1.17
p1 0.1643 ± 0.000215
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

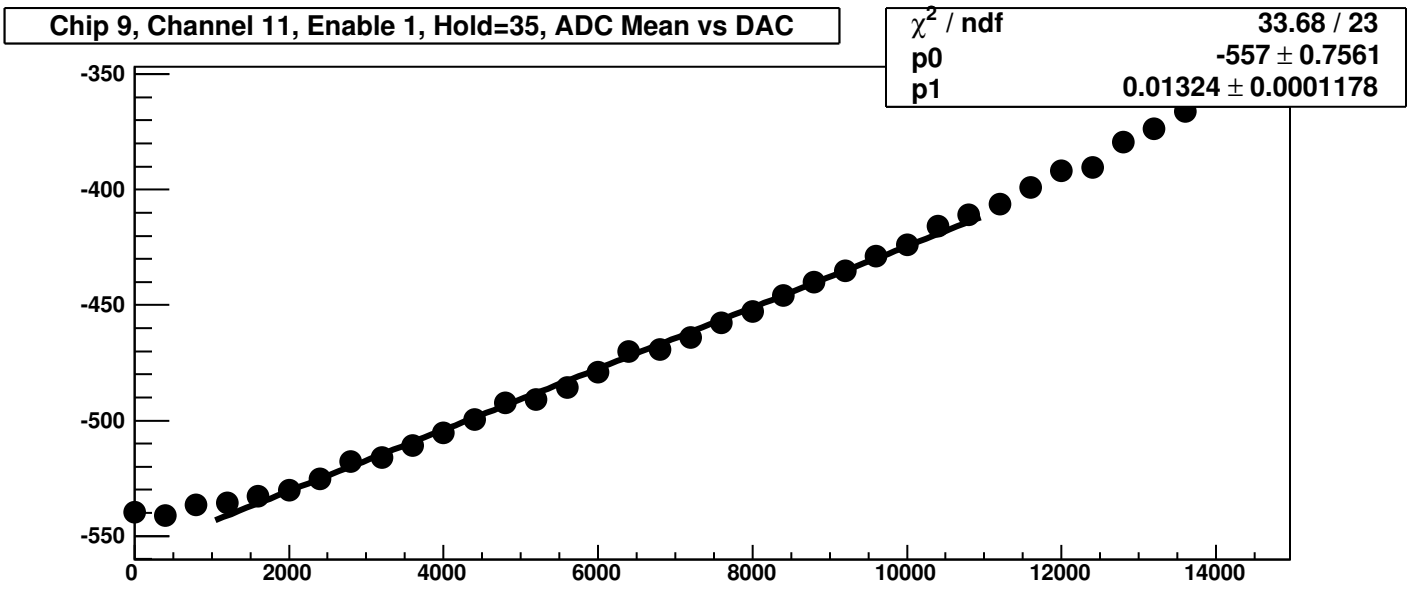

Chip 9, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

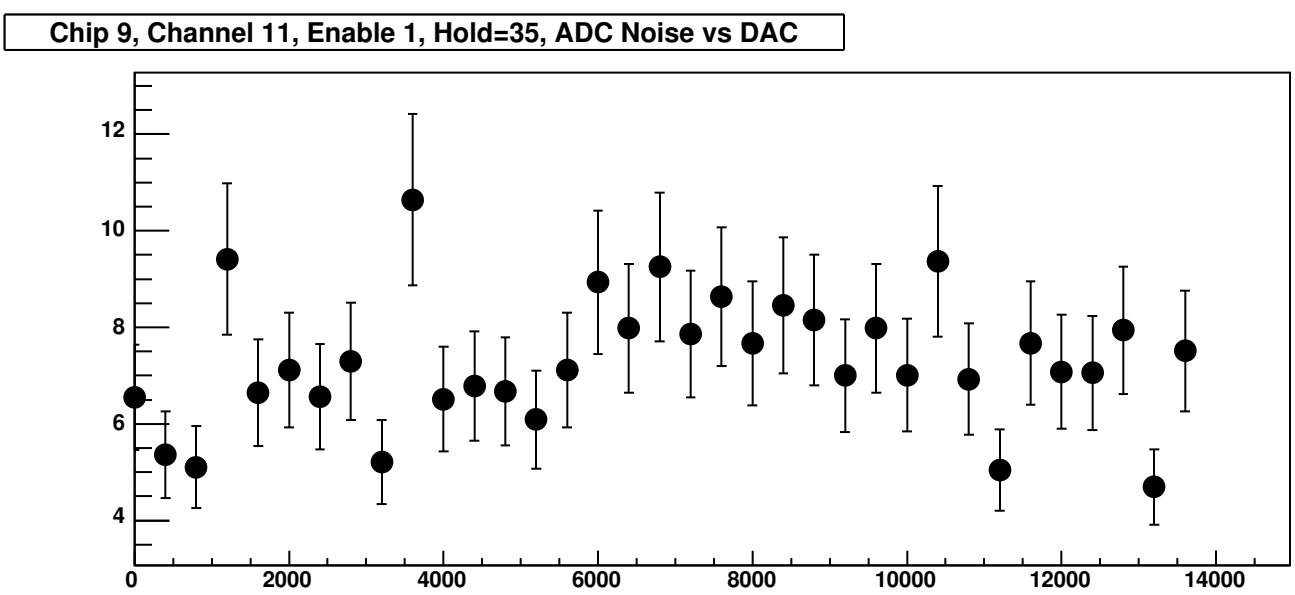

Chip 9, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

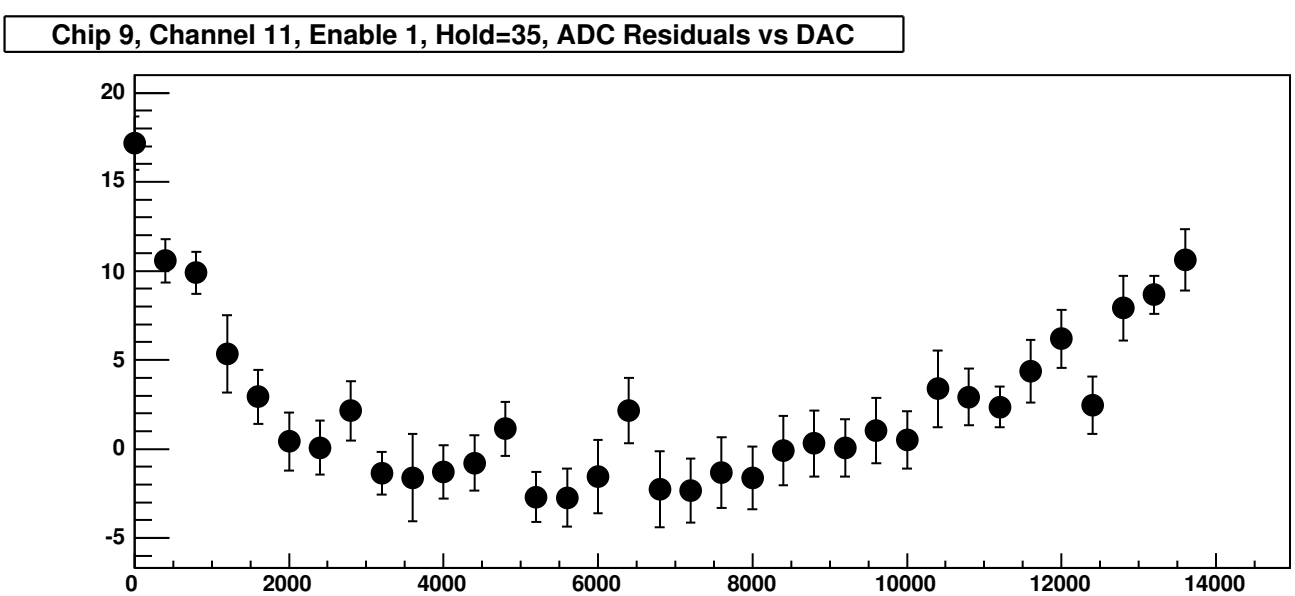

Chip 9, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.419e+04 / 23
p0 -1440 ± 1.515
p1 0.5994 ± 0.0002604
6000
5000
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

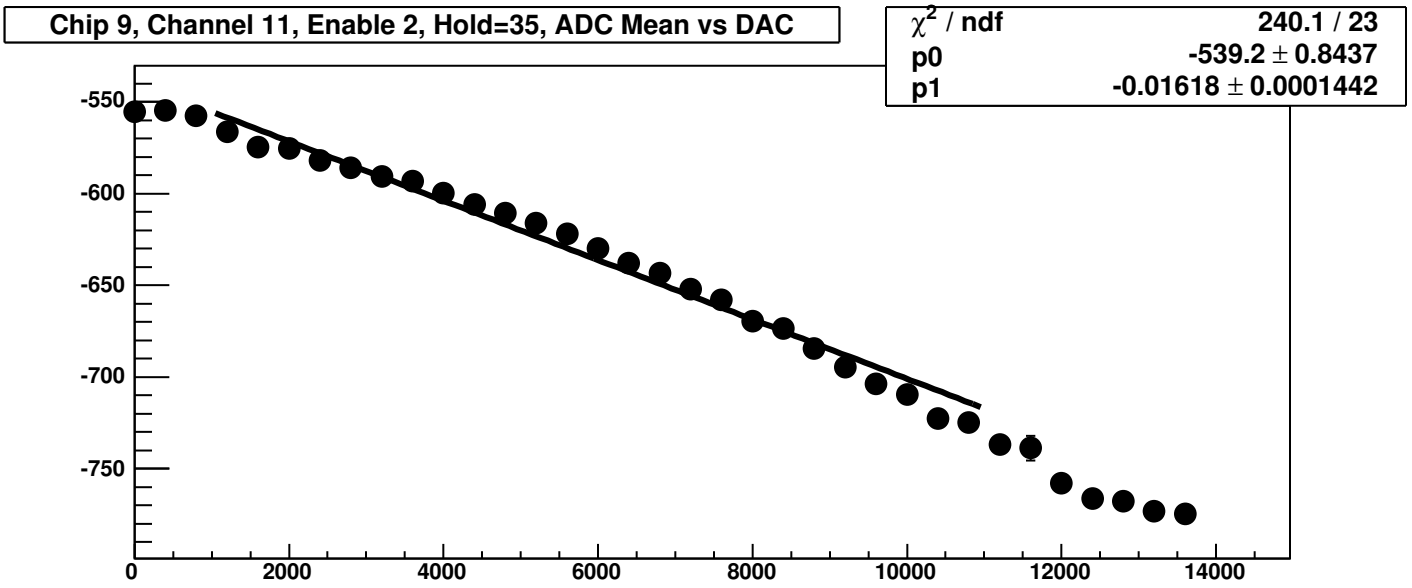

Chip 9, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

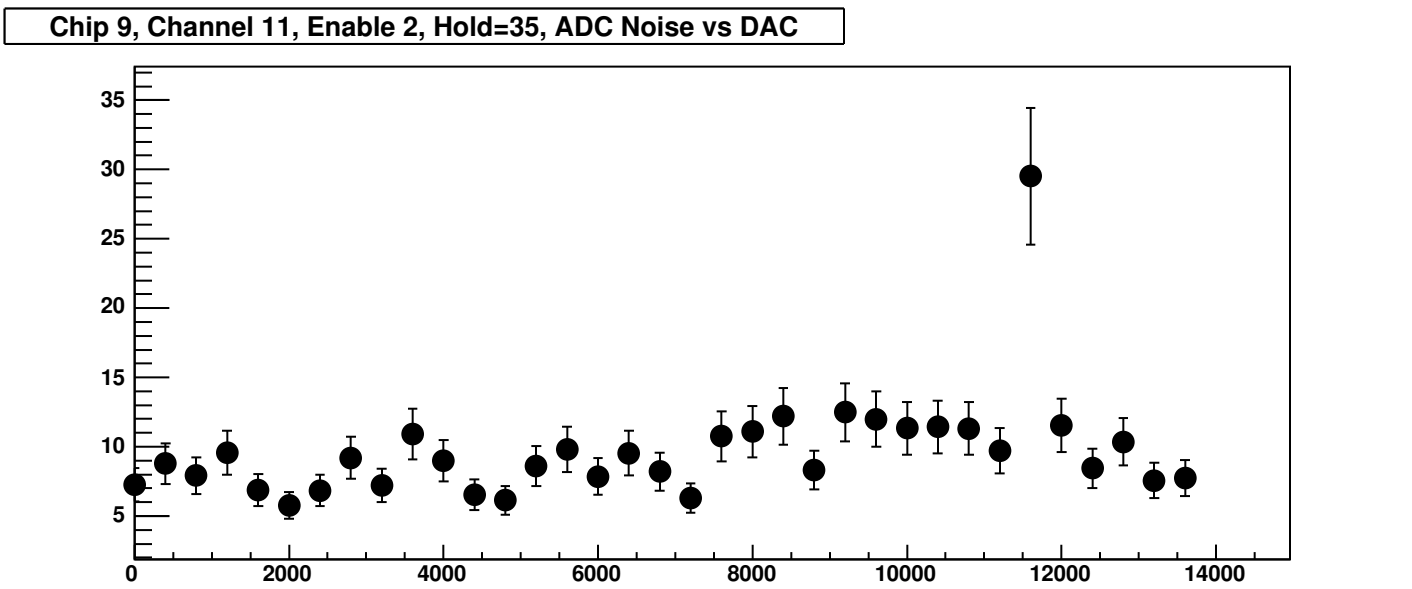

Chip 9, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

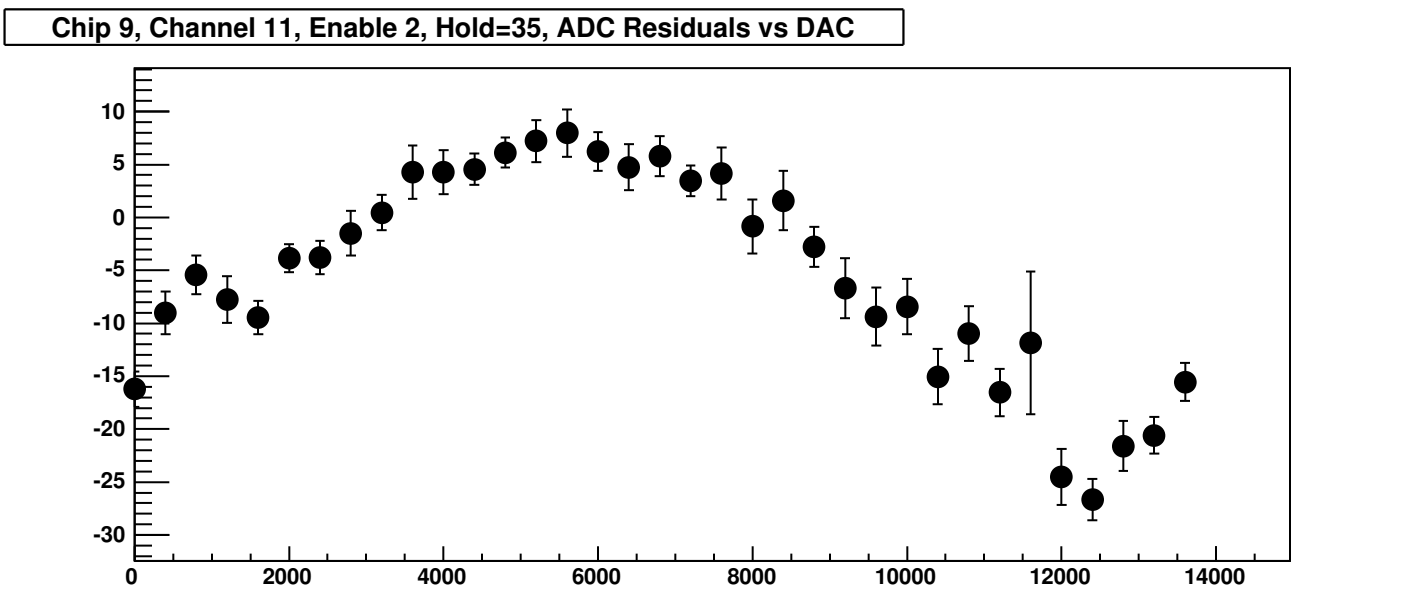

Chip 9, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.68 / 23
p0 -557 ± 0.7561
p1 0.01324 ± 0.0001178
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

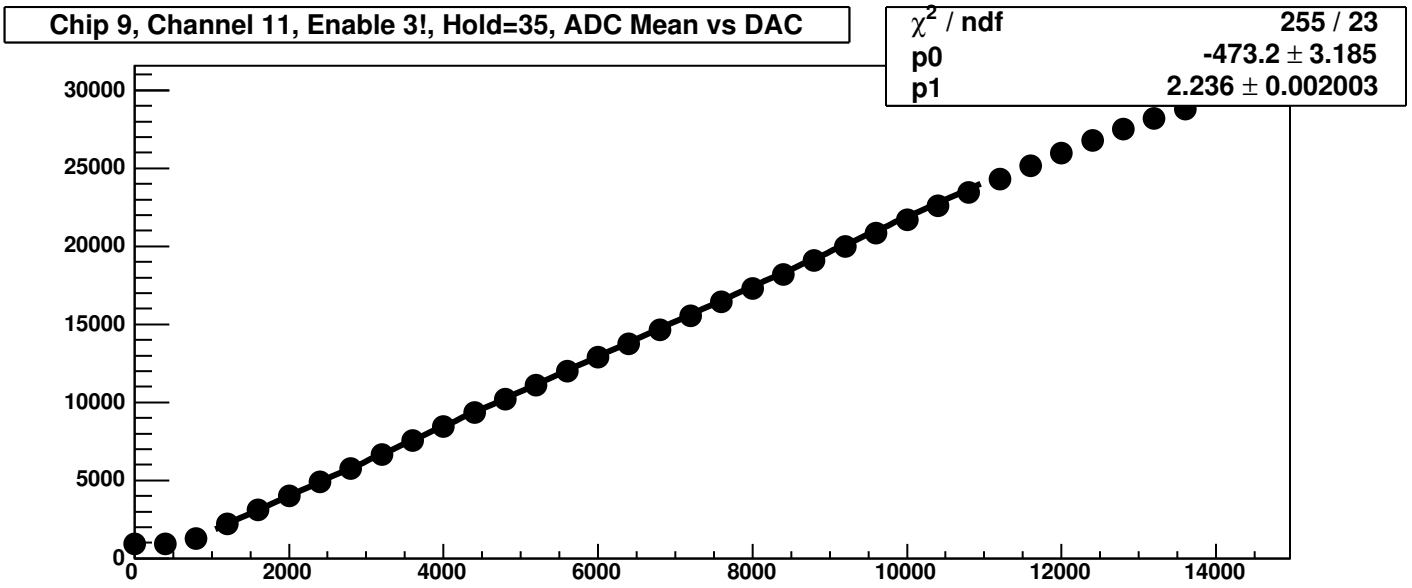

Chip 9, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

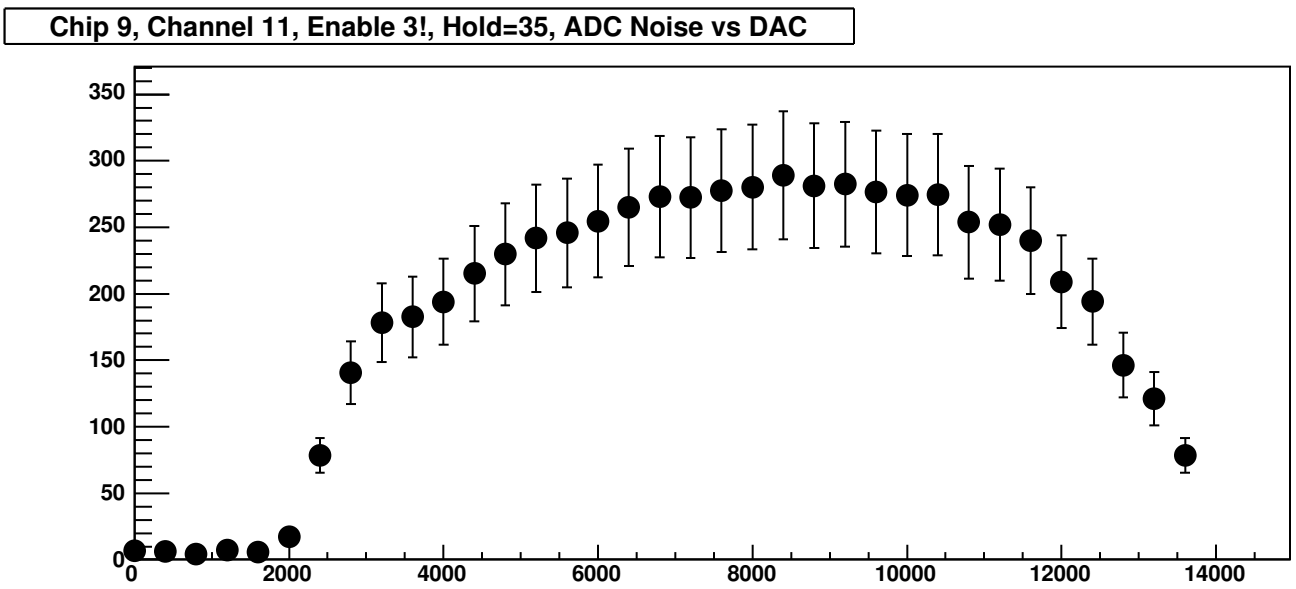

Chip 9, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

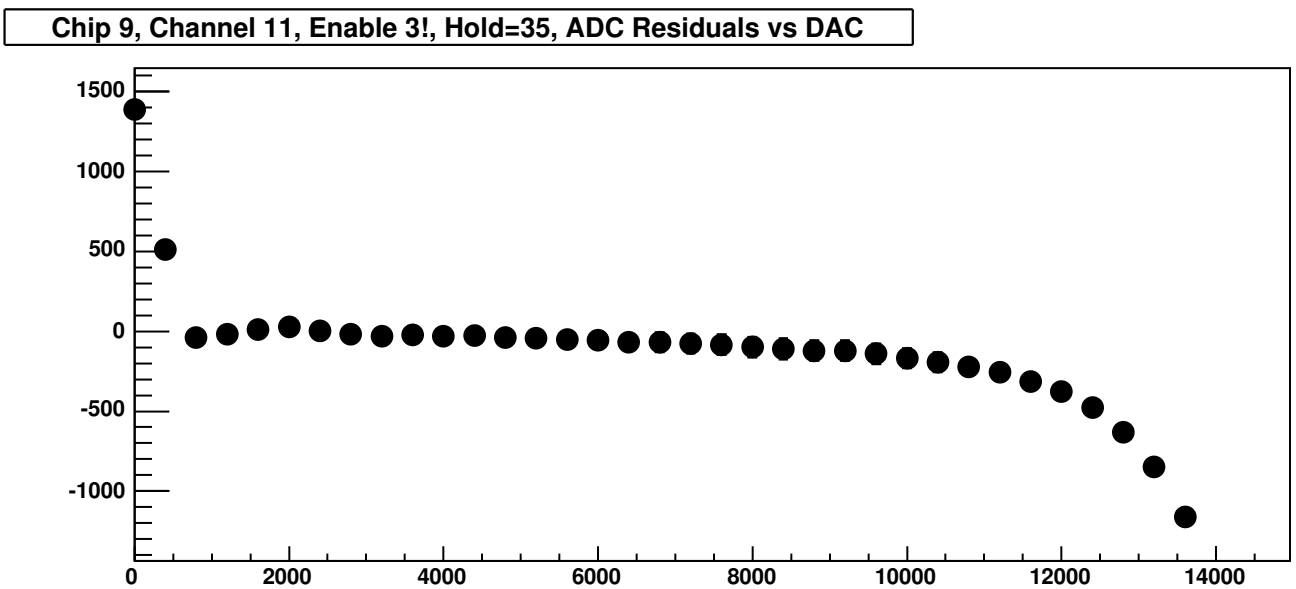

Chip 9, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 240.1 / 23
p0 -539.2 ± 0.8437
p1 -0.01618 ± 0.0001442
-550
-600
-650
-700
-750
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

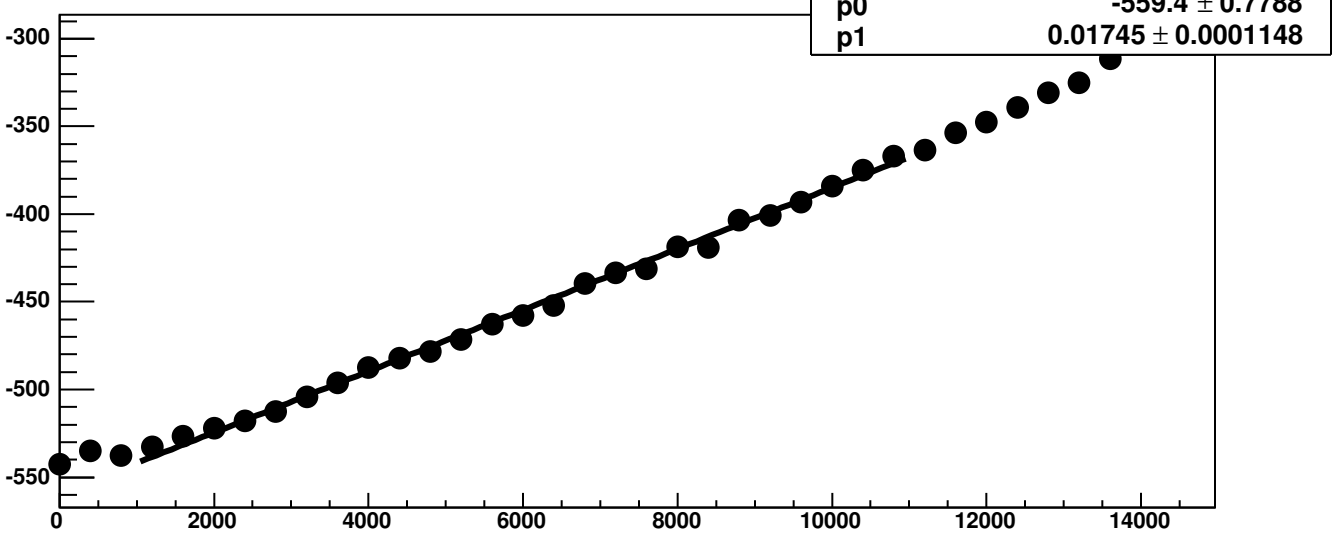

Chip 9, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 255 / 23
p0 -473.2 ± 3.185
p1 2.236 ± 0.002003
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

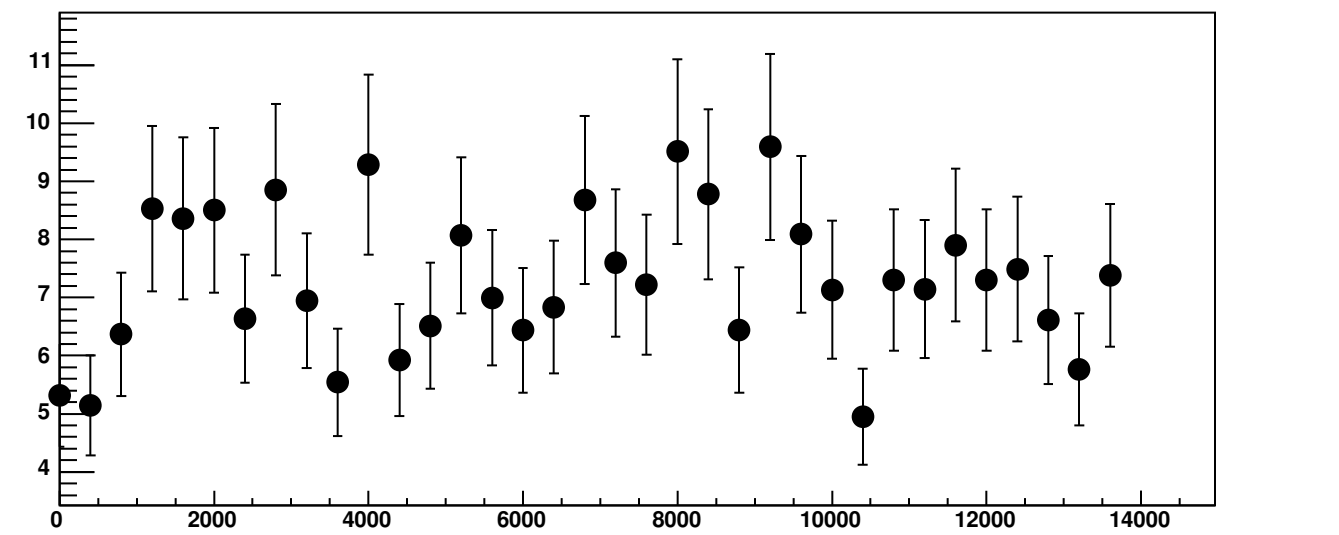

Chip 9, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

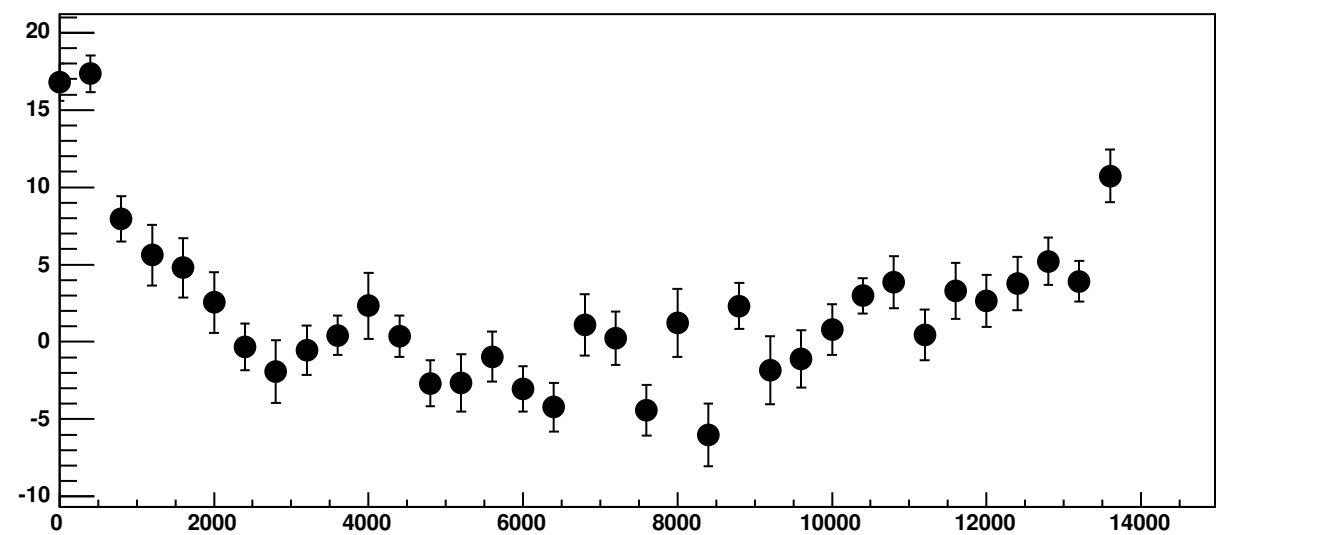
Chip 9, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

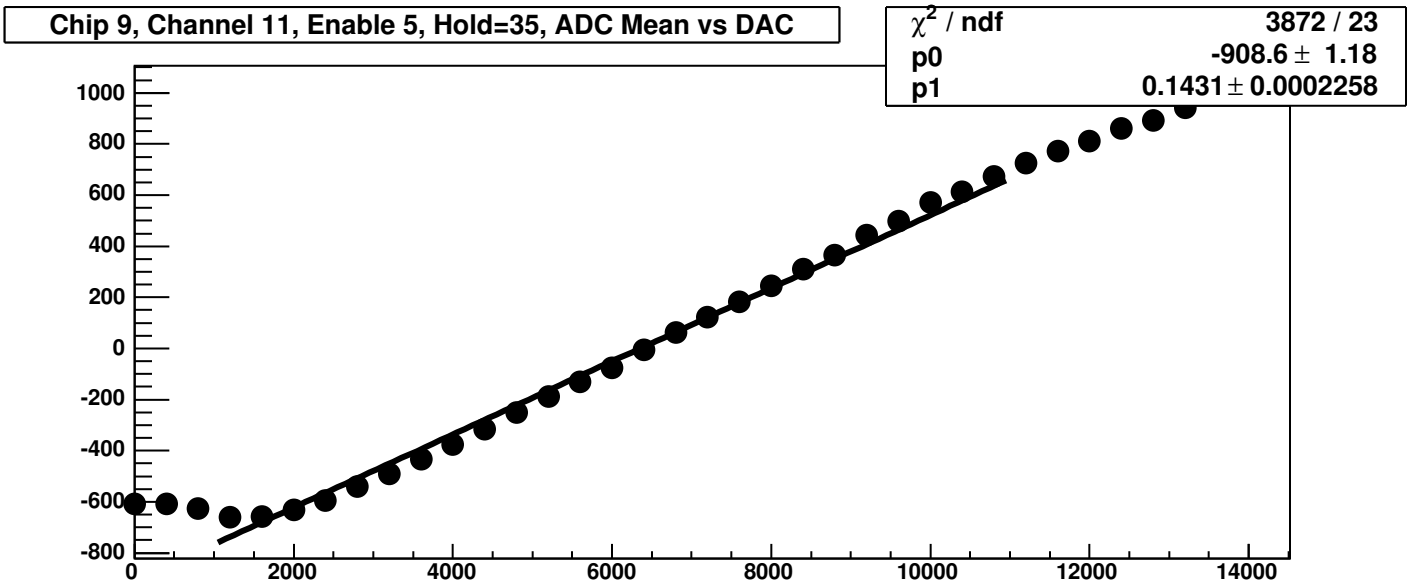


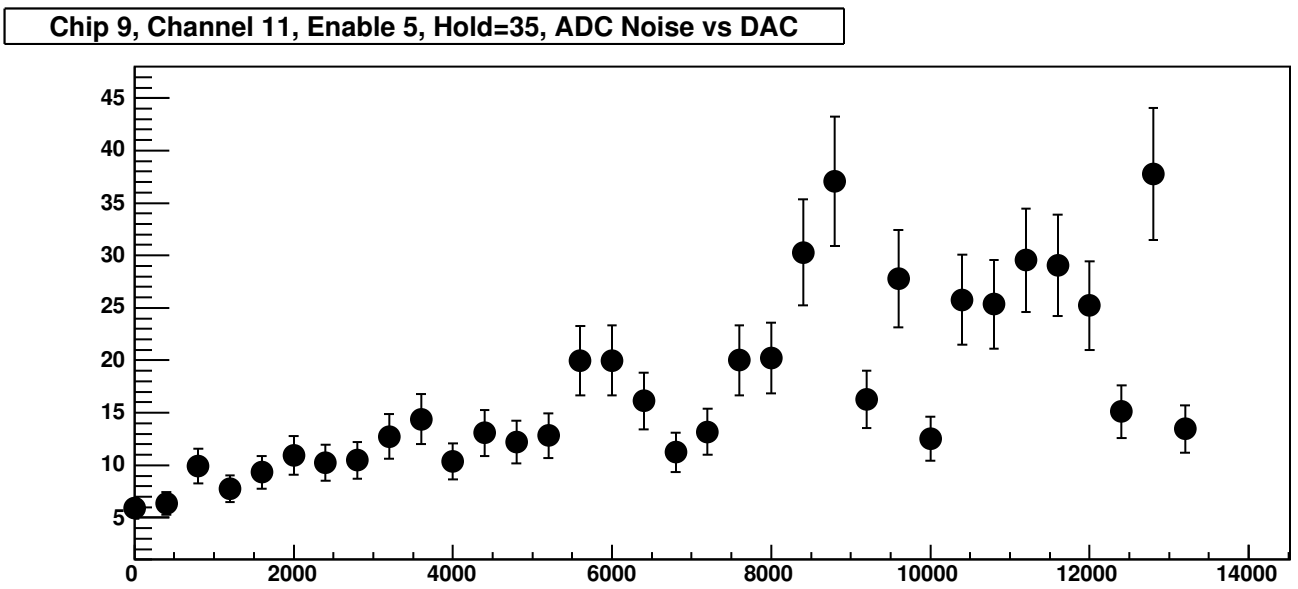
Chip 9, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

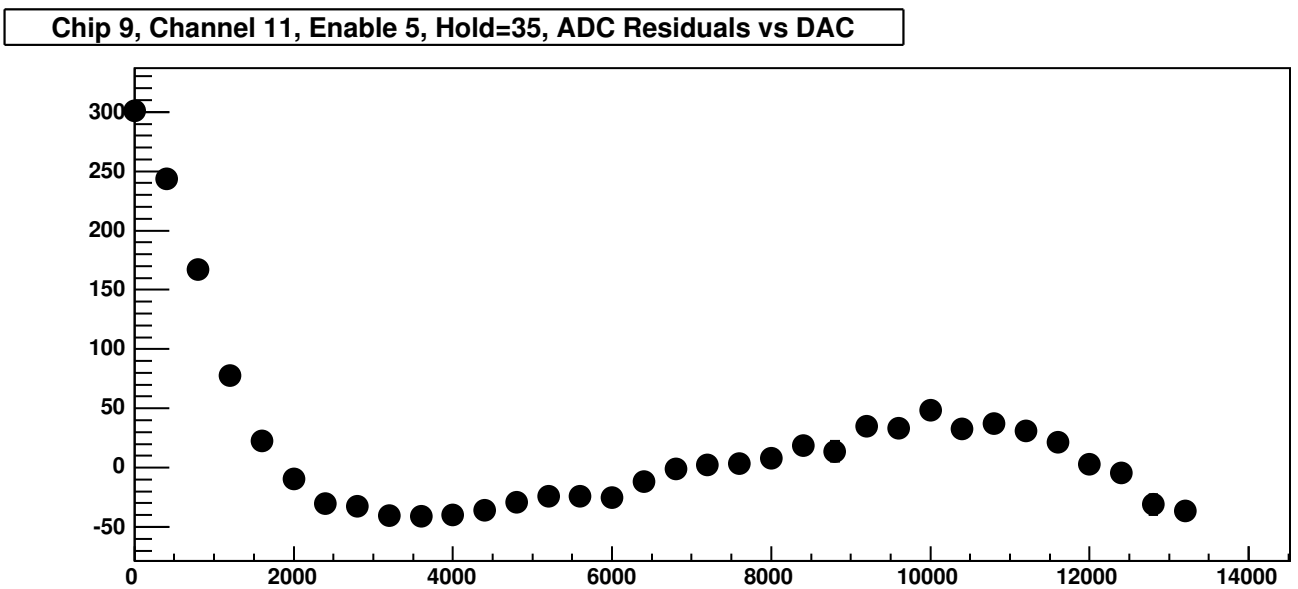


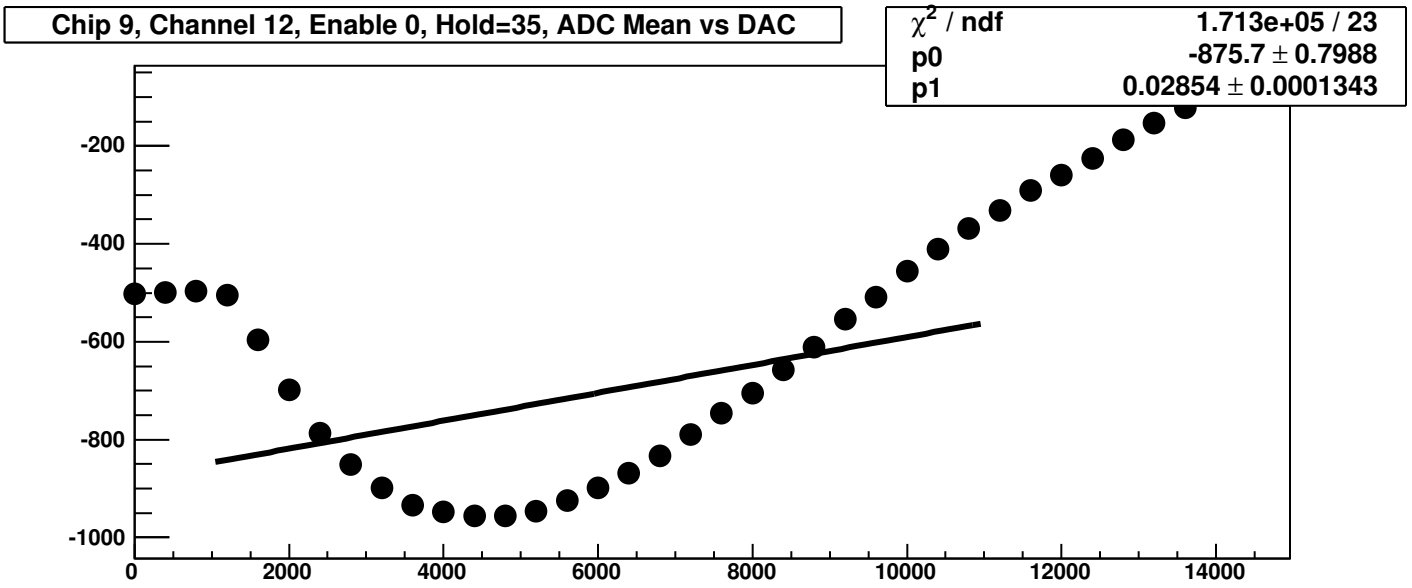
Chip 9, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

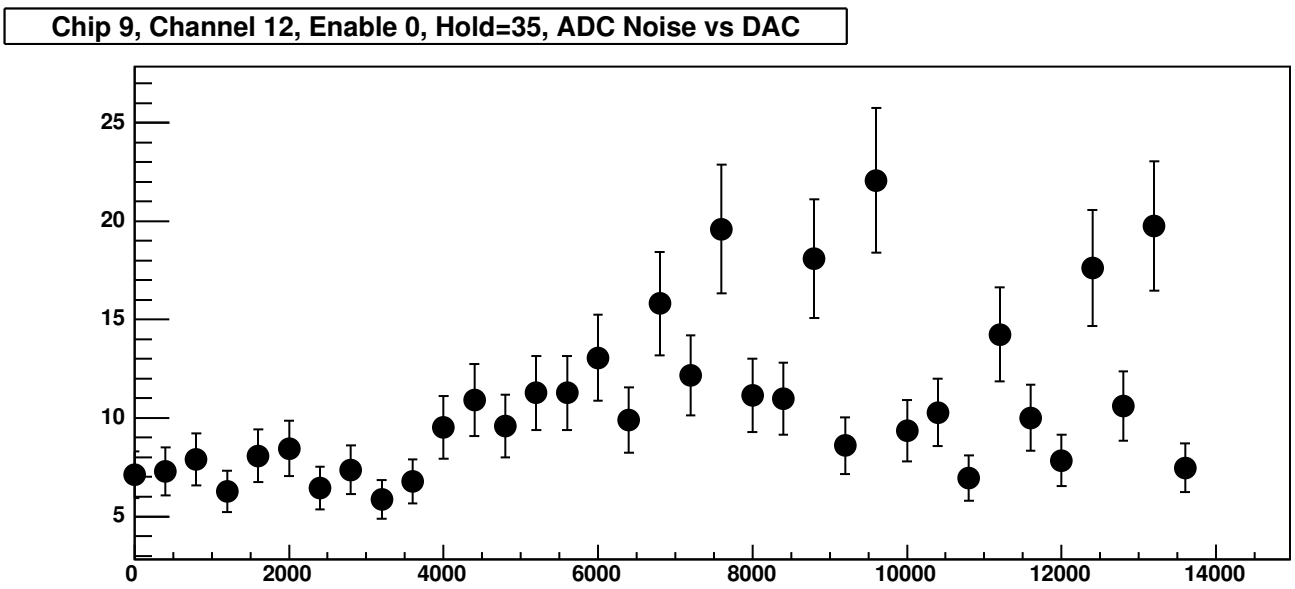


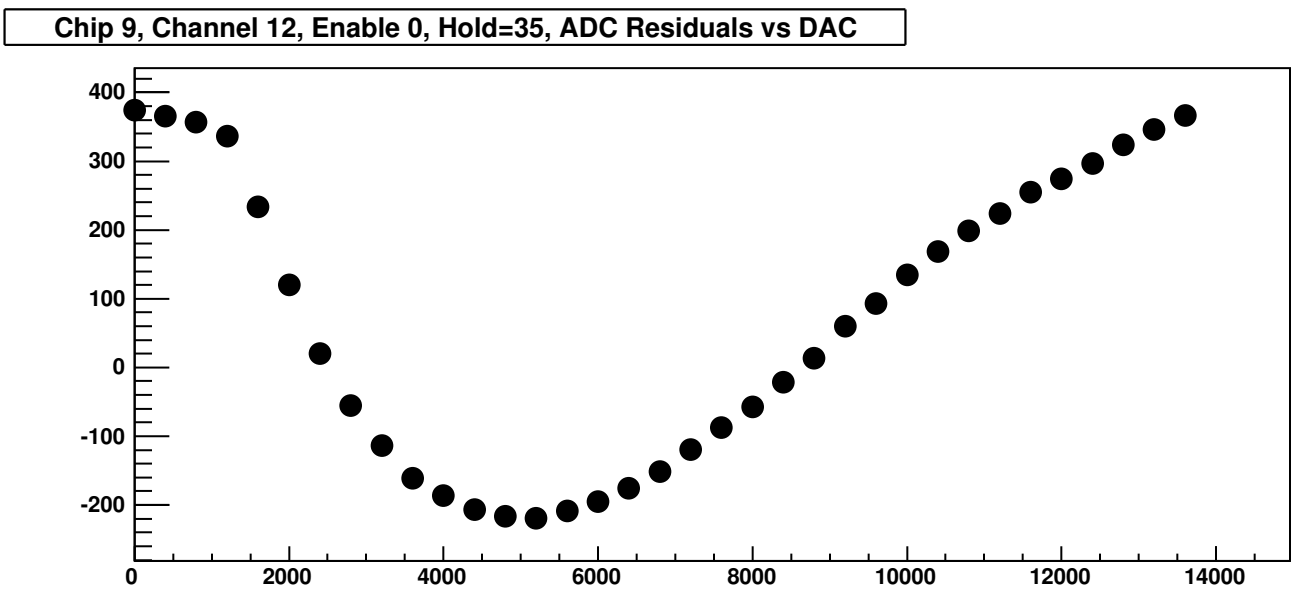

Chip 9, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3872 / 23
p0 -908.6 ± 1.18
p1 0.1431 ± 0.0002258
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

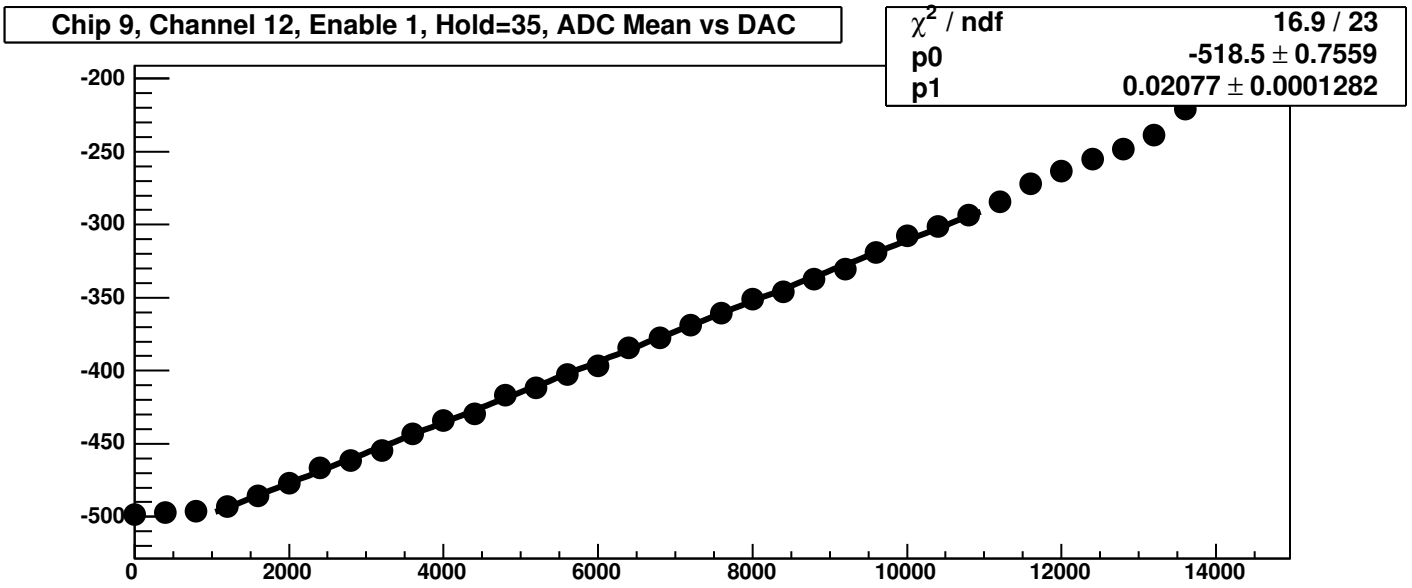

Chip 9, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

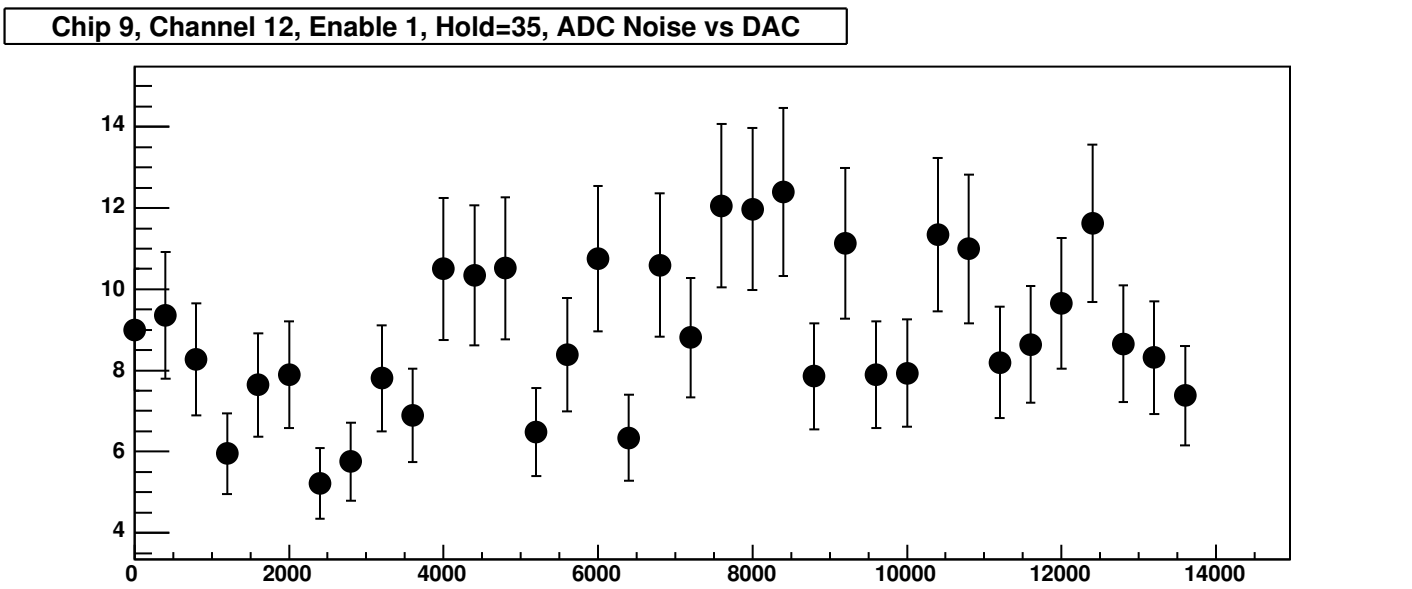

Chip 9, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

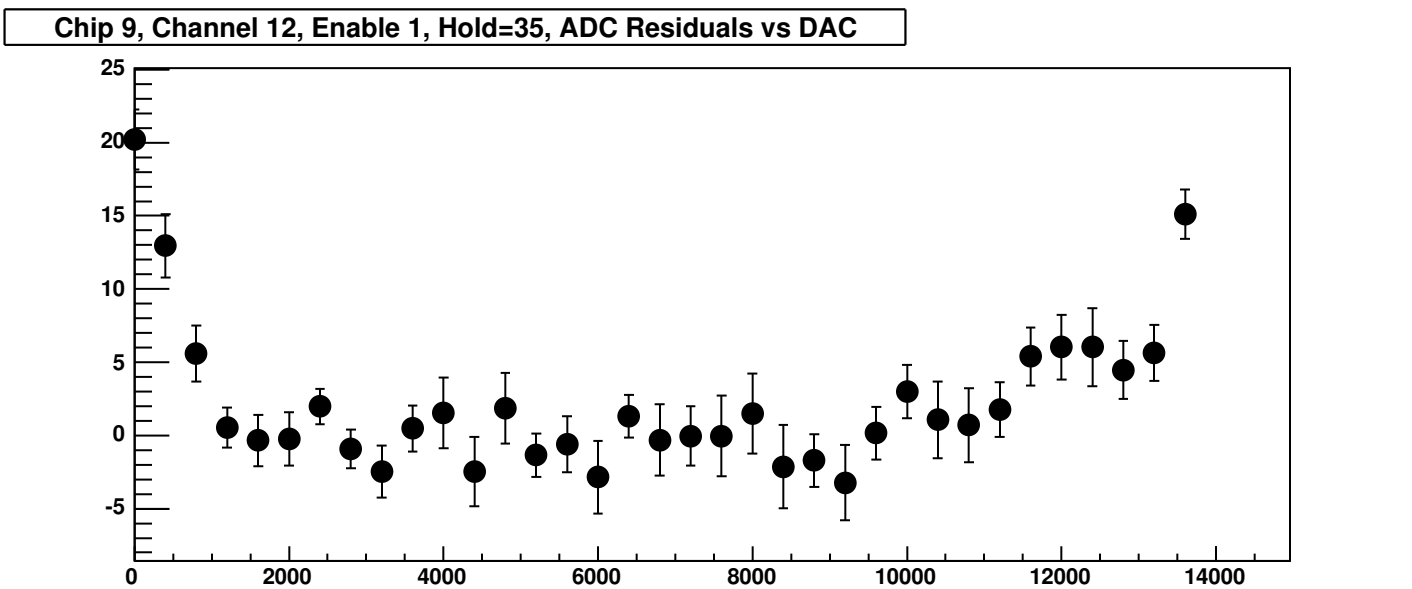

Chip 9, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.713e+05 / 23
p0 -875.7 ± 0.7988
p1 0.02854 ± 0.0001343
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

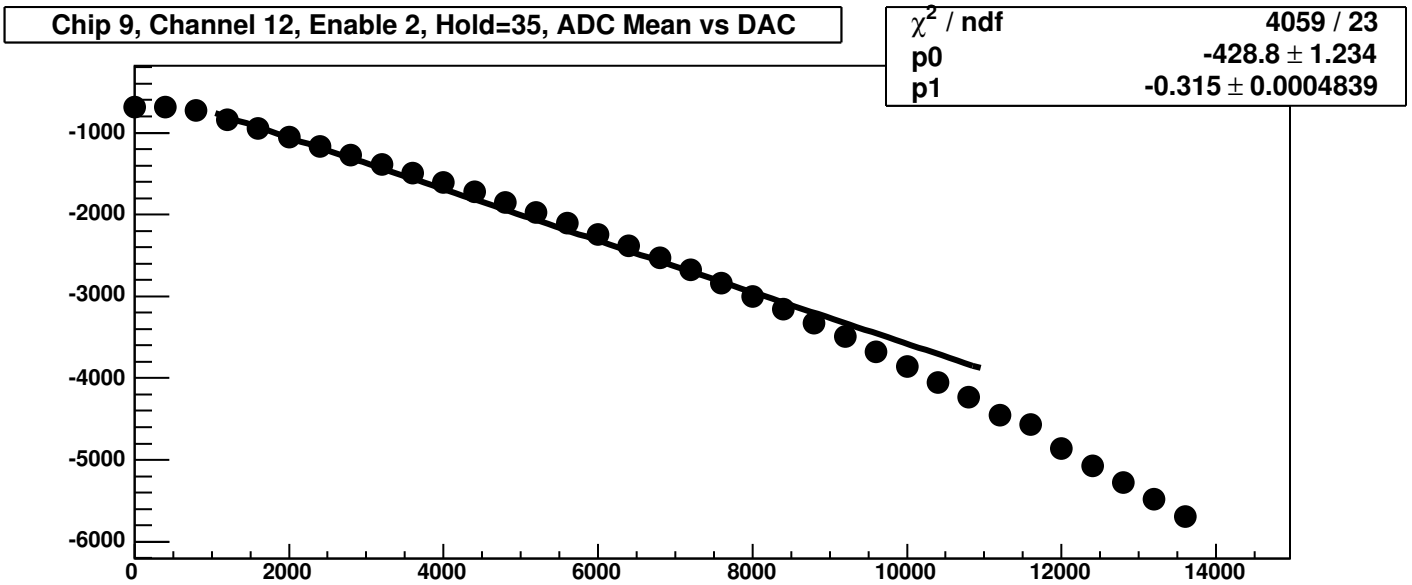

Chip 9, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

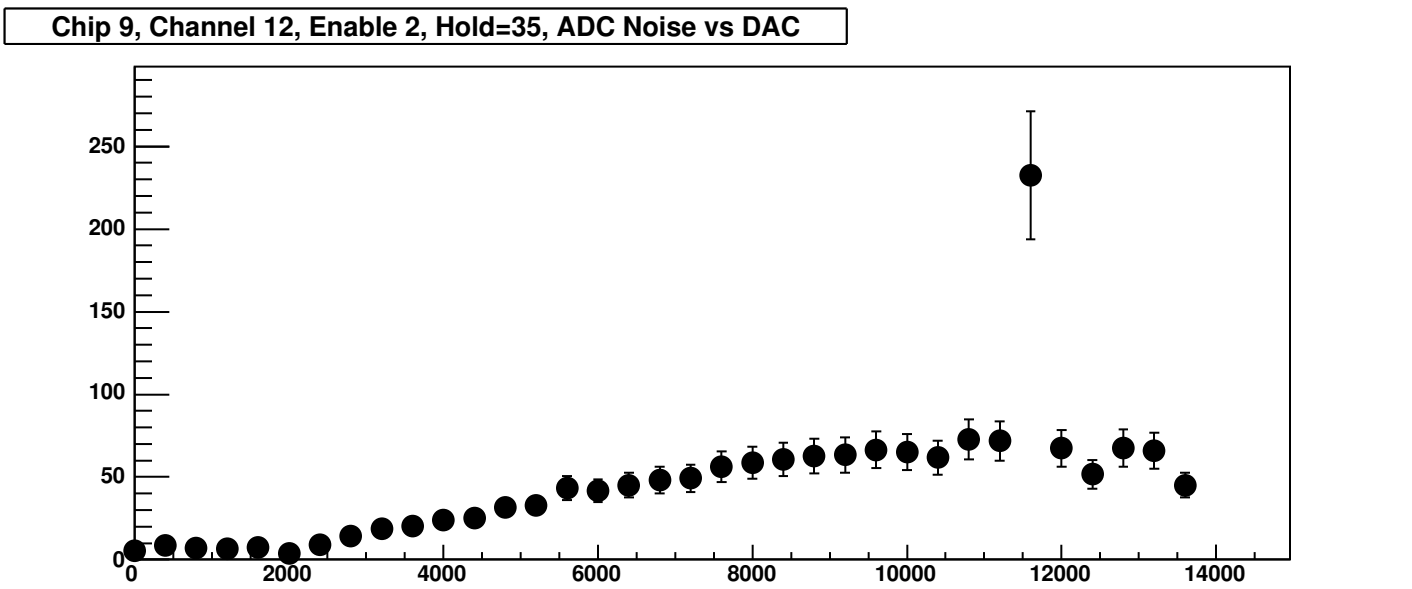

Chip 9, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

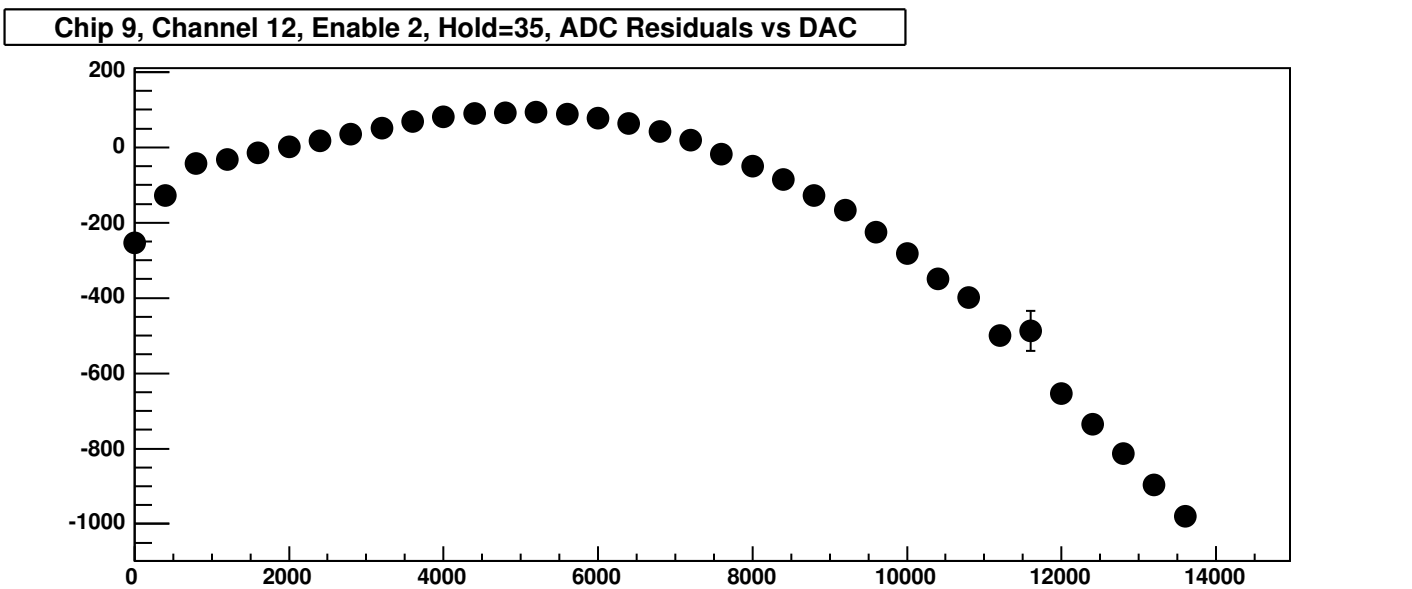

Chip 9, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 16.9 / 23
p0 -518.5 ± 0.7559
p1 0.02077 ± 0.0001282
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

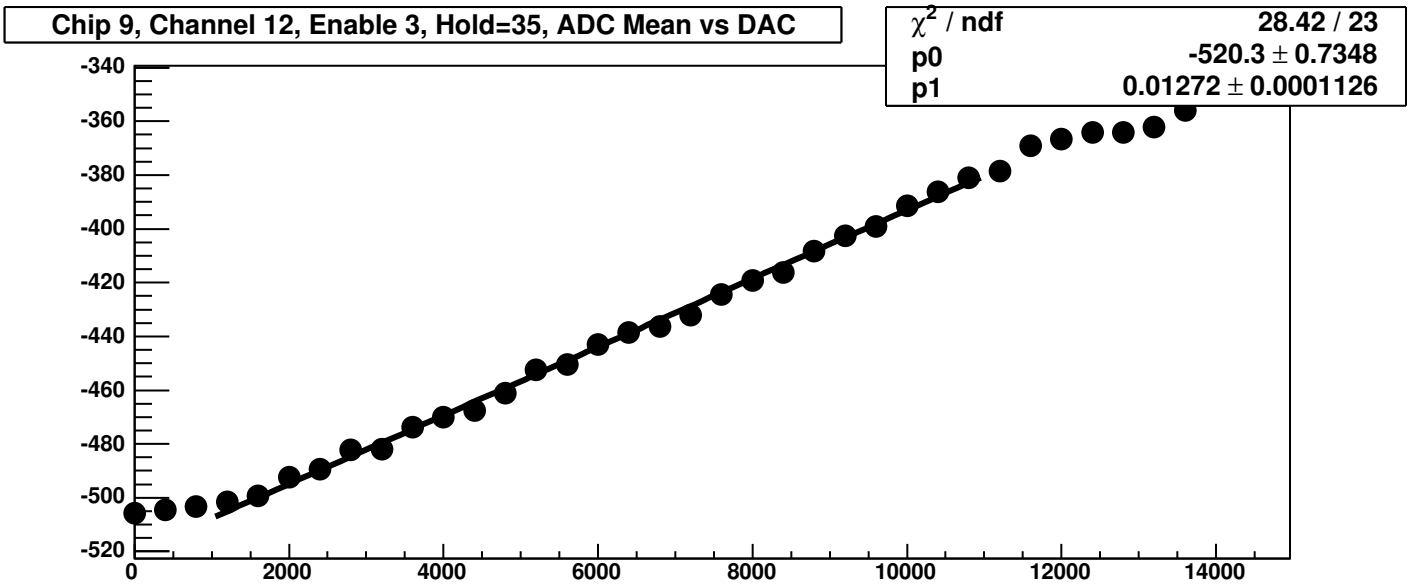

Chip 9, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

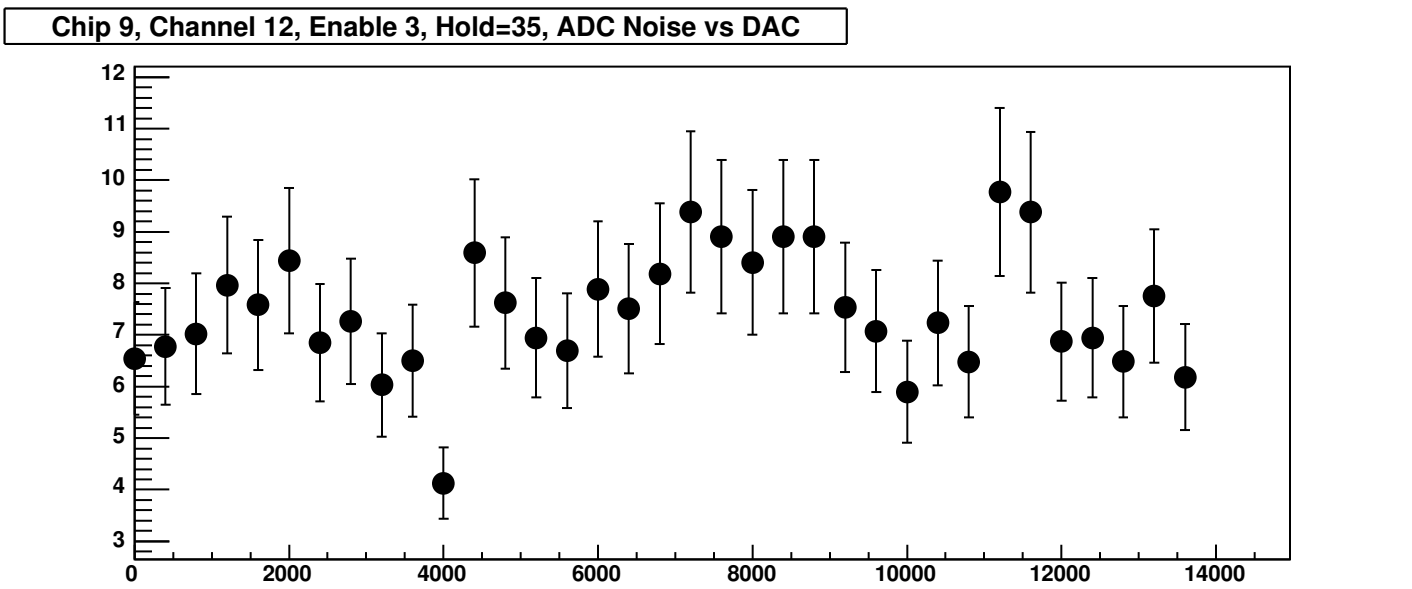

Chip 9, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

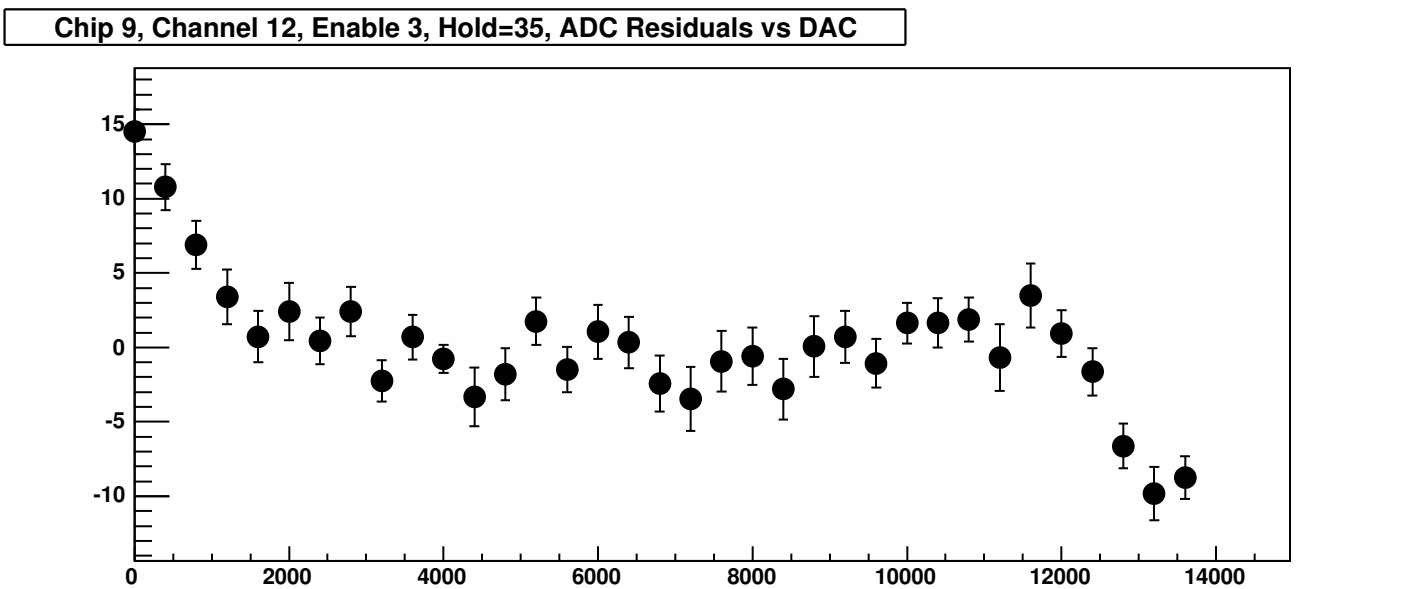

Chip 9, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 4059 / 23
p0 -428.8 ± 1.234
p1 -0.315 ± 0.0004839
-1000
-2000
-3000
-4000
-5000
-6000
0
2000
4000
6000
8000
10000
12000
14000

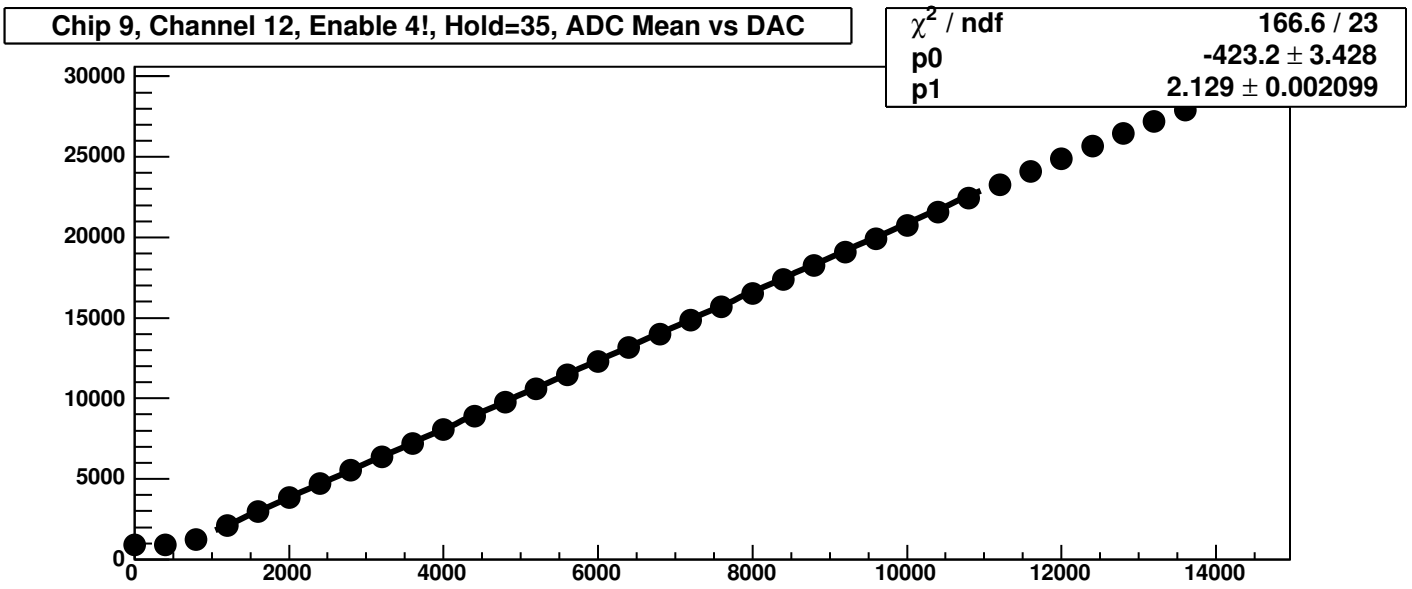

Chip 9, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

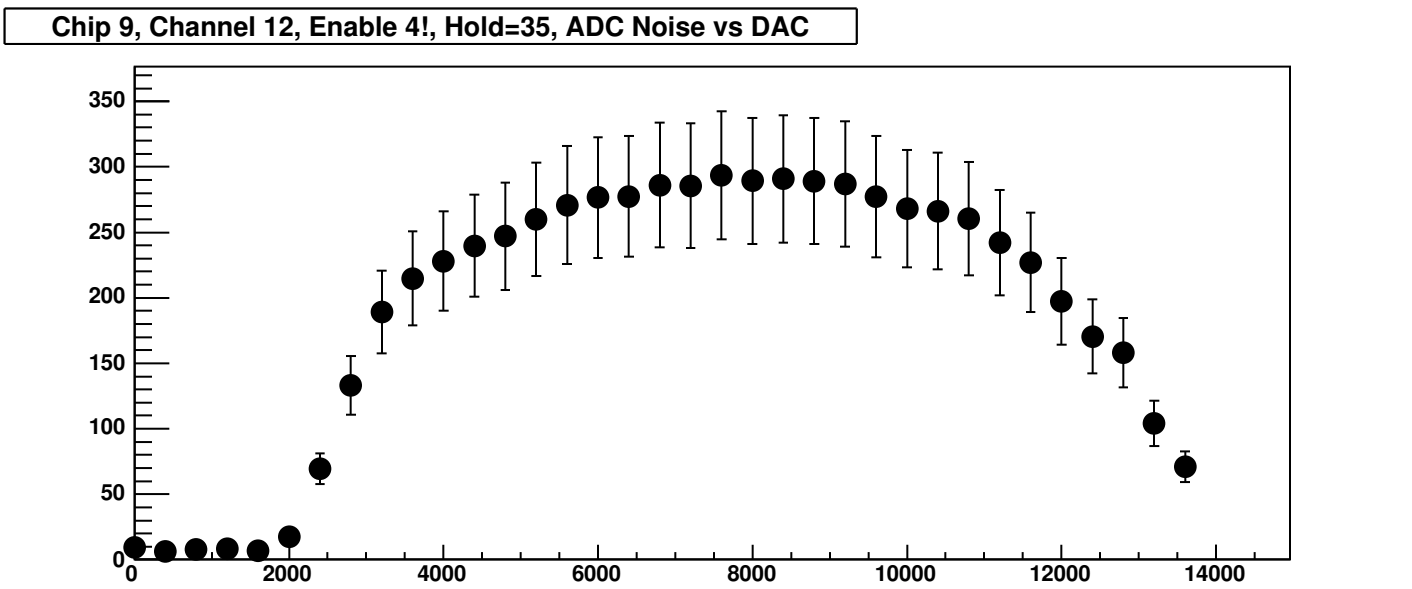

Chip 9, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

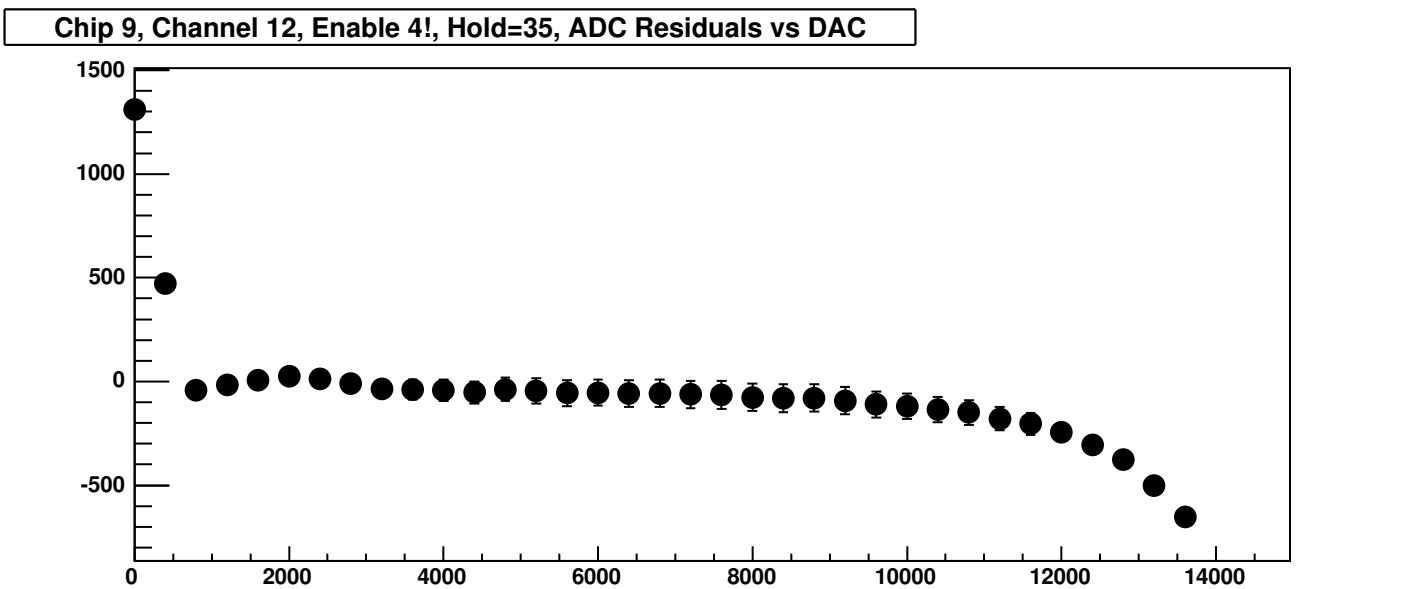

Chip 9, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.42 / 23
p0 -520.3 ± 0.7348
p1 0.01272 ± 0.0001126
-340
-360
-380
-400
-420
-440
-460
-480
-500
-520
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

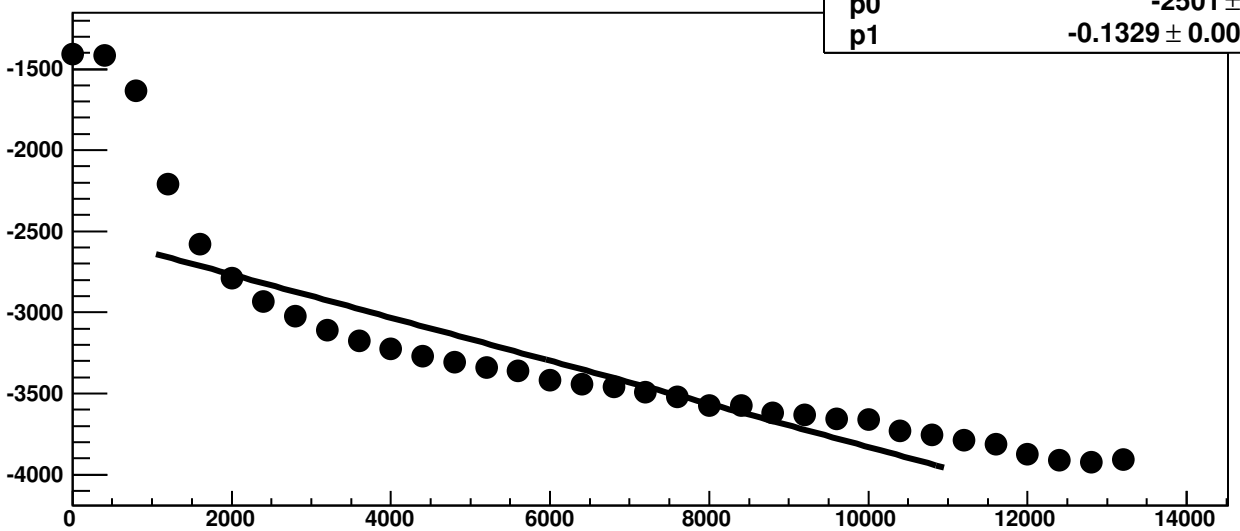

Chip 9, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 166.6 / 23
p0 -423.2 ± 3.428
p1 2.129 ± 0.002099
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

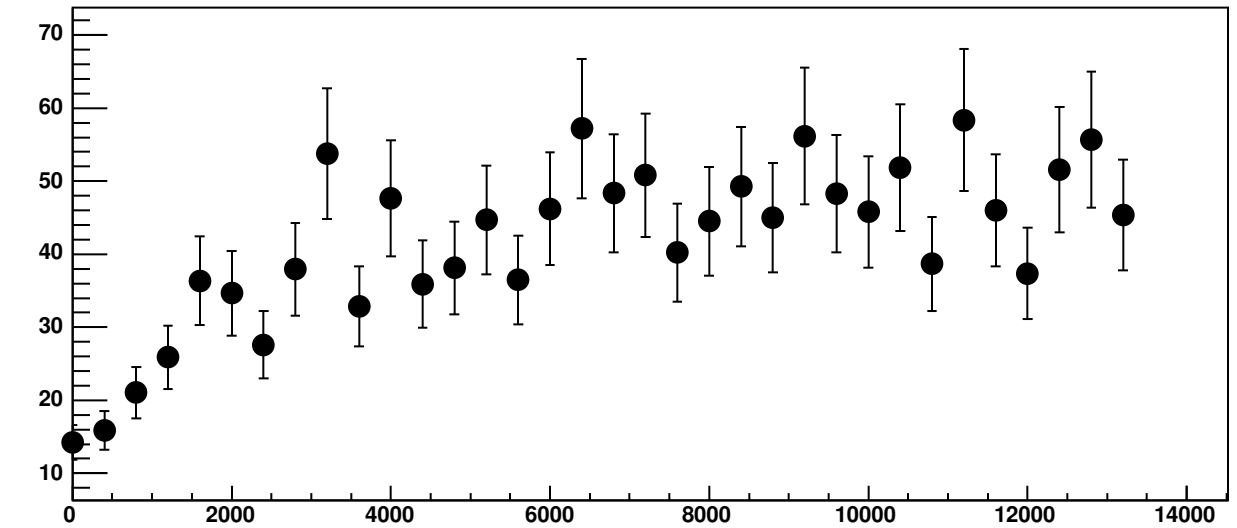

Chip 9, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

Chip 9, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

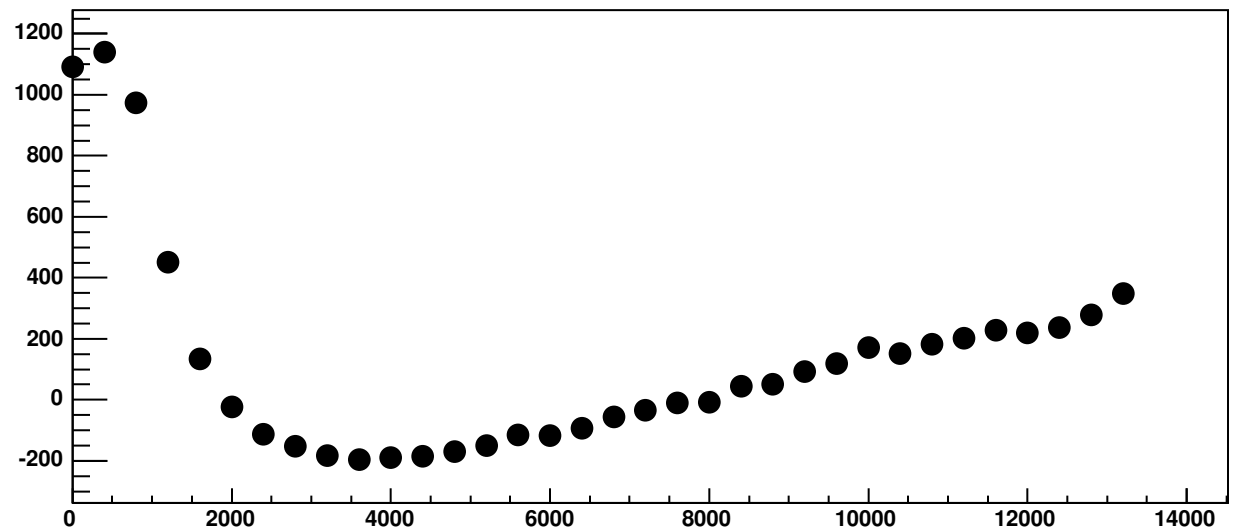
χ^2 / ndf	1.041e+04 / 23
p0	-2501 ± 3.677
p1	-0.1329 ± 0.0006164

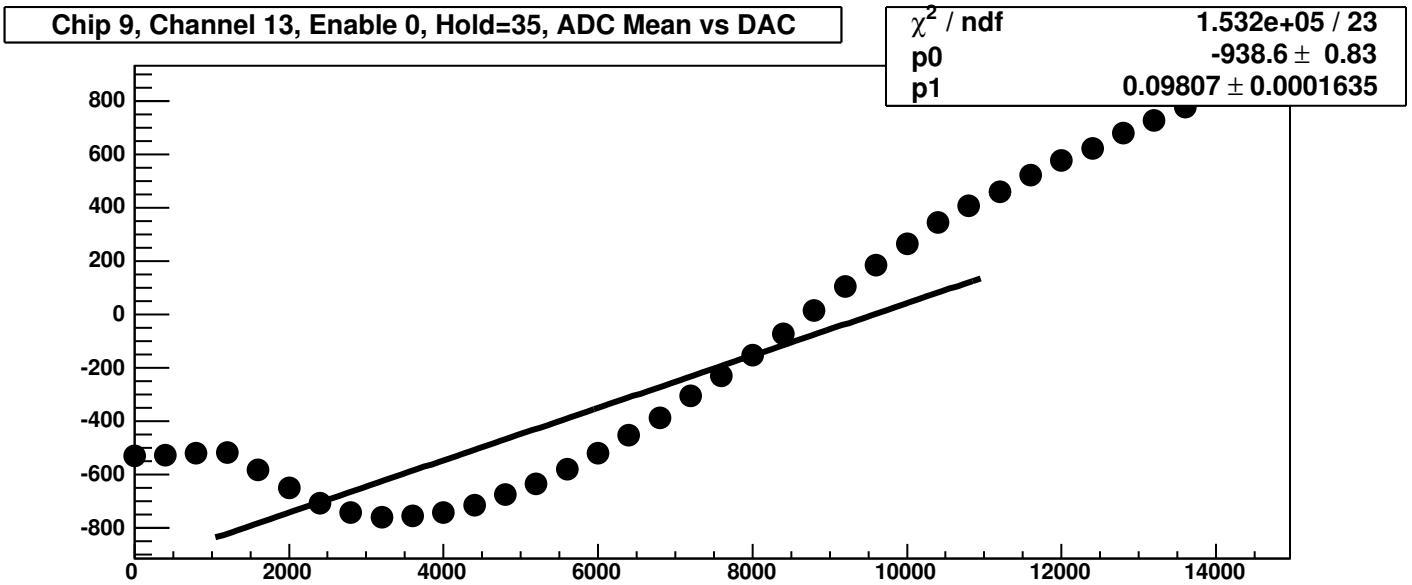


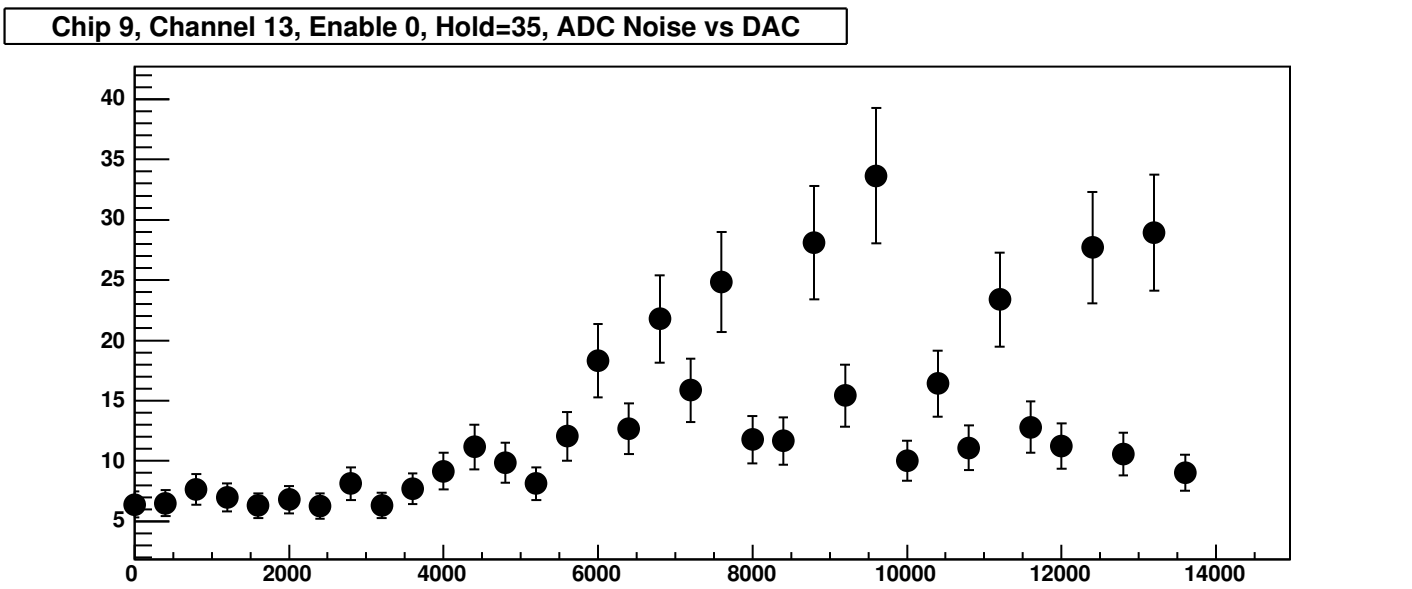
Chip 9, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

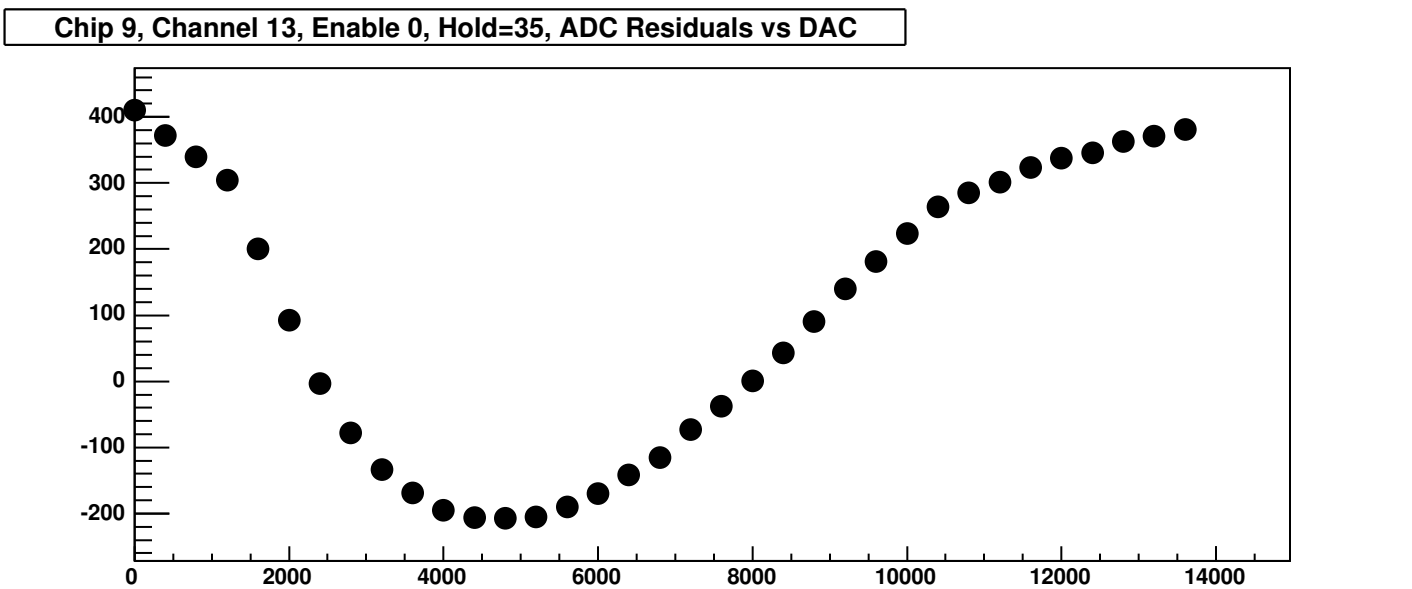


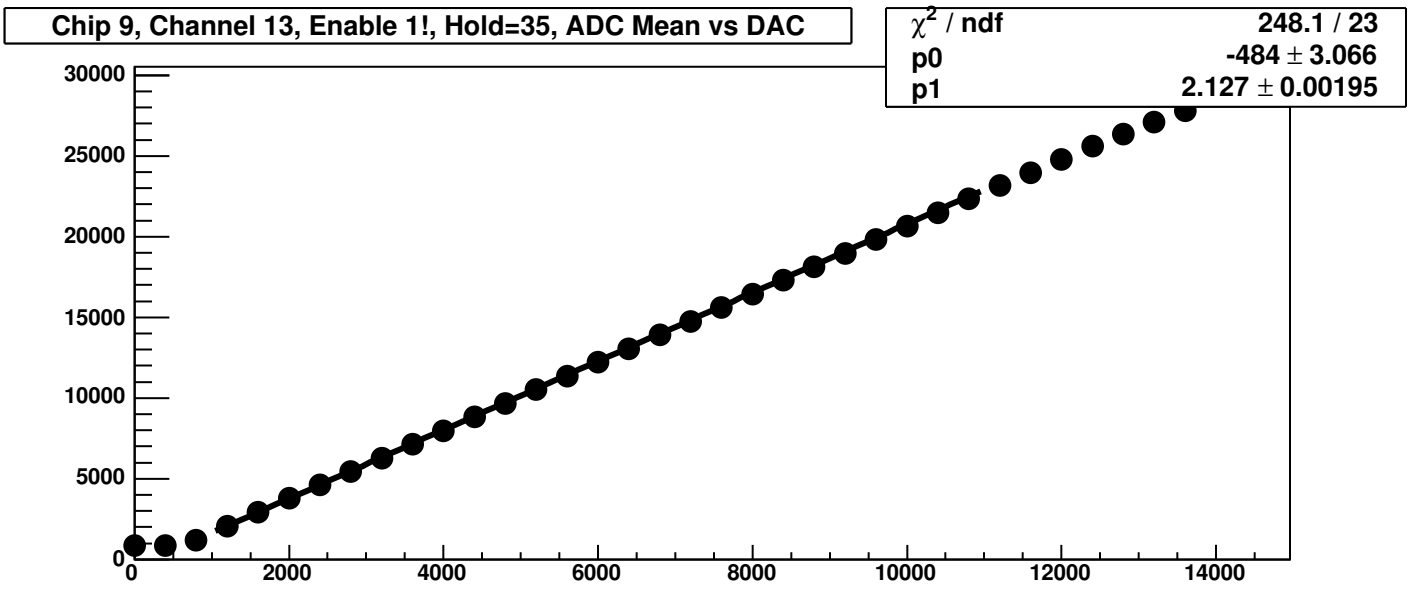
Chip 9, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

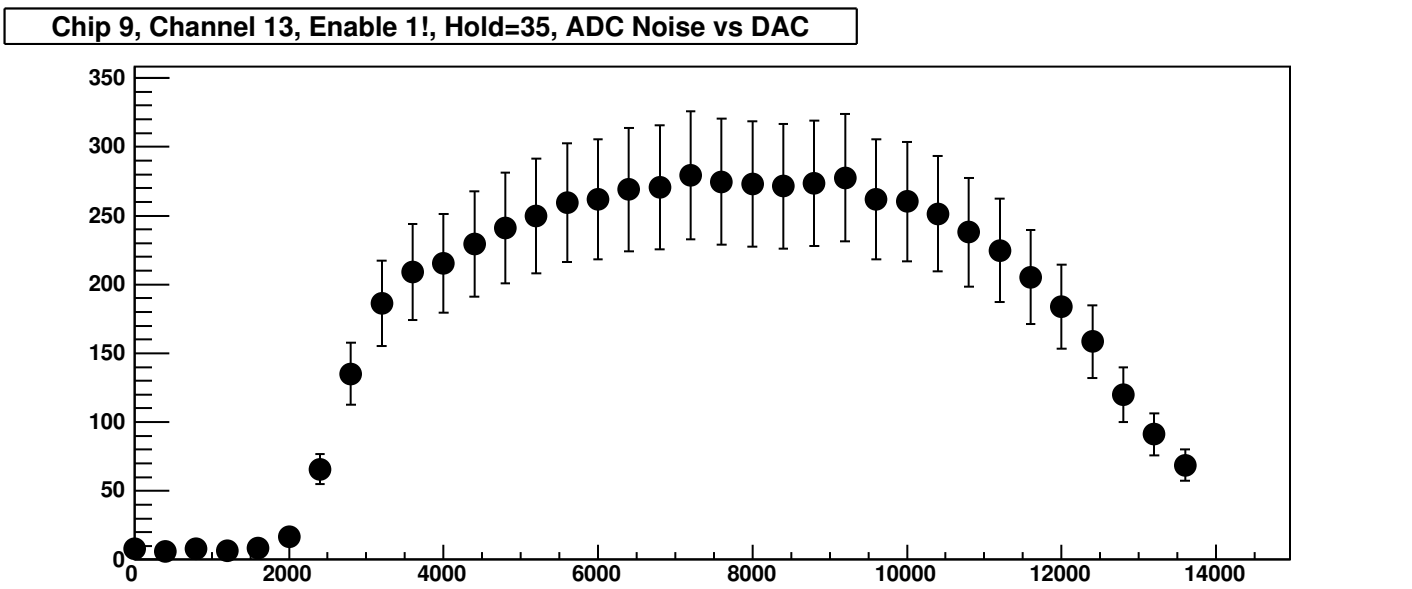


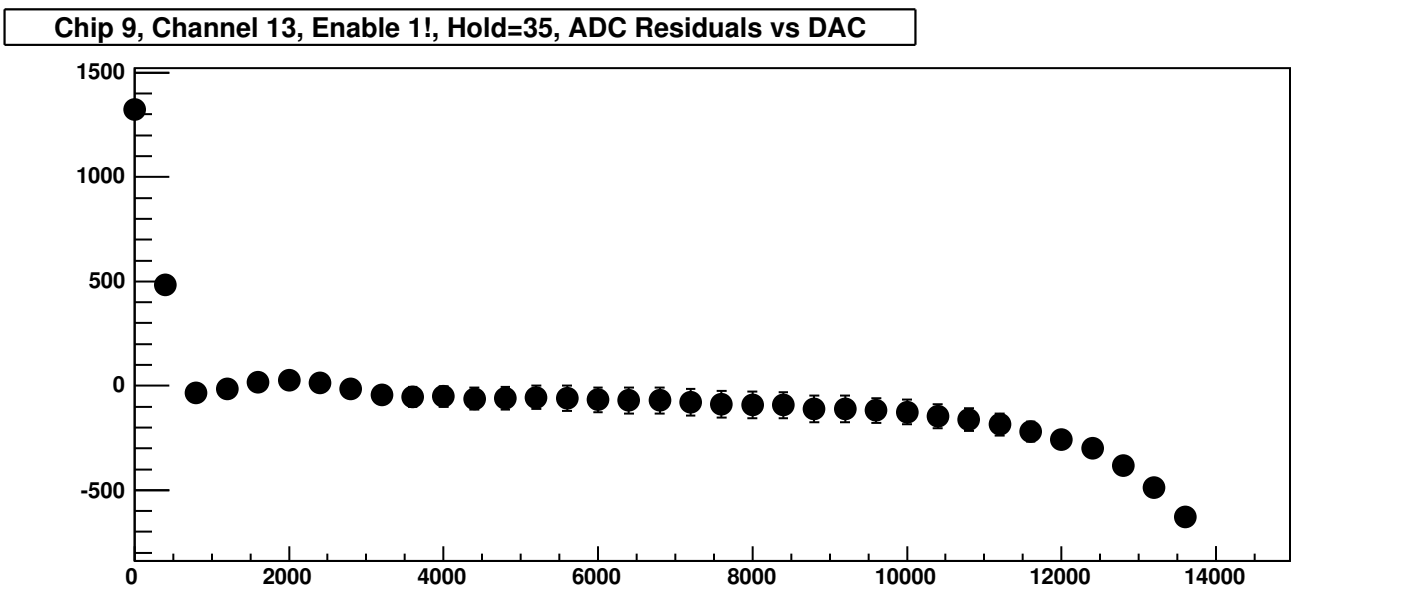

Chip 9, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.532e+05 / 23
p0 -938.6 ± 0.83
p1 0.09807 ± 0.0001635
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

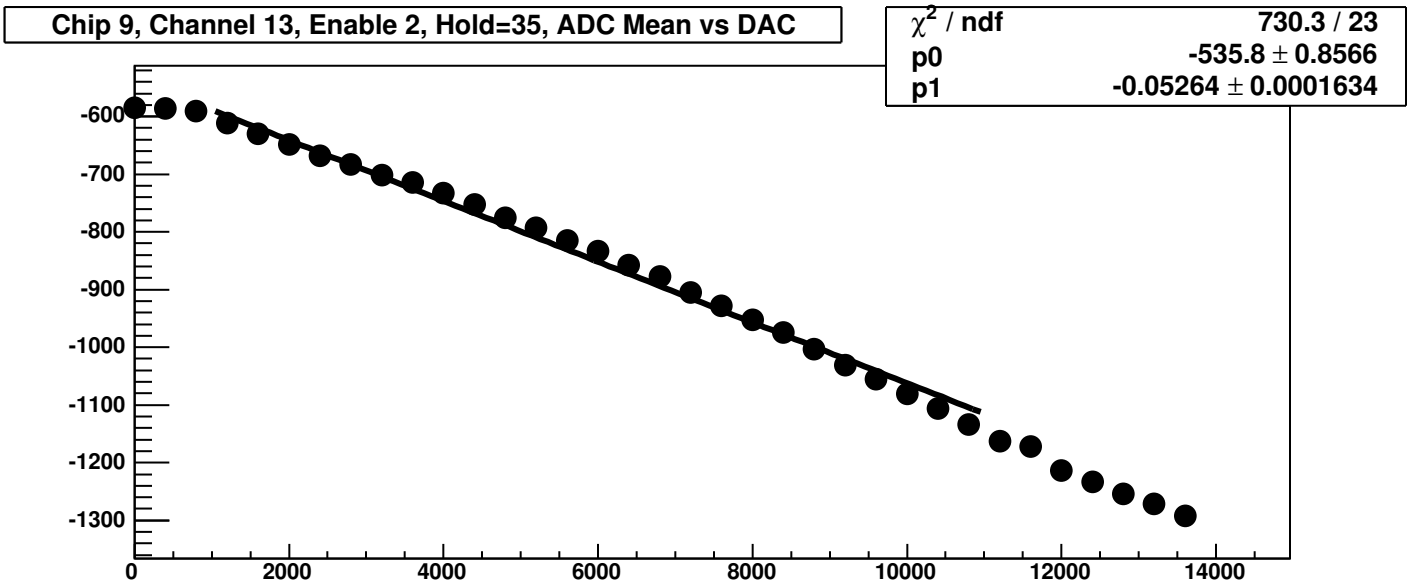

Chip 9, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

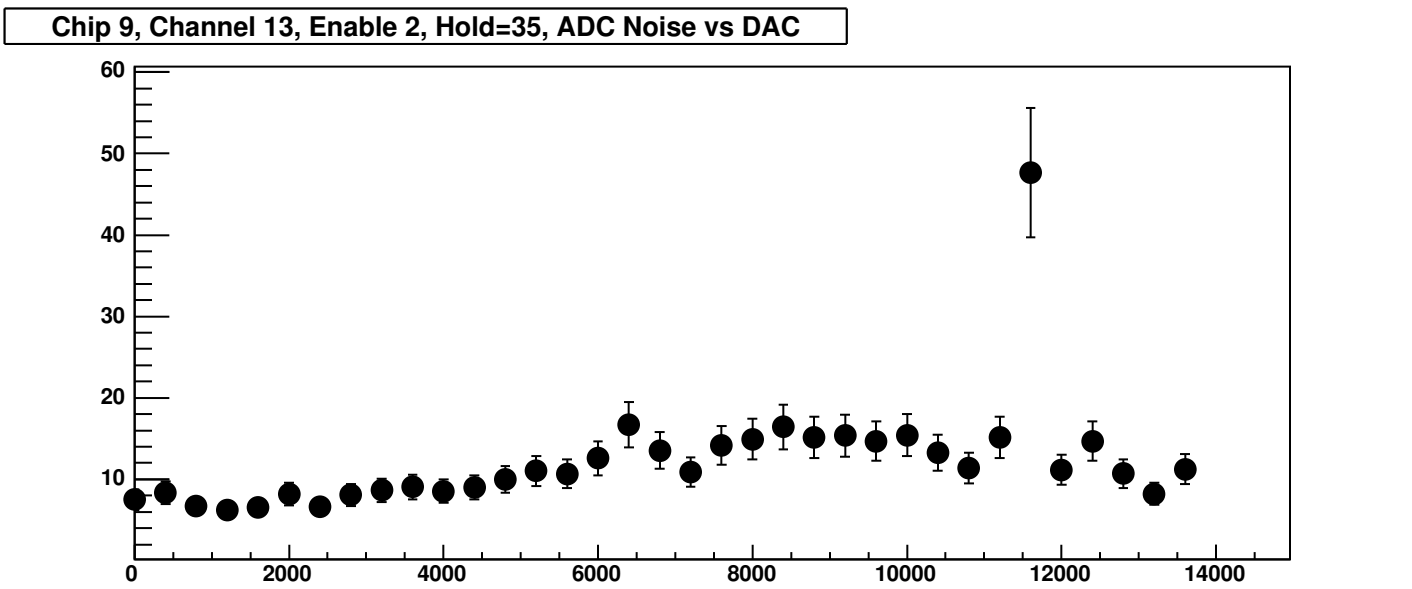

Chip 9, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

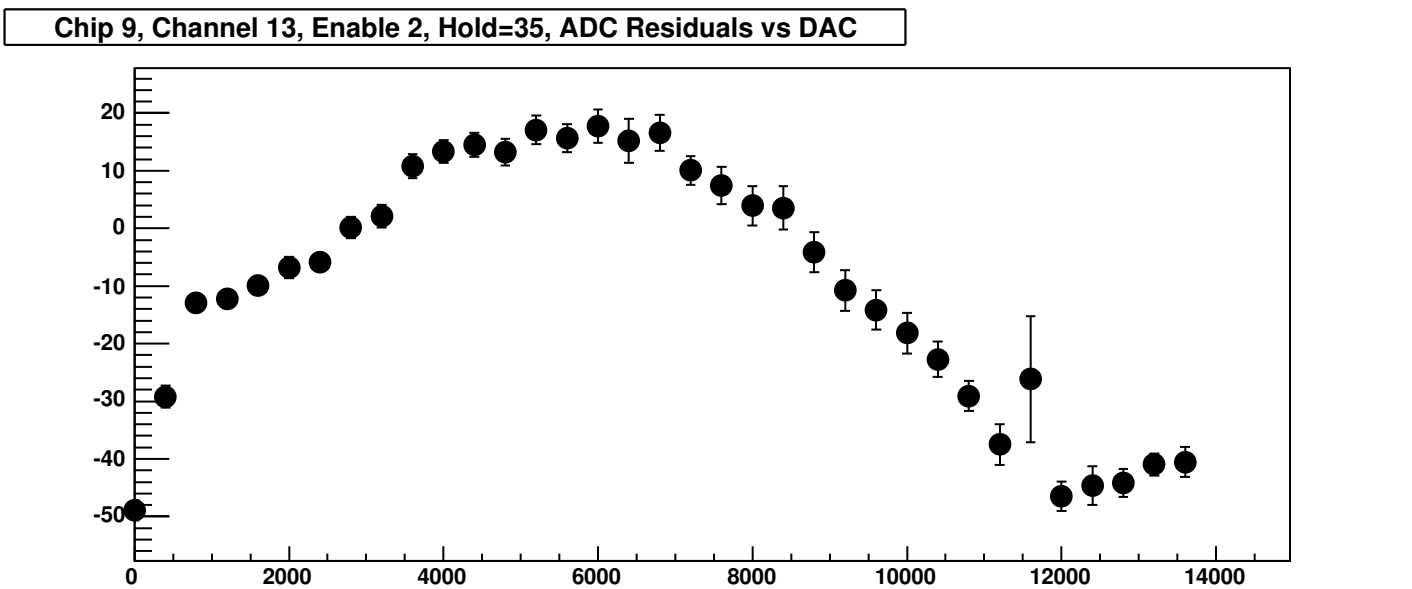

Chip 9, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 248.1 / 23
p0 -484 ± 3.066
p1 2.127 ± 0.00195
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

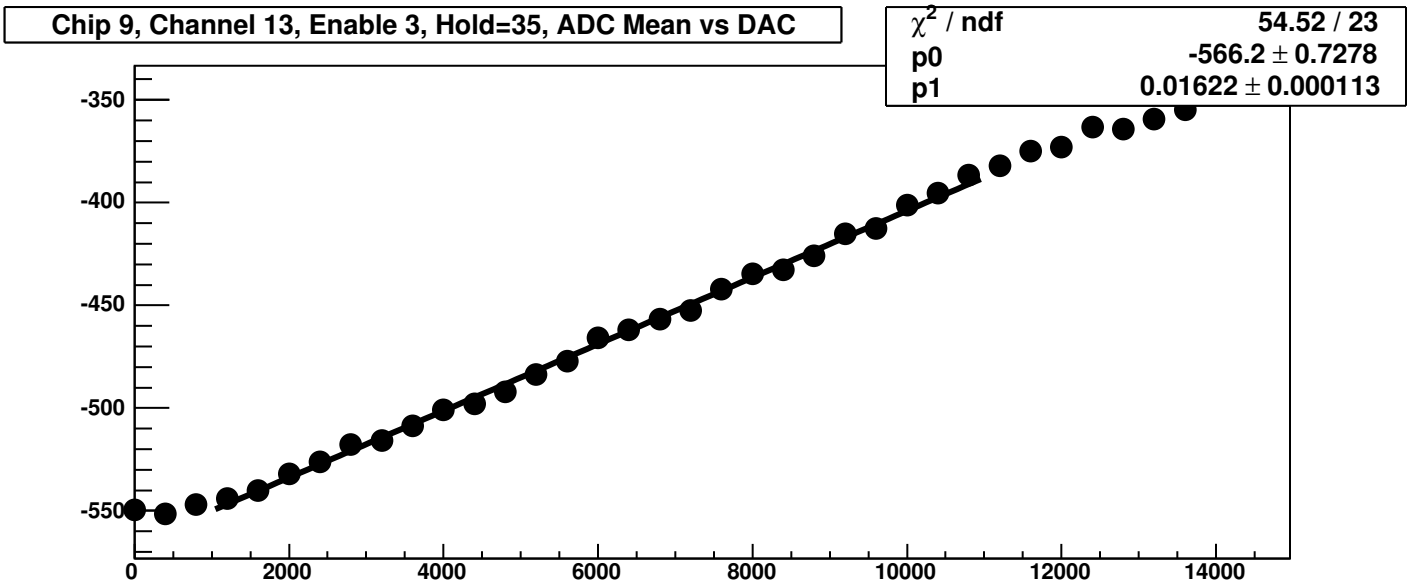

Chip 9, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

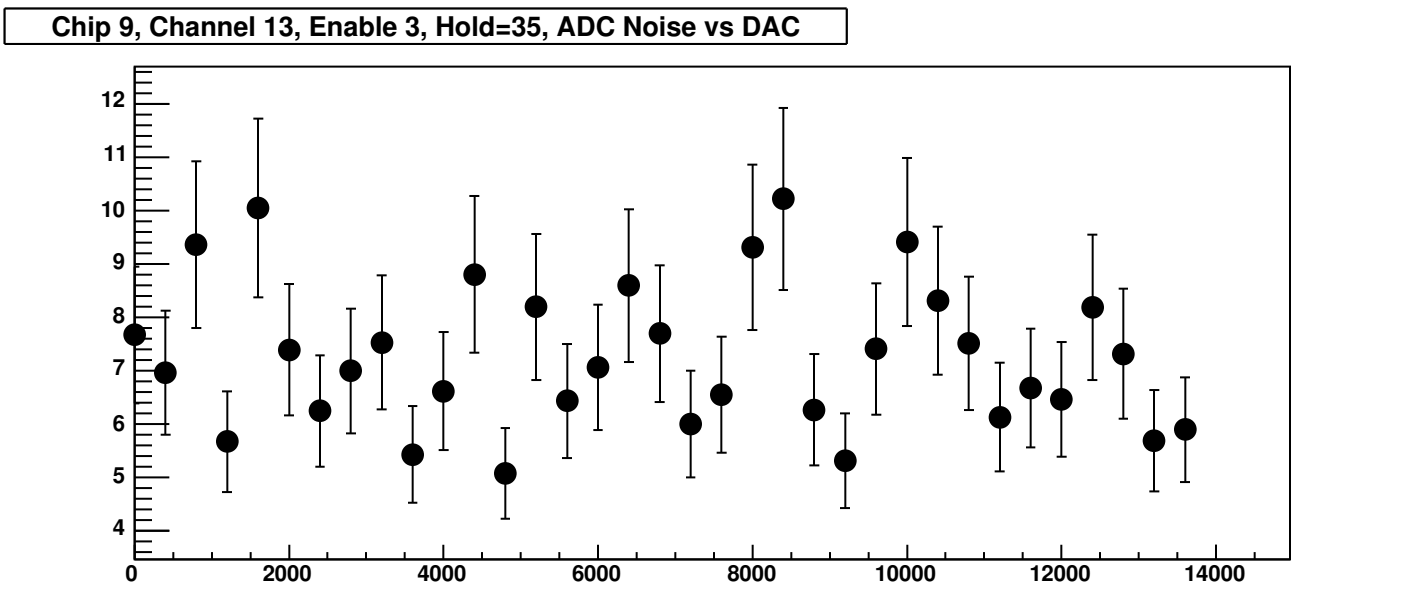

Chip 9, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

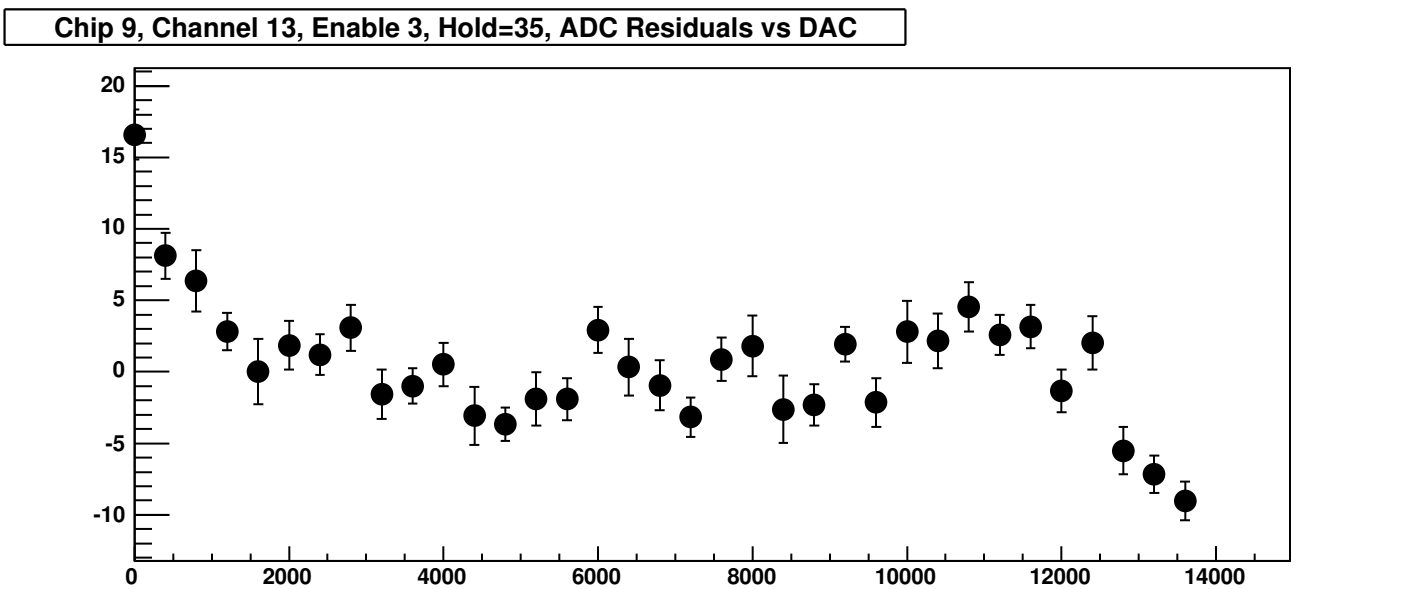

Chip 9, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 730.3 / 23
p0 -535.8 ± 0.8566
p1 -0.05264 ± 0.0001634
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
2000
4000
6000
8000
10000
12000
14000

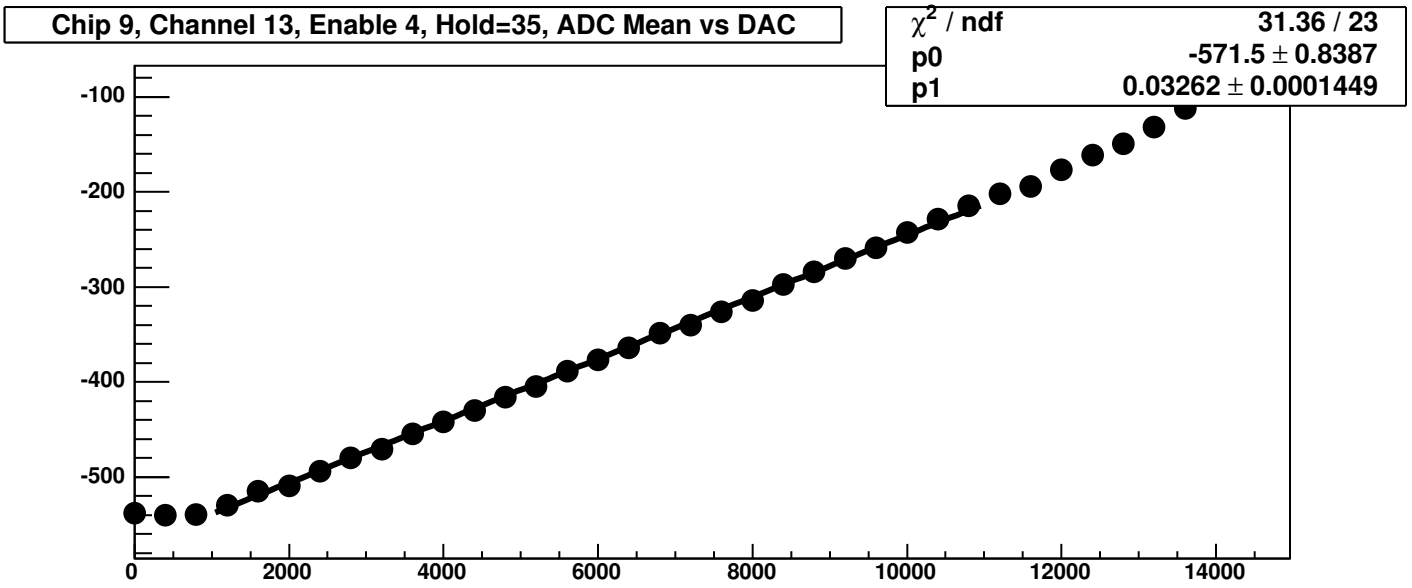

Chip 9, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

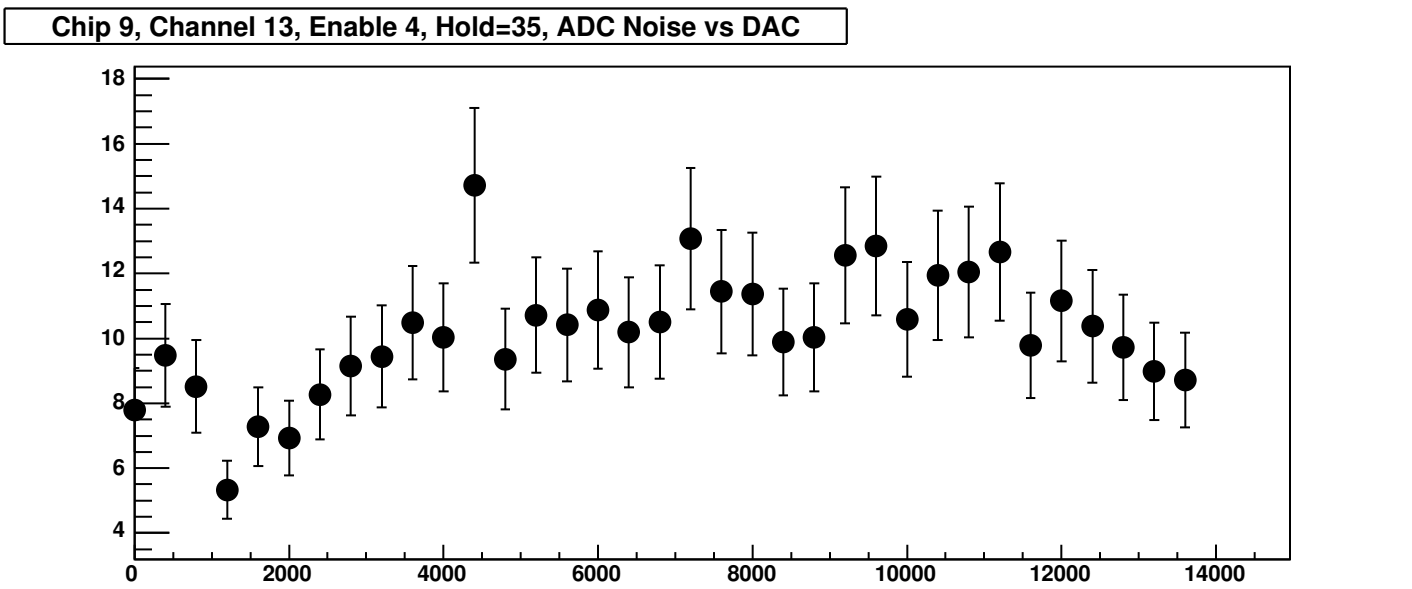

Chip 9, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

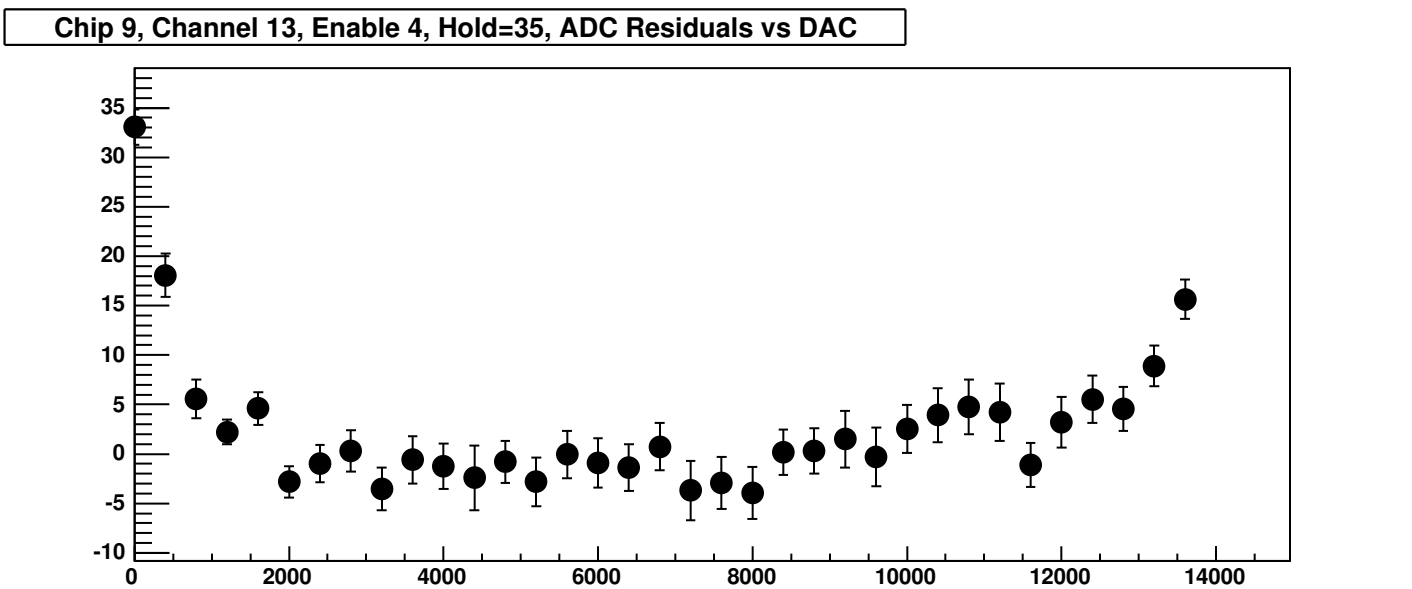

Chip 9, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 54.52 / 23
p0 -566.2 ± 0.7278
p1 0.01622 ± 0.000113
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

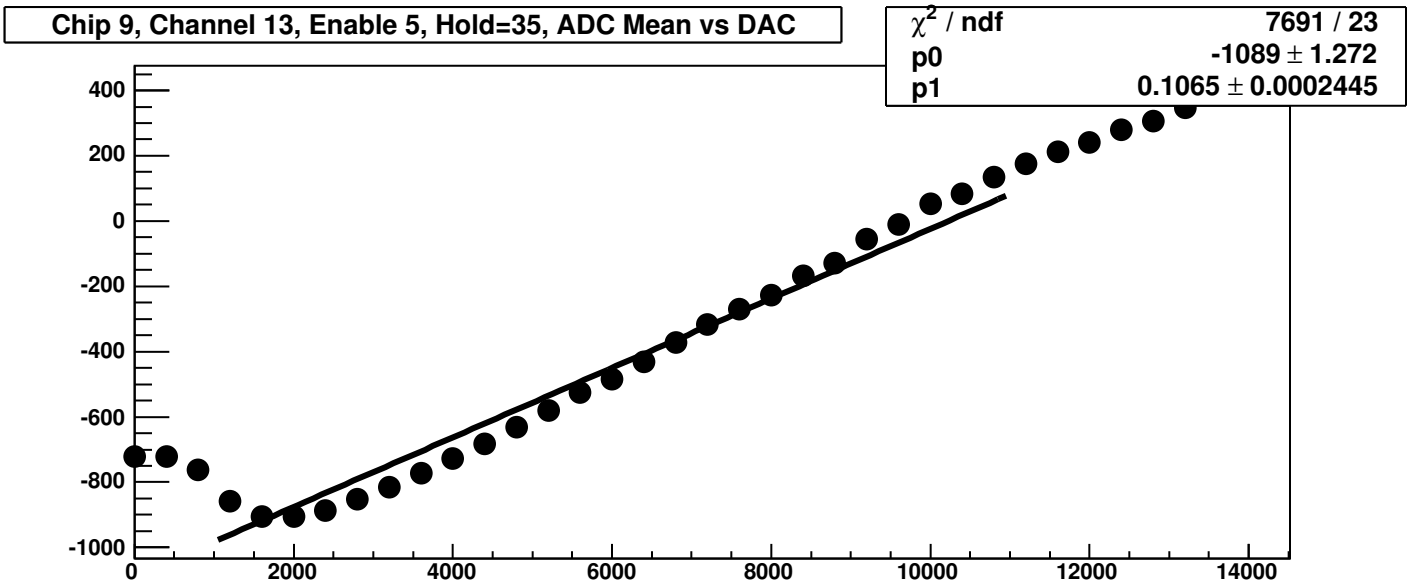

Chip 9, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

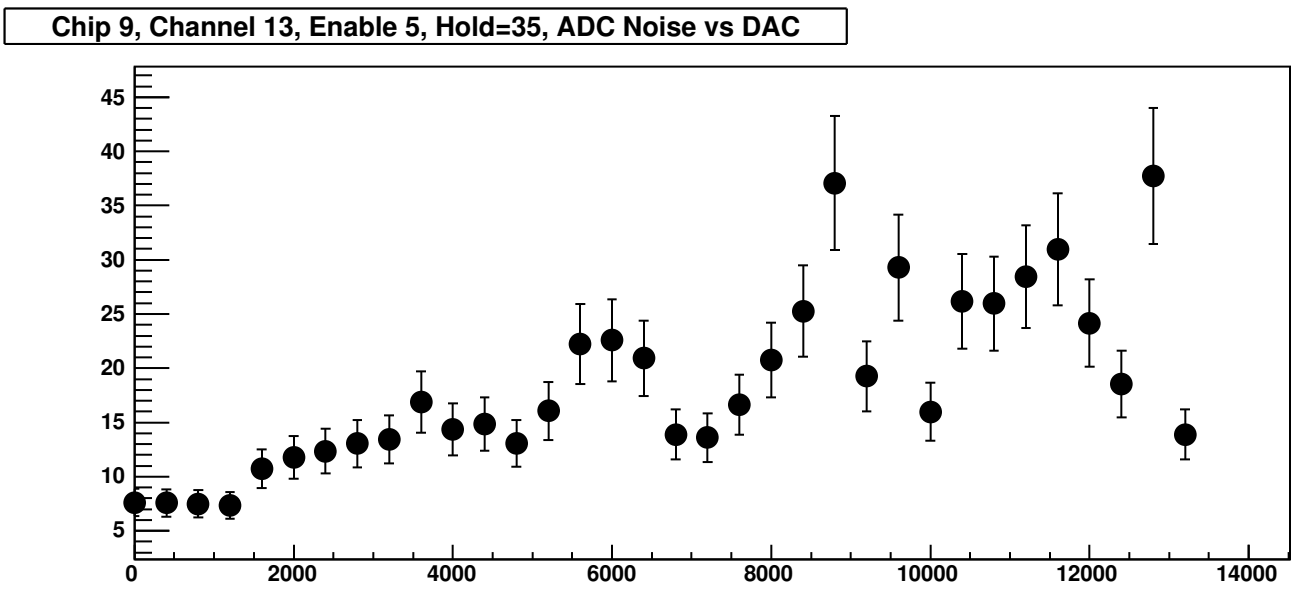

Chip 9, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

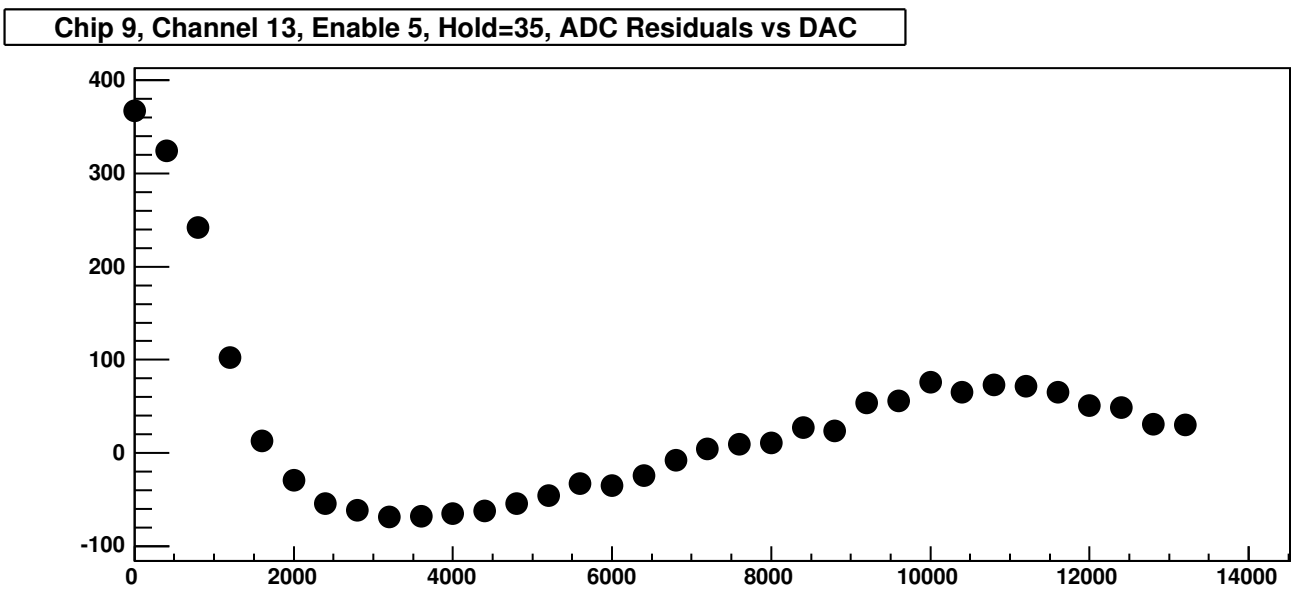

Chip 9, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 31.36 / 23
p0 -571.5 ± 0.8387
p1 0.03262 ± 0.0001449
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

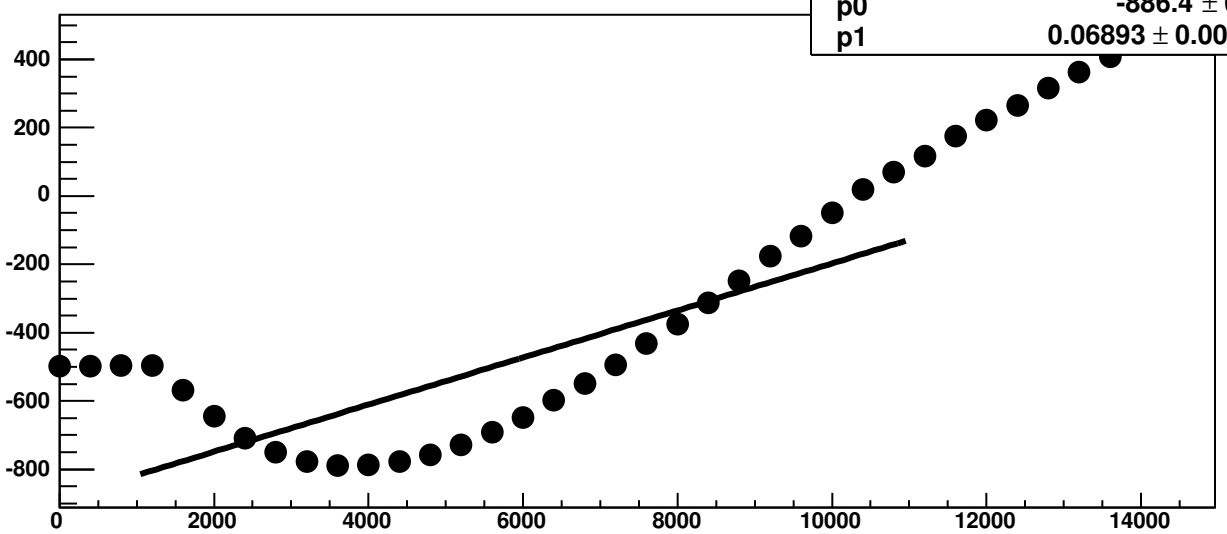

Chip 9, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 7691 / 23
p0 -1089 ± 1.272
p1 0.1065 ± 0.0002445
400
200
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

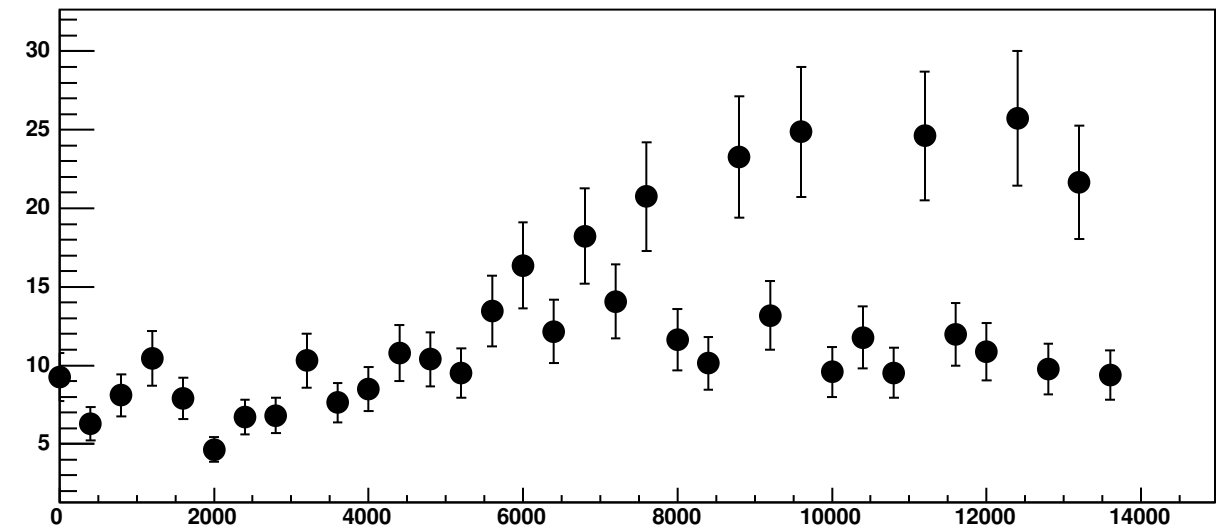

Chip 9, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

Chip 9, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC

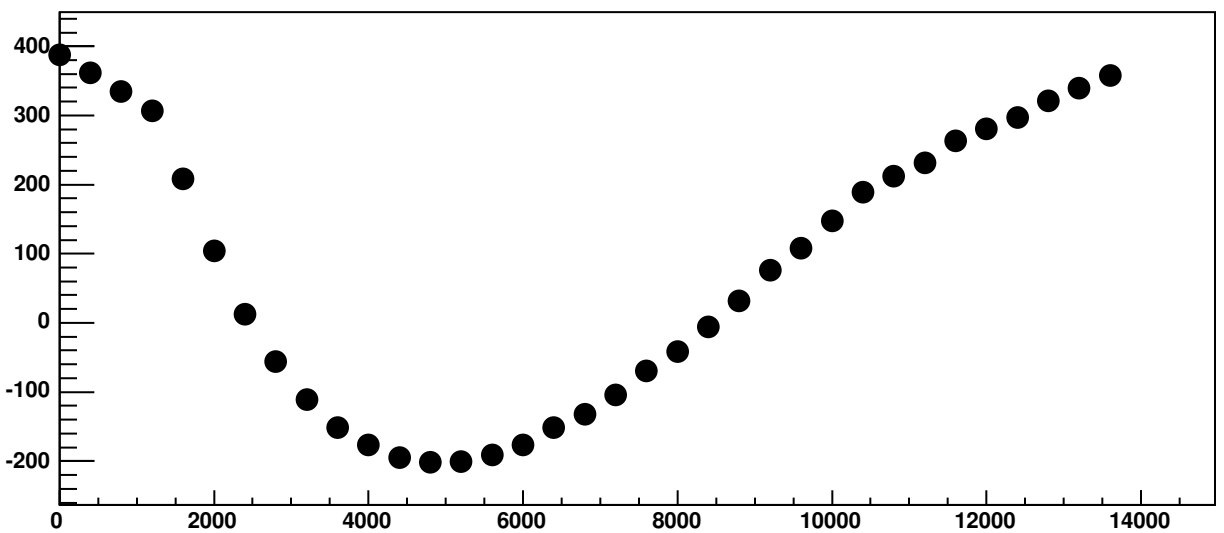
χ^2 / ndf	1.115e+05 / 23
p0	-886.4 ± 0.8281
p1	0.06893 ± 0.0001515

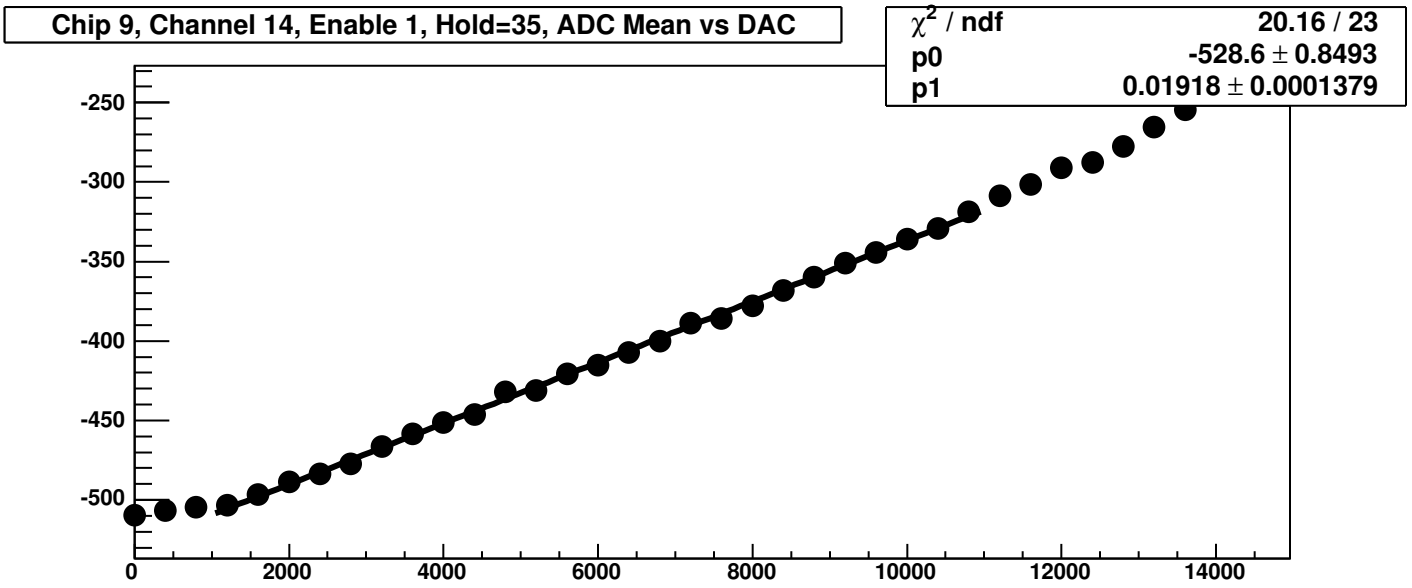


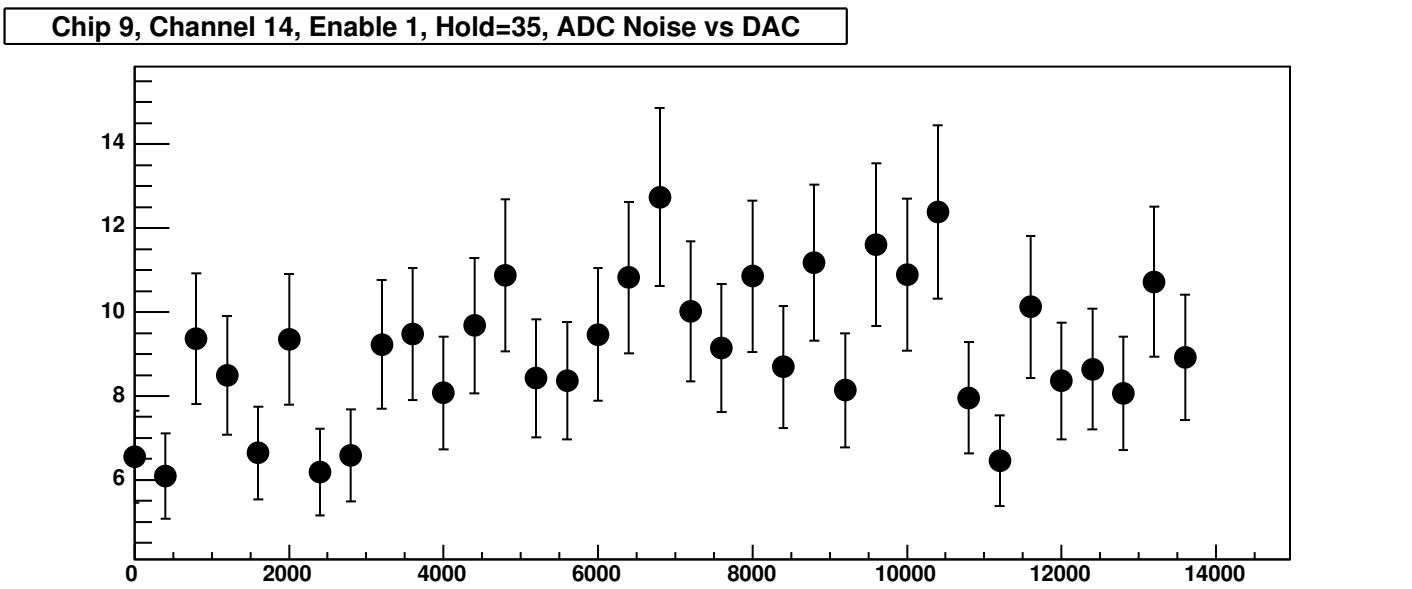
Chip 9, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC

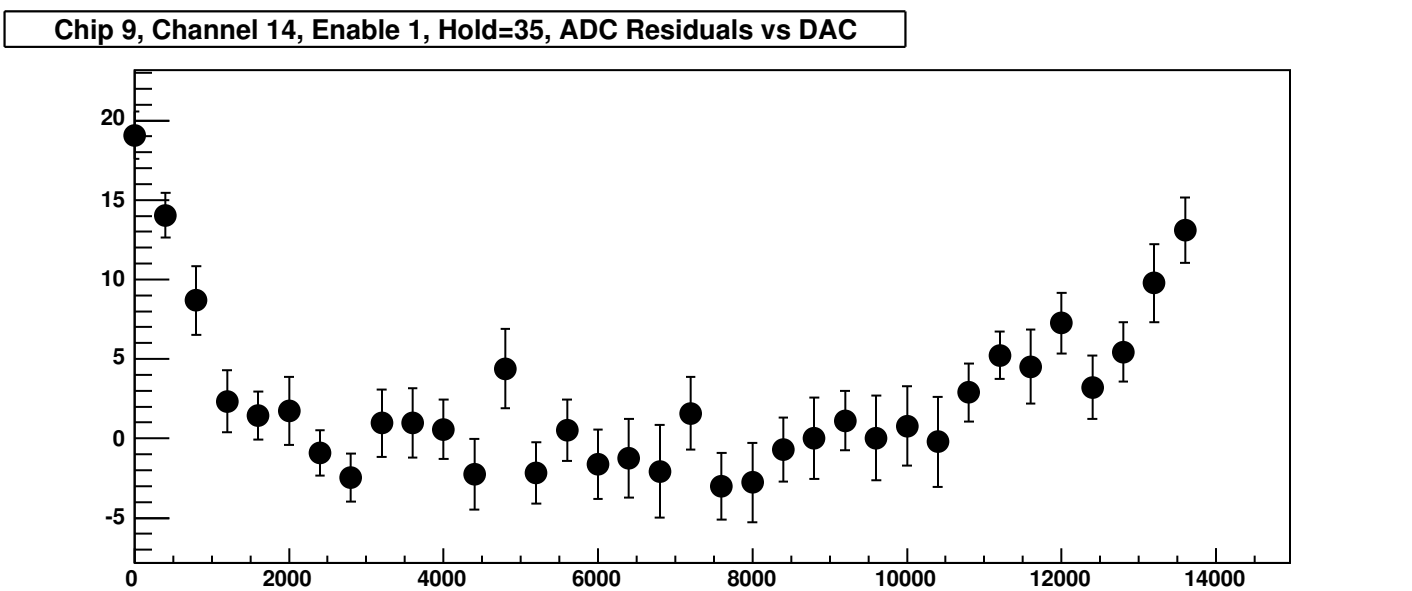


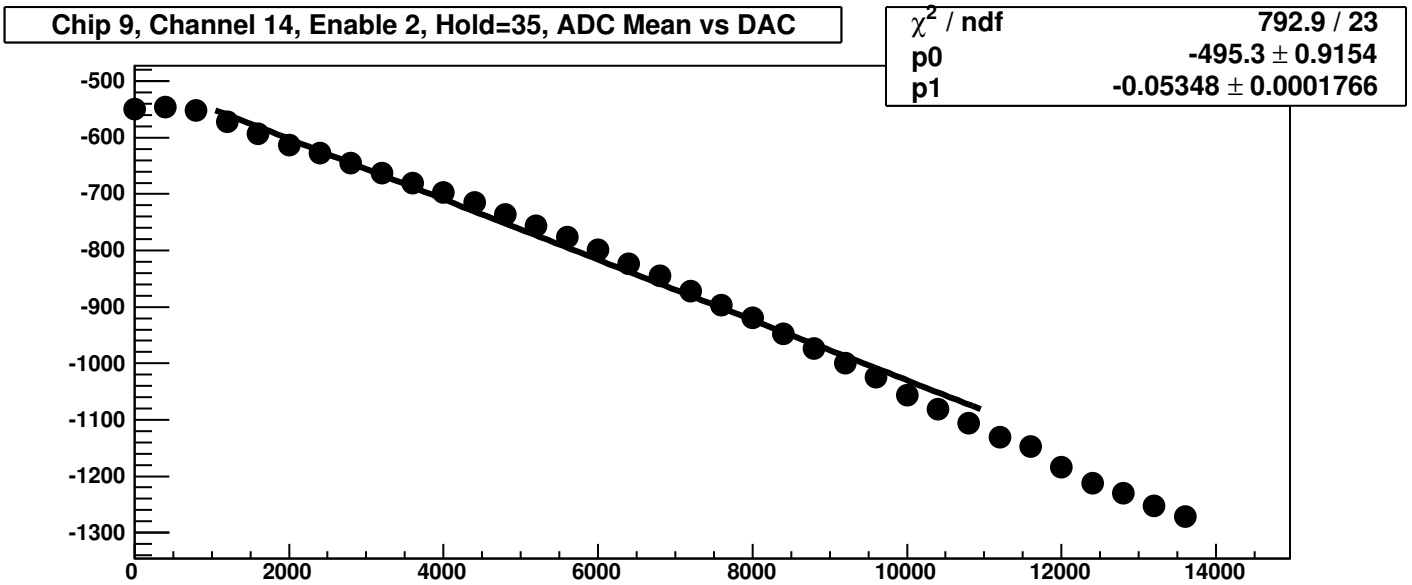
Chip 9, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC

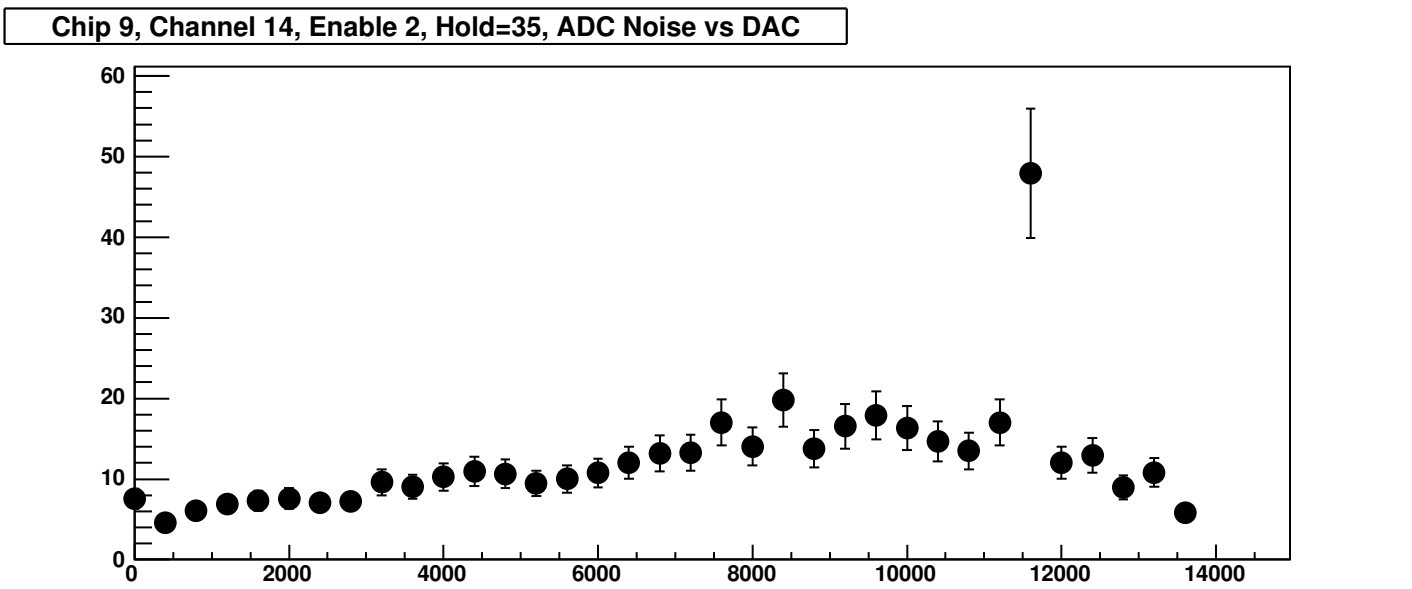


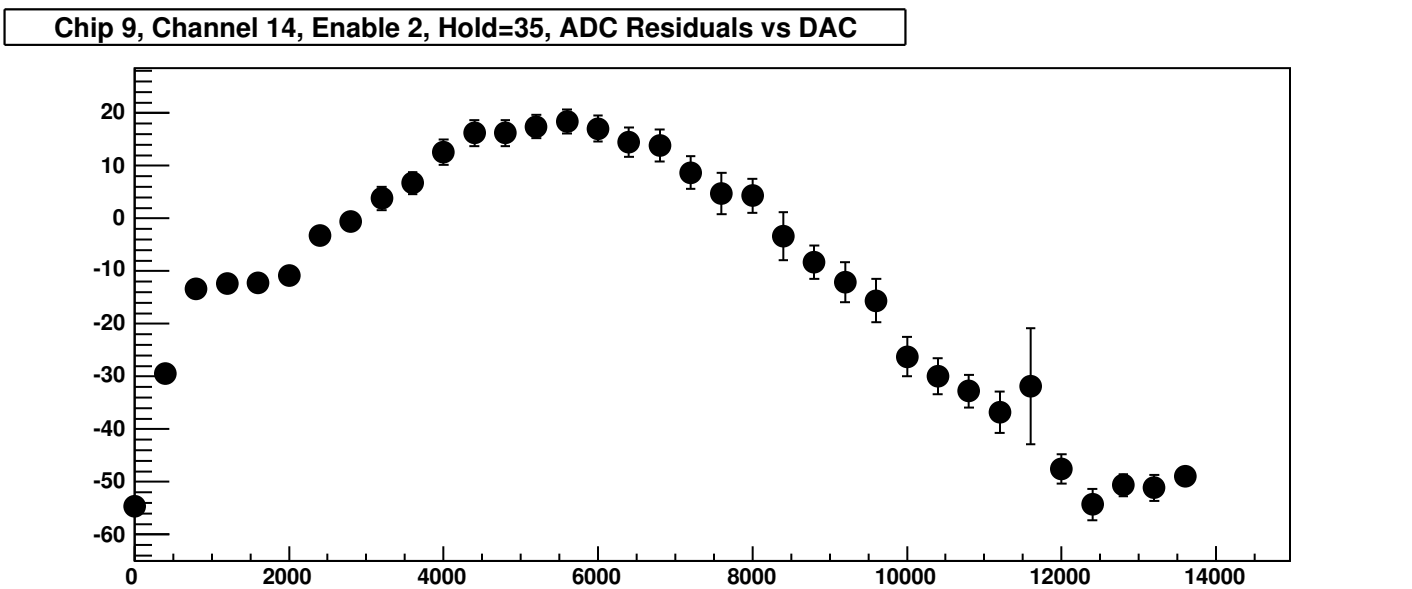

Chip 9, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 20.16 / 23
p0 -528.6 ± 0.8493
p1 0.01918 ± 0.0001379
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

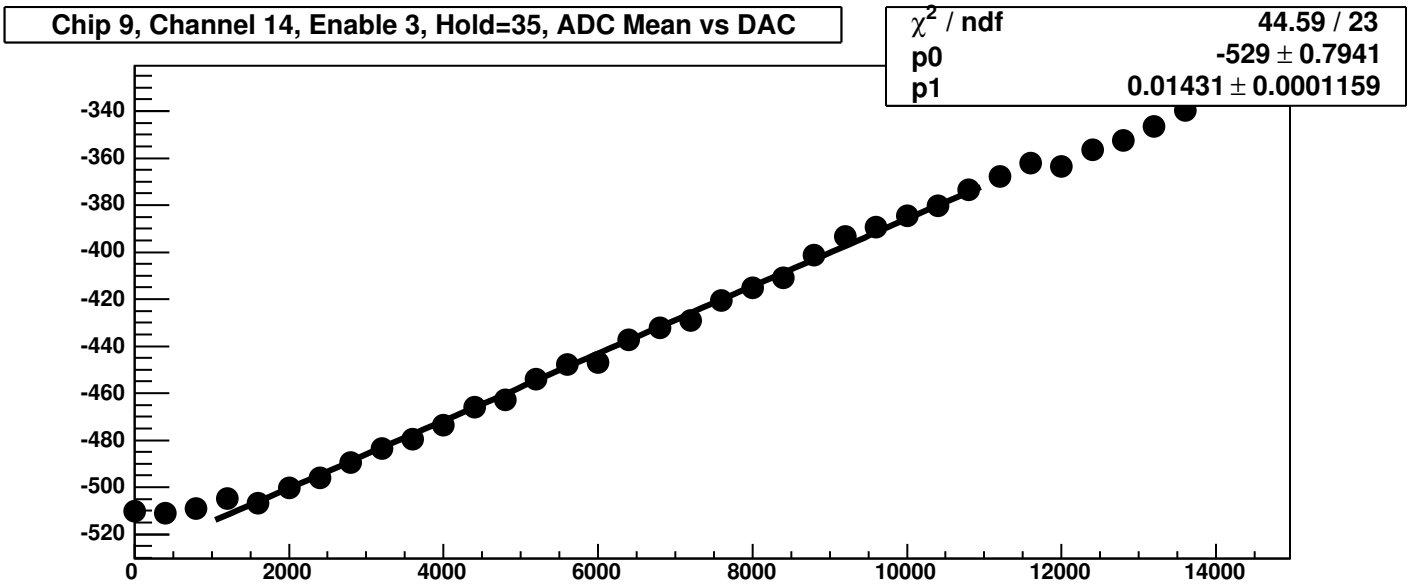

Chip 9, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

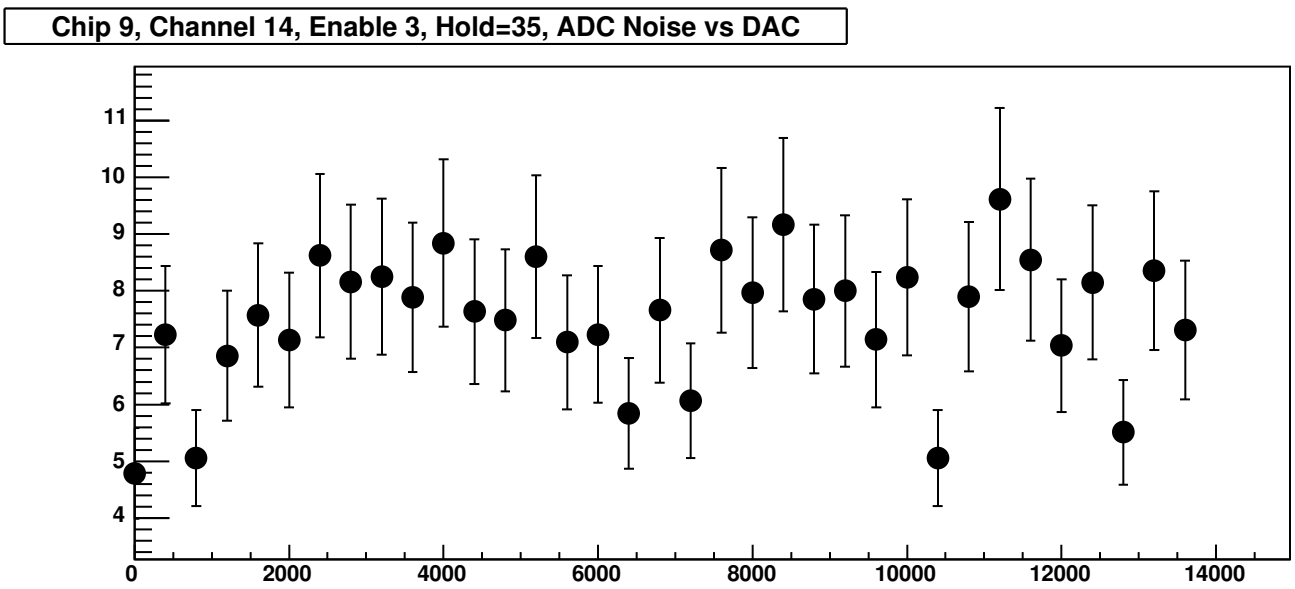

Chip 9, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

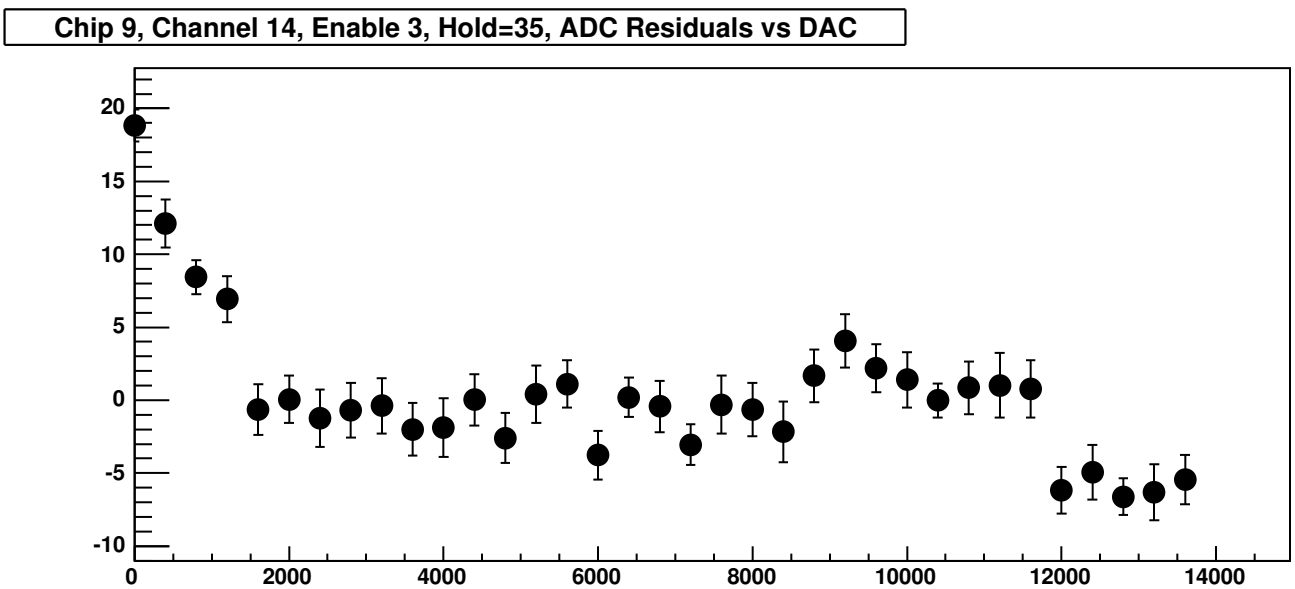

Chip 9, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 792.9 / 23
p0 -495.3 ± 0.9154
p1 -0.05348 ± 0.0001766
-500
-600
-700
-800
-900
-1000
-1100
-1200
-1300
0
2000
4000
6000
8000
10000
12000
14000

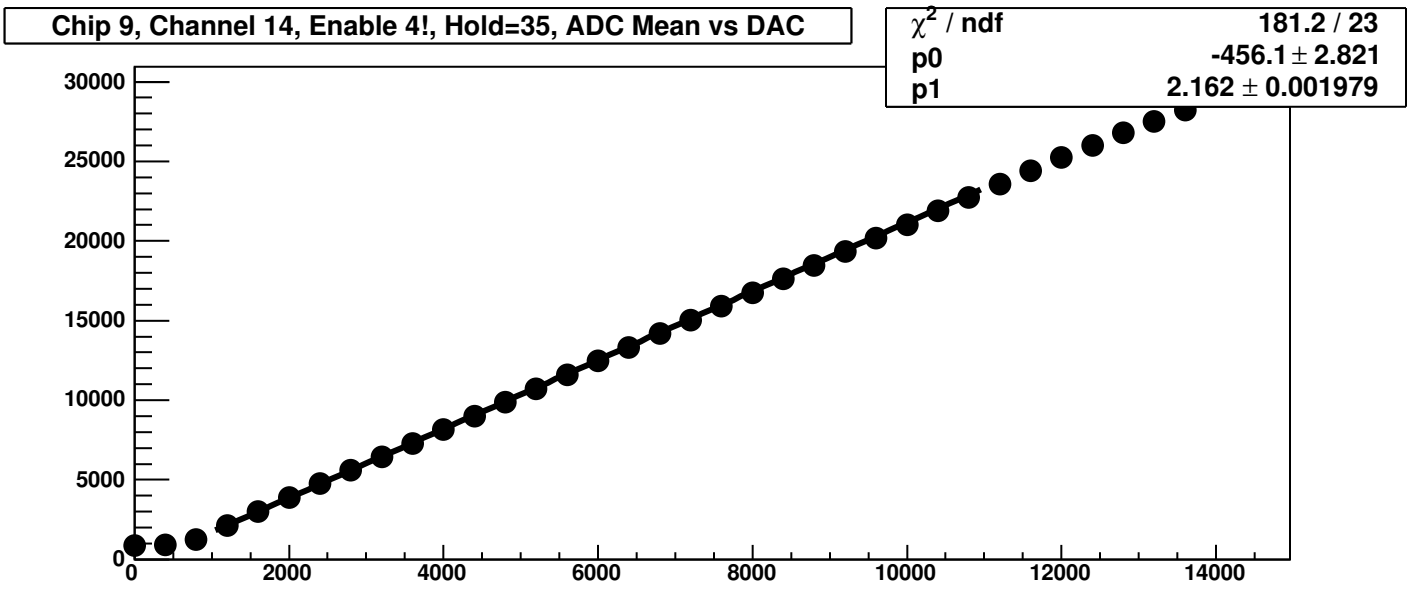

Chip 9, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

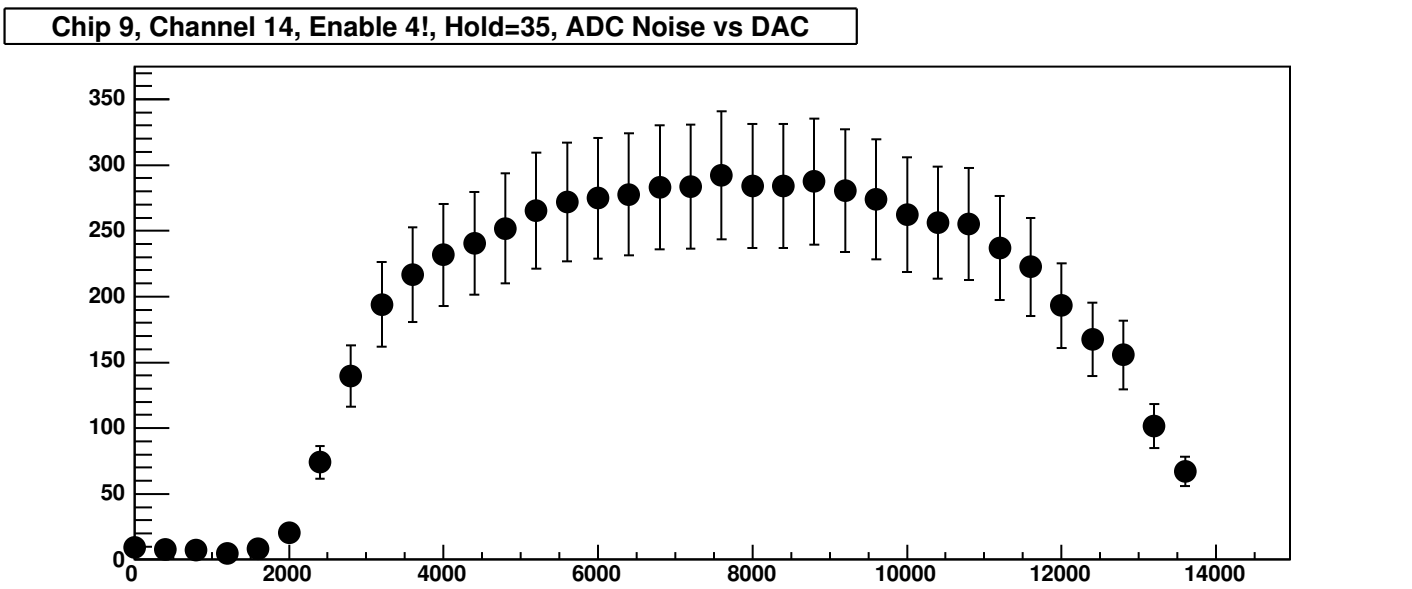

Chip 9, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
-60
0
2000
4000
6000
8000
10000
12000
14000

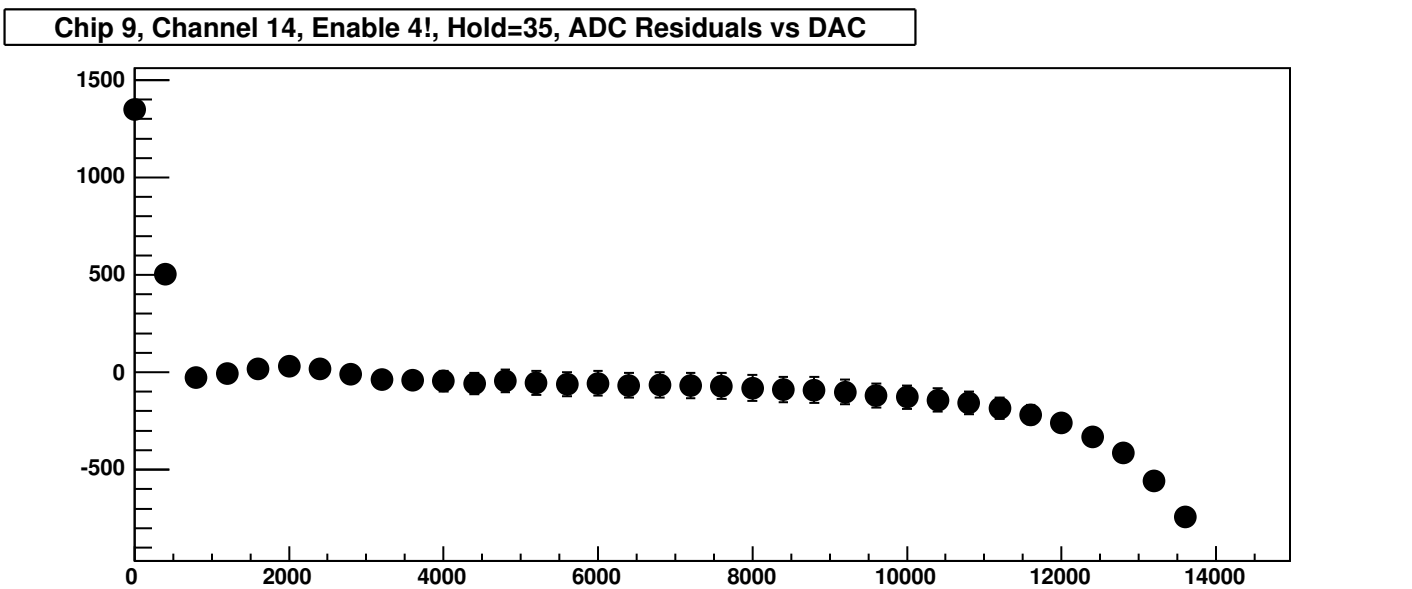

Chip 9, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 44.59 / 23
p0 -529 ± 0.7941
p1 0.01431 ± 0.0001159
-340
-360
-380
-400
-420
-440
-460
-480
-500
-520
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

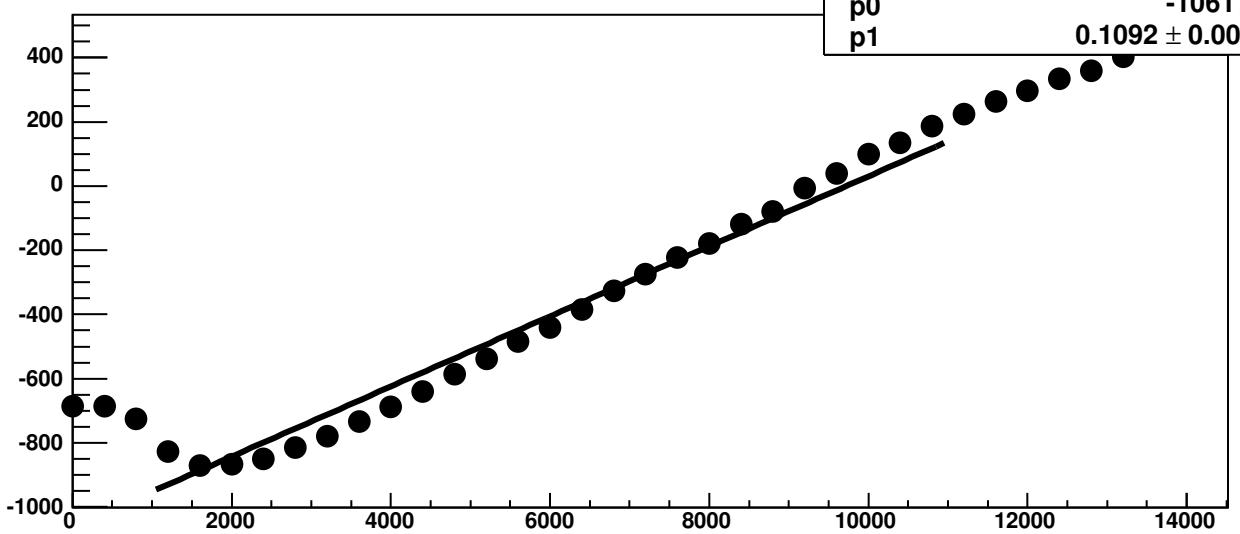

Chip 9, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 181.2 / 23
p0 -456.1 ± 2.821
p1 2.162 ± 0.001979
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

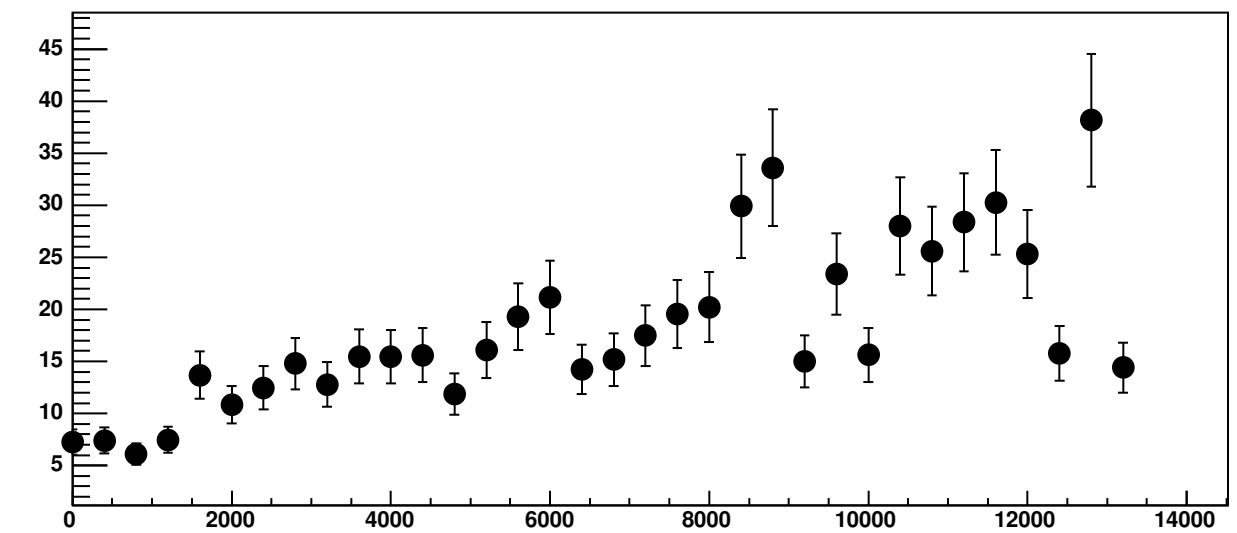

Chip 9, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

Chip 9, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

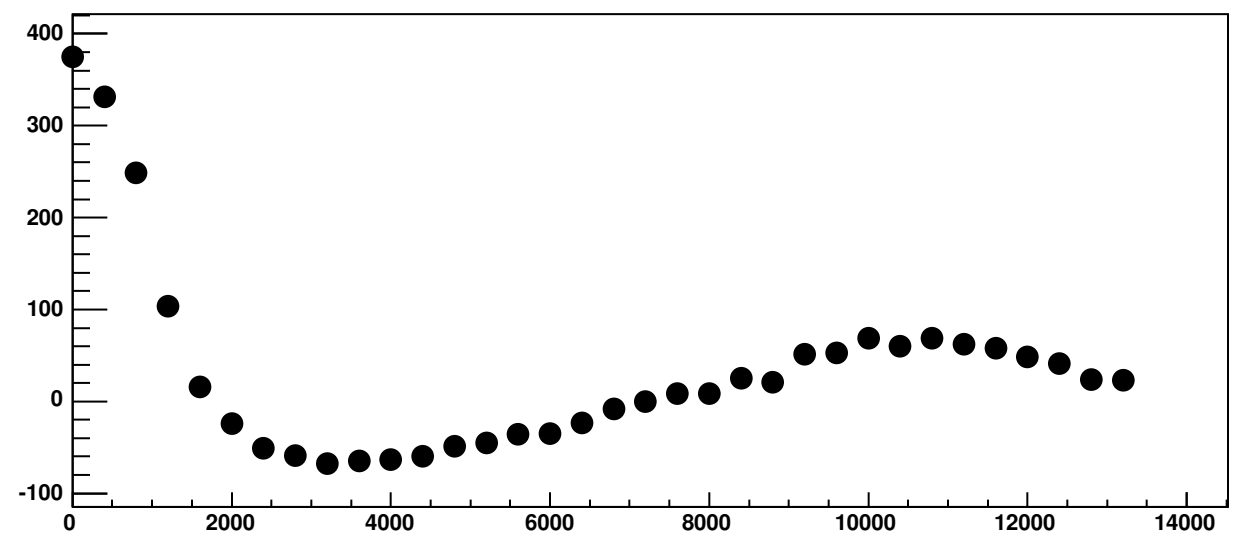
χ^2 / ndf	7443 / 23
p0	-1061 ± 1.3
p1	0.1092 ± 0.0002434



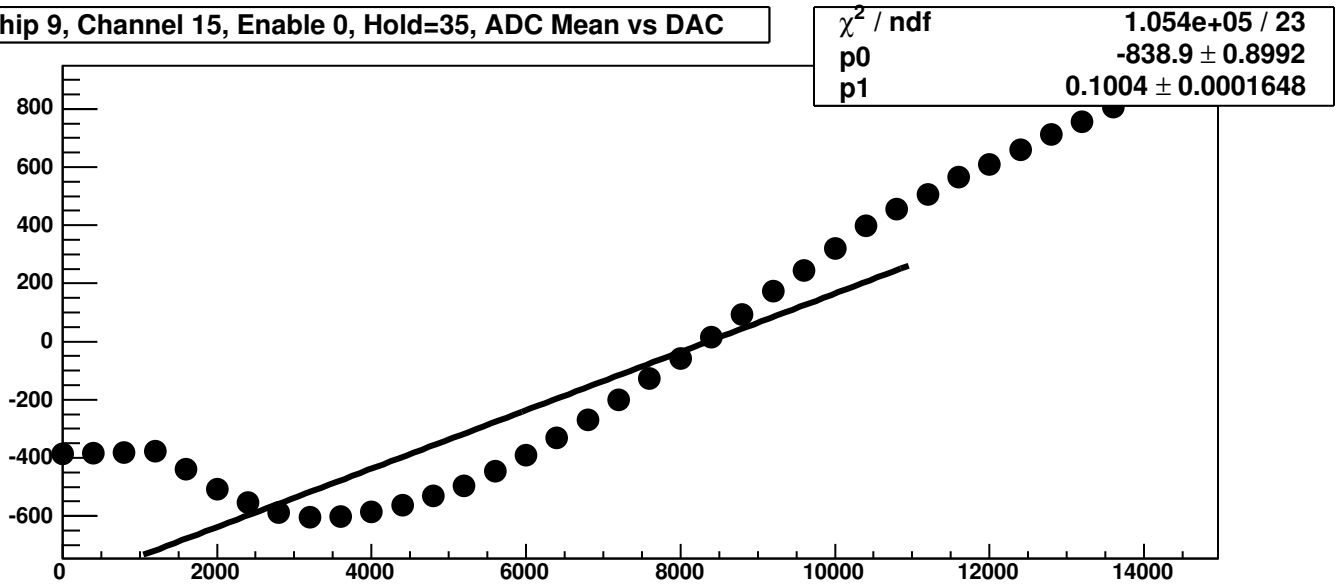
Chip 9, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



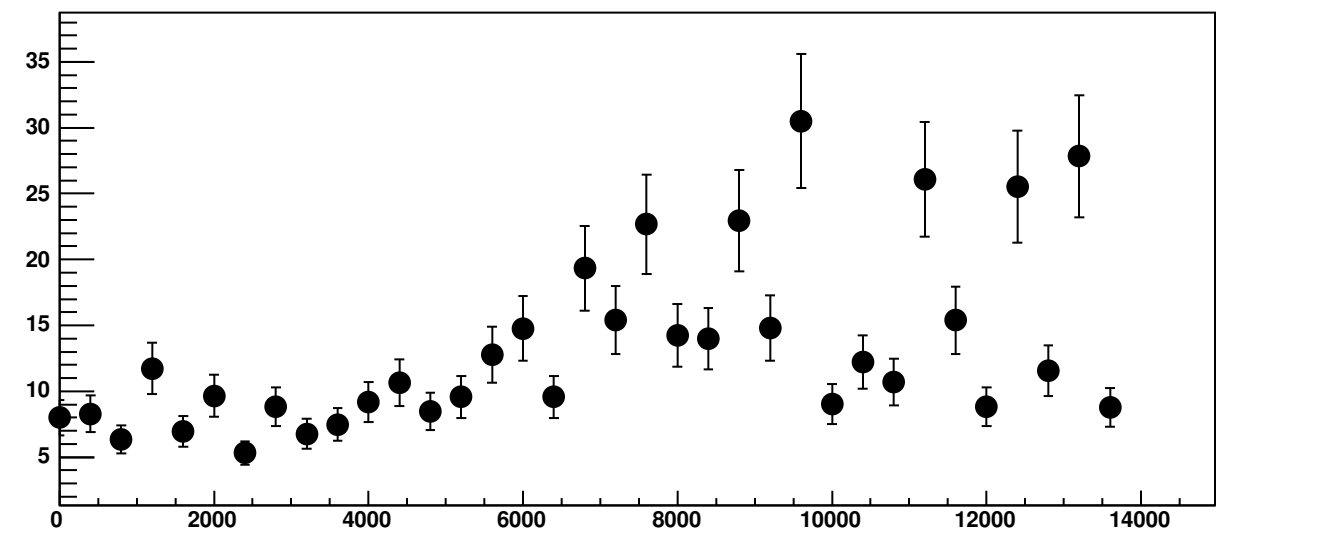
Chip 9, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



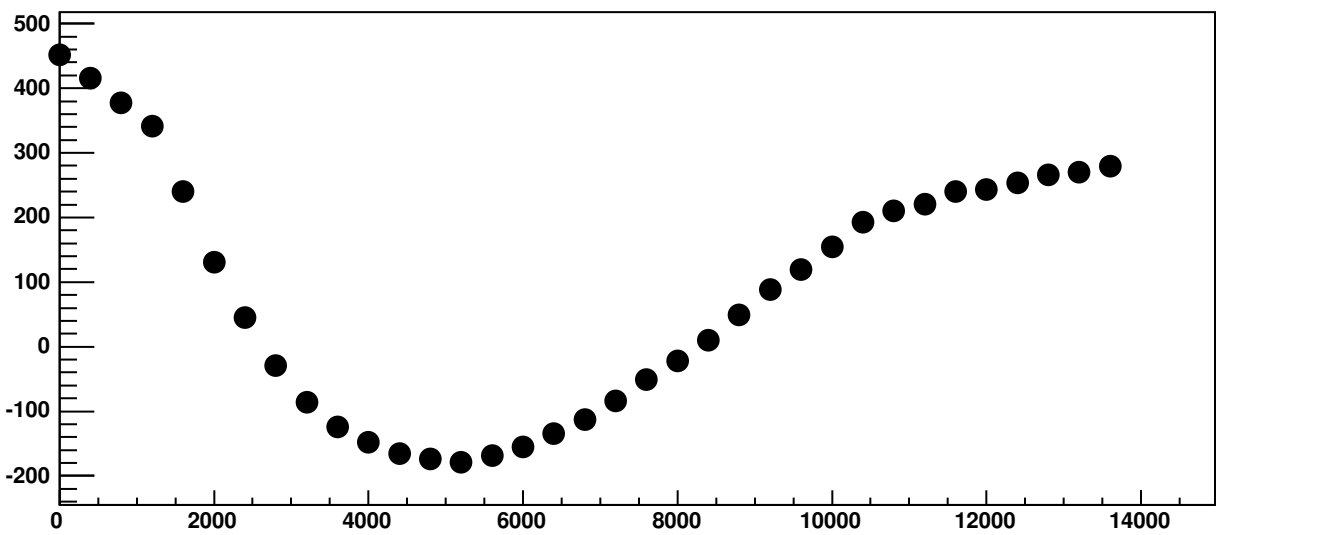
Chip 9, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

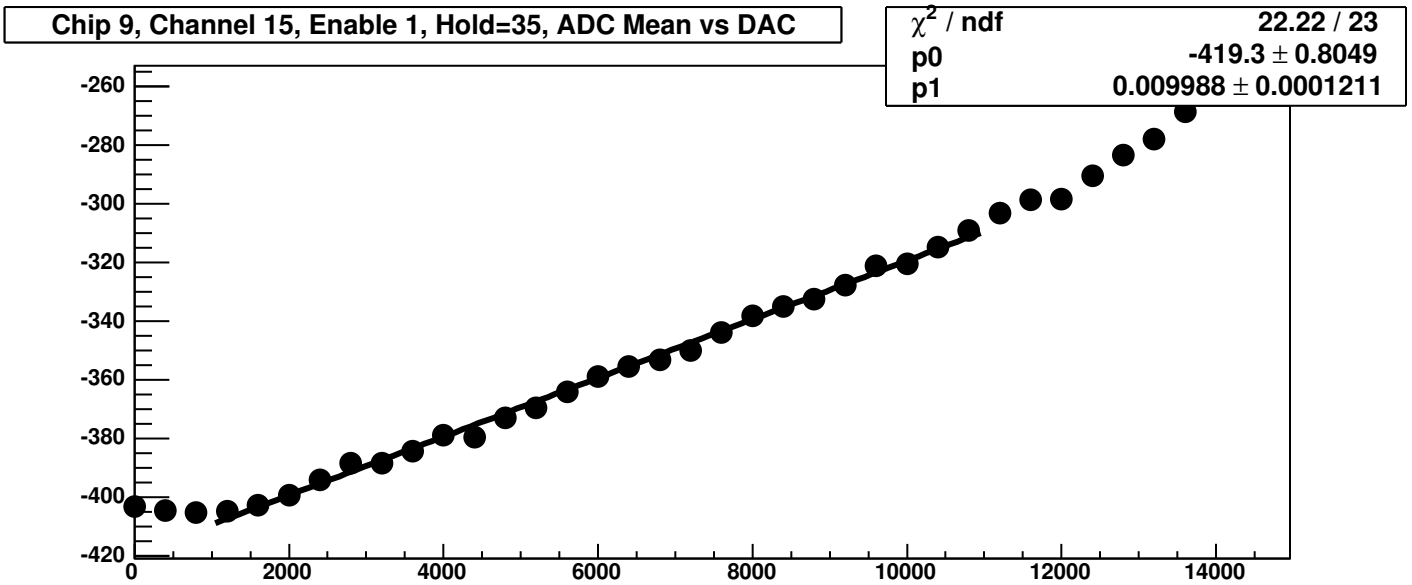


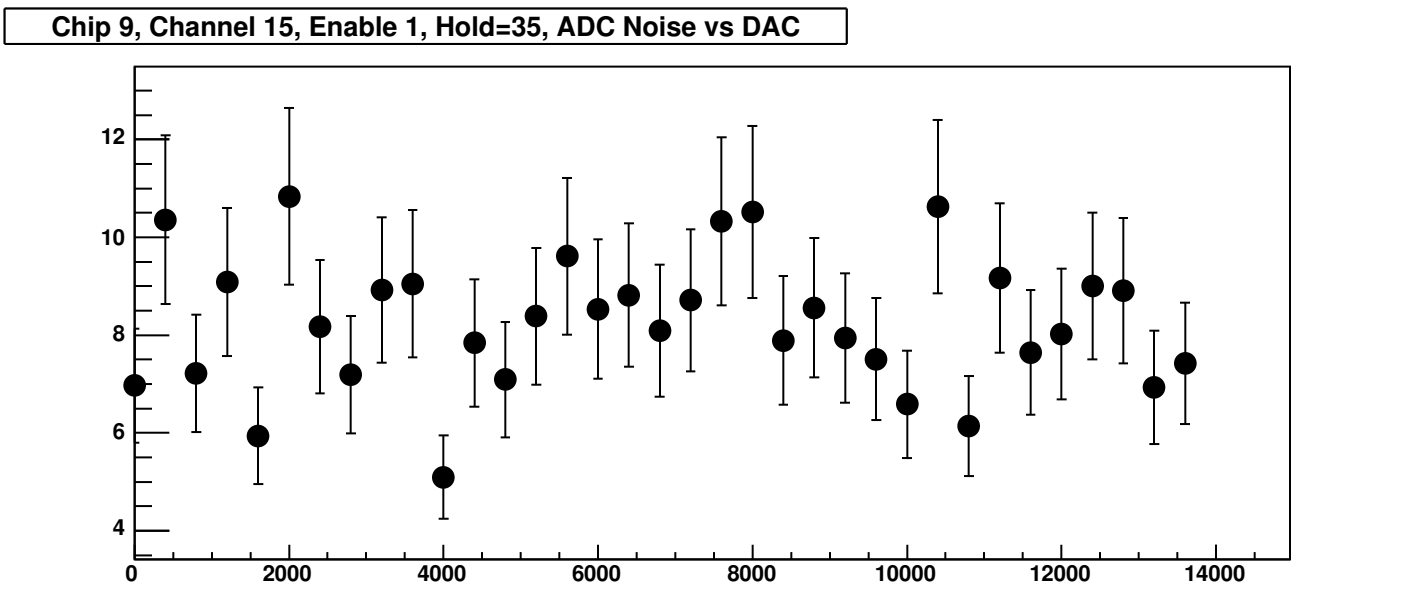
Chip 9, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC

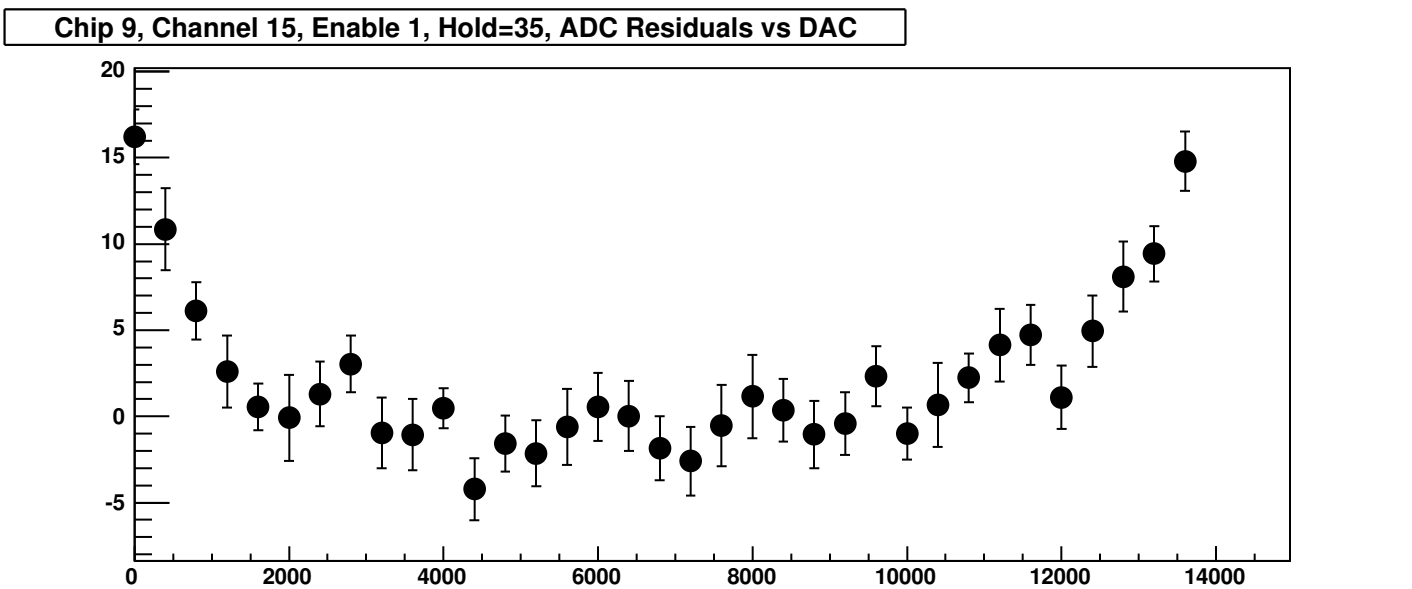


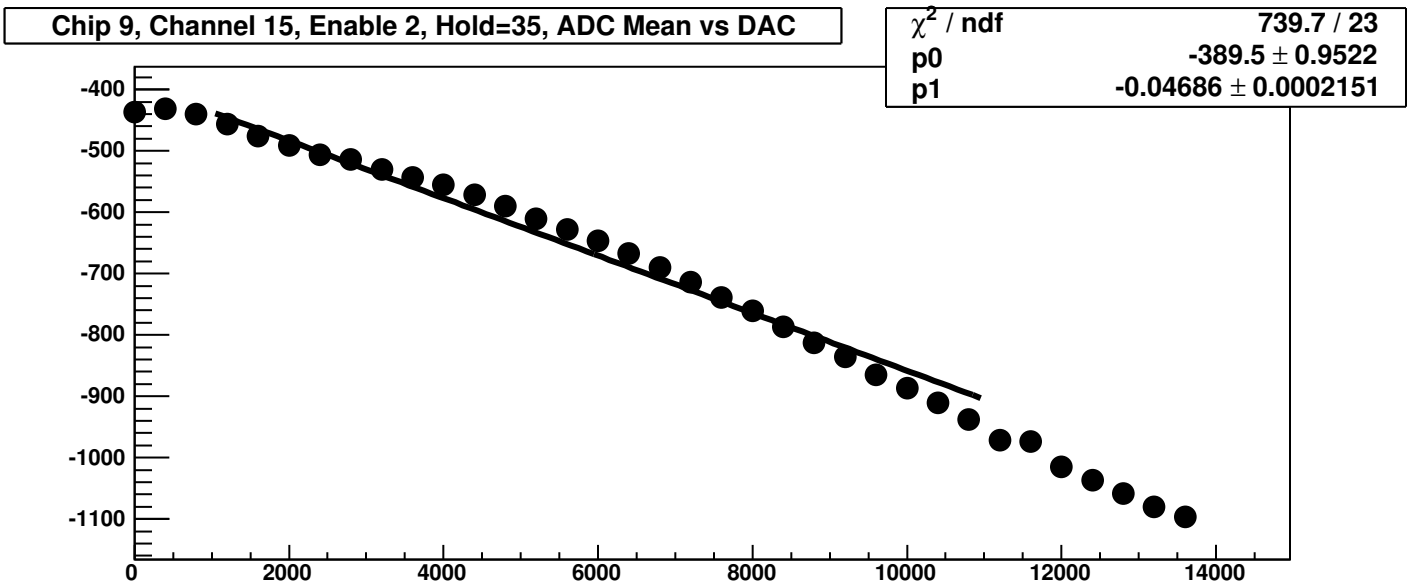
Chip 9, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

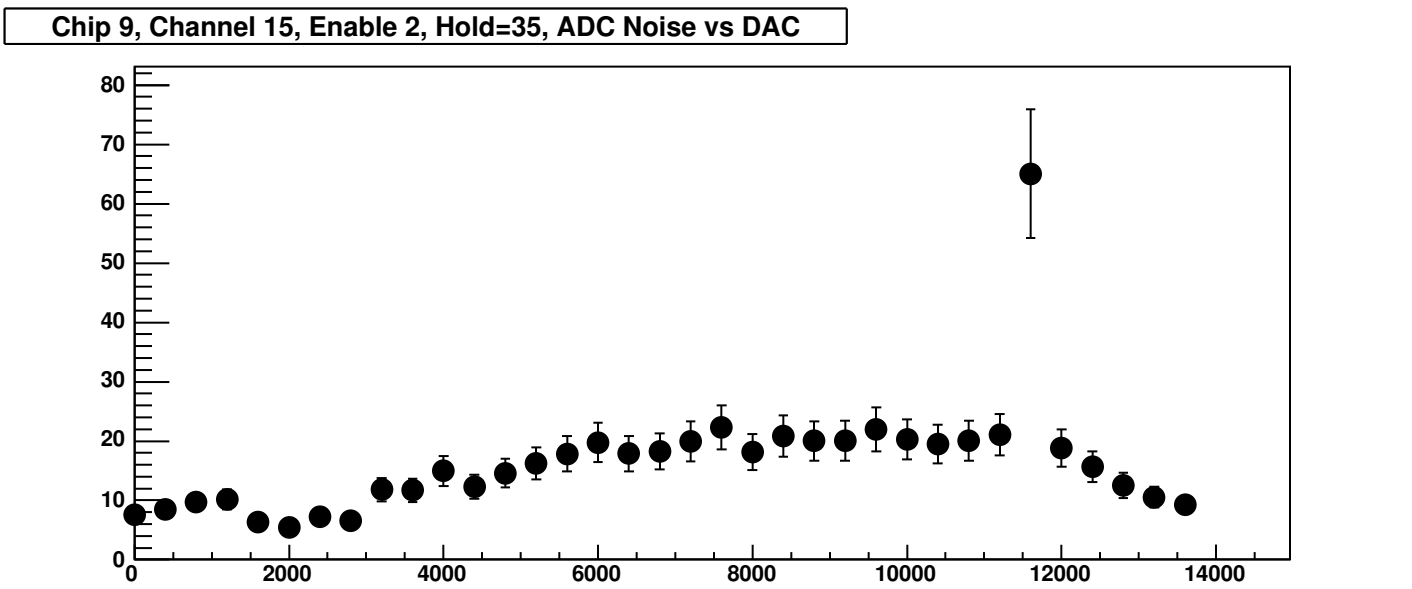


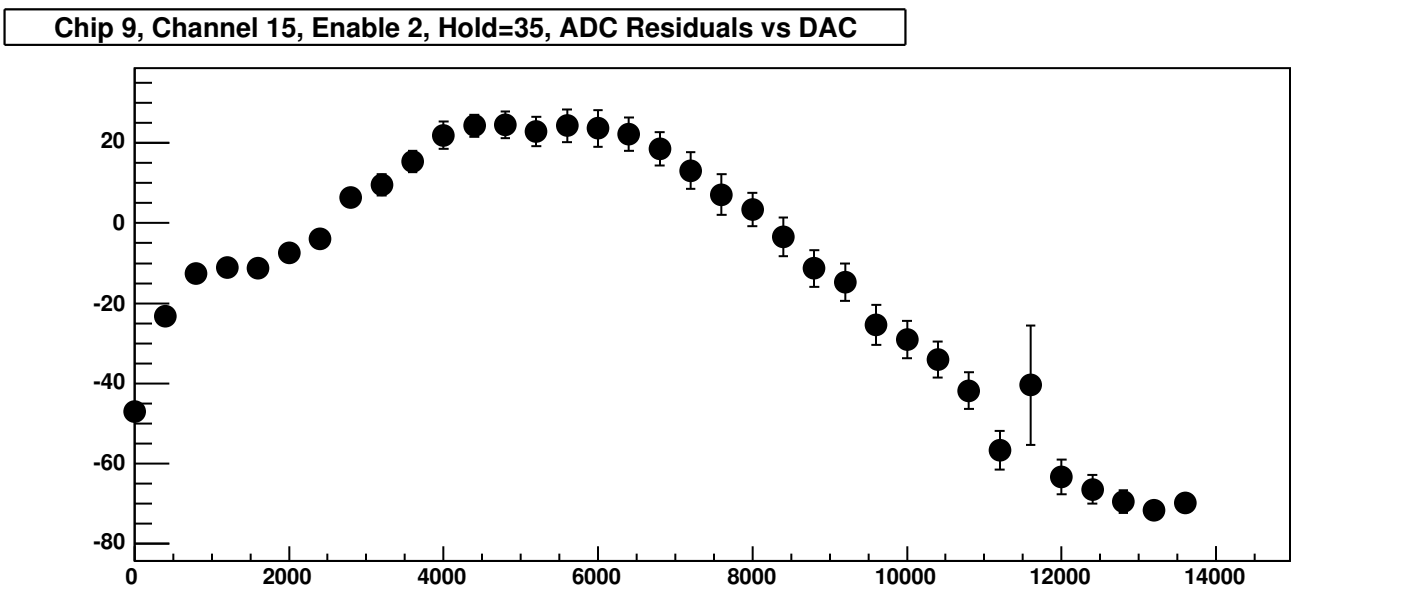

Chip 9, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 22.22 / 23
p0 -419.3 ± 0.8049
p1 0.009988 ± 0.0001211
-260
-280
-300
-320
-340
-360
-380
-400
-420
0
2000
4000
6000
8000
10000
12000
14000

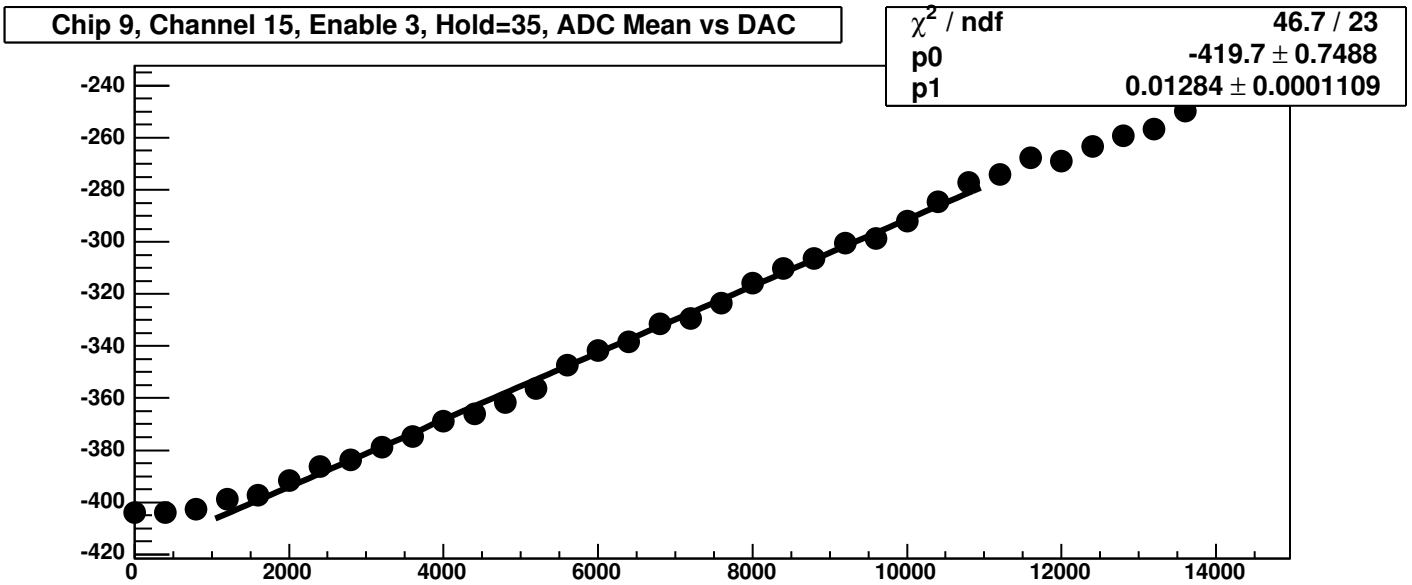

Chip 9, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

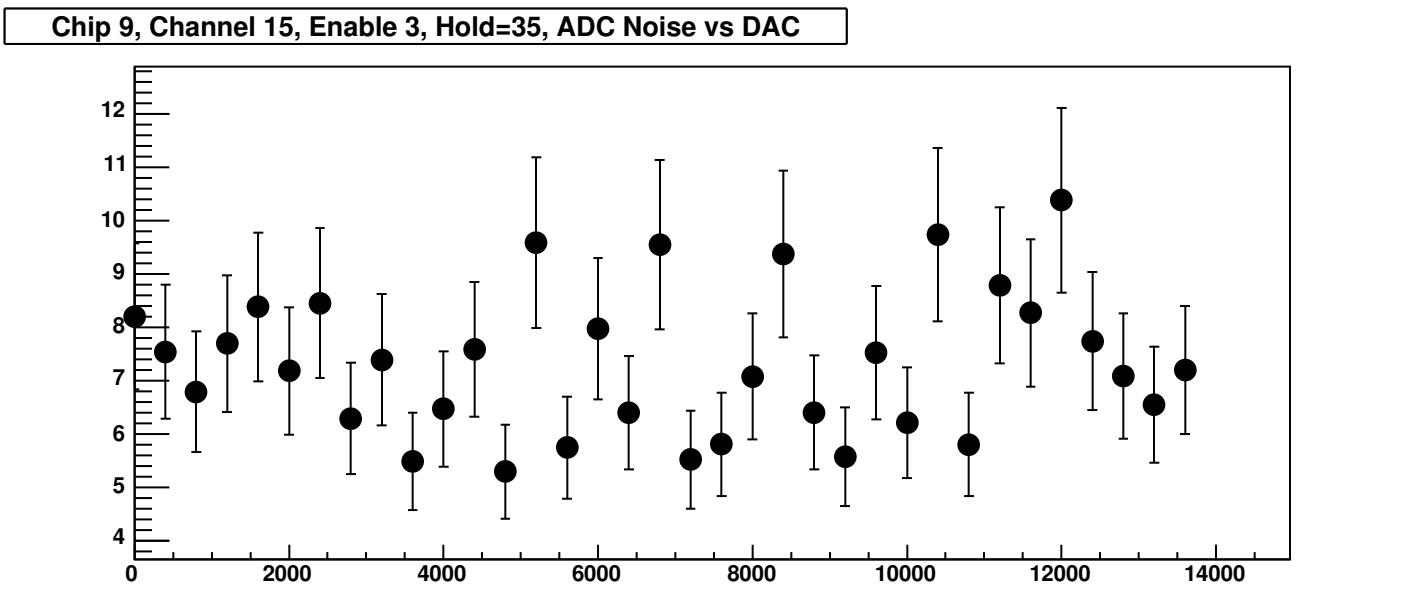

Chip 9, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

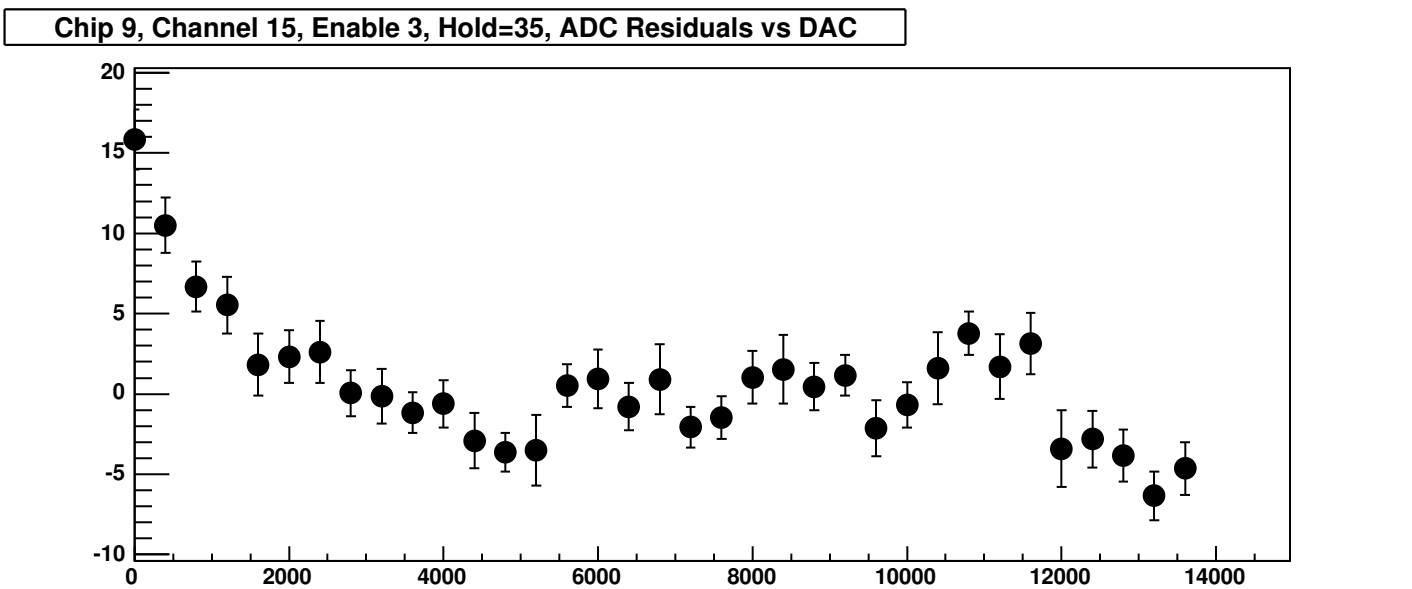

Chip 9, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 739.7 / 23
p0 -389.5 ± 0.9522
p1 -0.04686 ± 0.0002151
-400
-500
-600
-700
-800
-900
-1000
-1100
0
2000
4000
6000
8000
10000
12000
14000

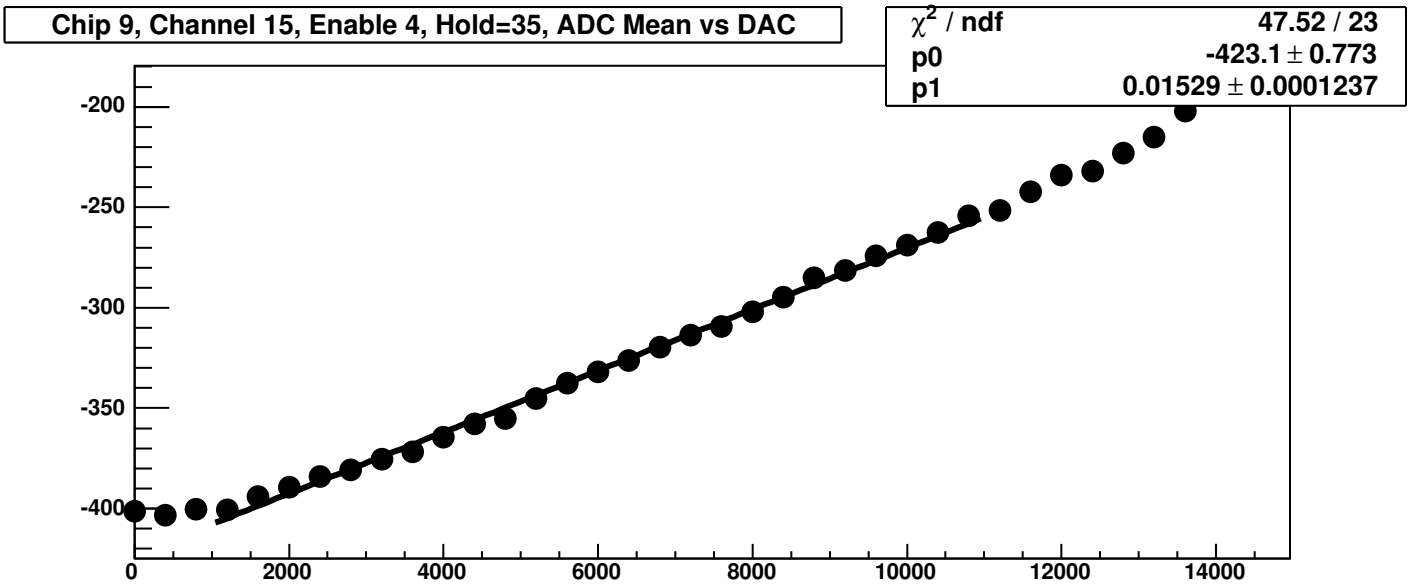

Chip 9, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

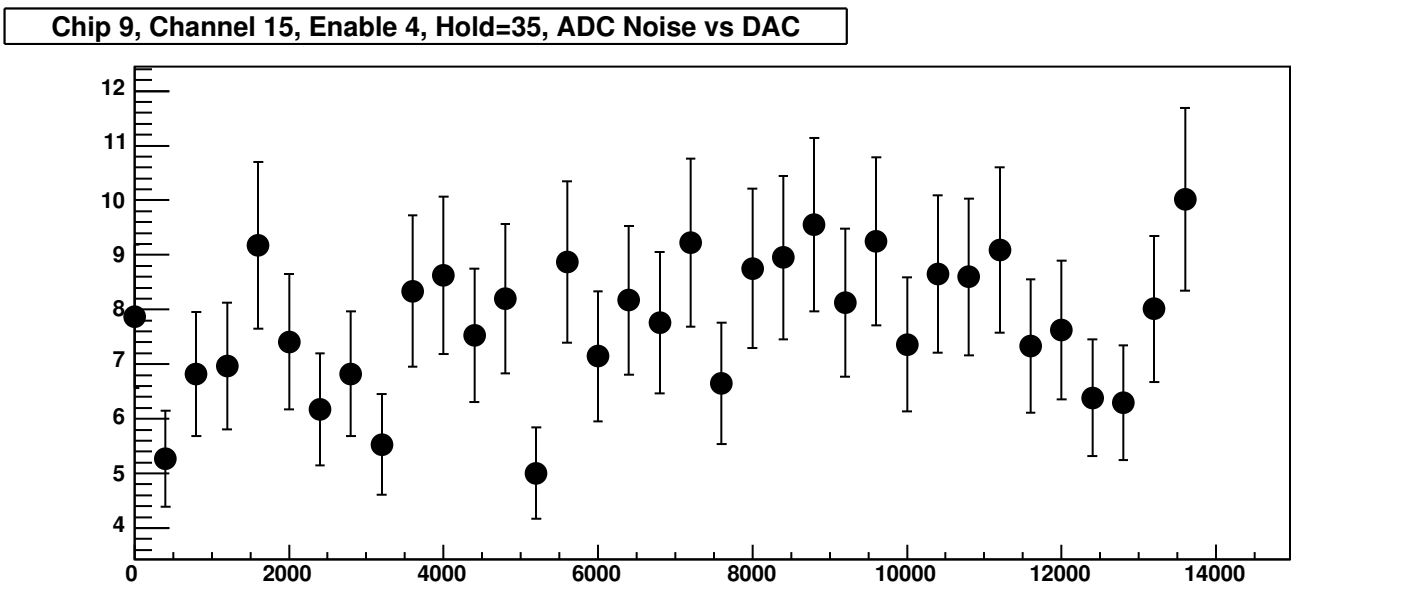

Chip 9, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000

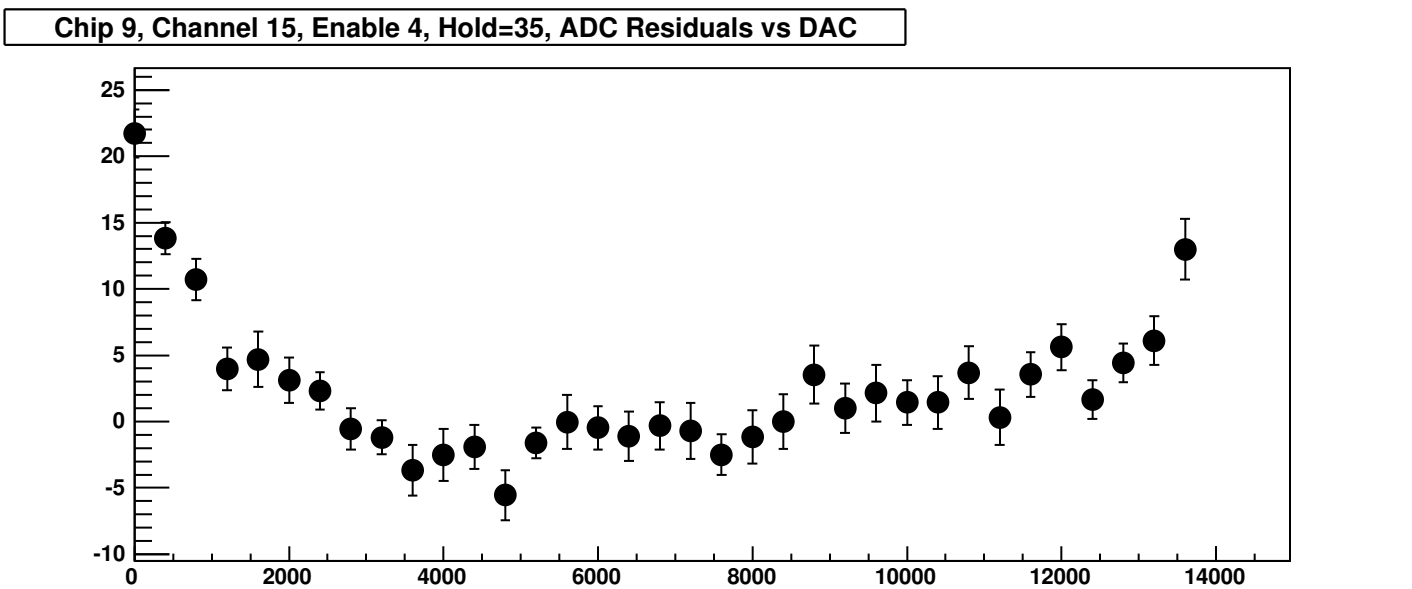

Chip 9, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 46.7 / 23
p0 -419.7 ± 0.7488
p1 0.01284 ± 0.0001109
-240
-260
-280
-300
-320
-340
-360
-380
-400
-420
0
2000
4000
6000
8000
10000
12000
14000

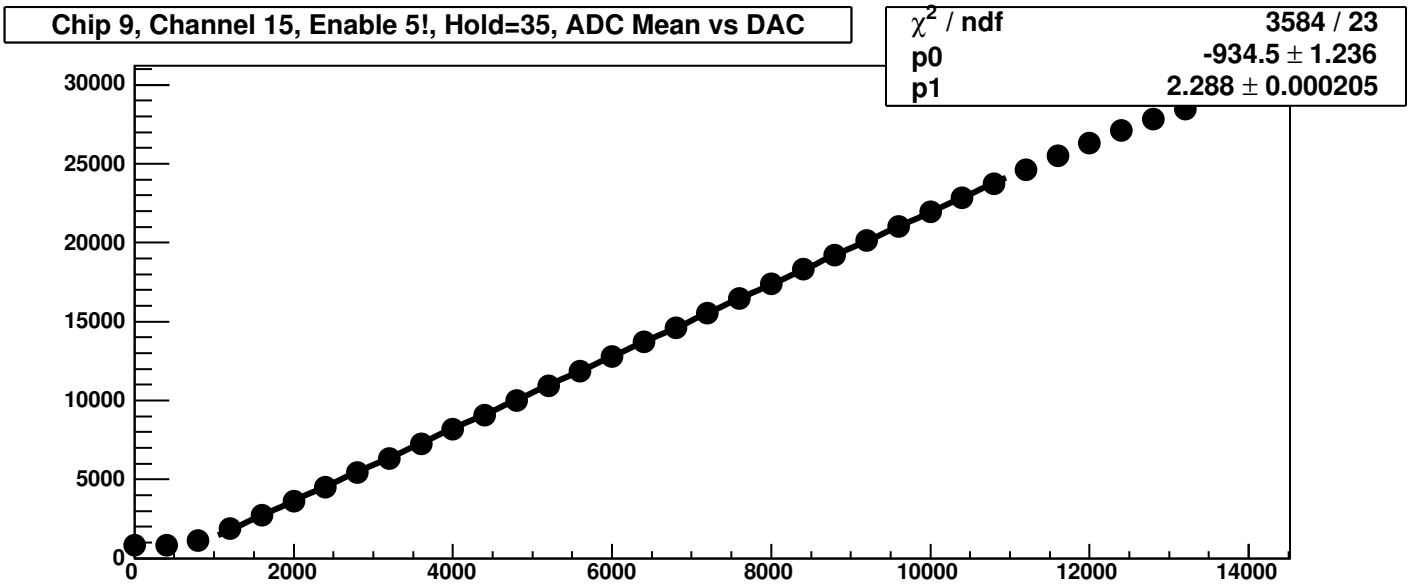

Chip 9, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

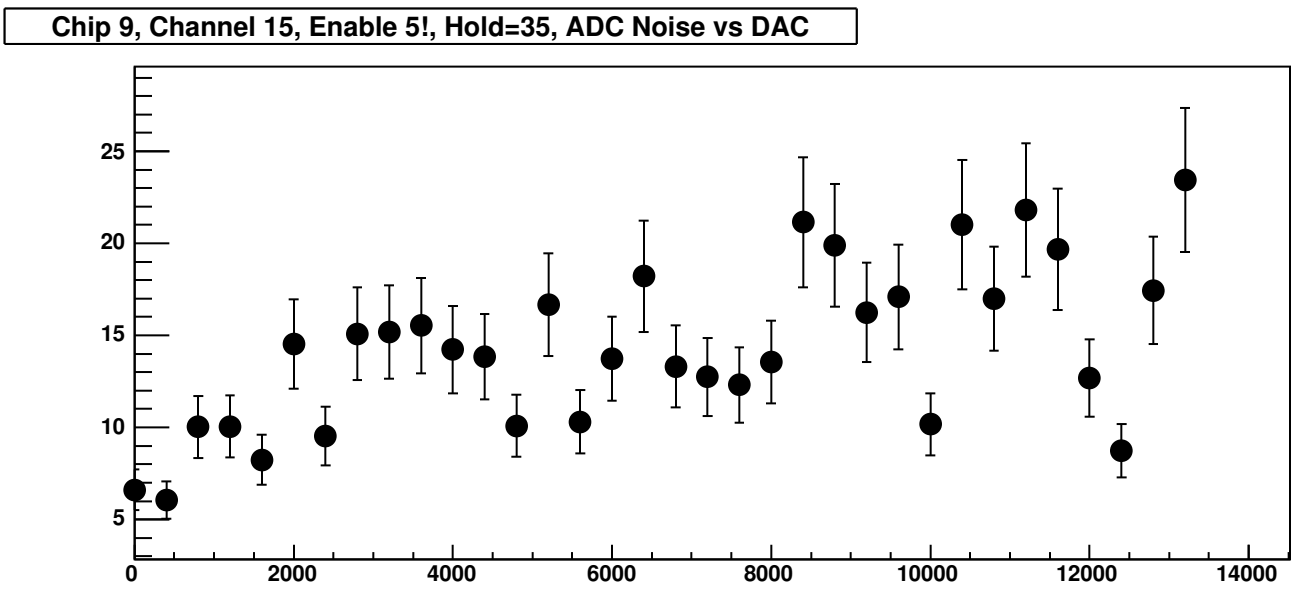

Chip 9, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

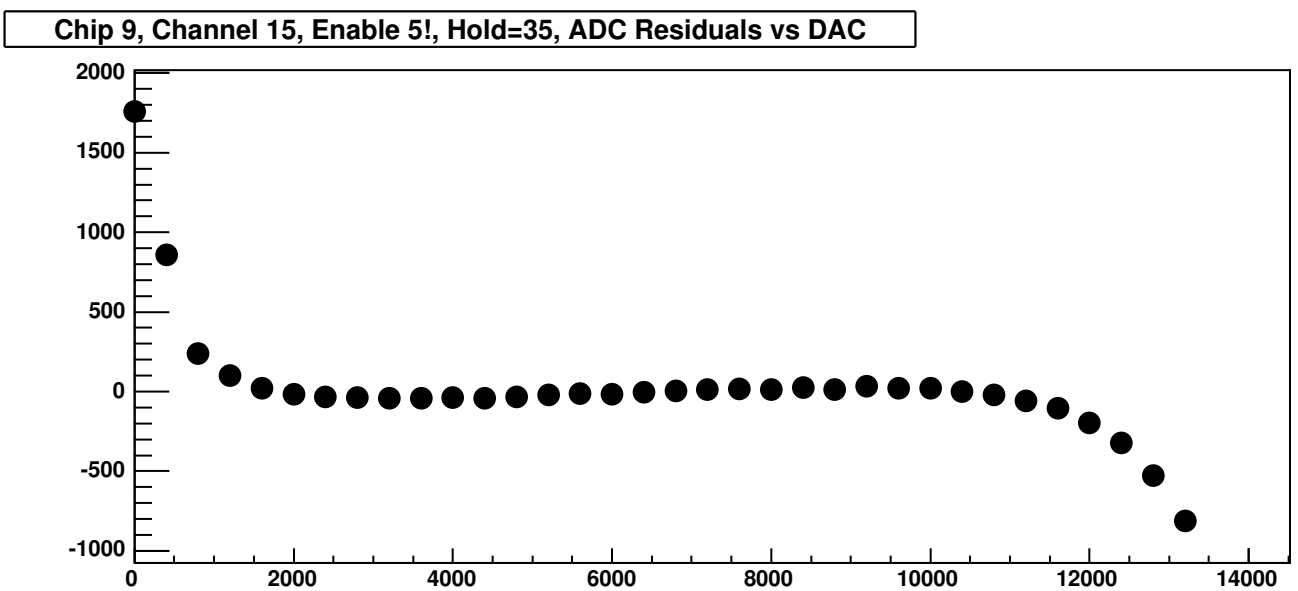

Chip 9, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 47.52 / 23
p0 -423.1 ± 0.773
p1 0.01529 ± 0.0001237
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

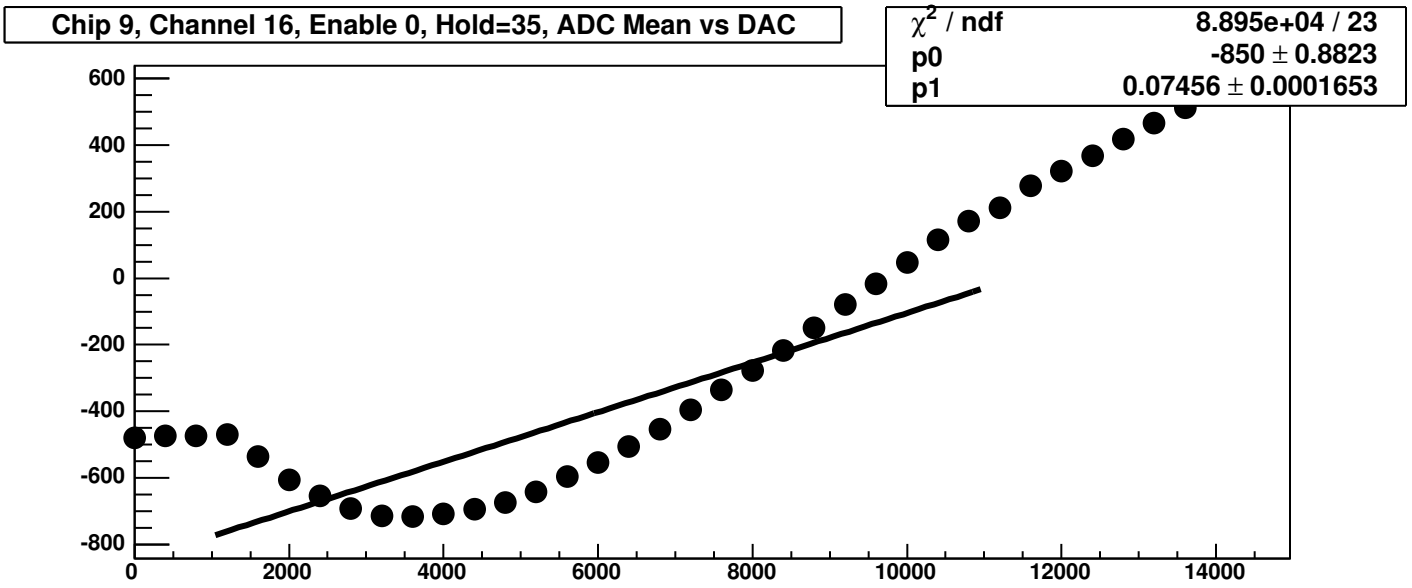

Chip 9, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

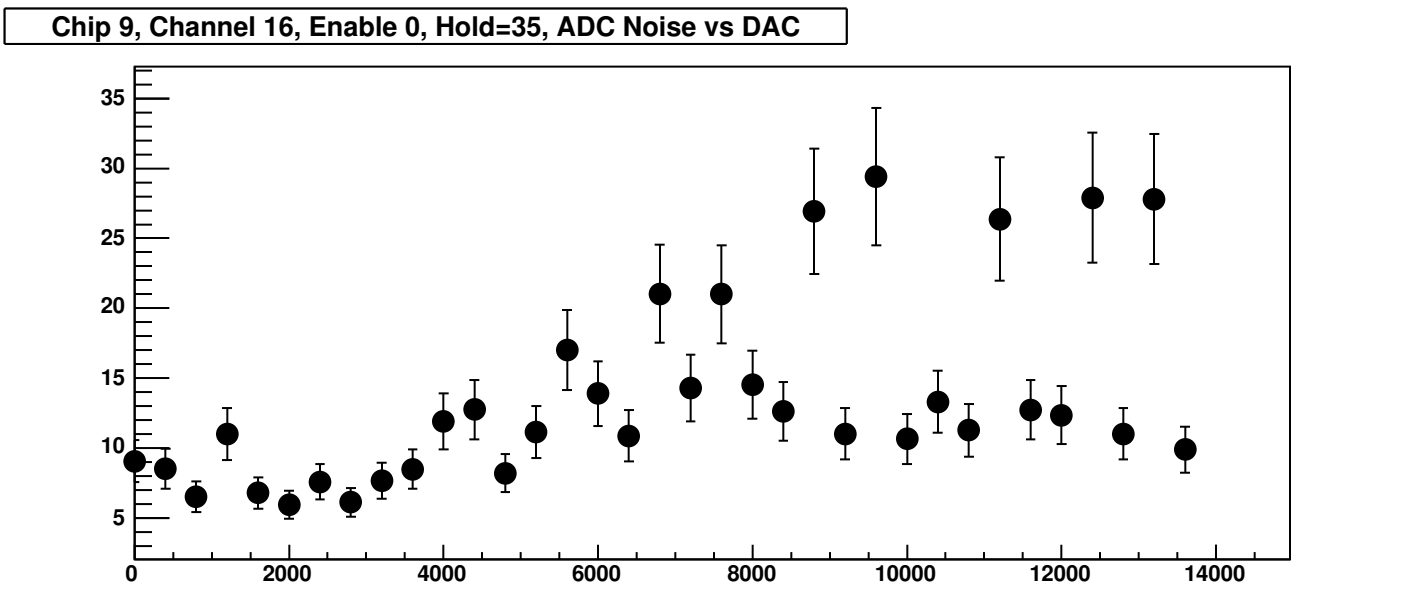

Chip 9, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

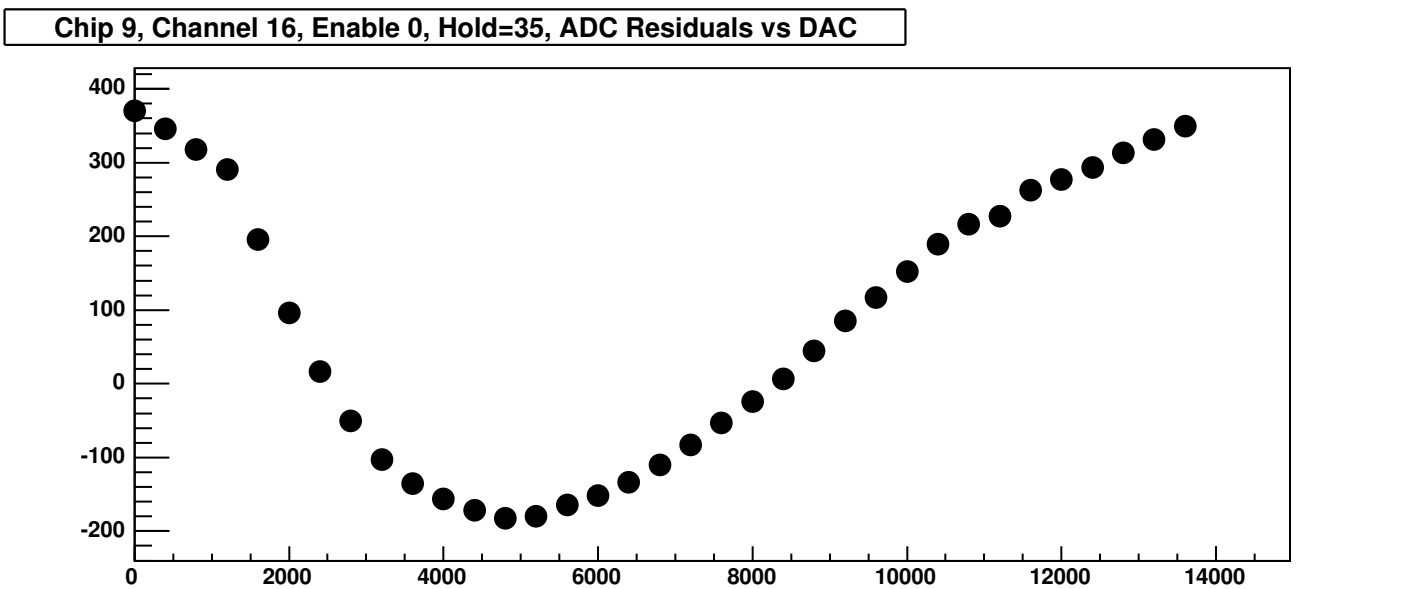

Chip 9, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3584 / 23
p0 -934.5 ± 1.236
p1 2.288 ± 0.000205
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

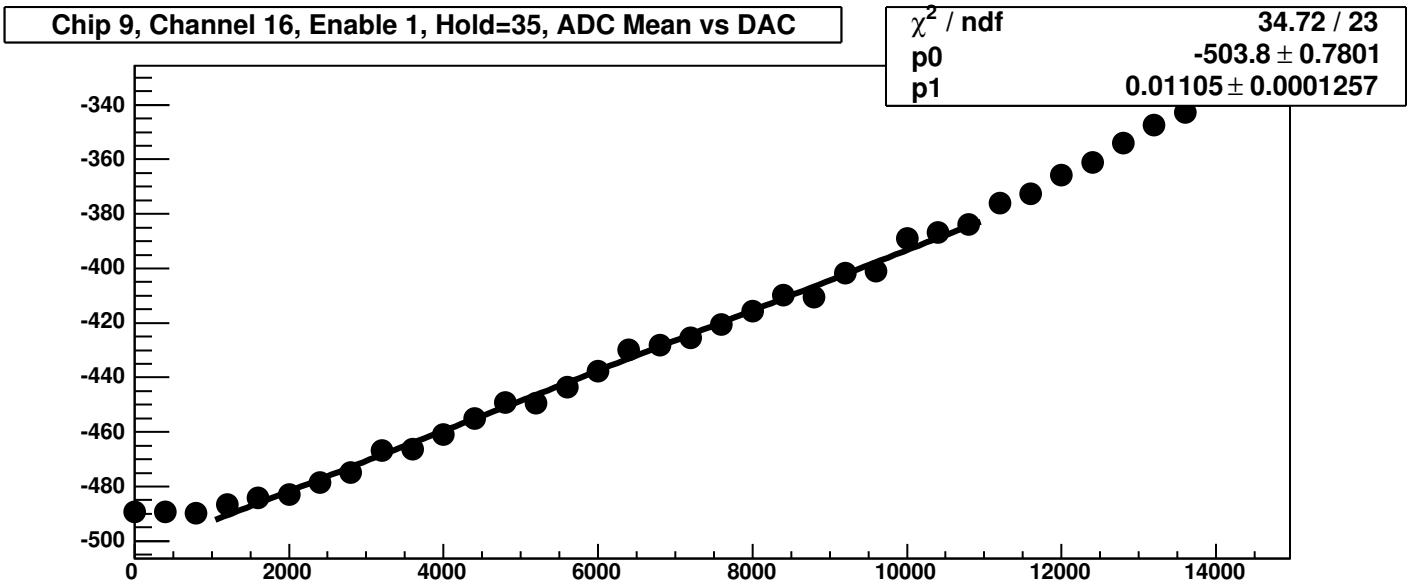

Chip 9, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

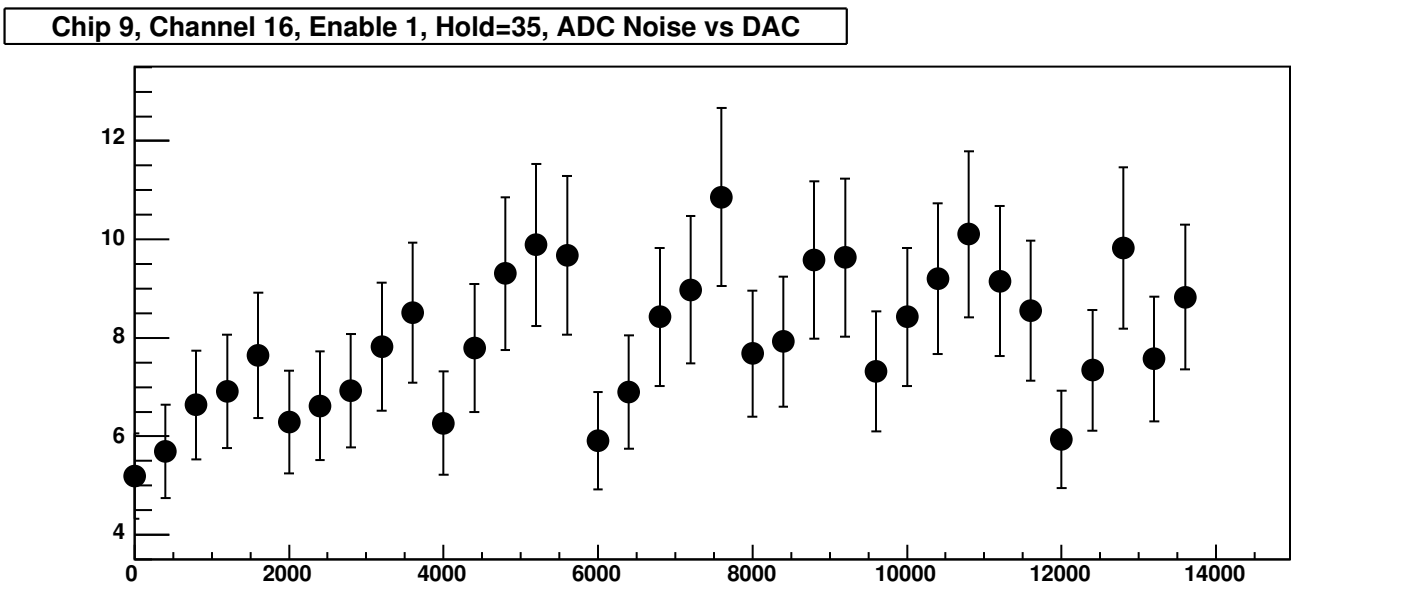

Chip 9, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

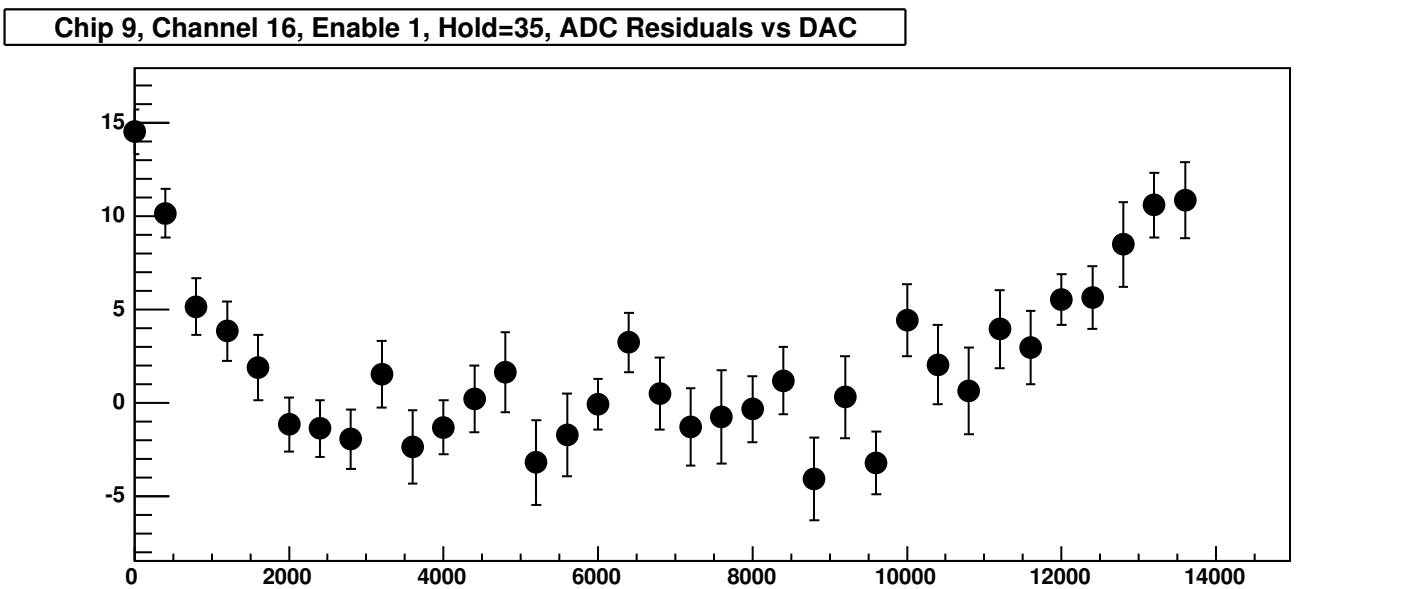

Chip 9, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 8.895e+04 / 23
p0 -850 ± 0.8823
p1 0.07456 ± 0.0001653
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

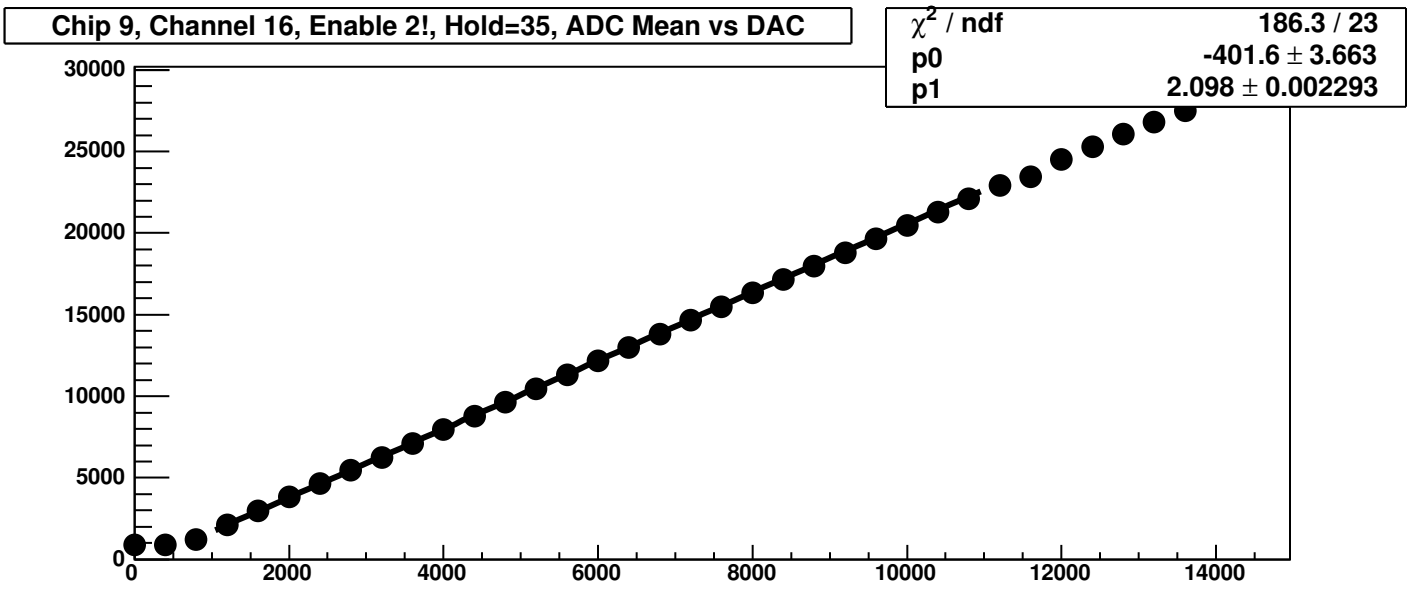

Chip 9, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

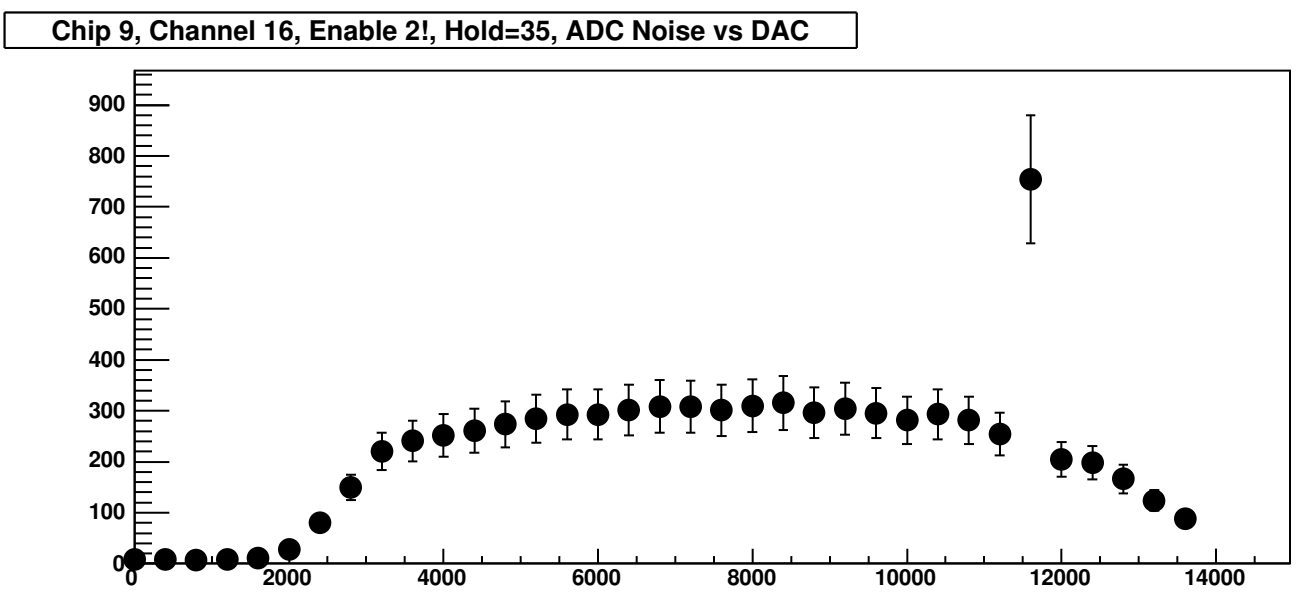

Chip 9, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

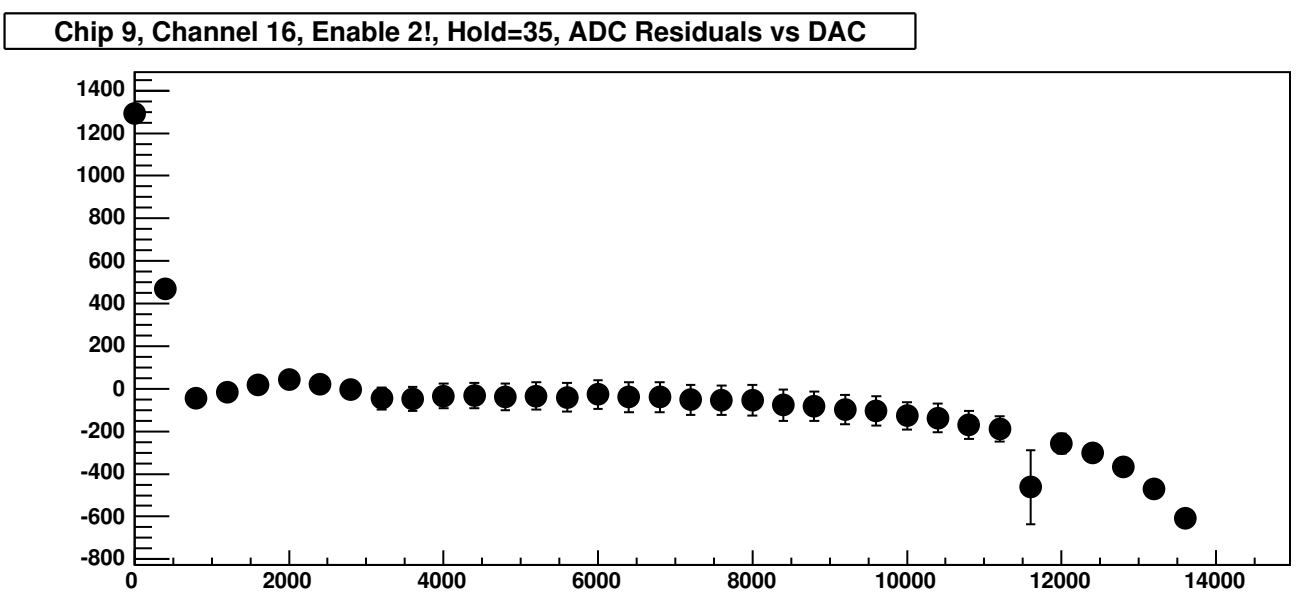

Chip 9, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 34.72 / 23
p0 -503.8 ± 0.7801
p1 0.01105 ± 0.0001257
-340
-360
-380
-400
-420
-440
-460
-480
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

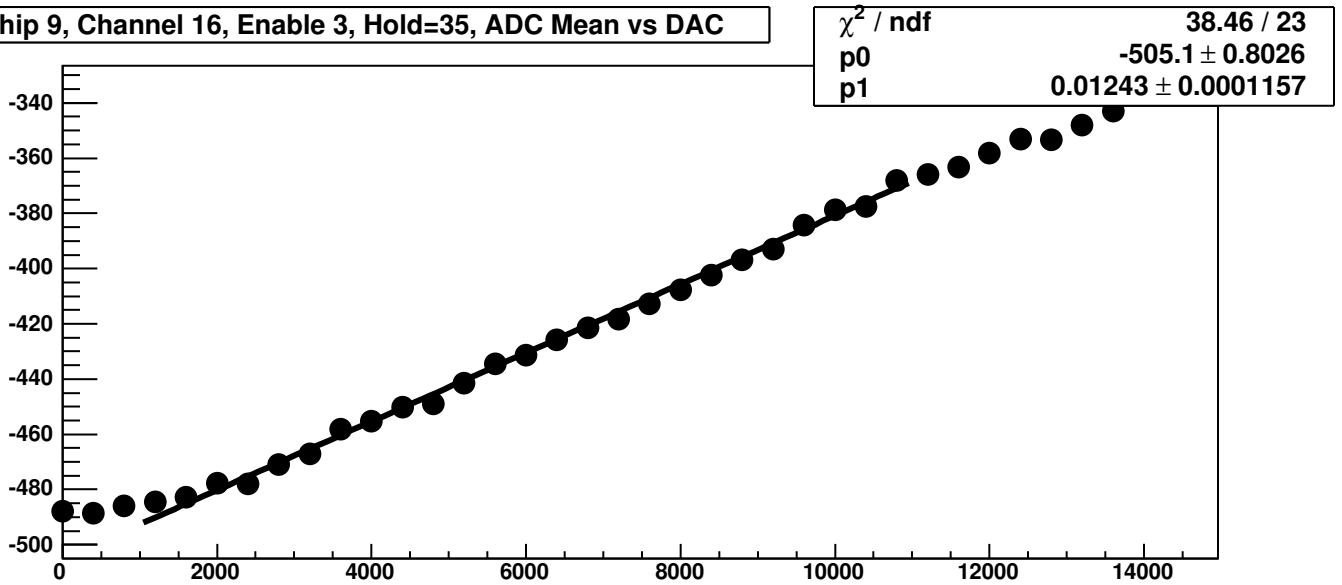

Chip 9, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 186.3 / 23
p0 -401.6 ± 3.663
p1 2.098 ± 0.002293
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

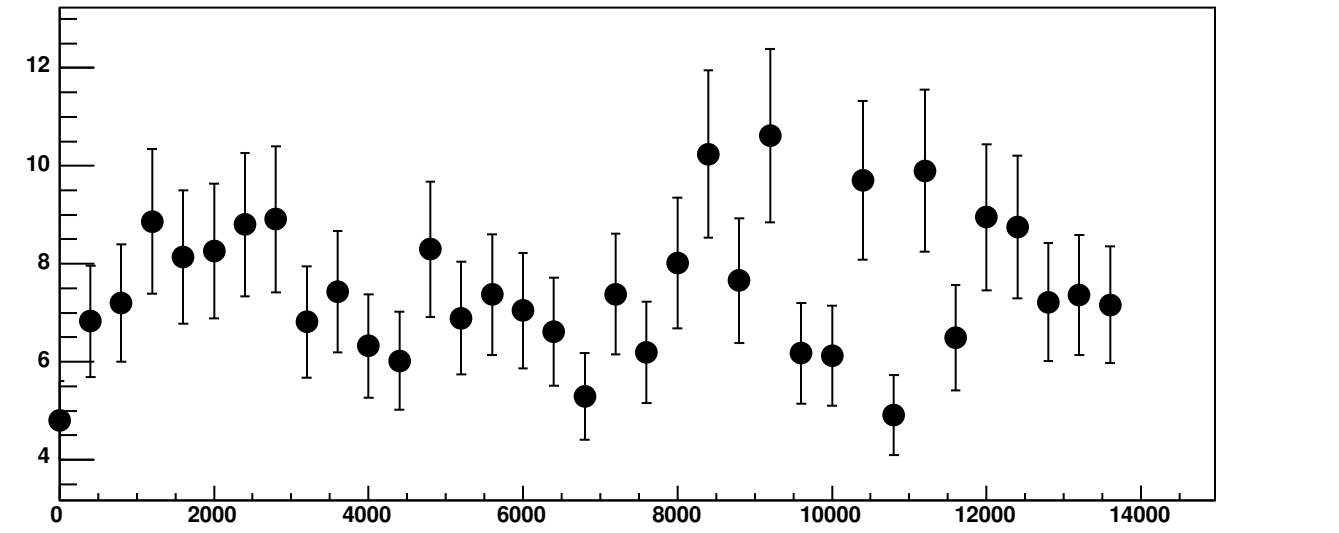

Chip 9, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
900
800
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
1400
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

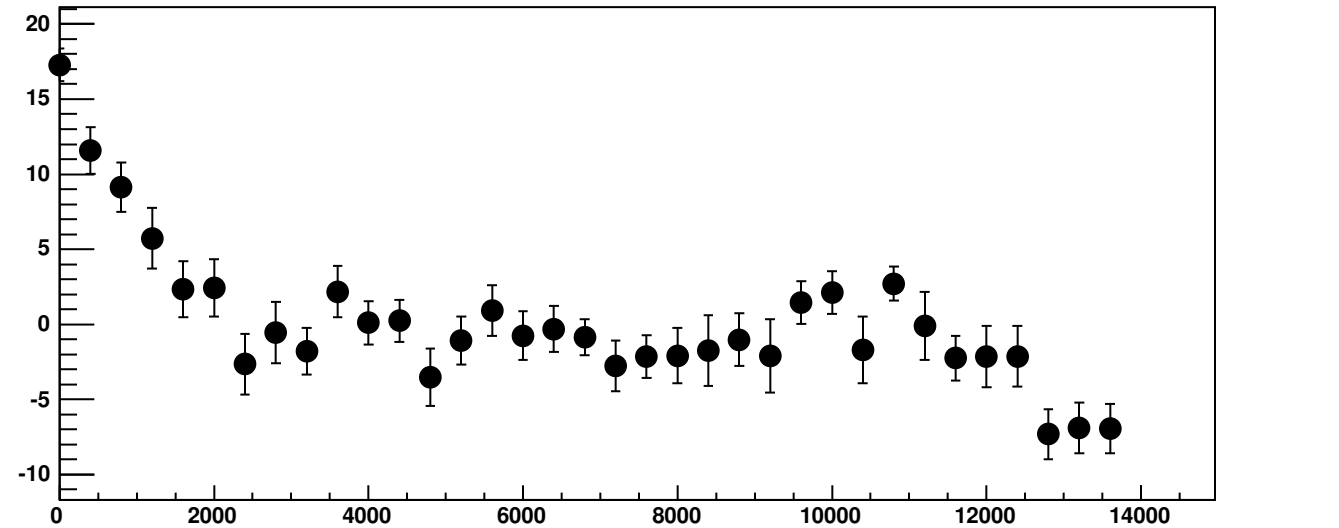
Chip 9, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

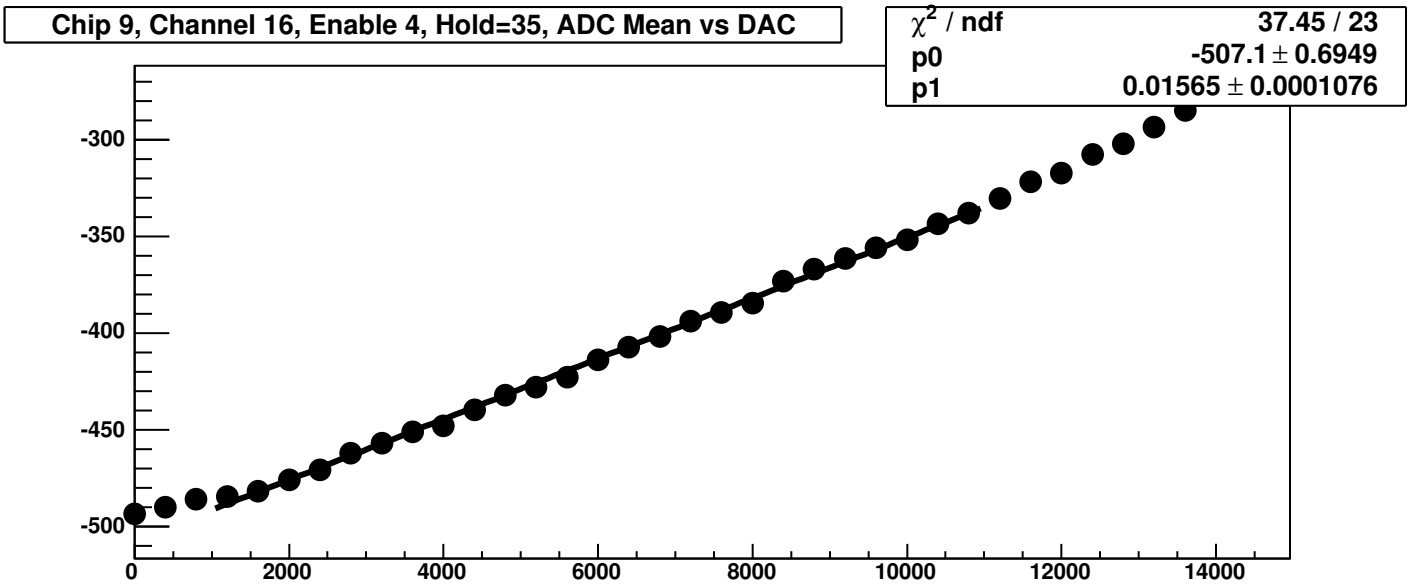


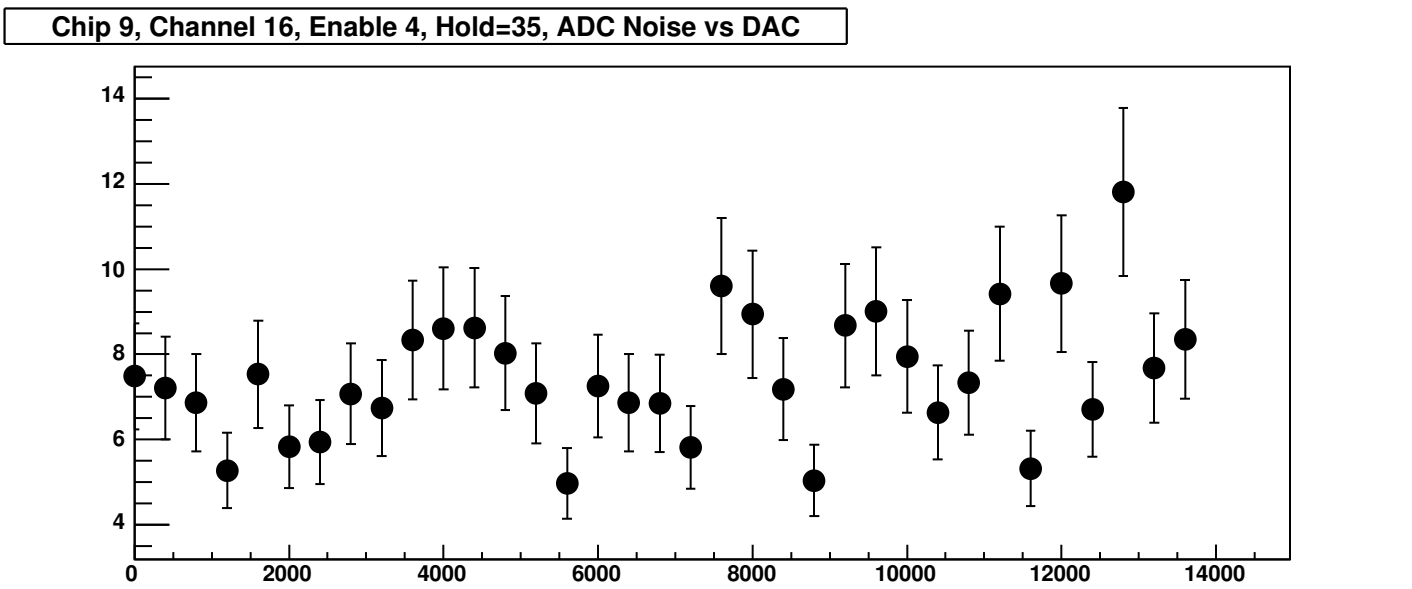
Chip 9, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC

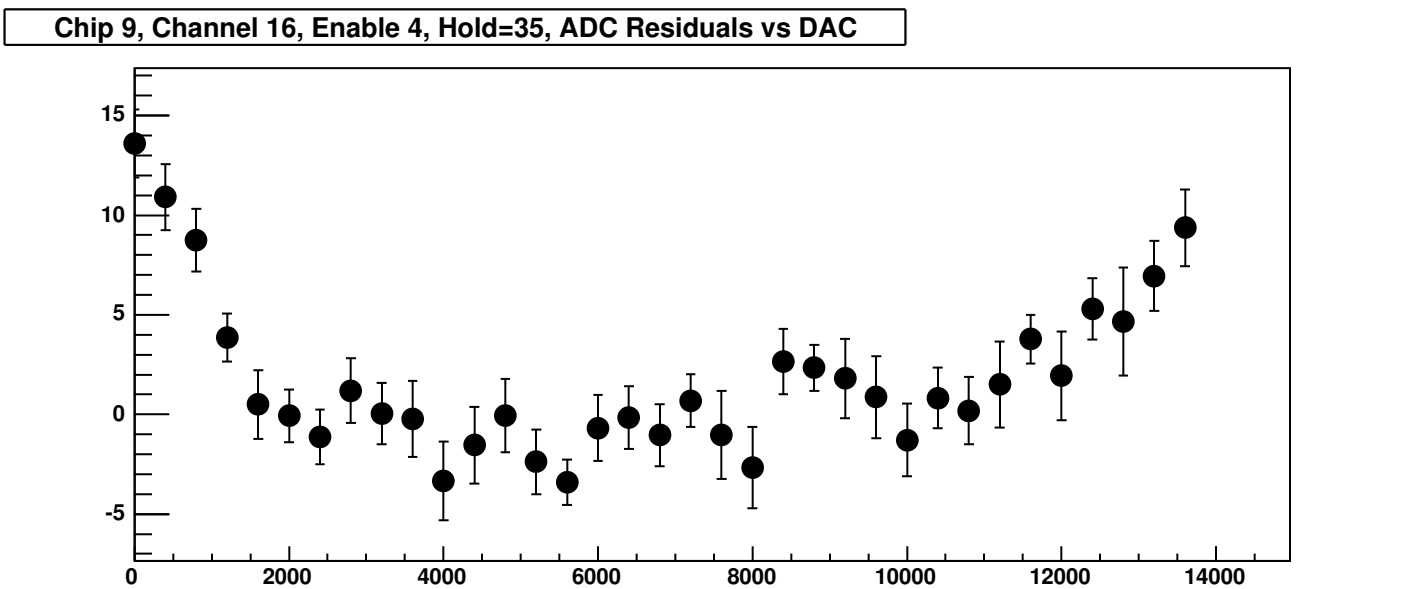


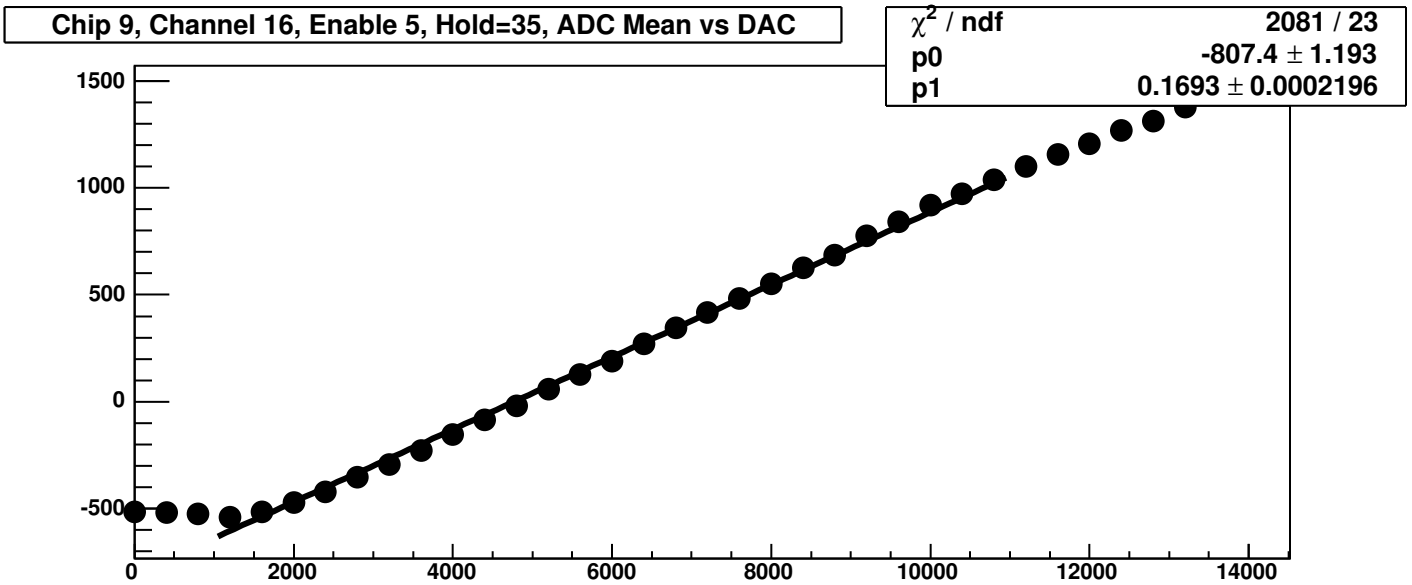
Chip 9, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC

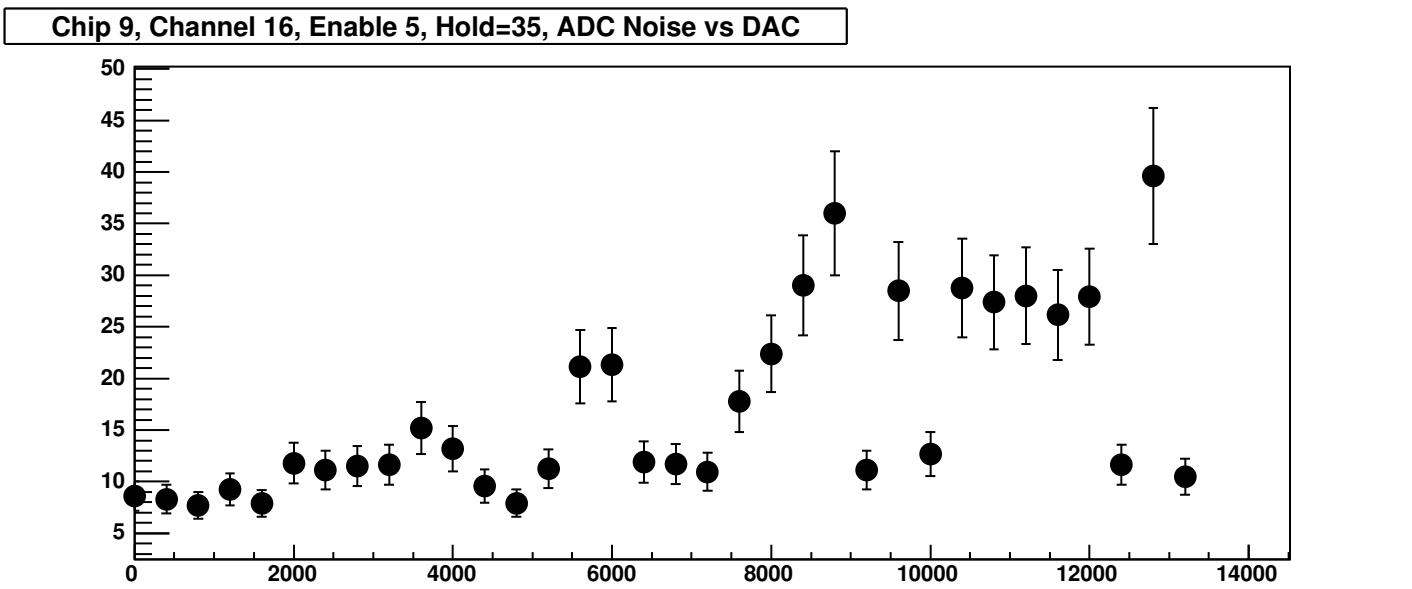


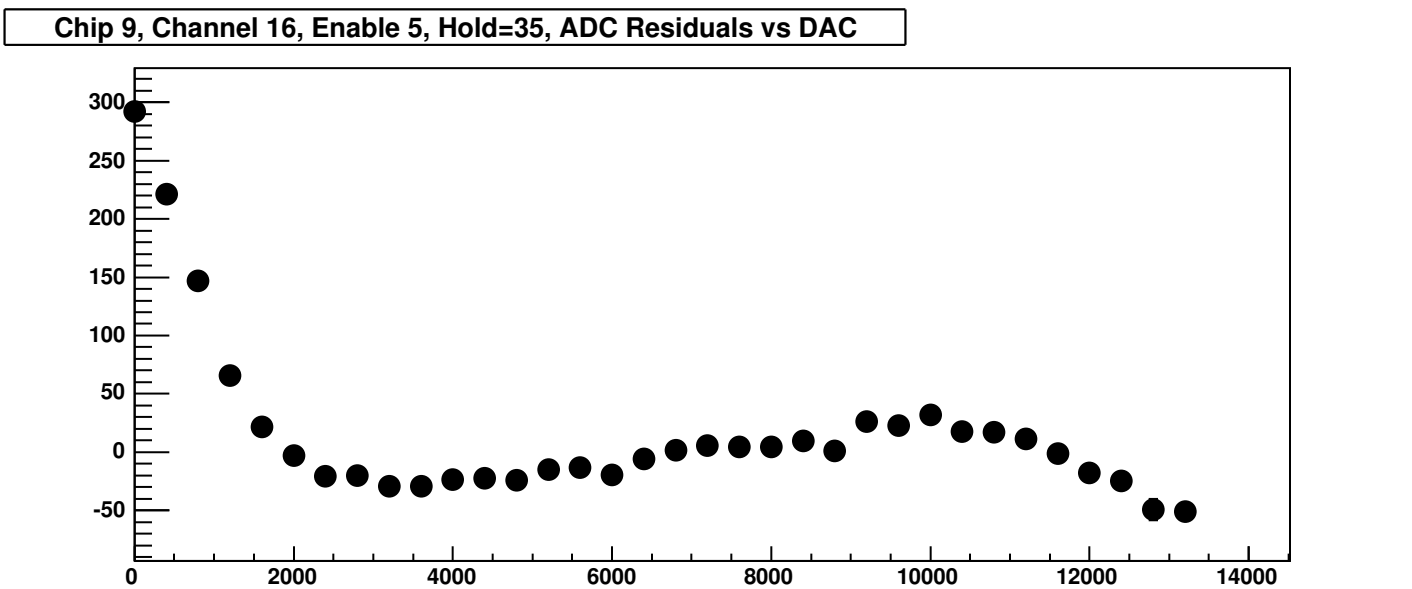

Chip 9, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 37.45 / 23
p0 -507.1 ± 0.6949
p1 0.01565 ± 0.0001076
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

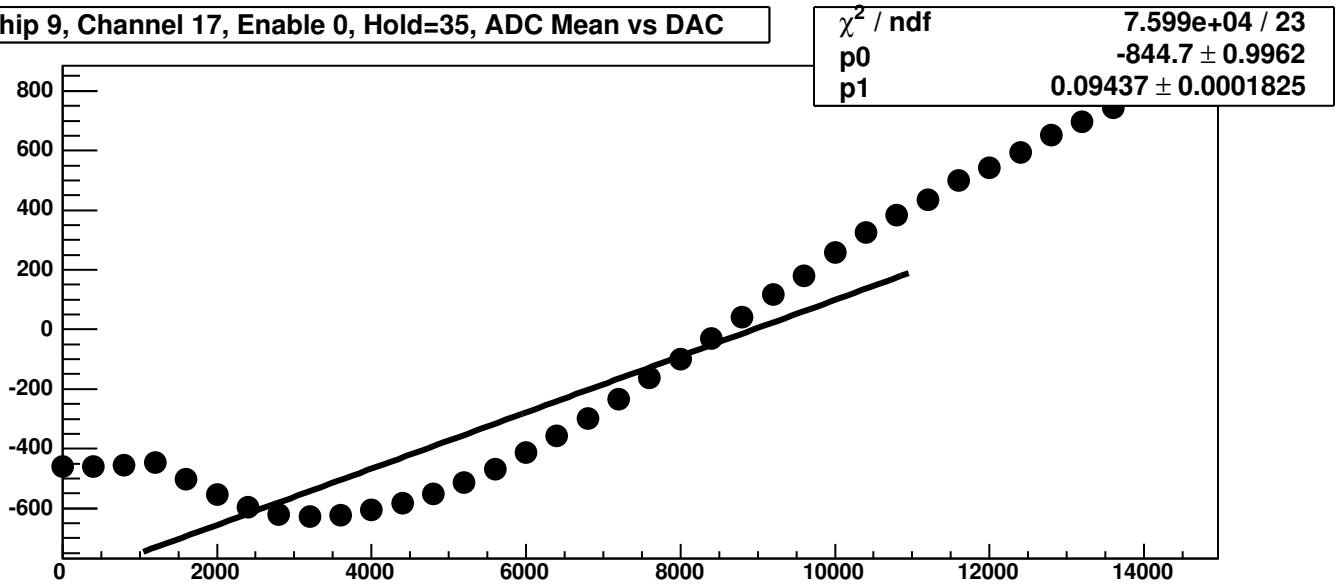

Chip 9, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2081 / 23
p0 -807.4 ± 1.193
p1 0.1693 ± 0.0002196
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

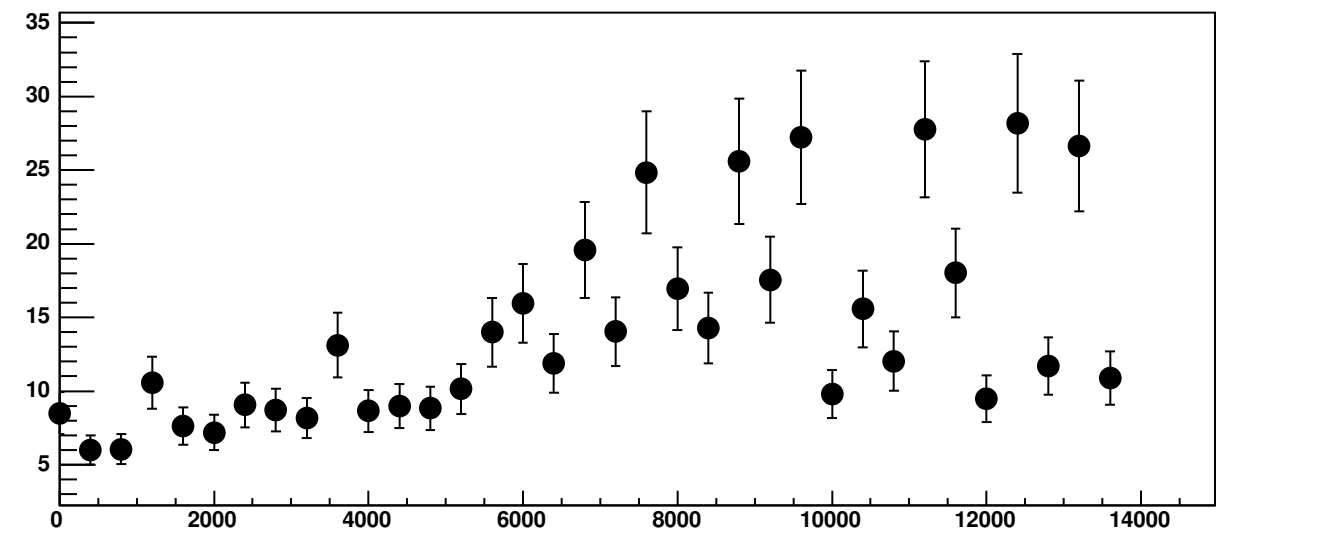

Chip 9, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 9, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

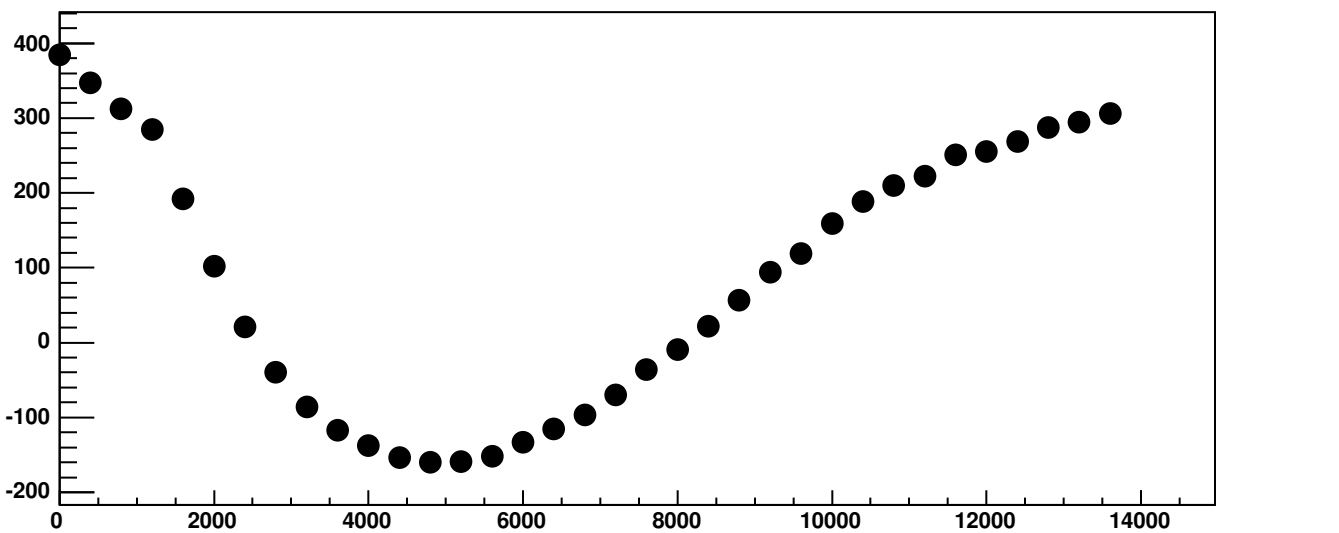
Chip 9, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC



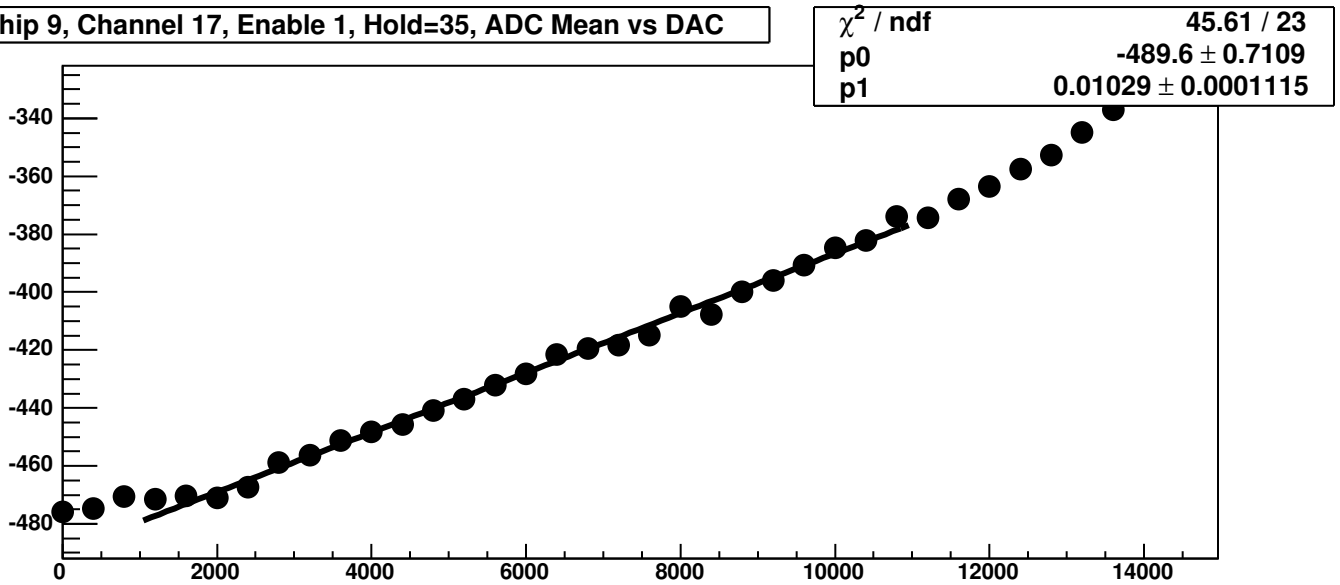
Chip 9, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC



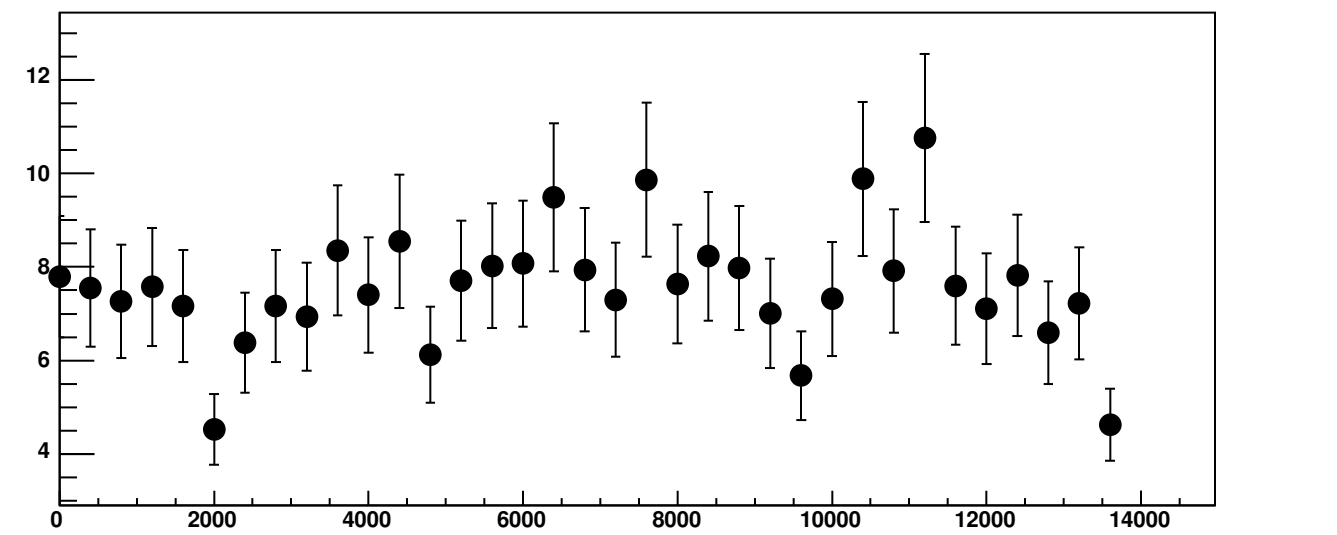
Chip 9, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC



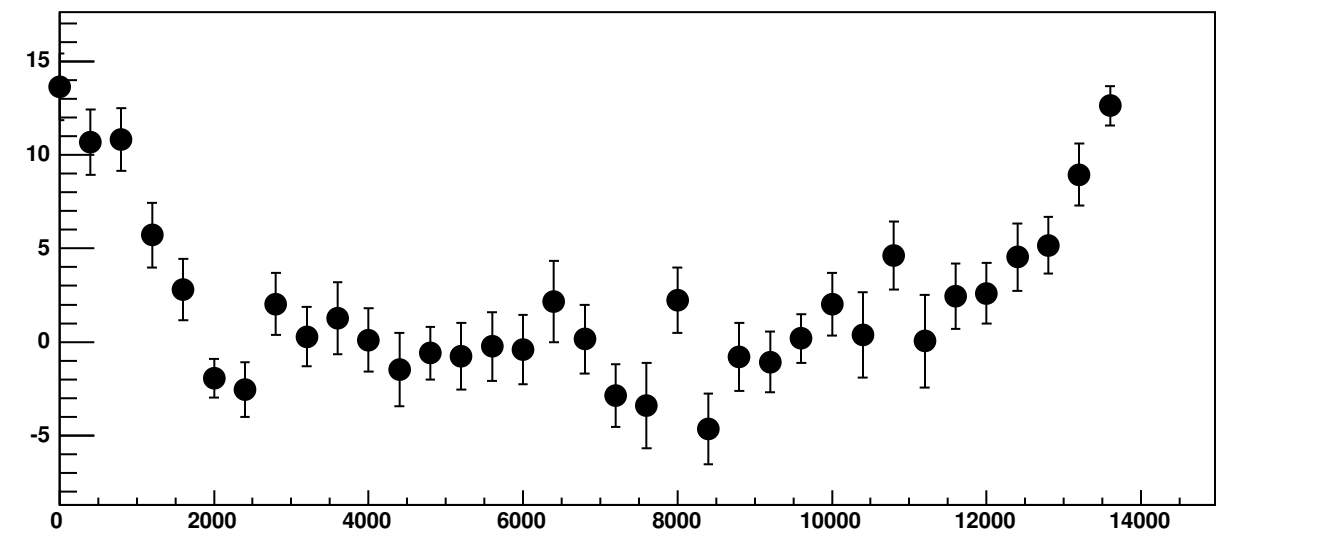
Chip 9, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

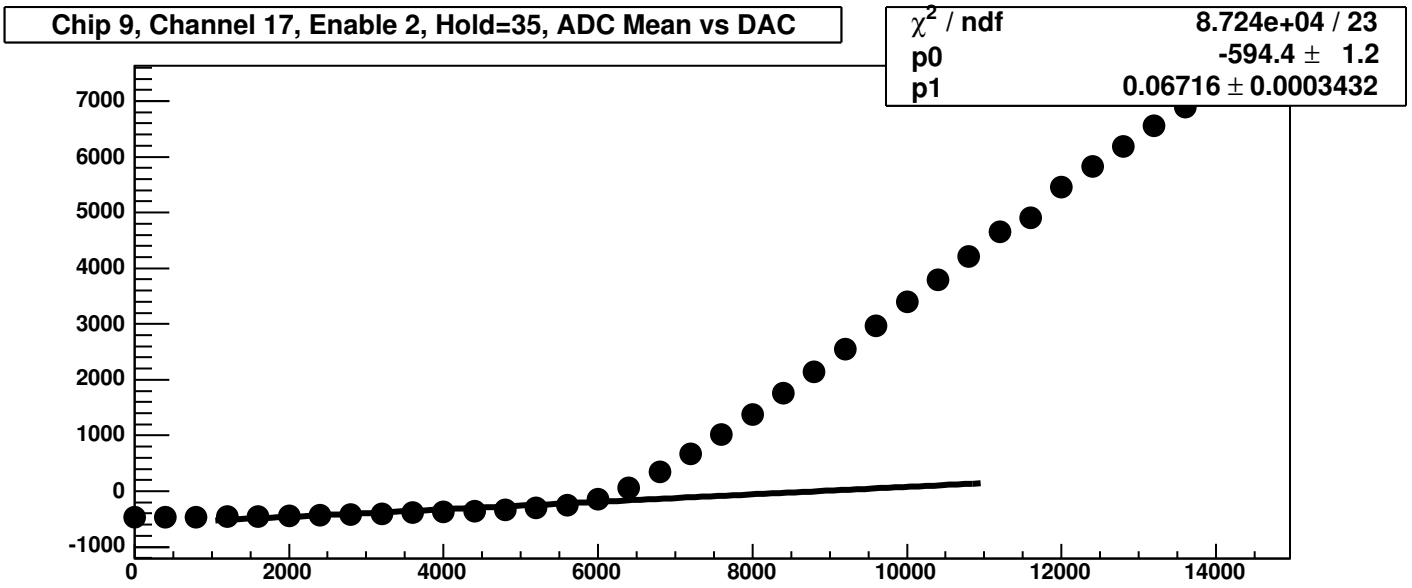


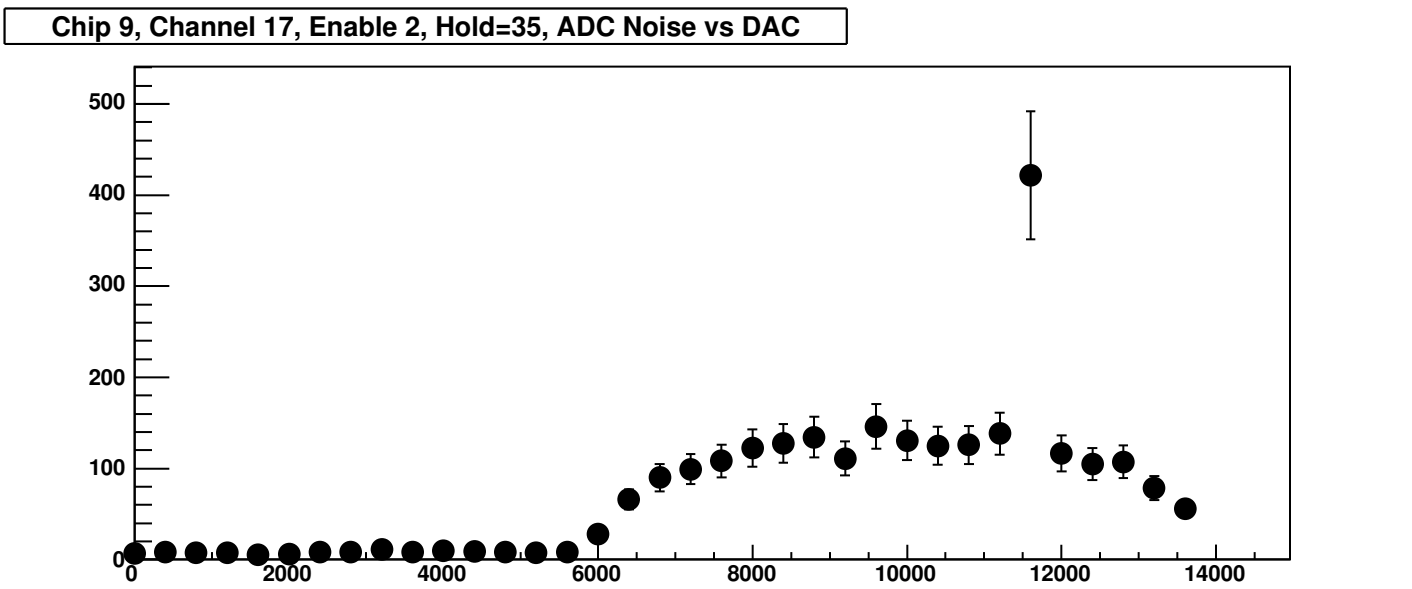
Chip 9, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

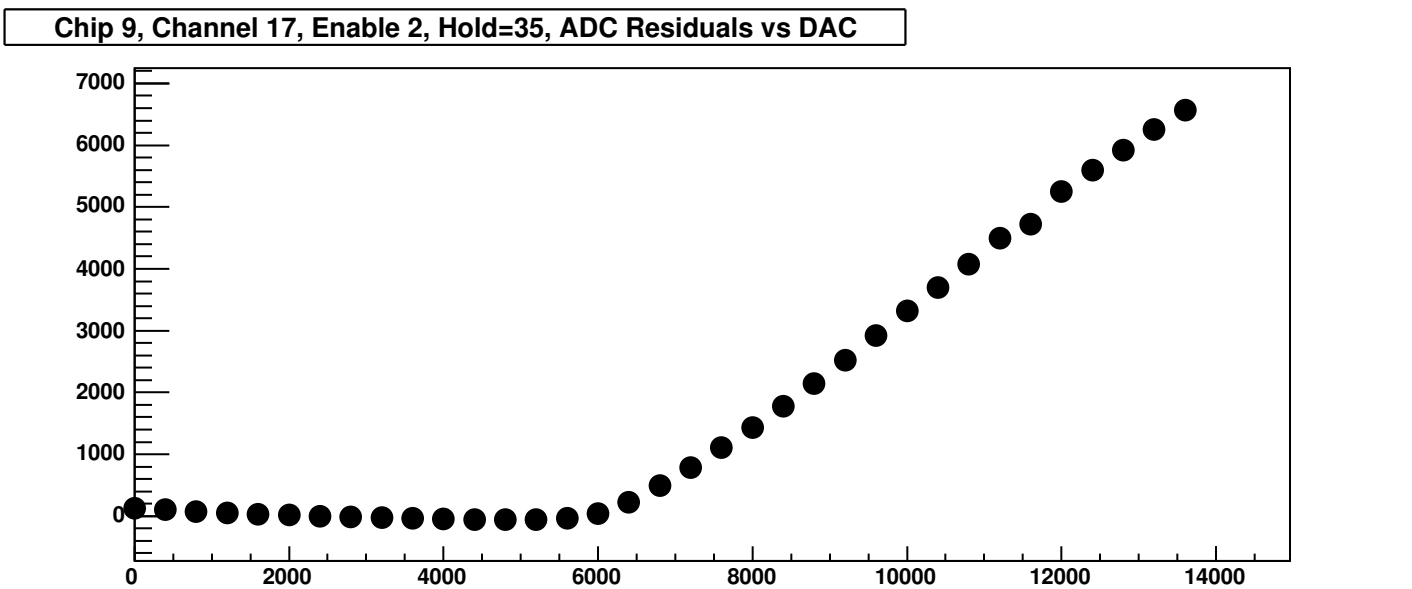


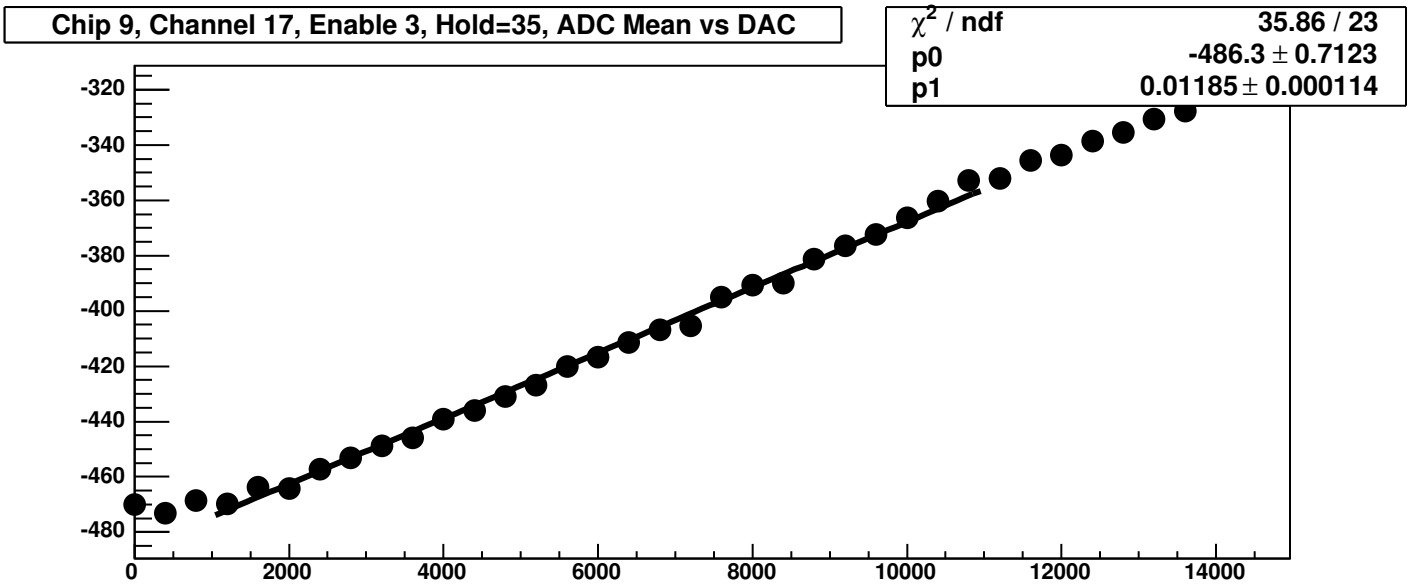
Chip 9, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

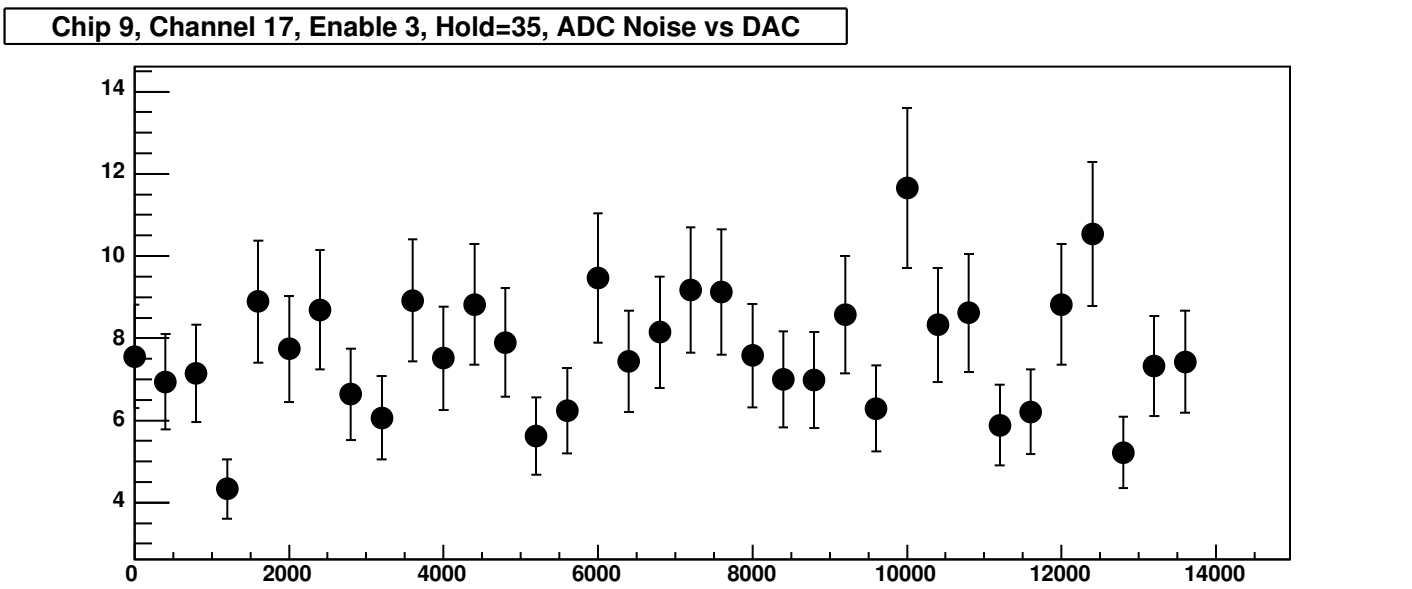


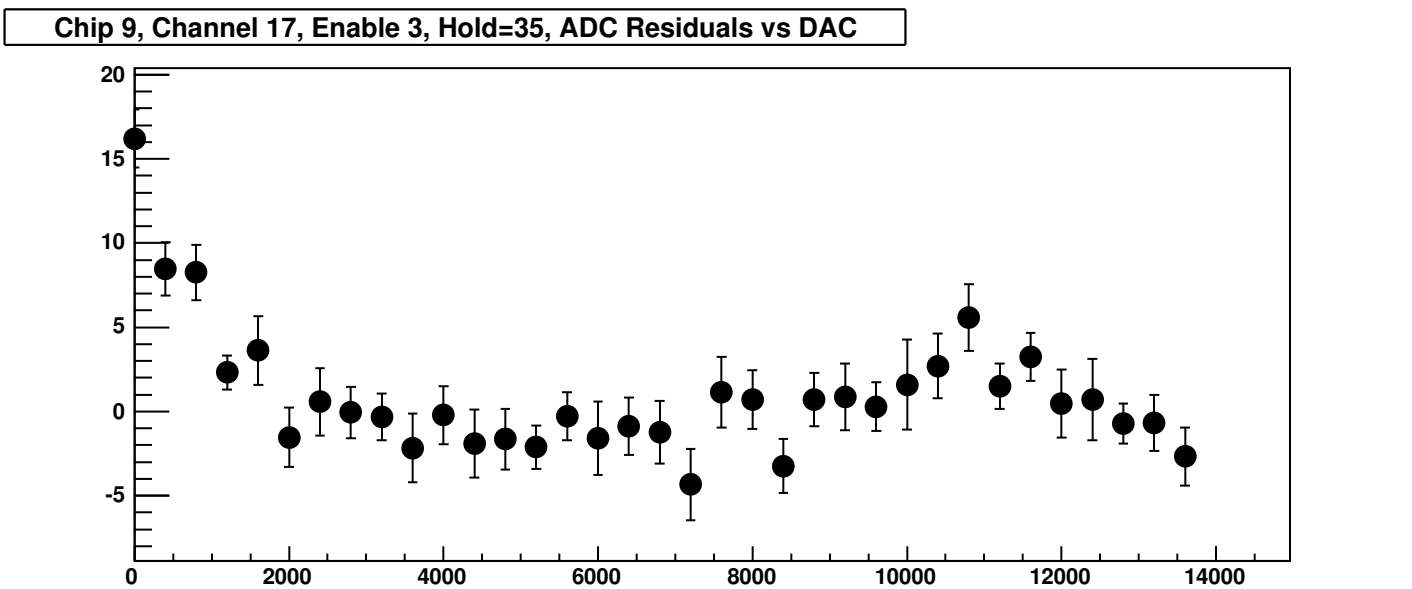

Chip 9, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 8.724e+04 / 23
p0 -594.4 ± 1.2
p1 0.06716 ± 0.0003432
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

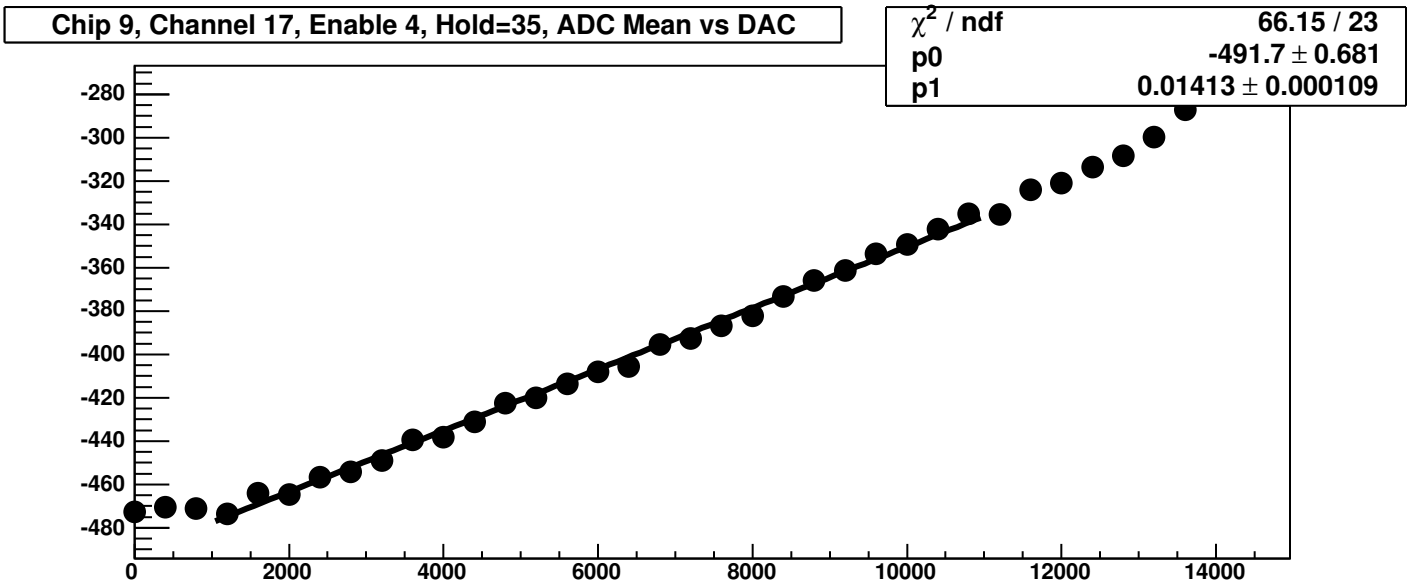

Chip 9, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

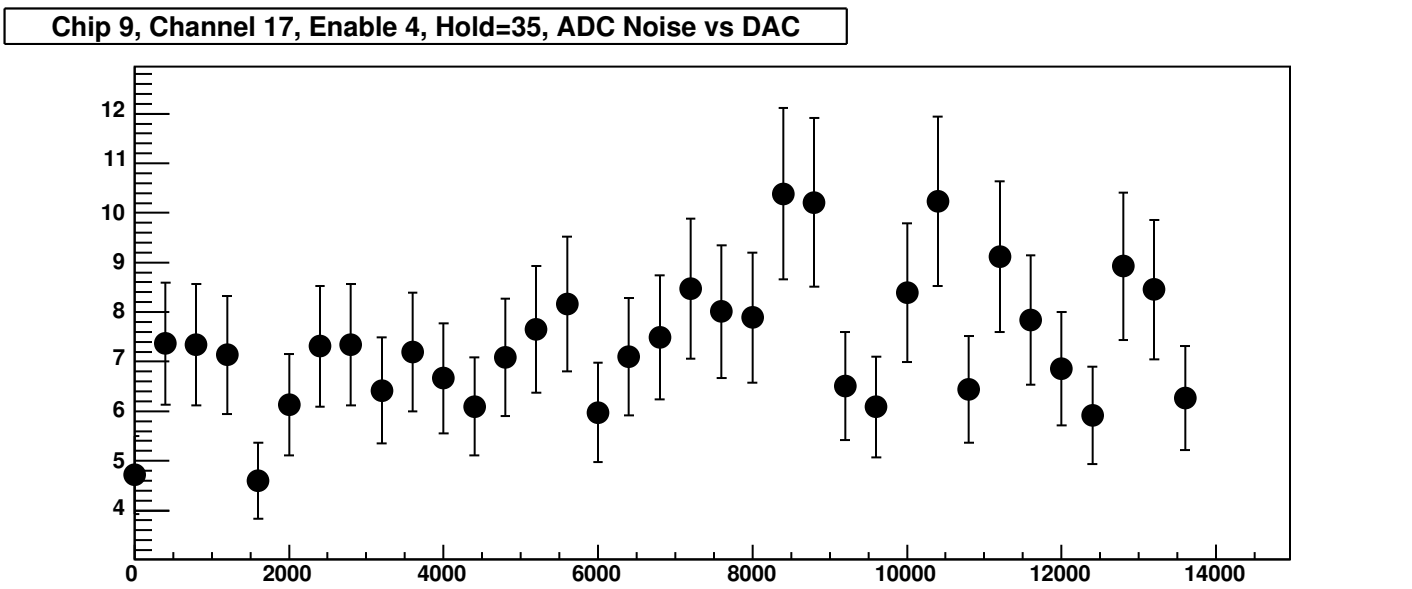

Chip 9, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
7000
6000
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

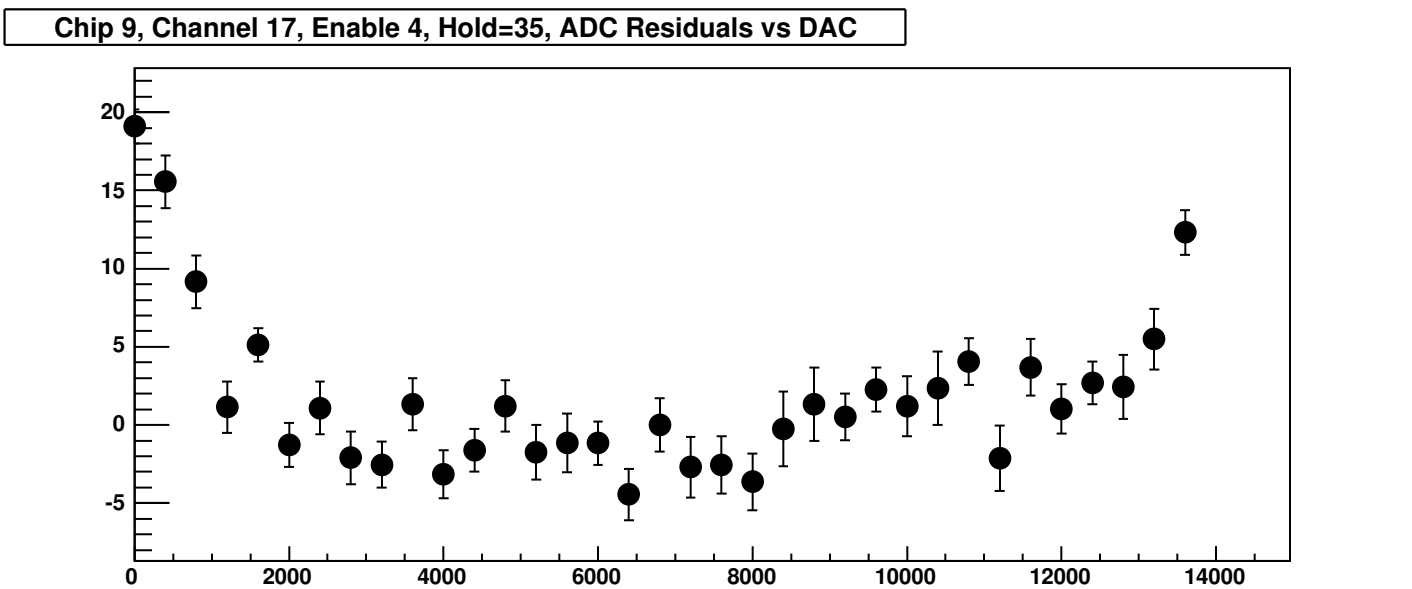

Chip 9, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 35.86 / 23
p0 -486.3 ± 0.7123
p1 0.01185 ± 0.000114
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
2000
4000
6000
8000
10000
12000
14000

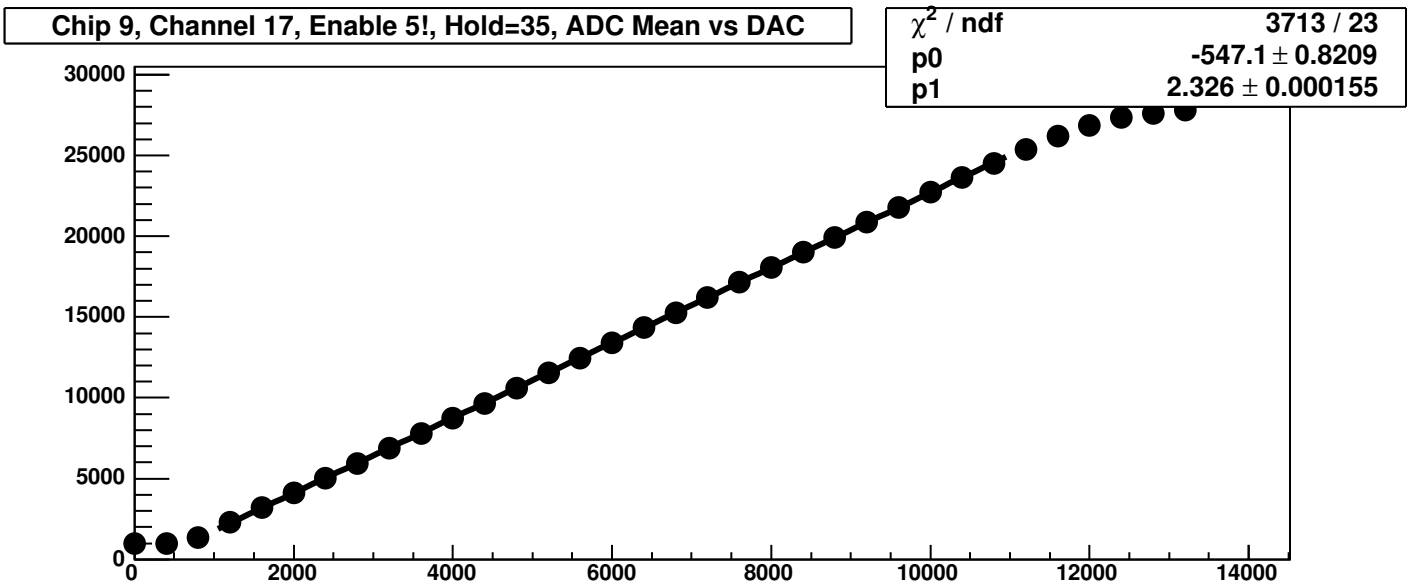

Chip 9, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

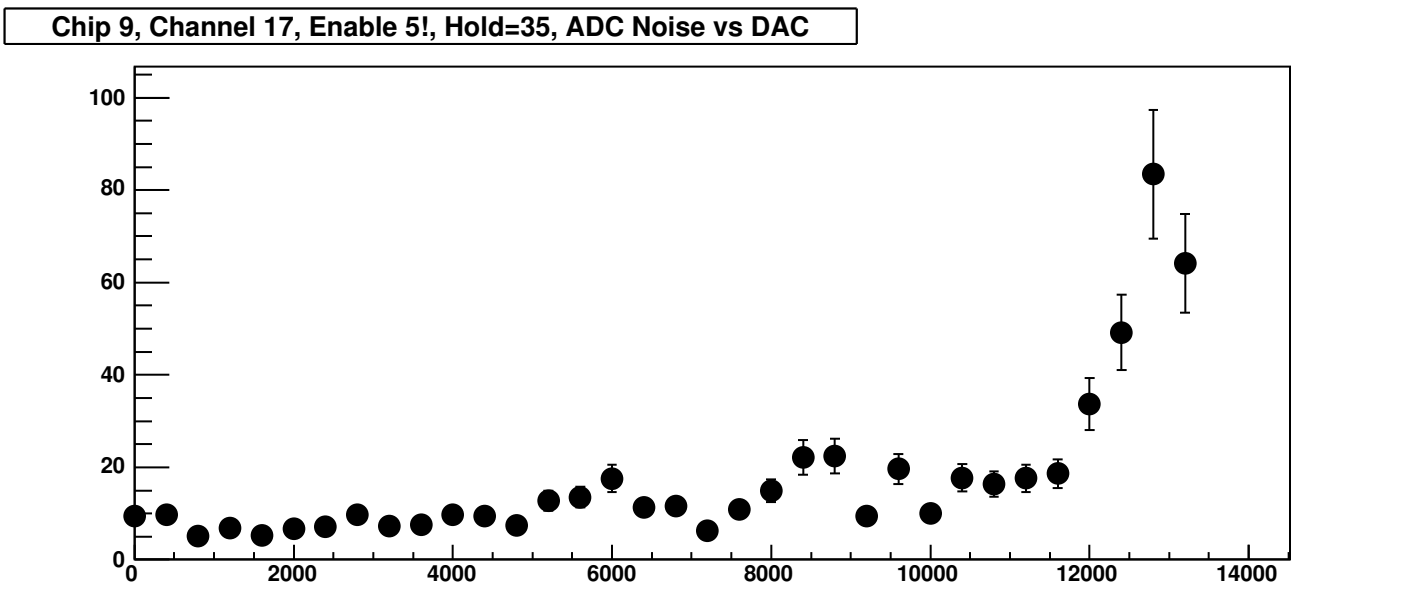

Chip 9, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

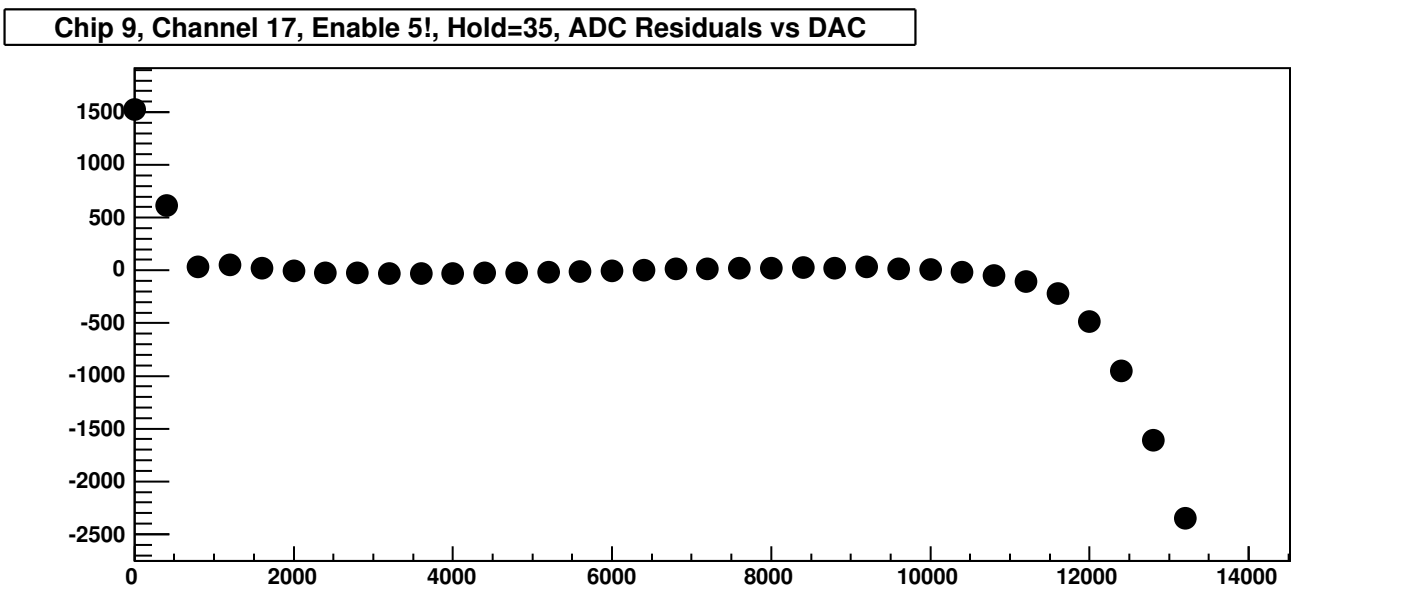

Chip 9, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 66.15 / 23
p0 -491.7 ± 0.681
p1 0.01413 ± 0.000109
-280
-300
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
2000
4000
6000
8000
10000
12000
14000

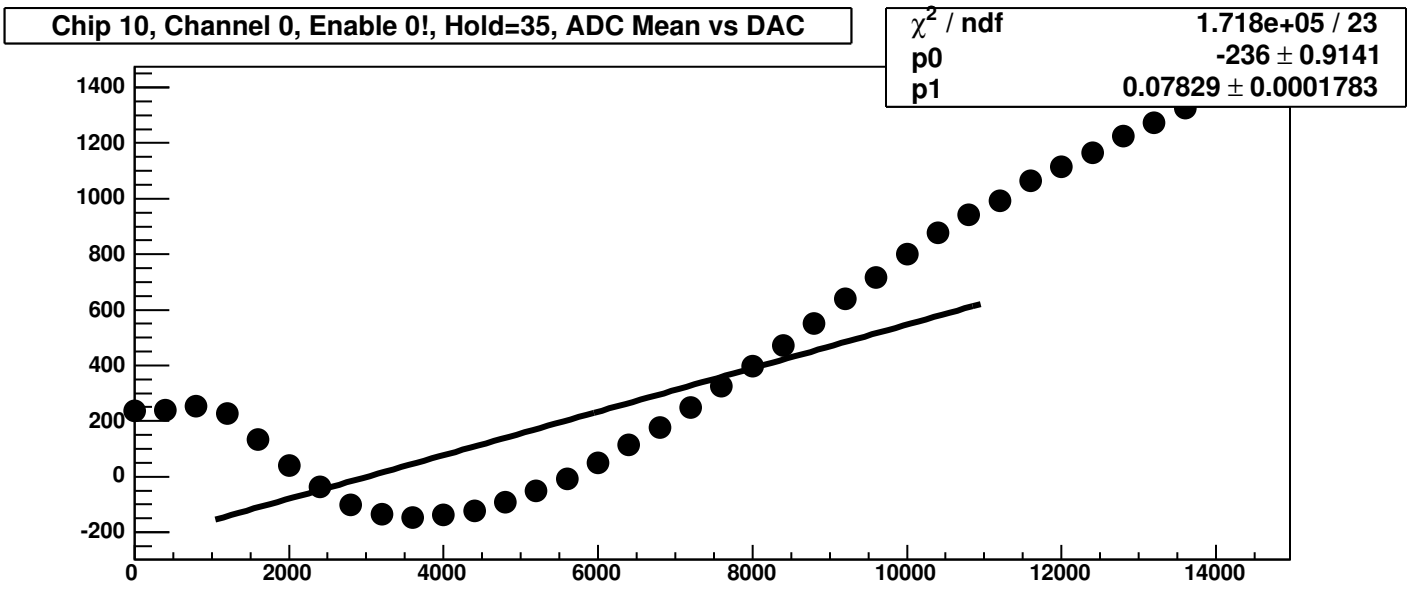

Chip 9, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

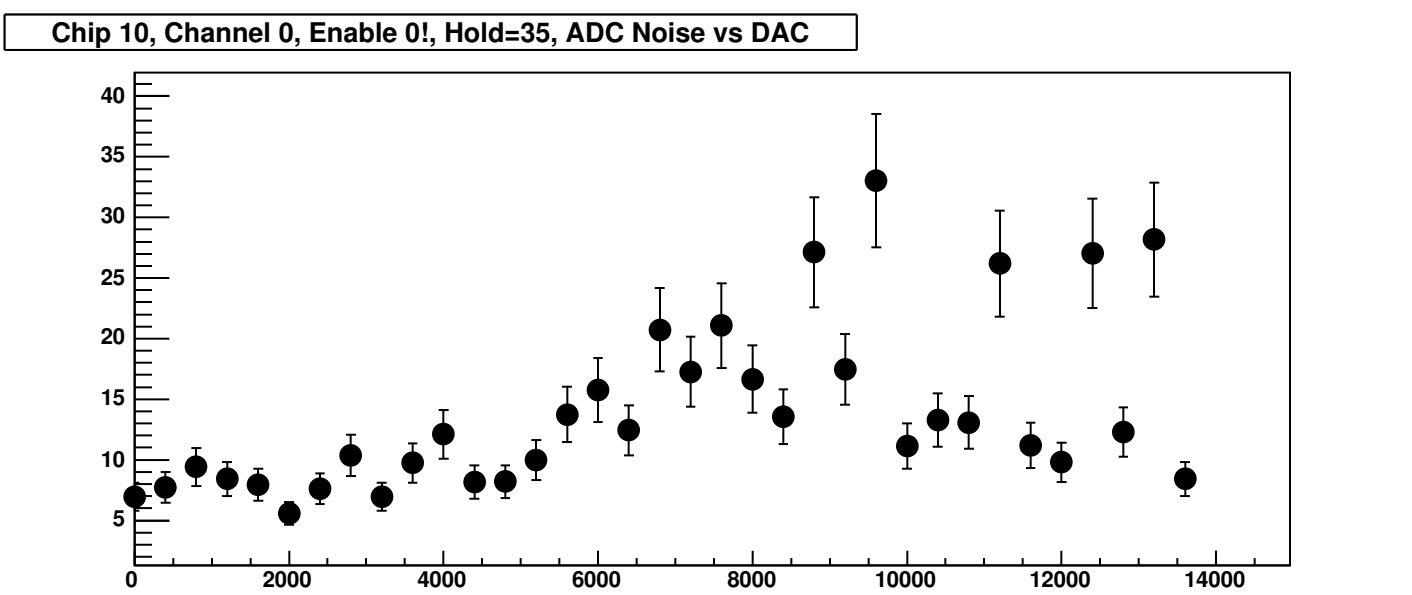

Chip 9, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

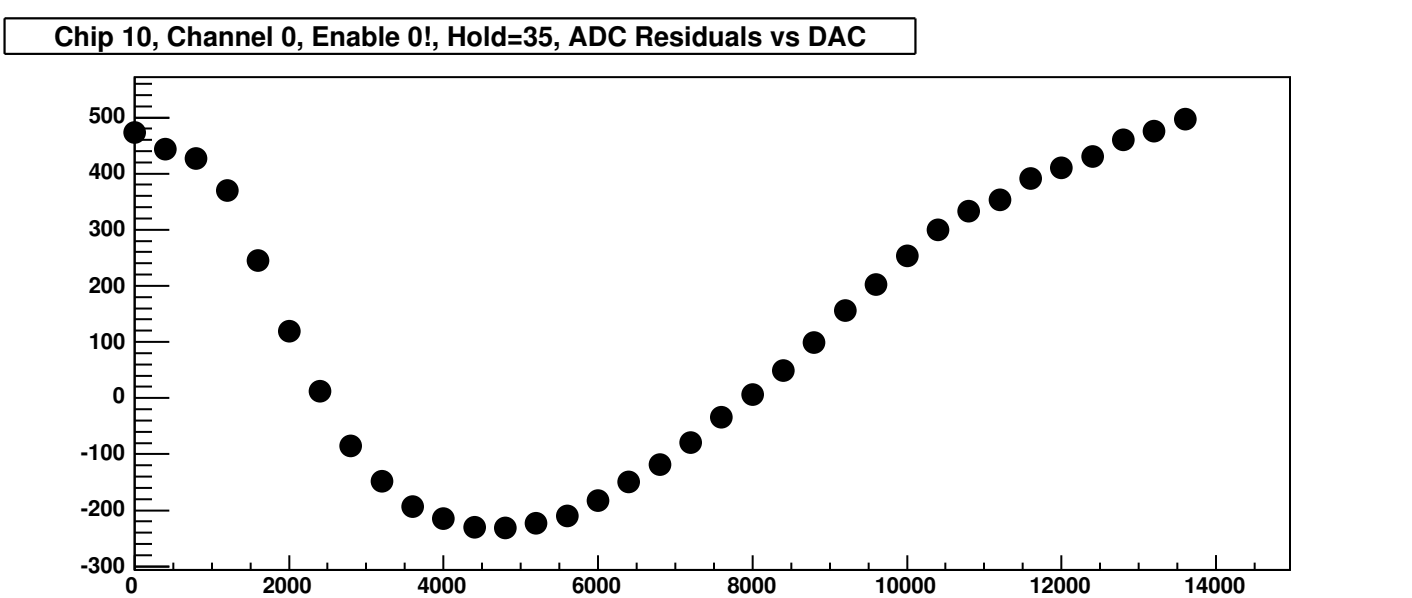

Chip 9, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 3713 / 23
p0 -547.1 ± 0.8209
p1 2.326 ± 0.000155
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

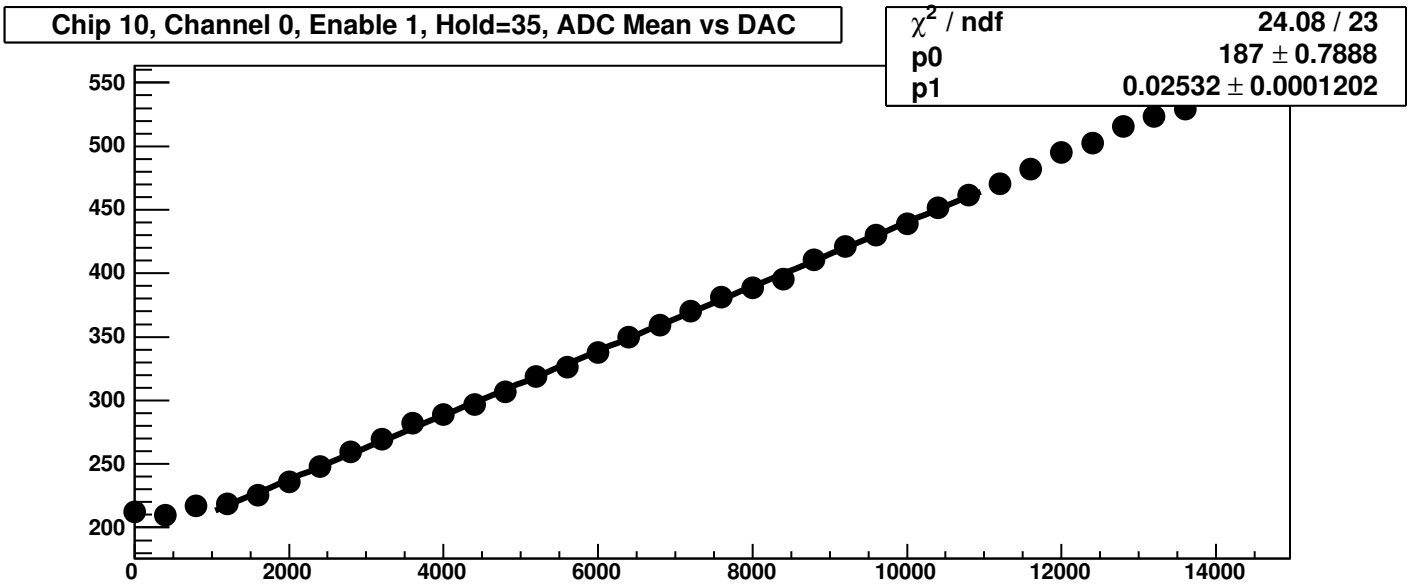

Chip 9, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

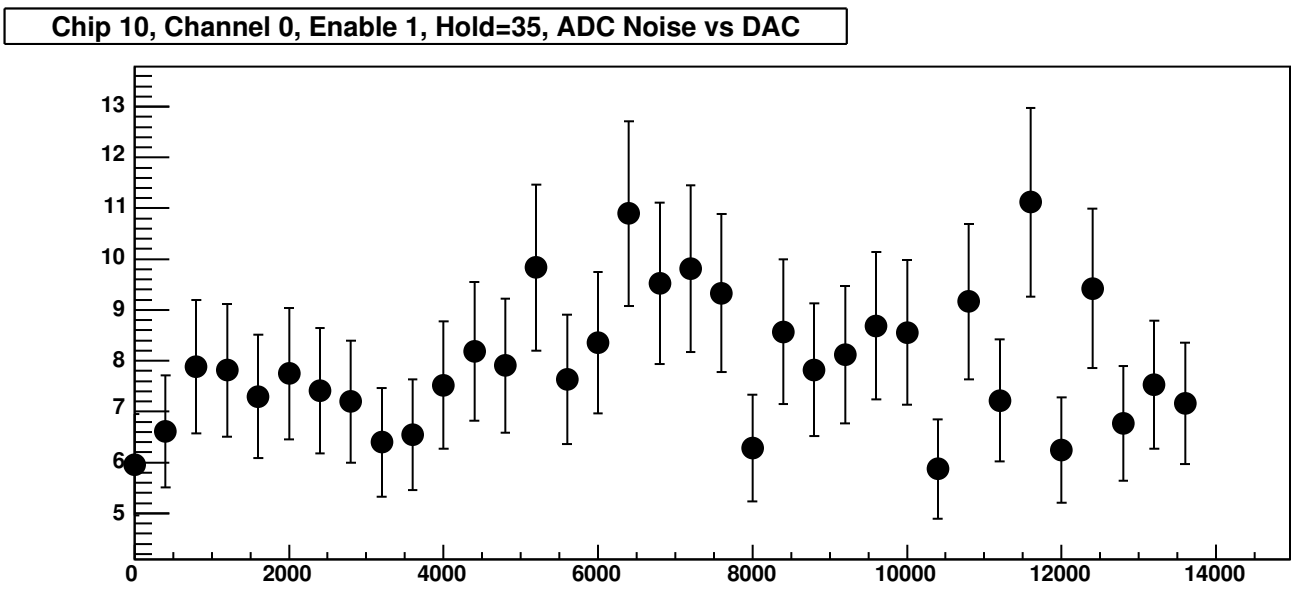

Chip 9, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

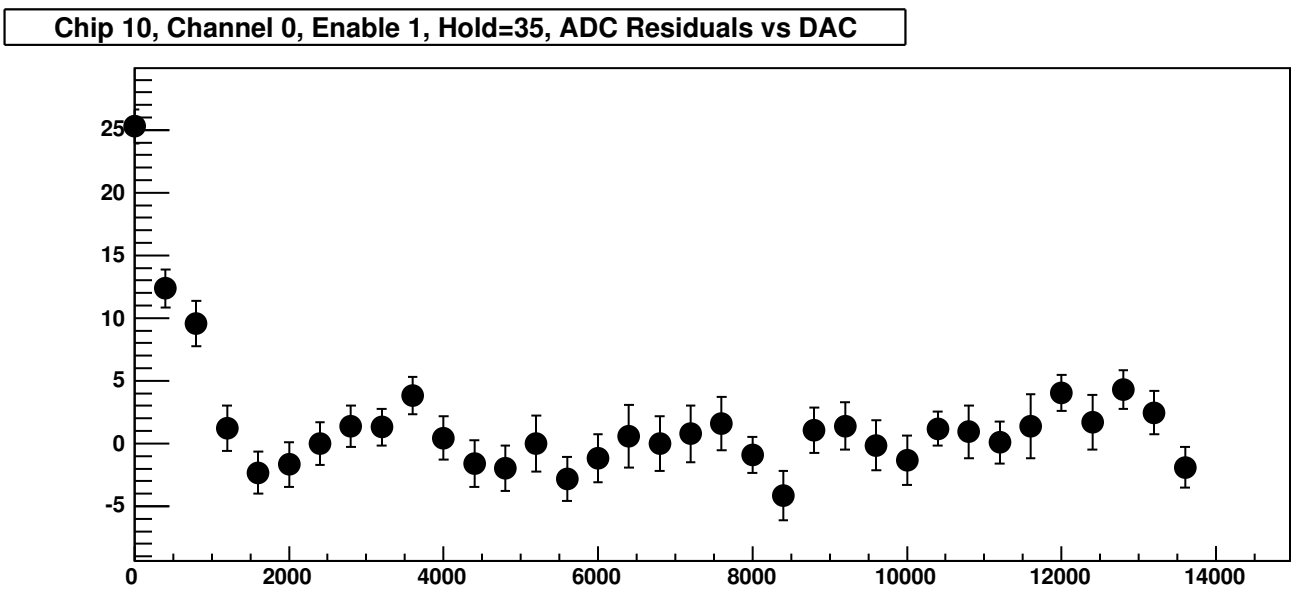

Chip 10, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 1.718e+05 / 23
p0 -236 ± 0.9141
p1 0.07829 ± 0.0001783
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

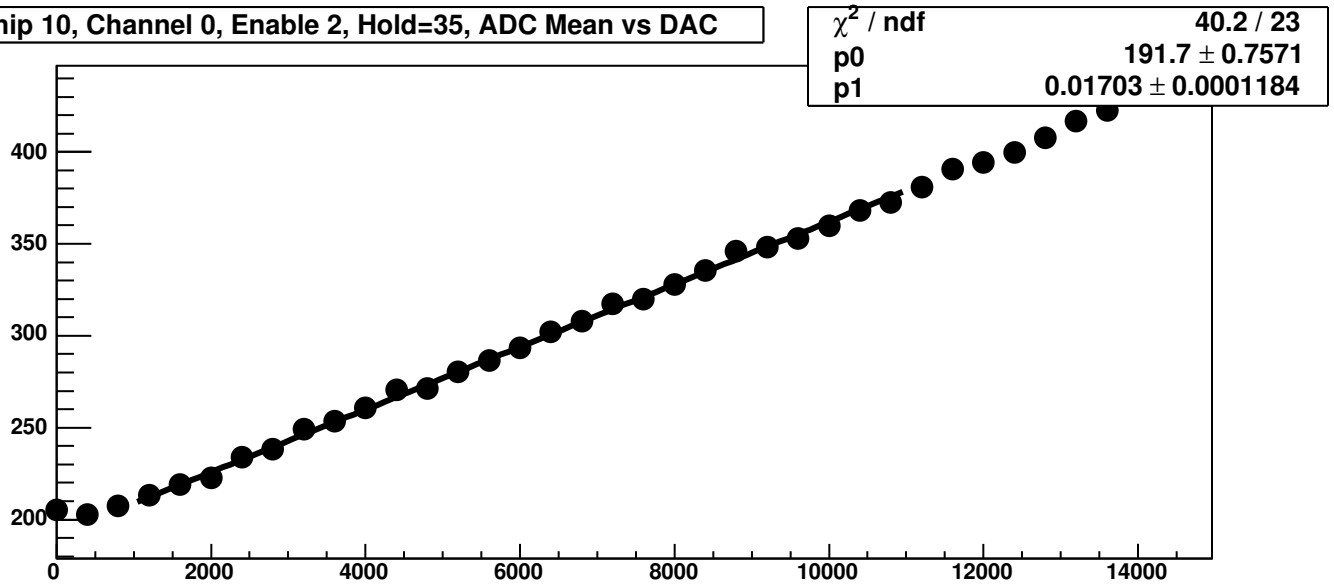

Chip 10, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 24.08 / 23
p0 187 ± 0.7888
p1 0.02532 ± 0.0001202
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

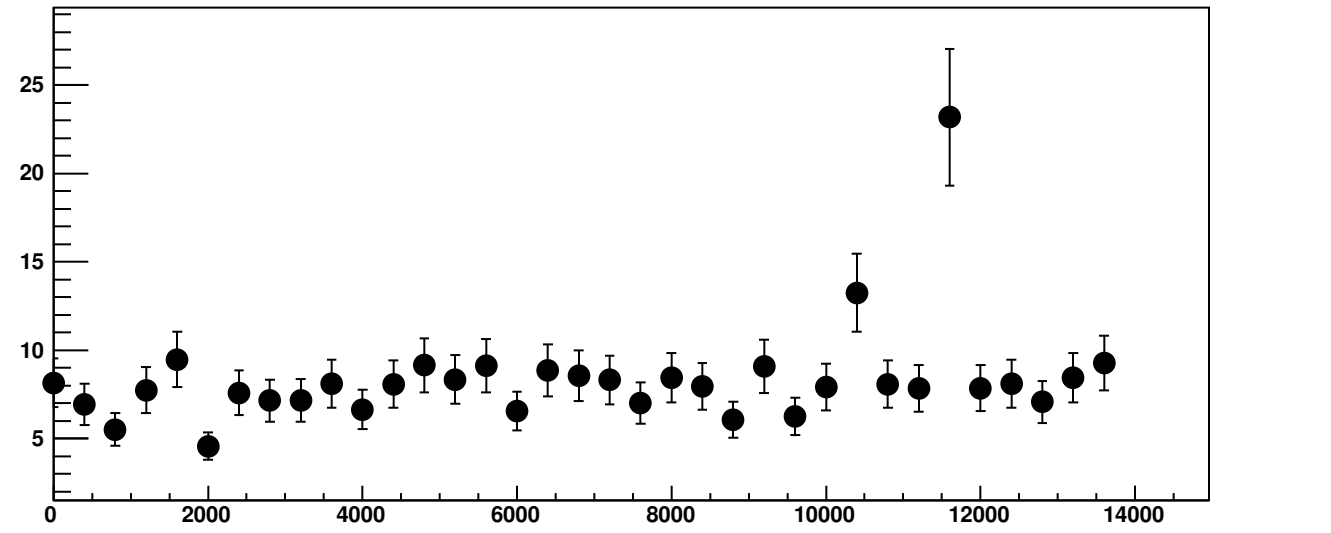

Chip 10, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

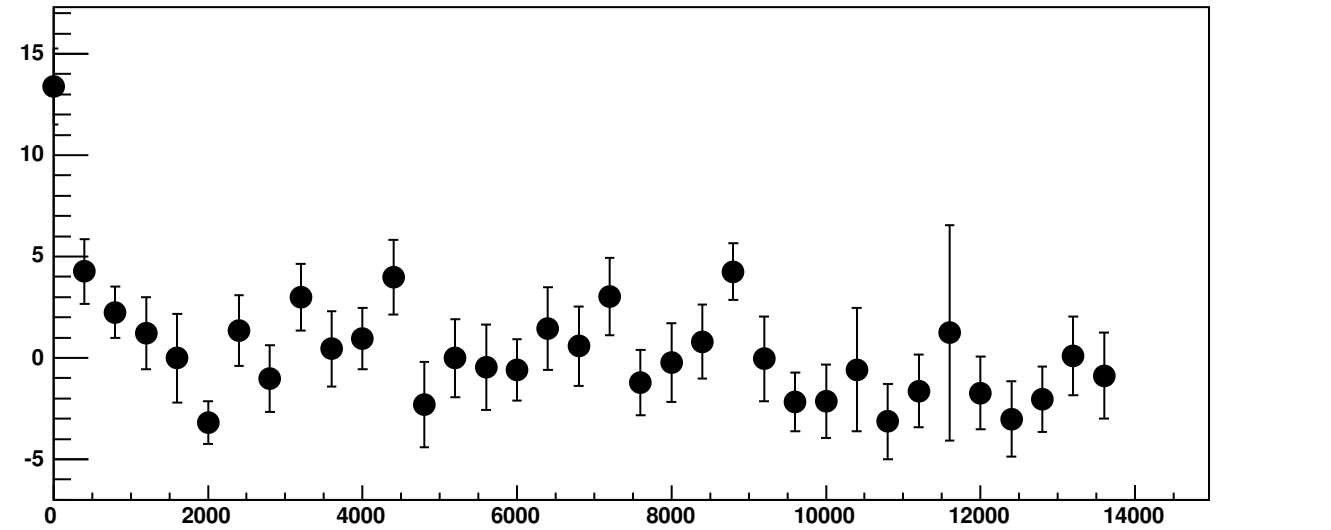
Chip 10, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

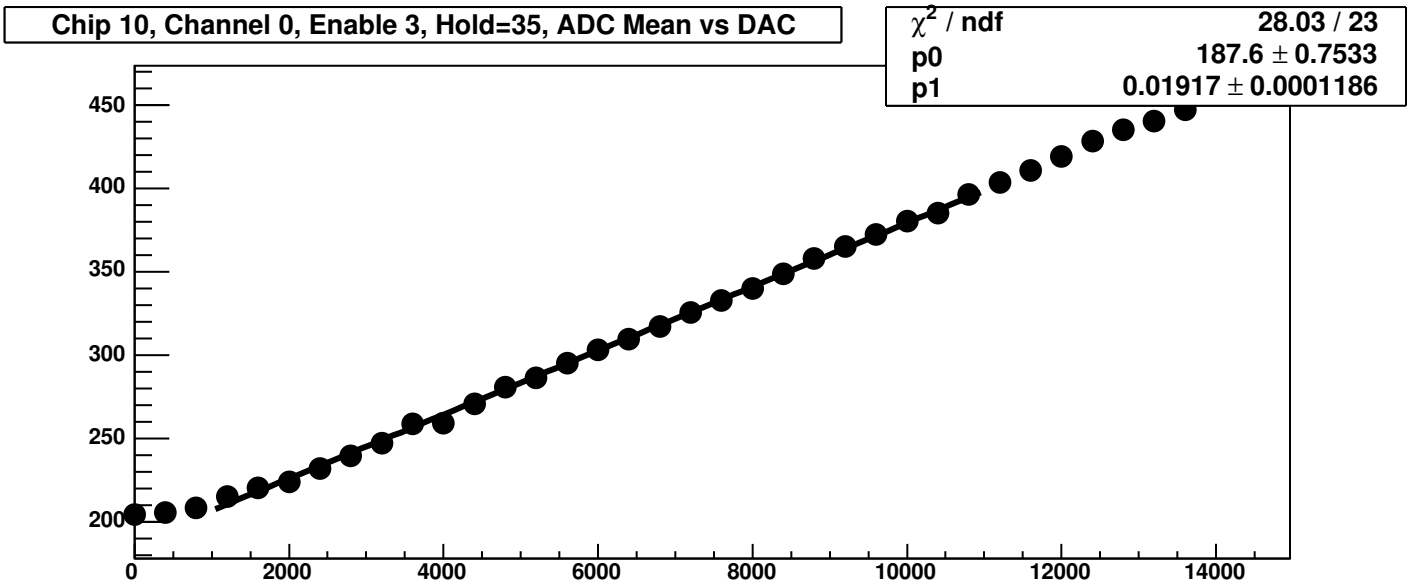


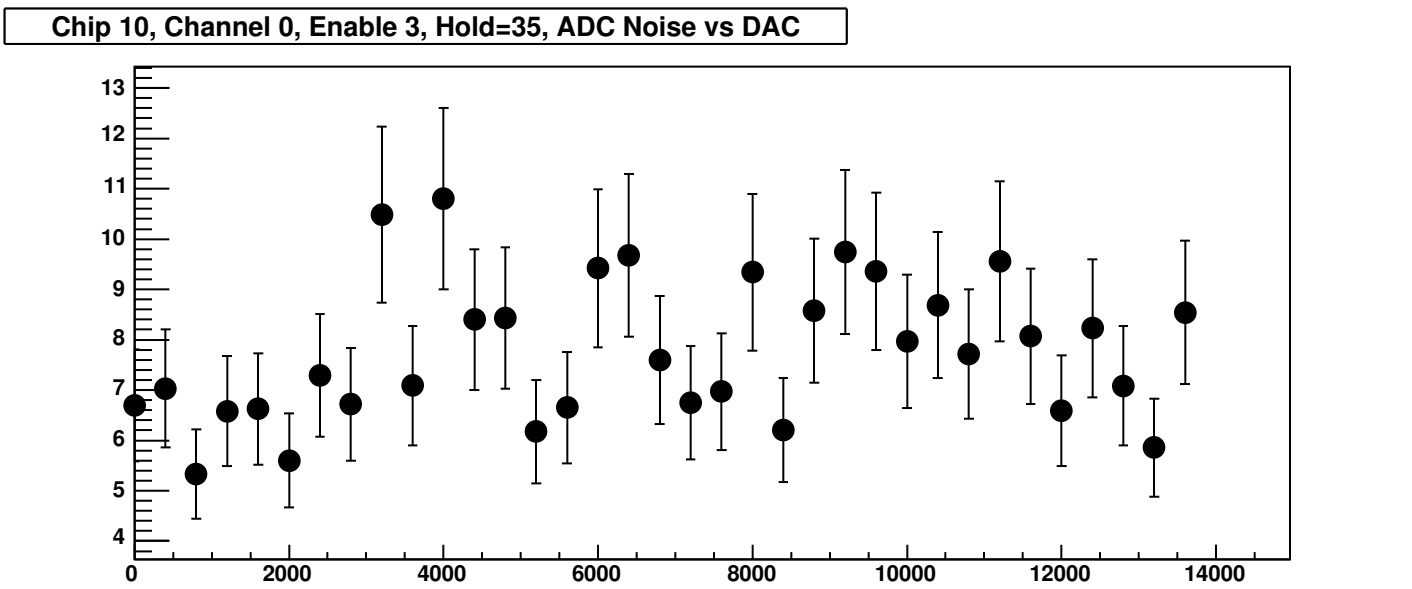
Chip 10, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC

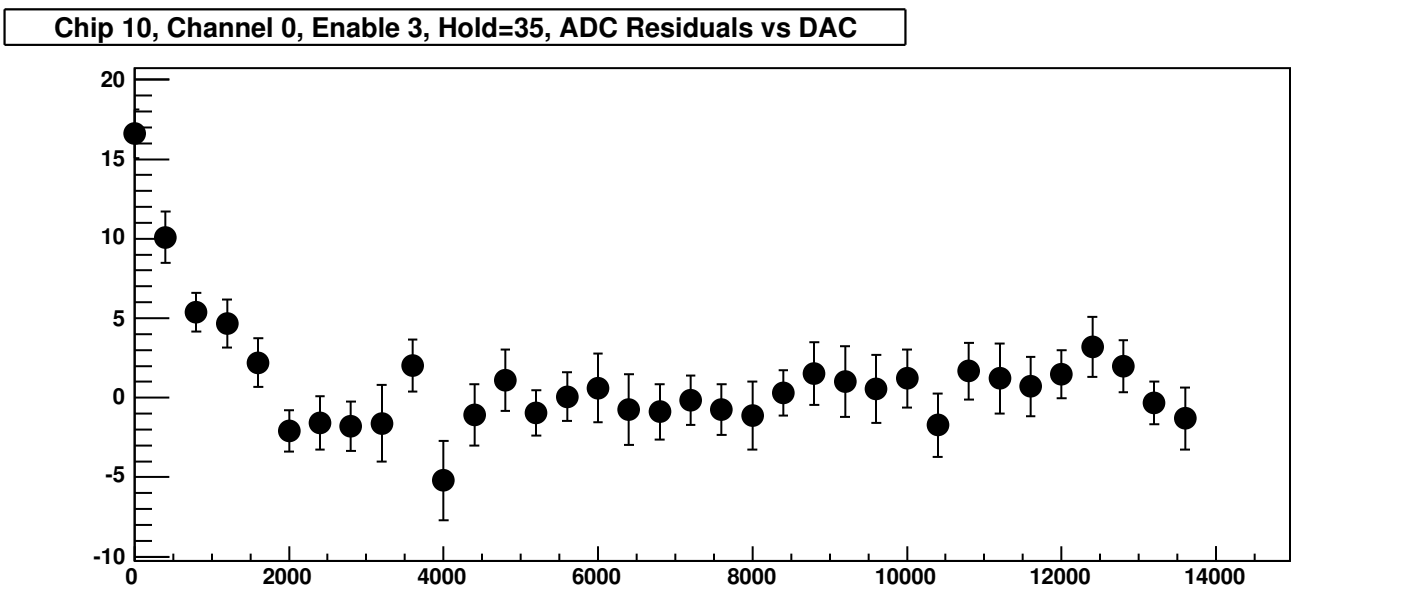


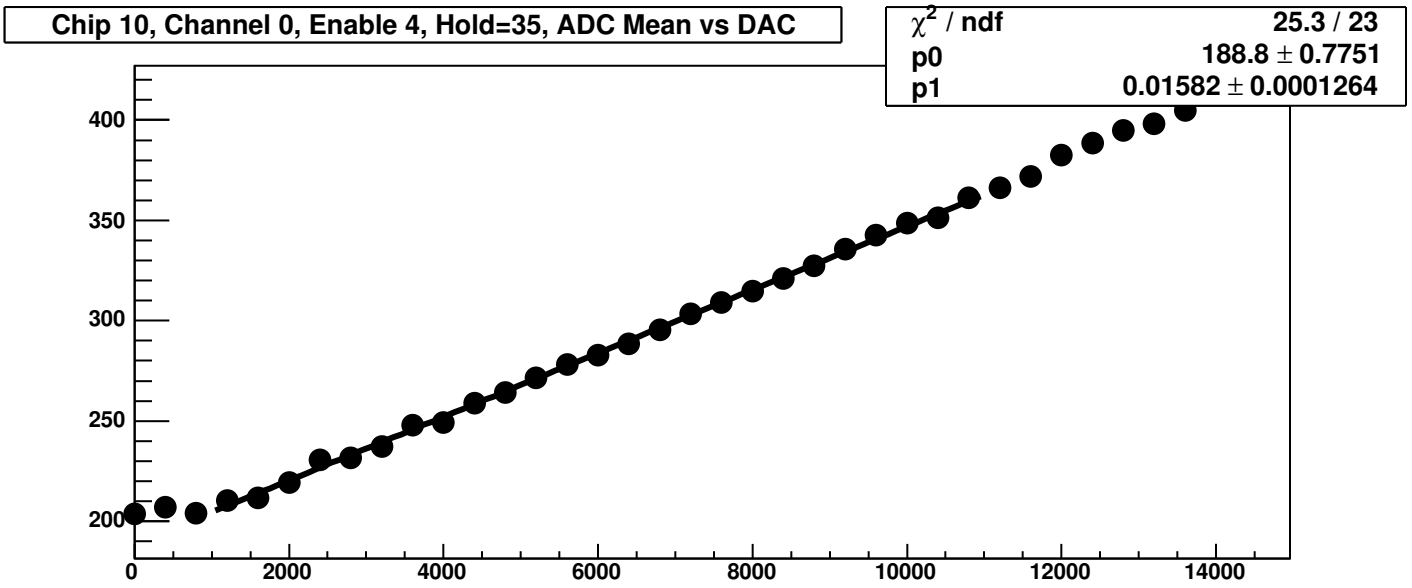
Chip 10, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

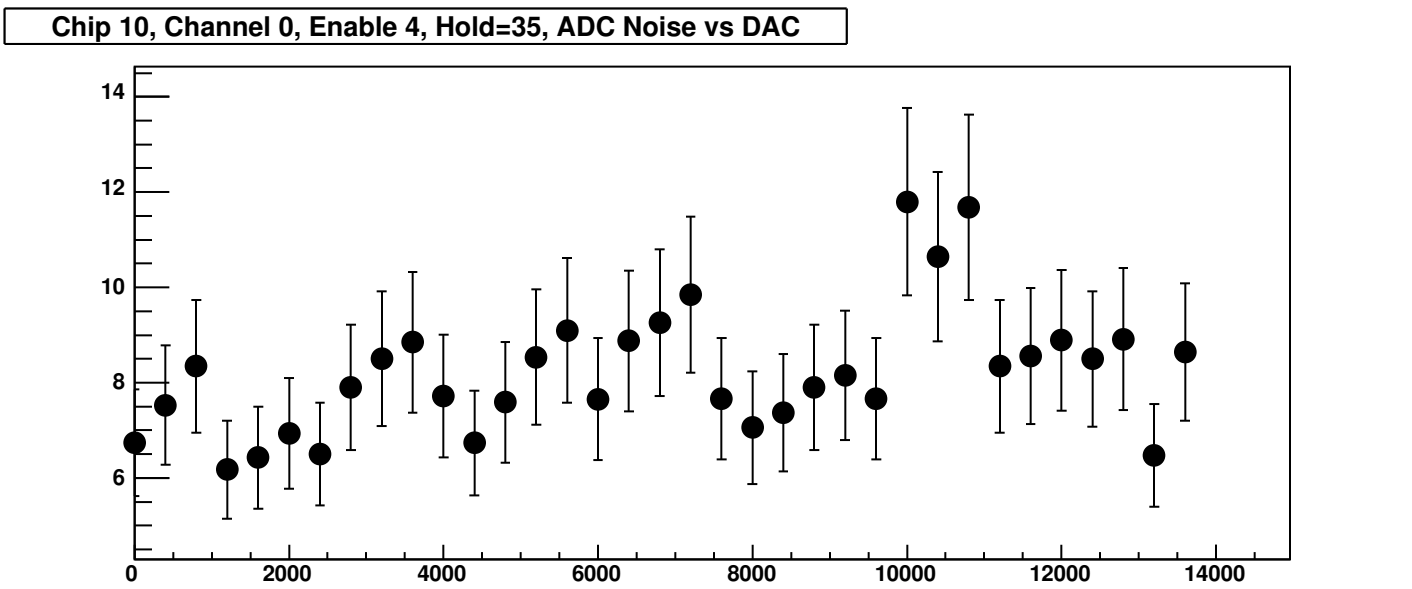


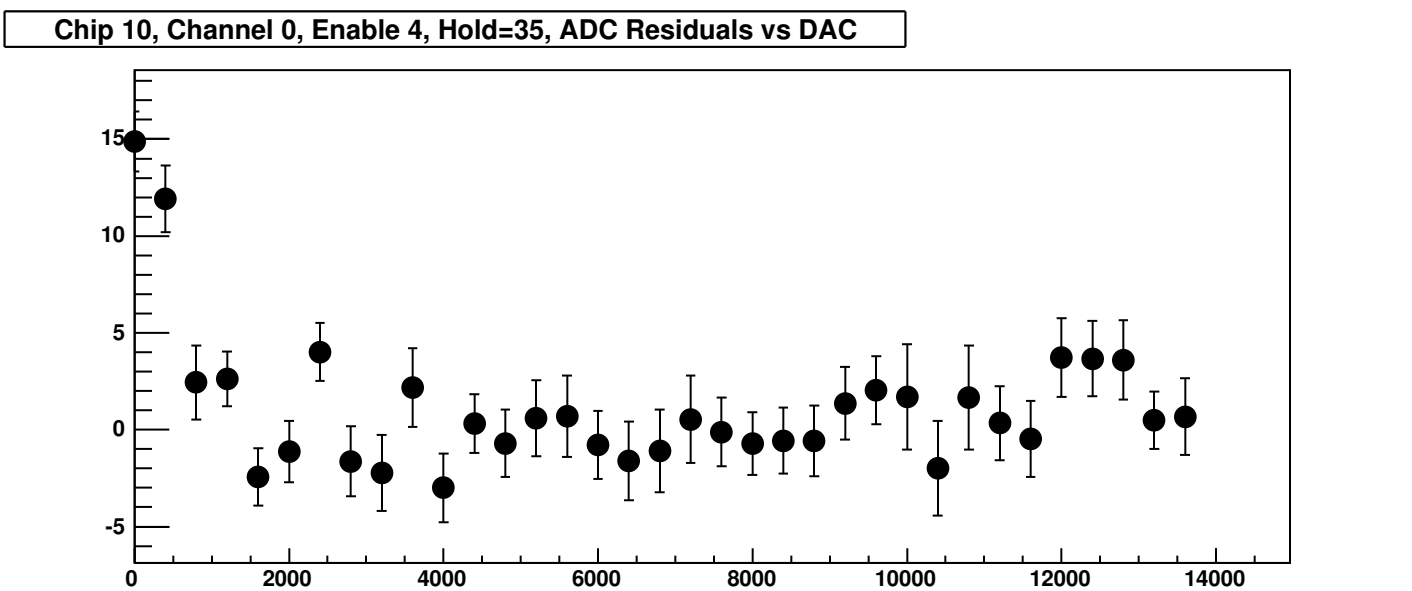

Chip 10, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.03 / 23
p0 187.6 ± 0.7533
p1 0.01917 ± 0.0001186
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

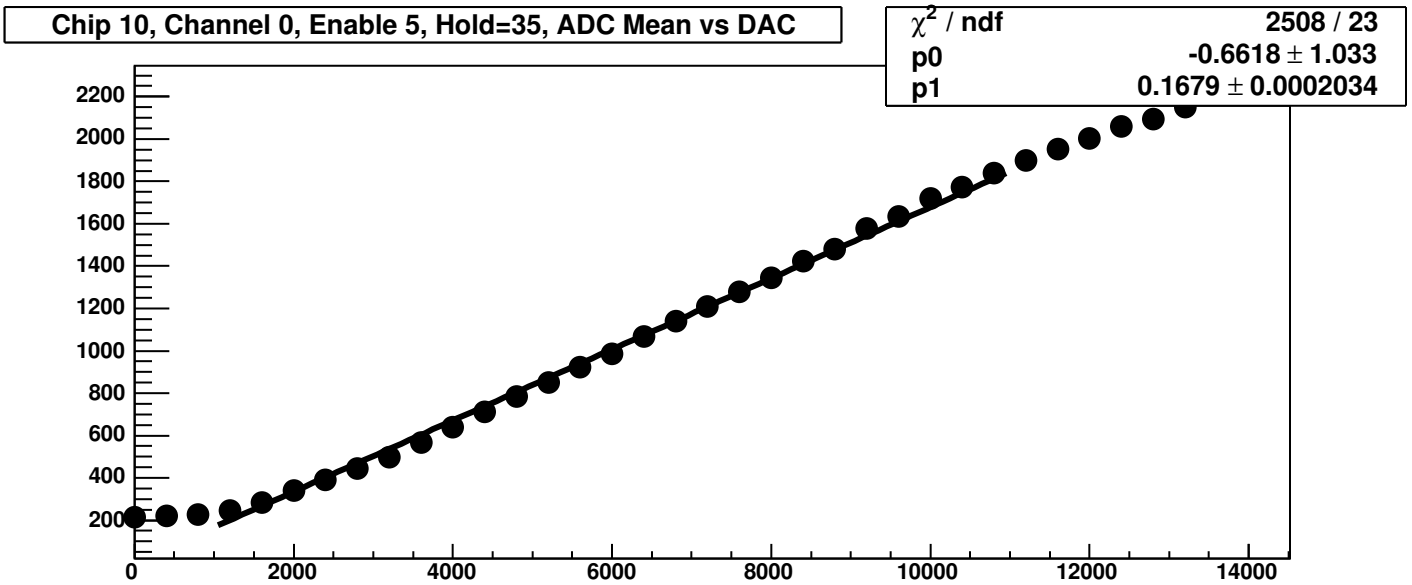

Chip 10, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

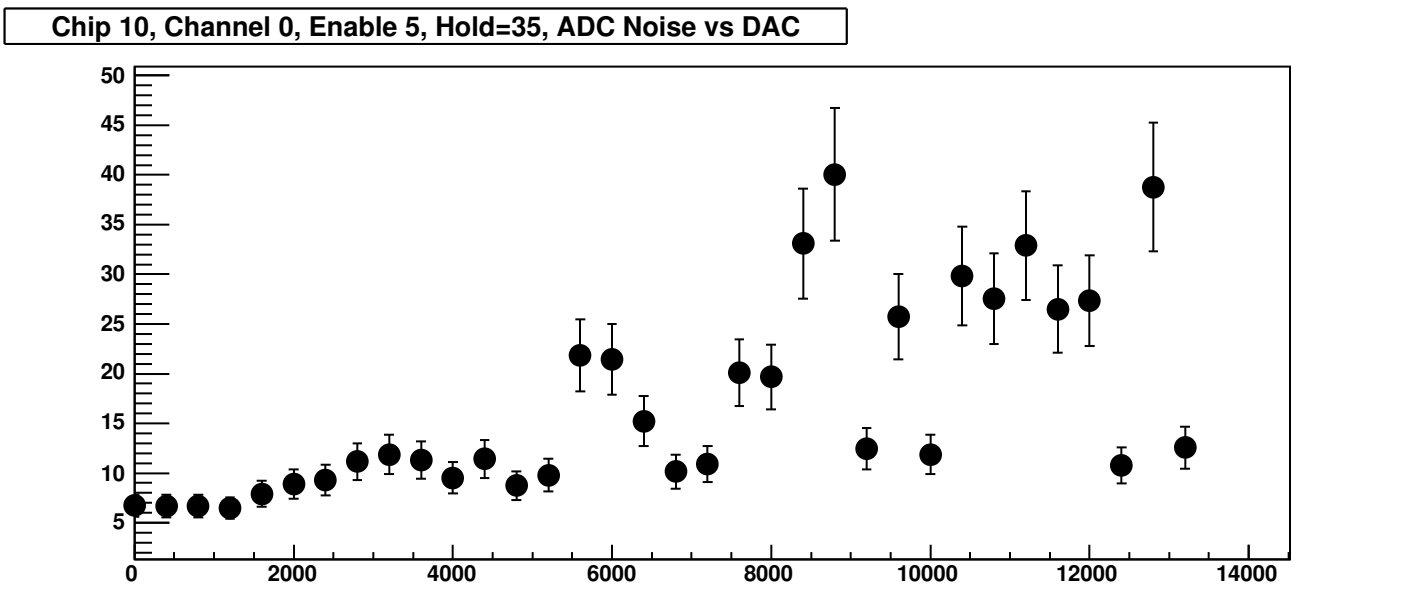

Chip 10, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

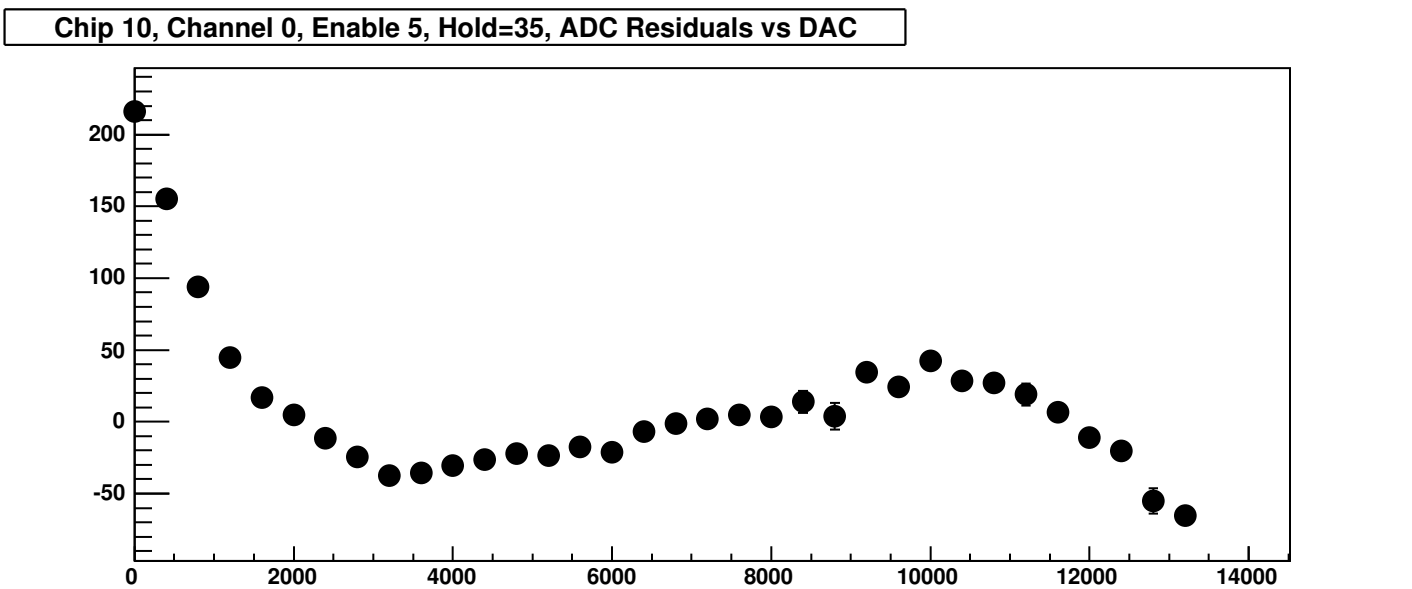

Chip 10, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 25.3 / 23
p0 188.8 ± 0.7751
p1 0.01582 ± 0.0001264
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

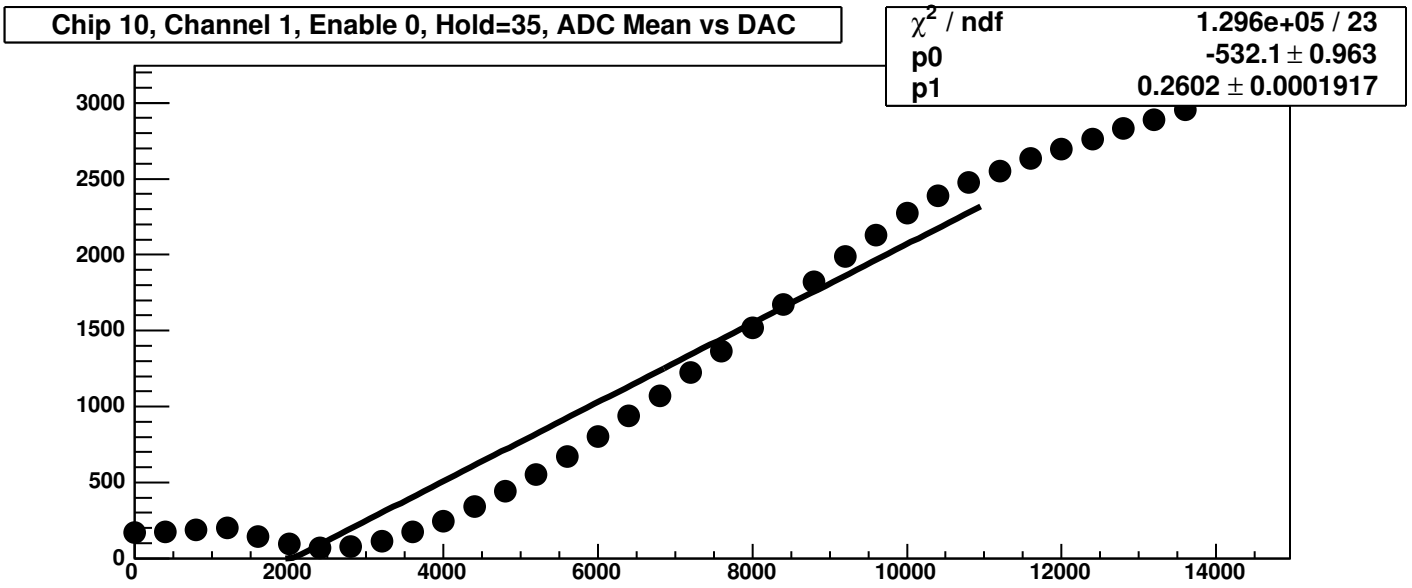

Chip 10, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

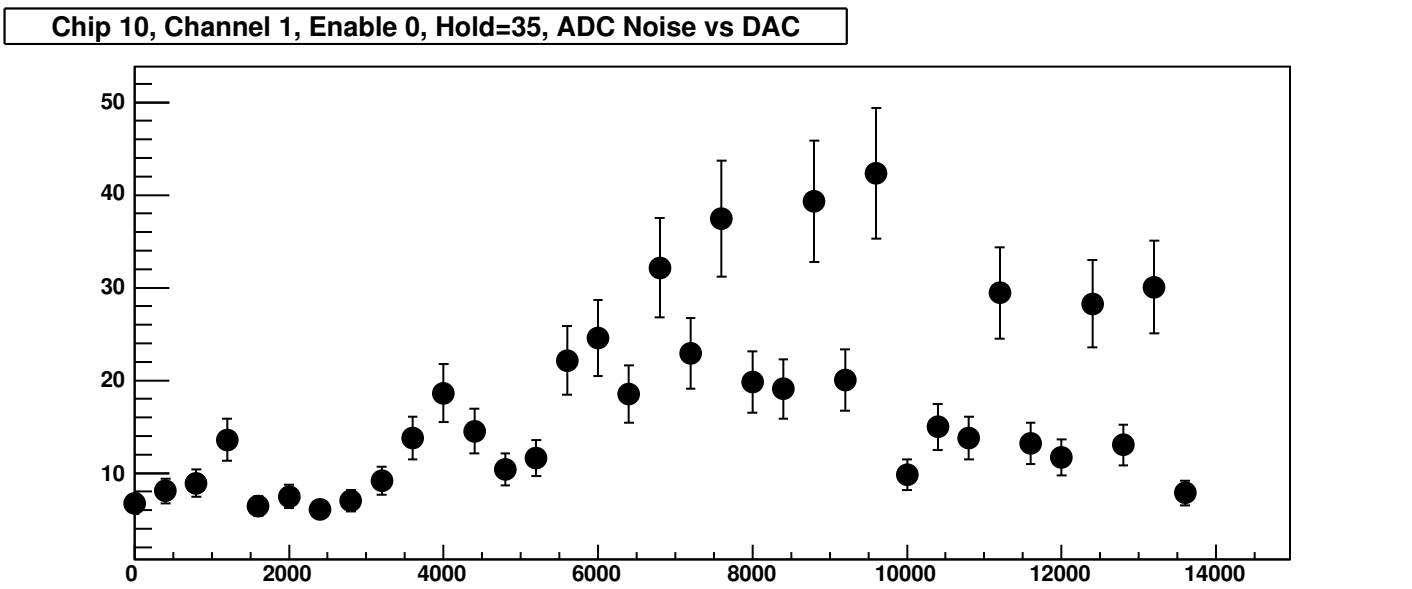

Chip 10, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

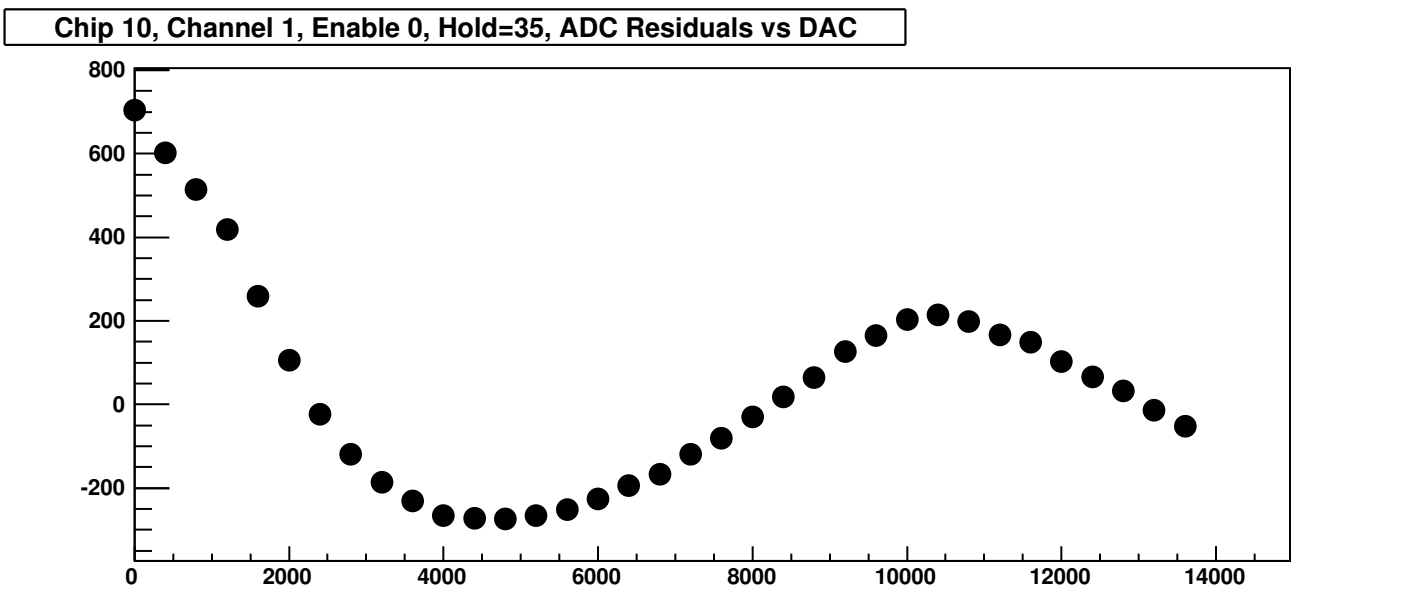

Chip 10, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2508 / 23
p0 -0.6618 ± 1.033
p1 0.1679 ± 0.0002034
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

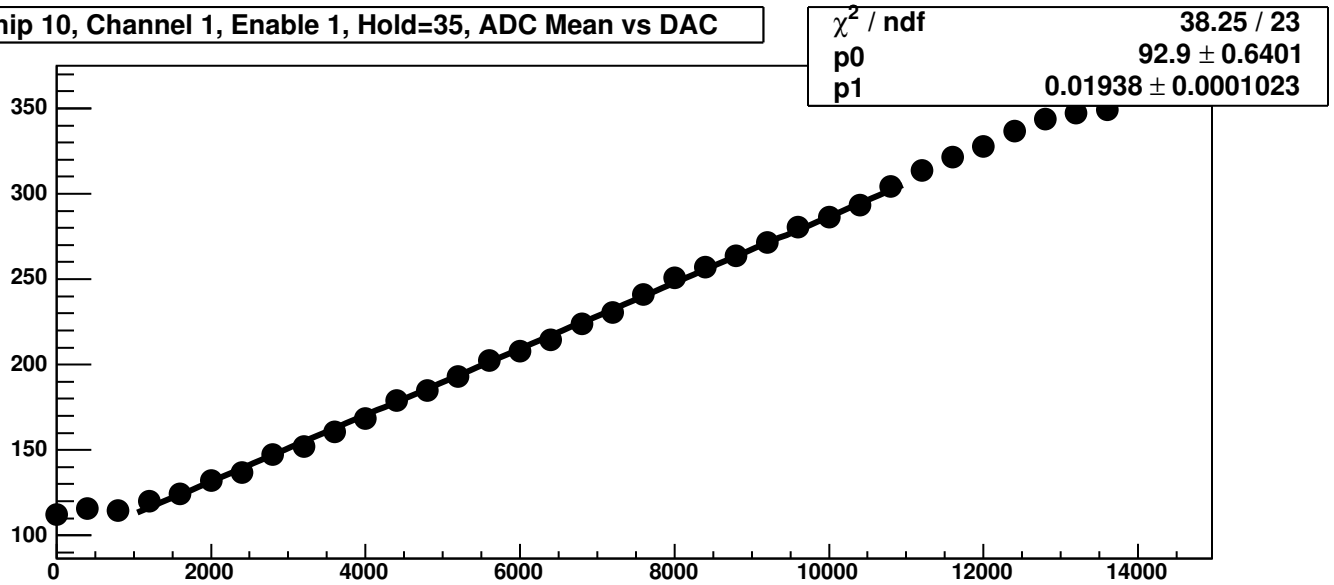

Chip 10, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.296e+05 / 23
p0 -532.1 ± 0.963
p1 0.2602 ± 0.0001917
3000
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

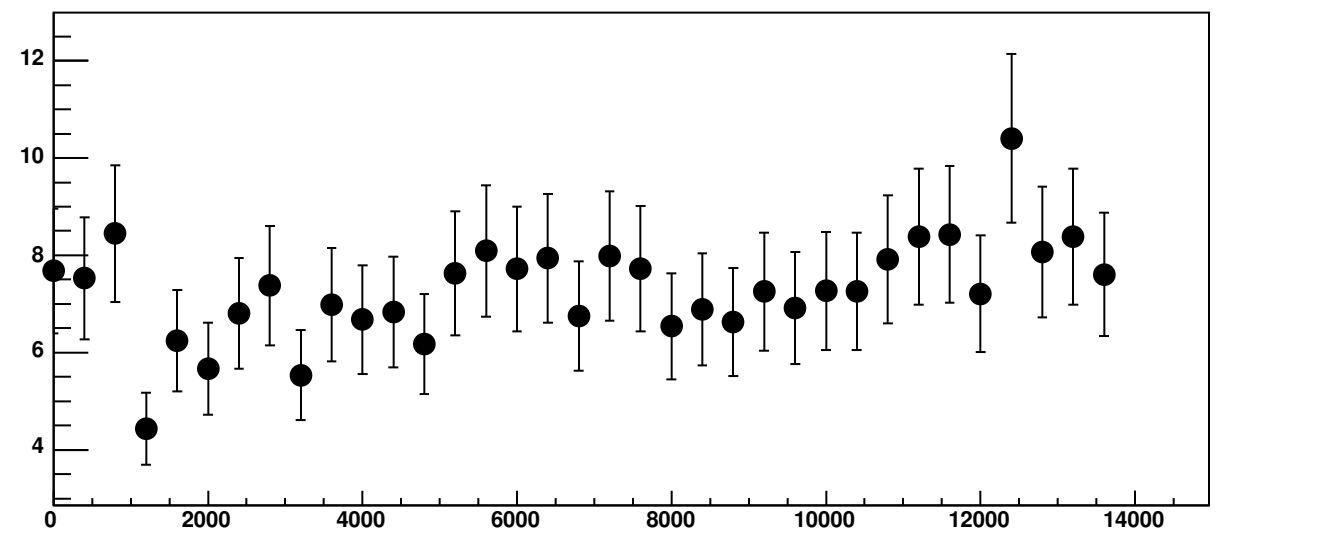

Chip 10, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

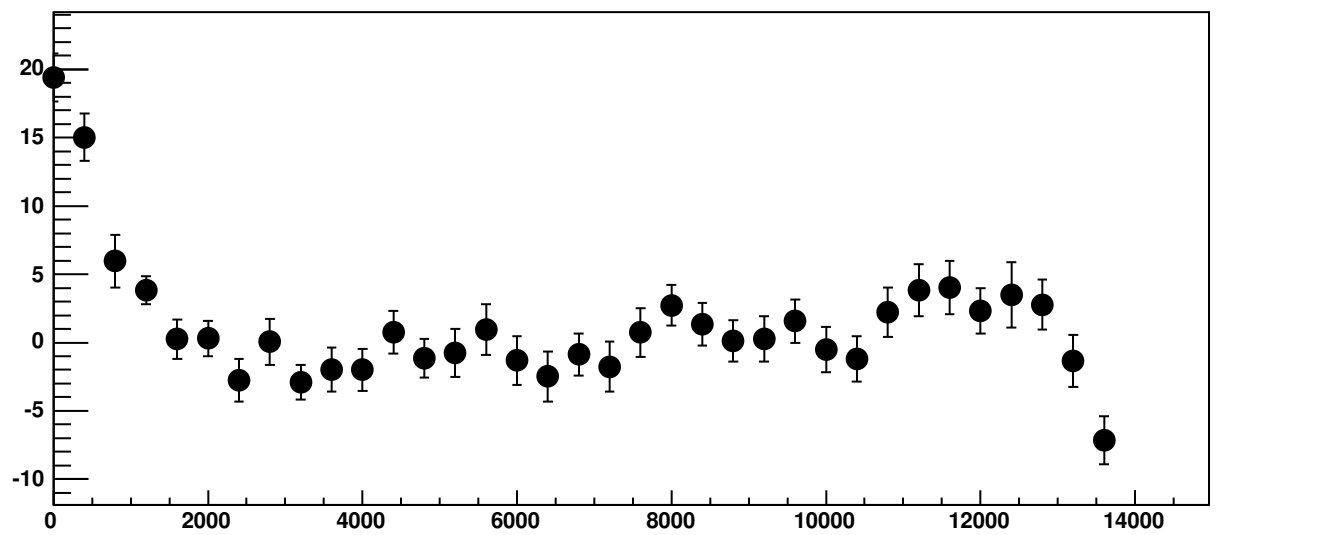
Chip 10, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

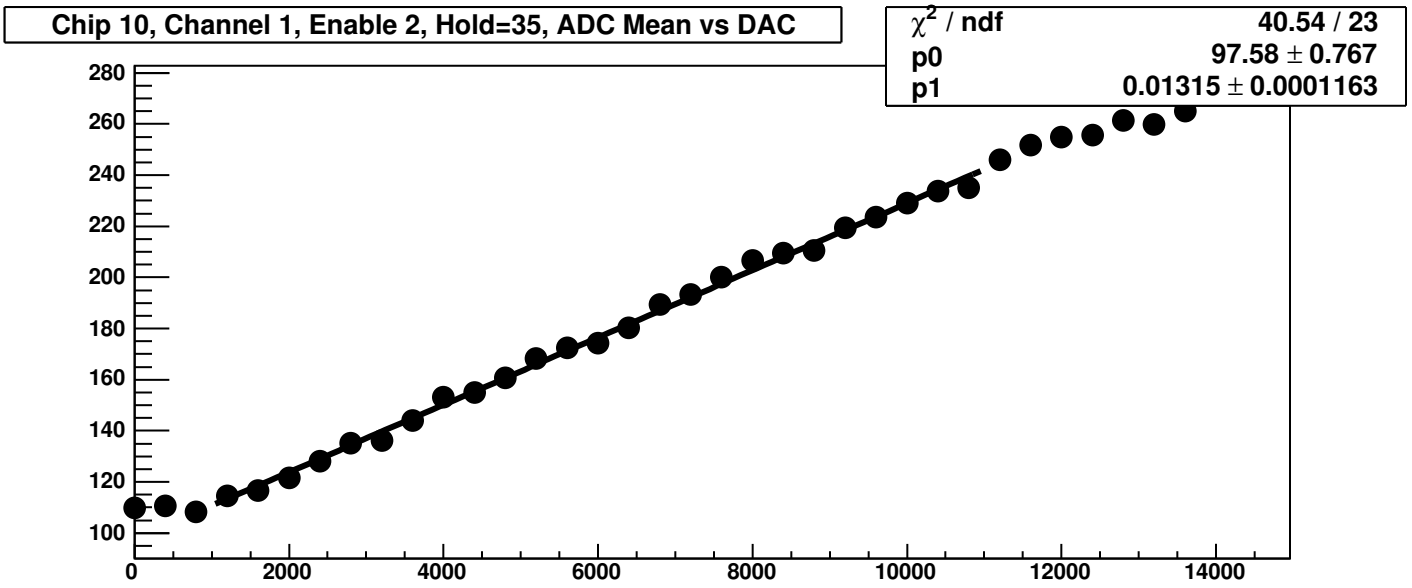


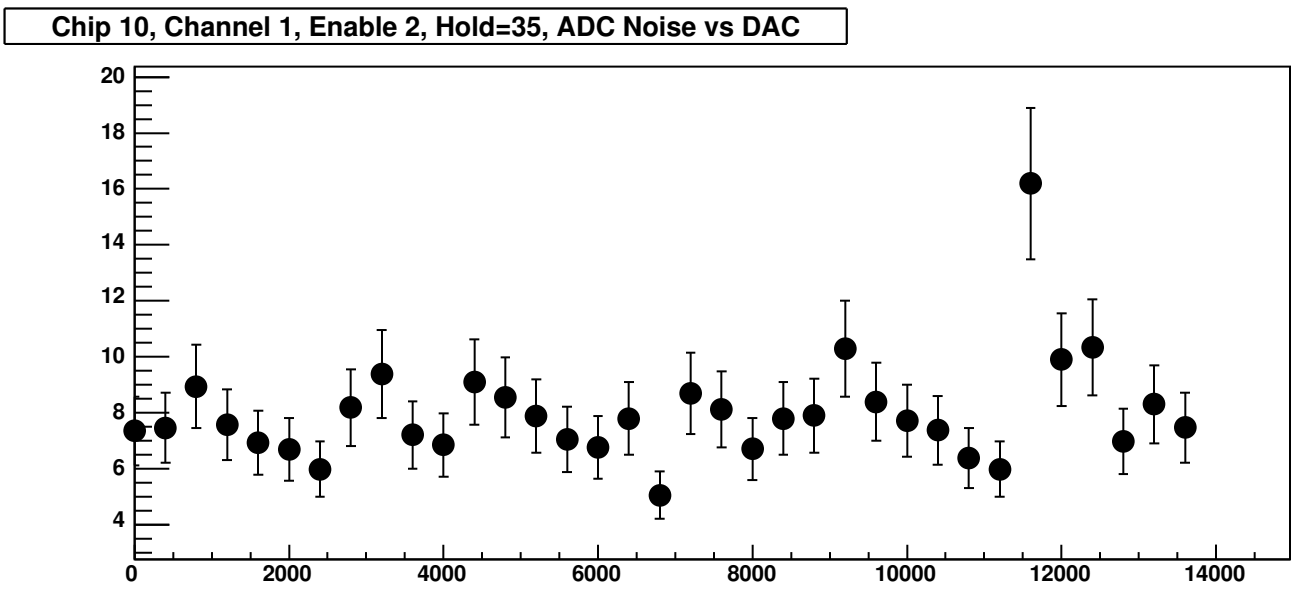
Chip 10, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC

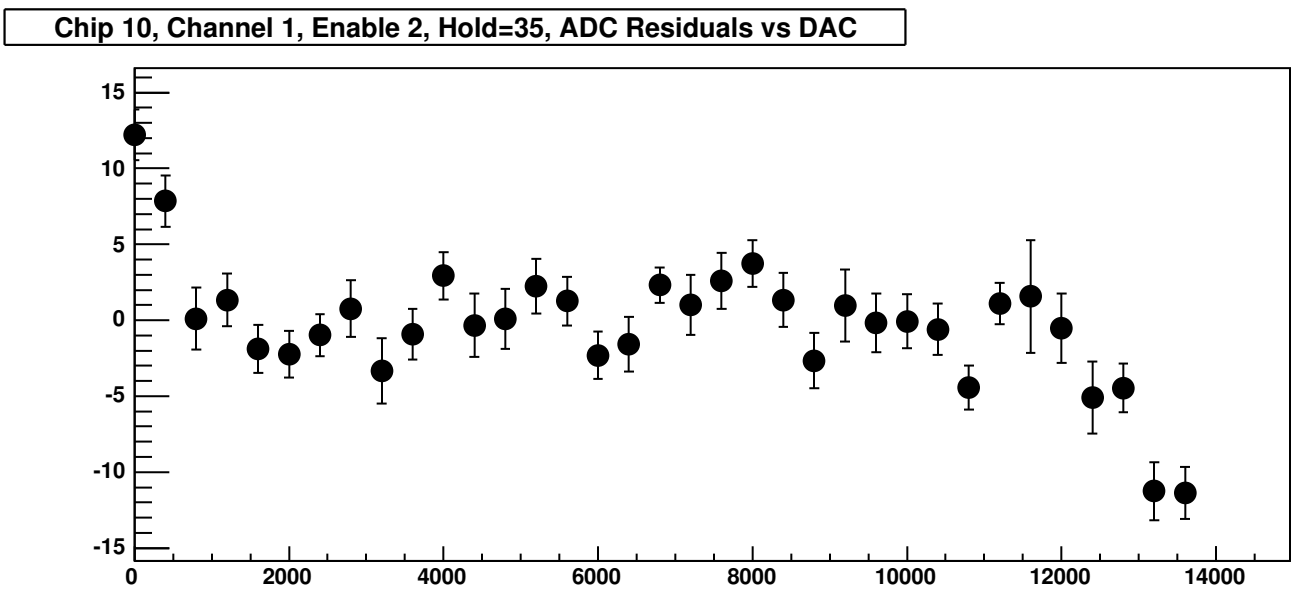


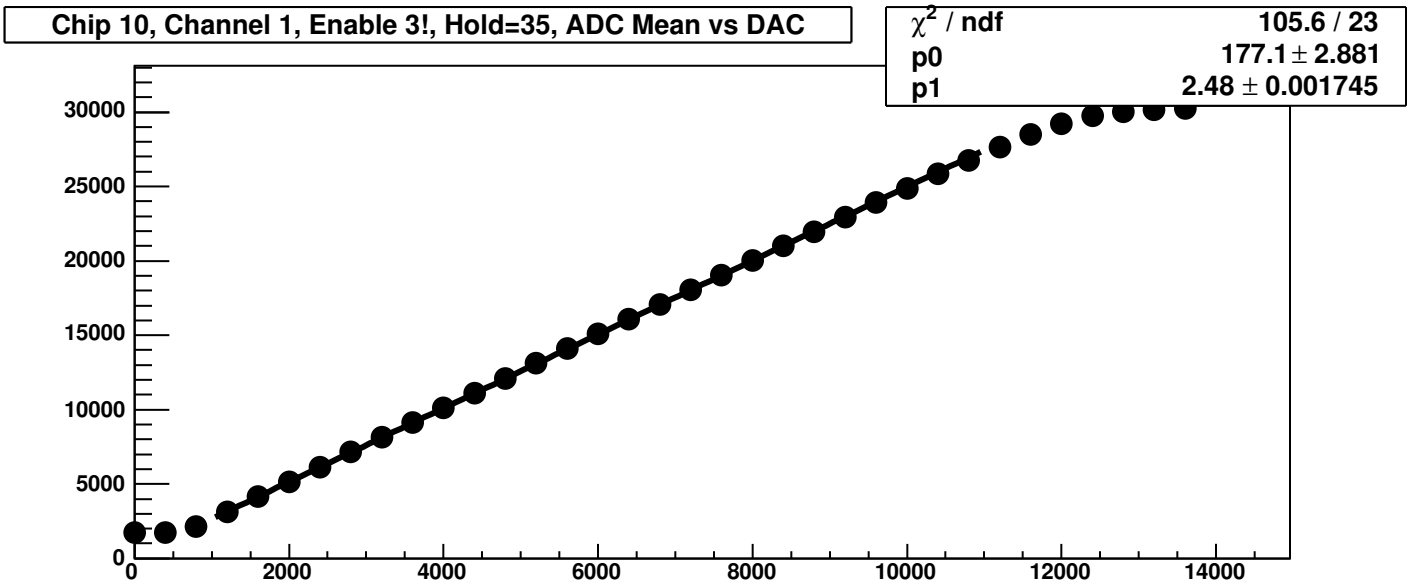
Chip 10, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

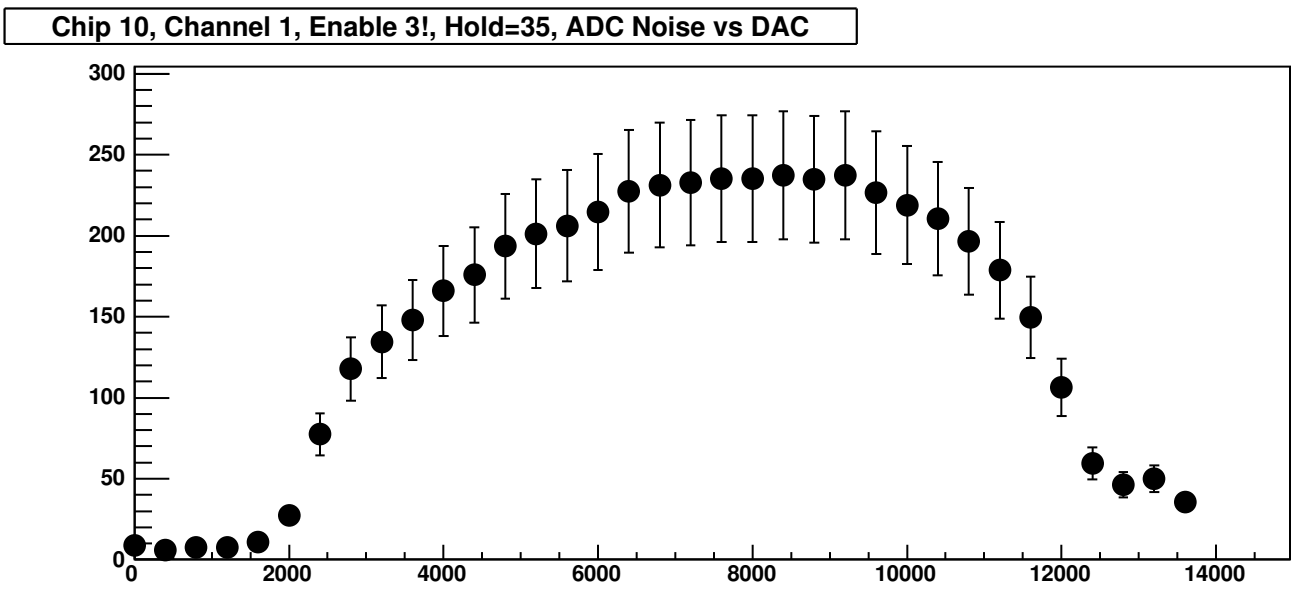


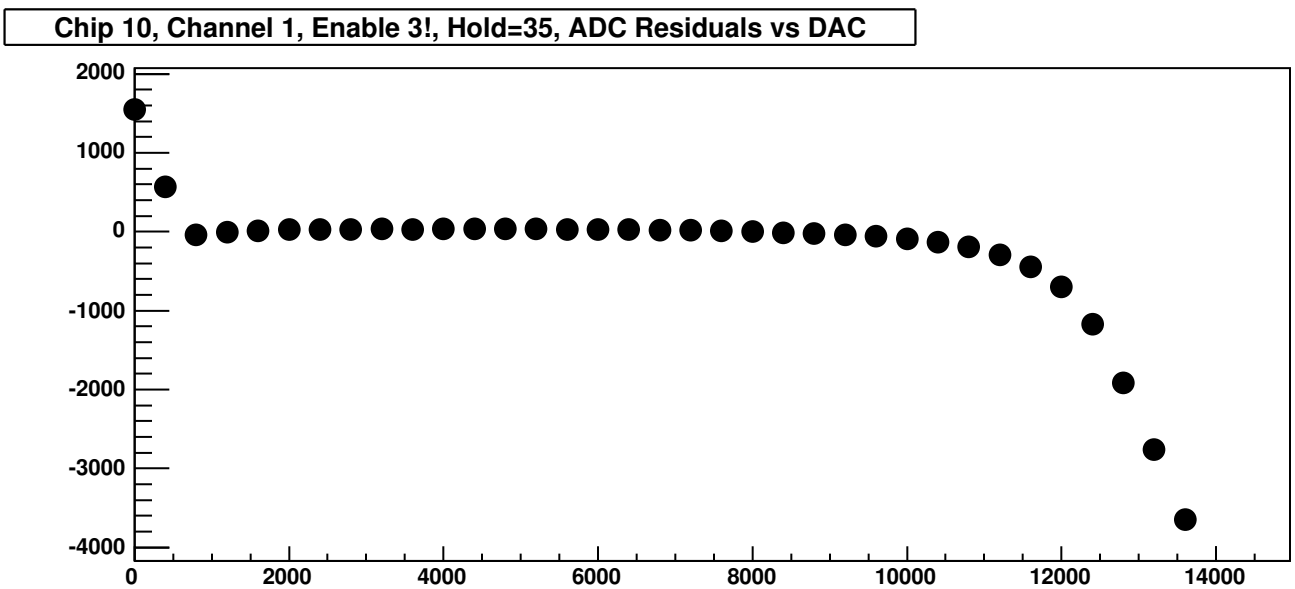

Chip 10, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 40.54 / 23
p0 97.58 ± 0.767
p1 0.01315 ± 0.0001163
280
260
240
220
200
180
160
140
120
100
0
2000
4000
6000
8000
10000
12000
14000

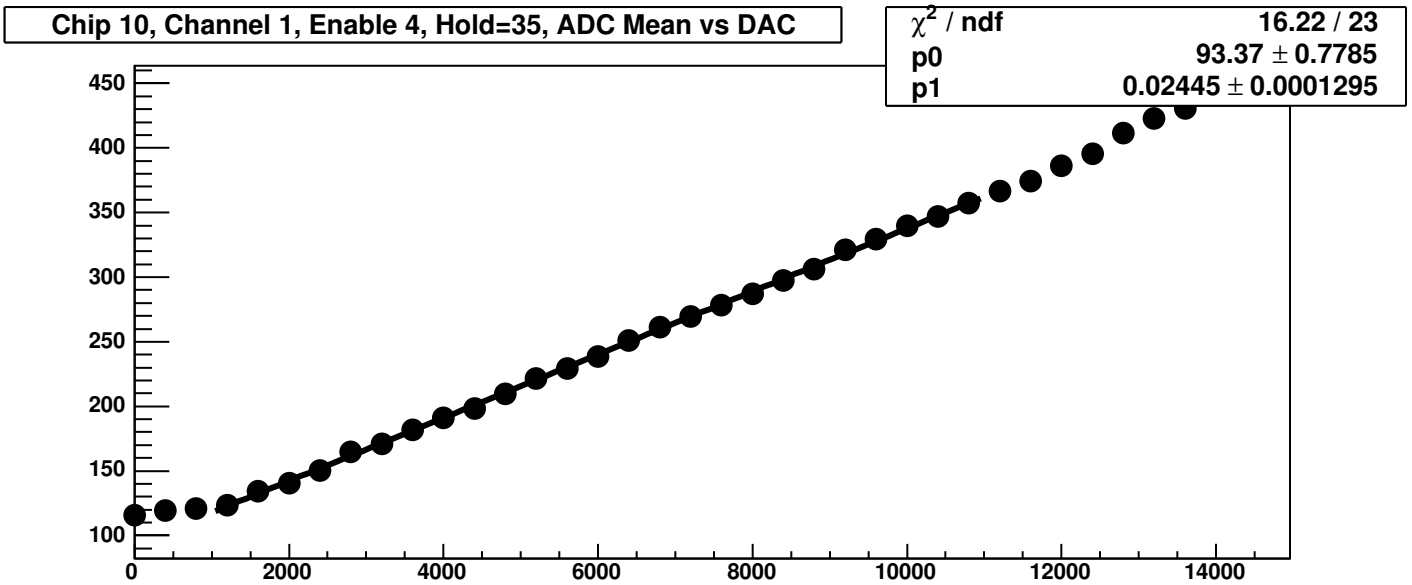

Chip 10, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

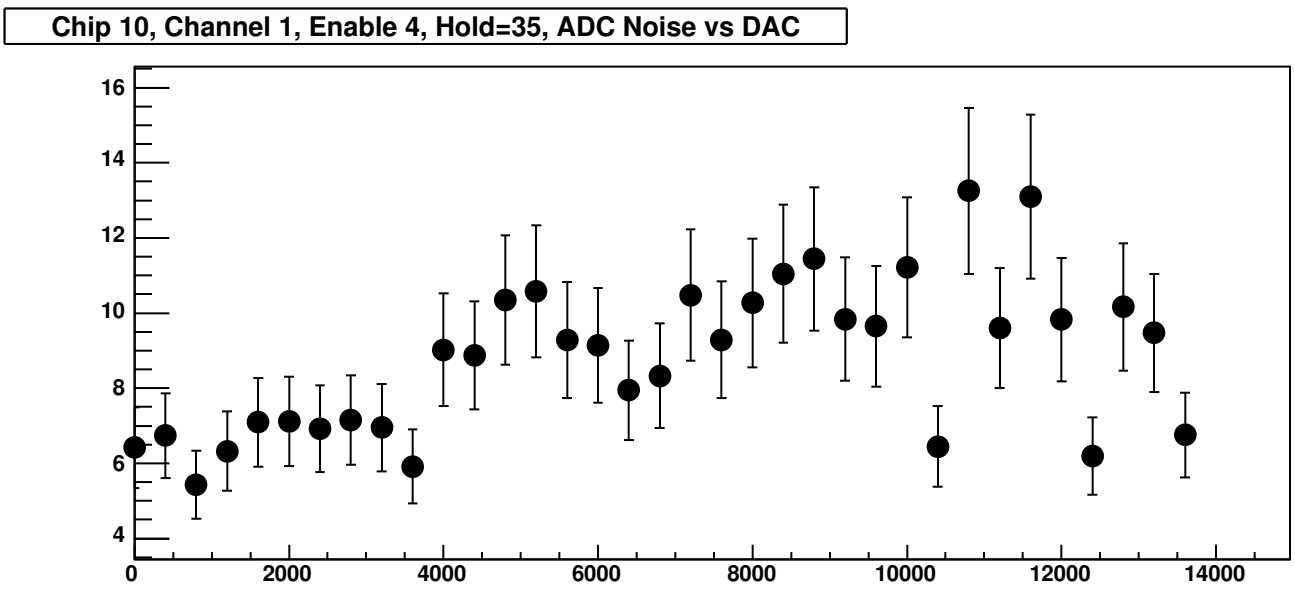

Chip 10, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

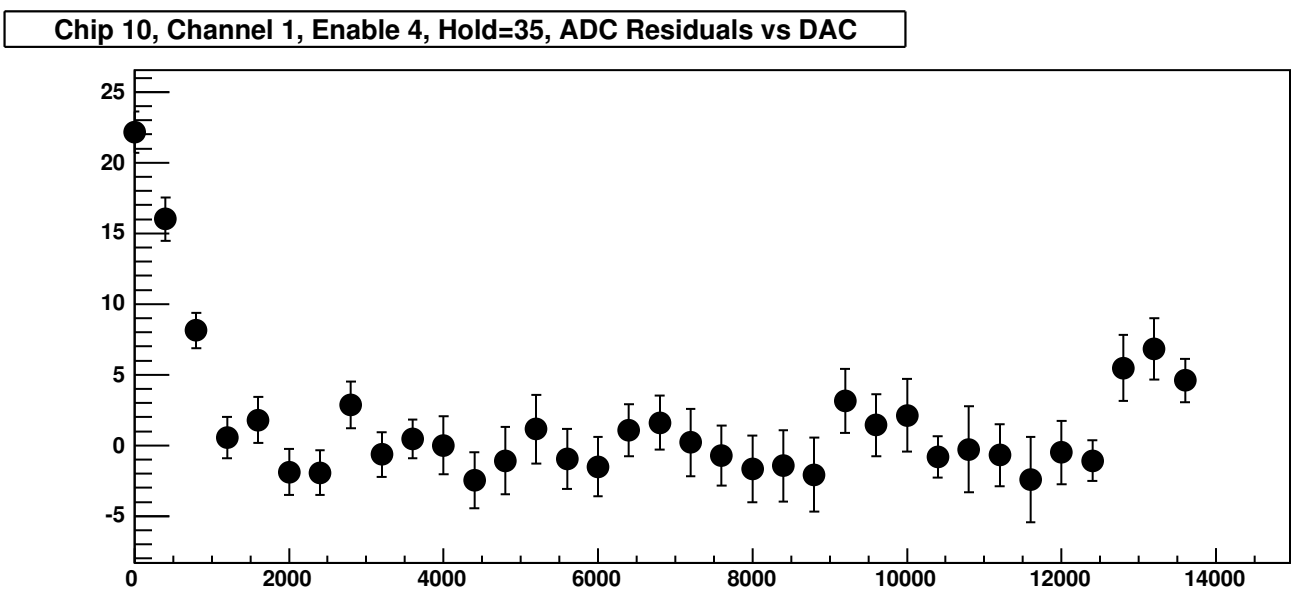

Chip 10, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 105.6 / 23
p0 177.1 ± 2.881
p1 2.48 ± 0.001745
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

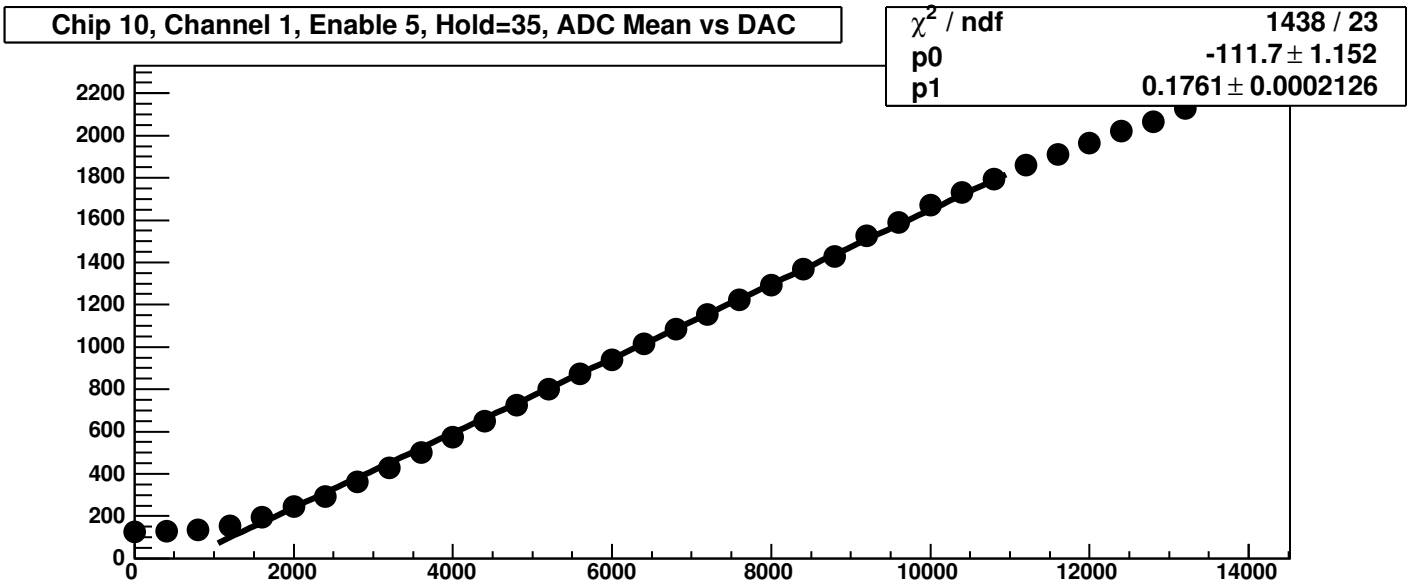

Chip 10, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

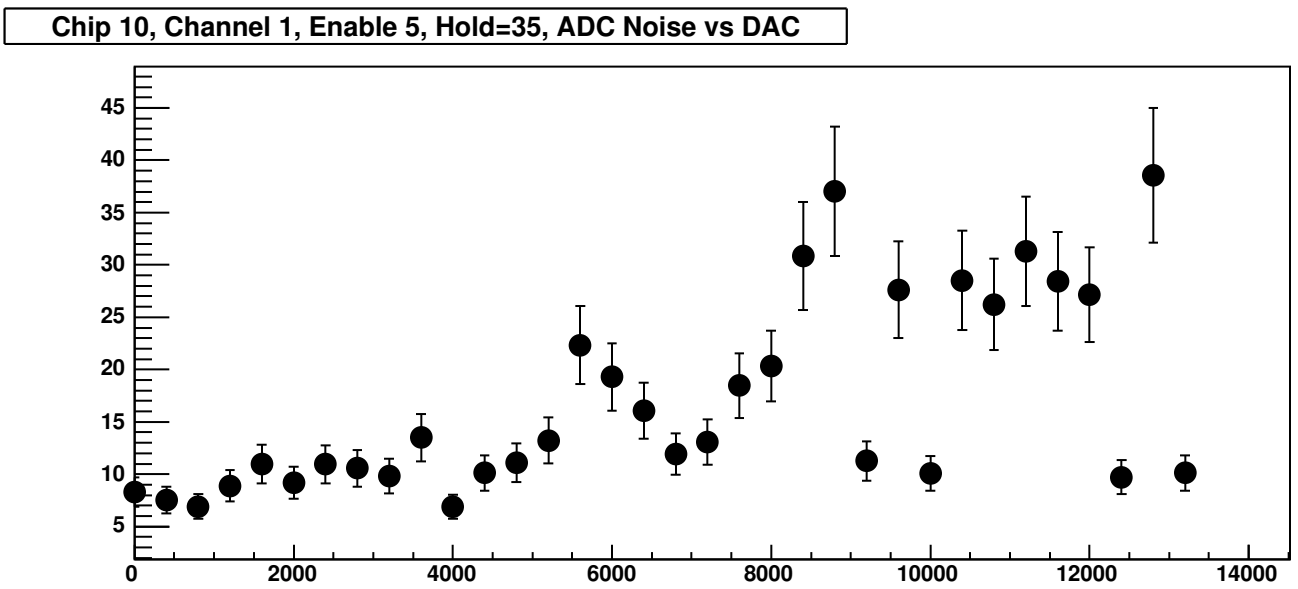

Chip 10, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

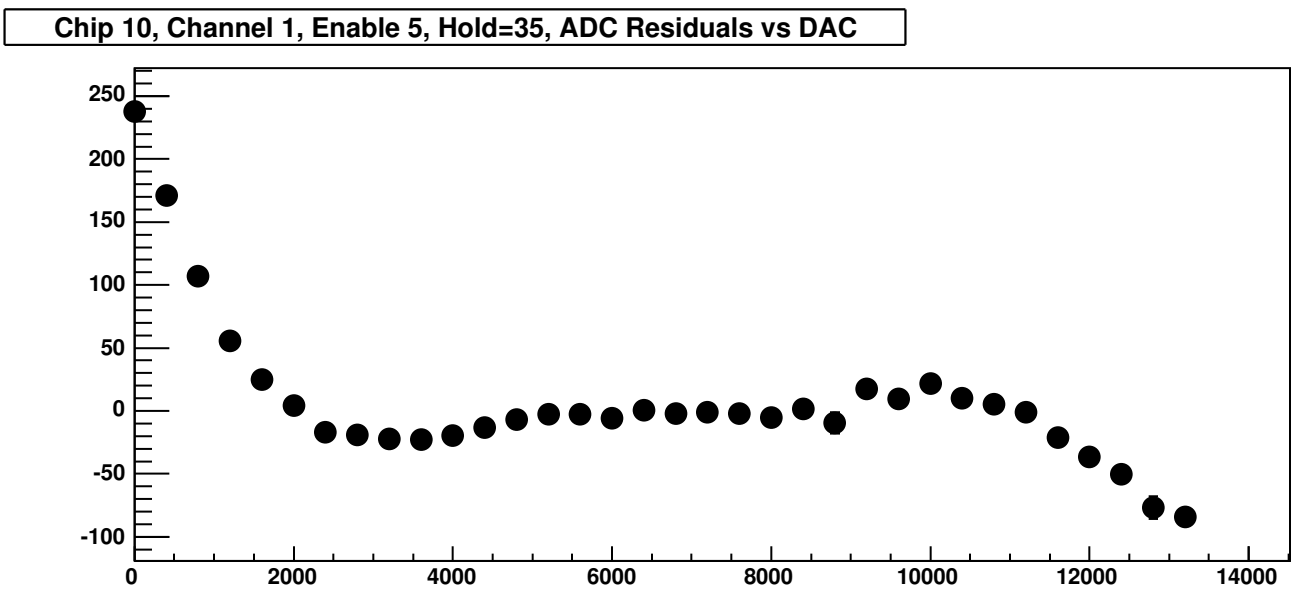

Chip 10, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 16.22 / 23
p0 93.37 ± 0.7785
p1 0.02445 ± 0.0001295
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

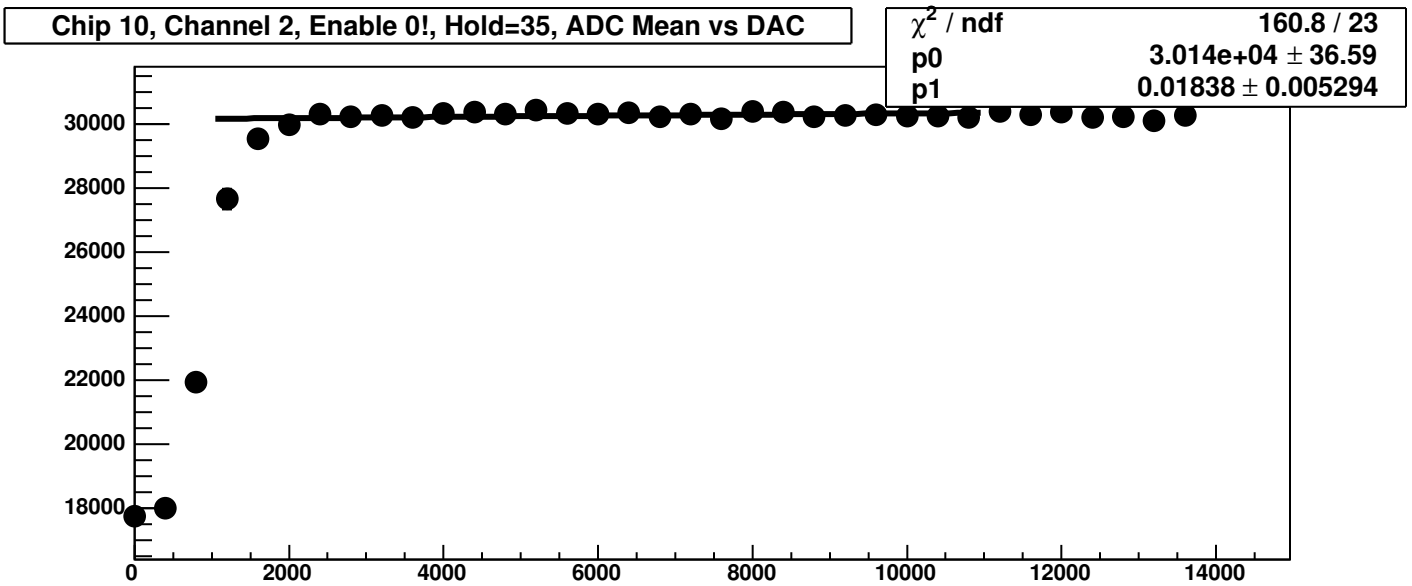

Chip 10, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

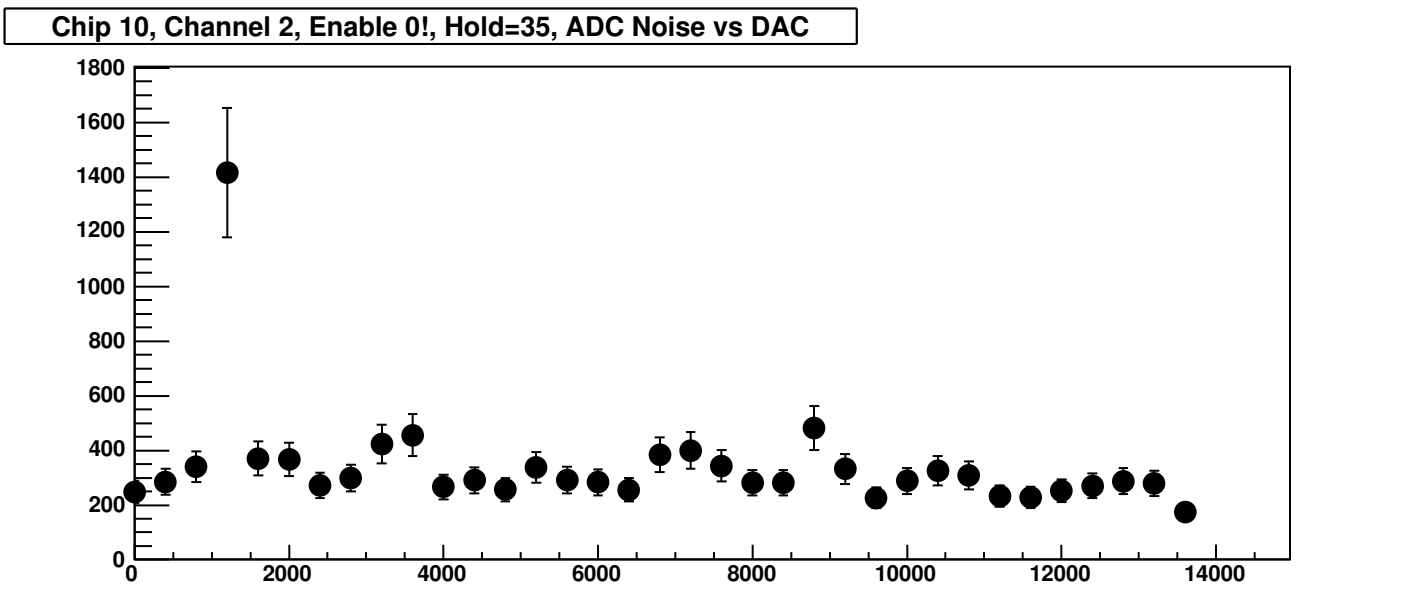

Chip 10, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

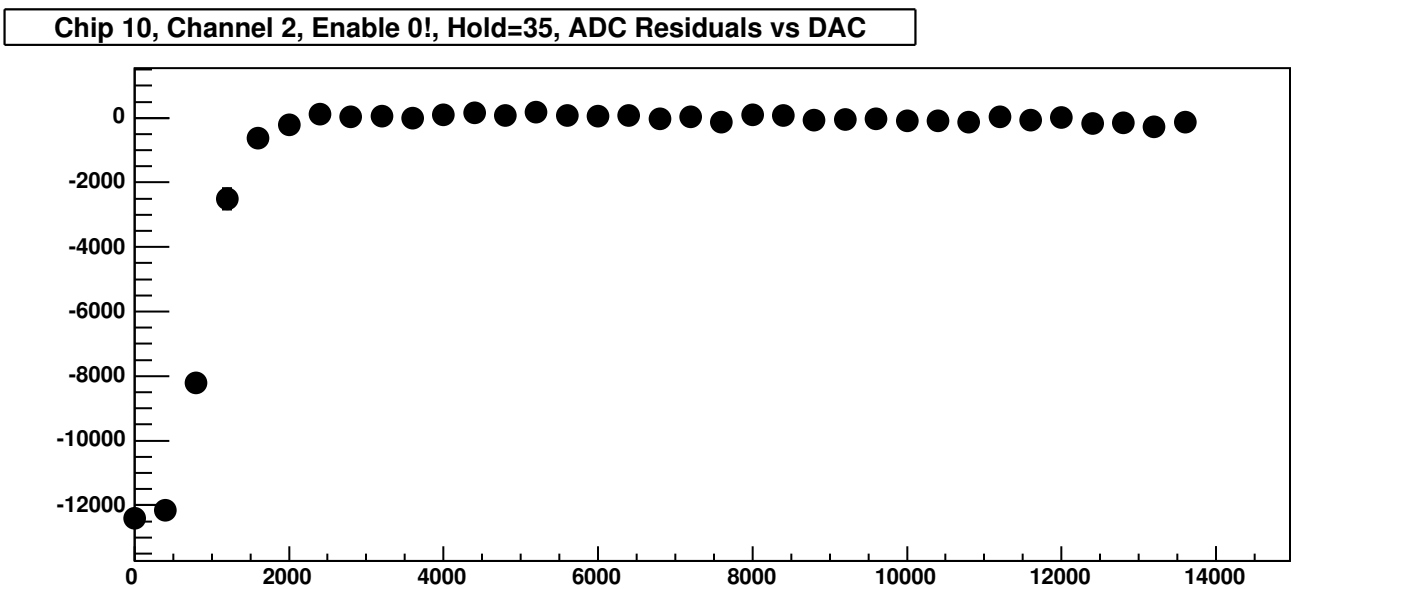

Chip 10, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1438 / 23
p0 -111.7 ± 1.152
p1 0.1761 ± 0.0002126
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

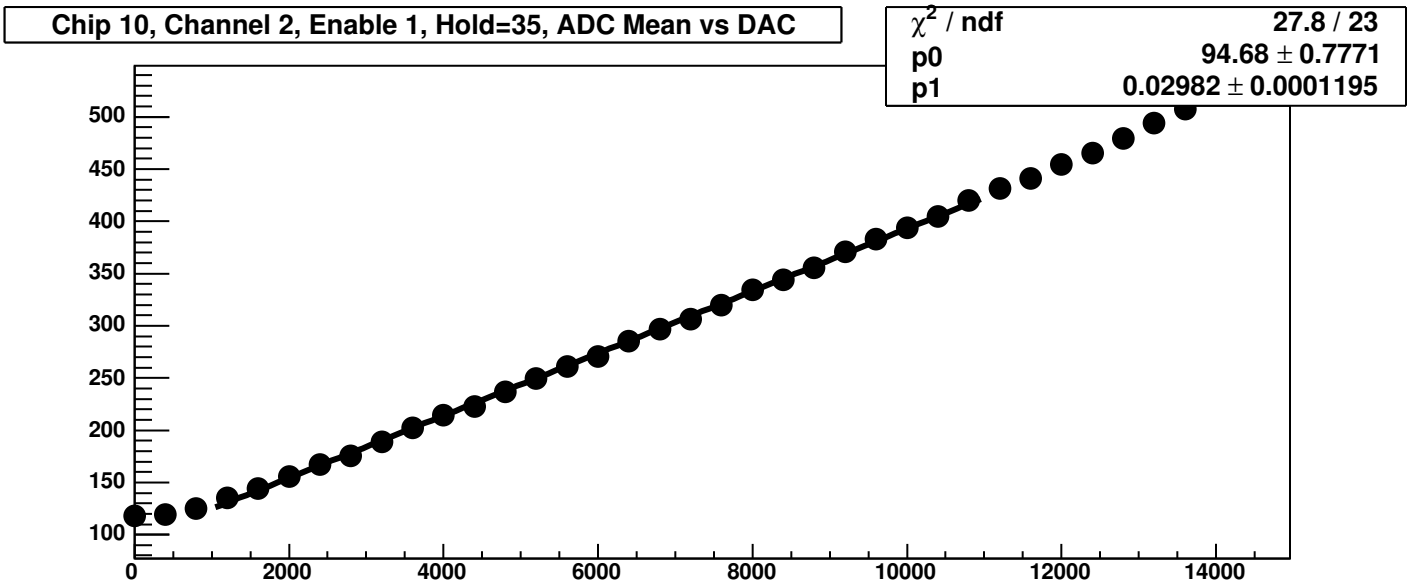

Chip 10, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

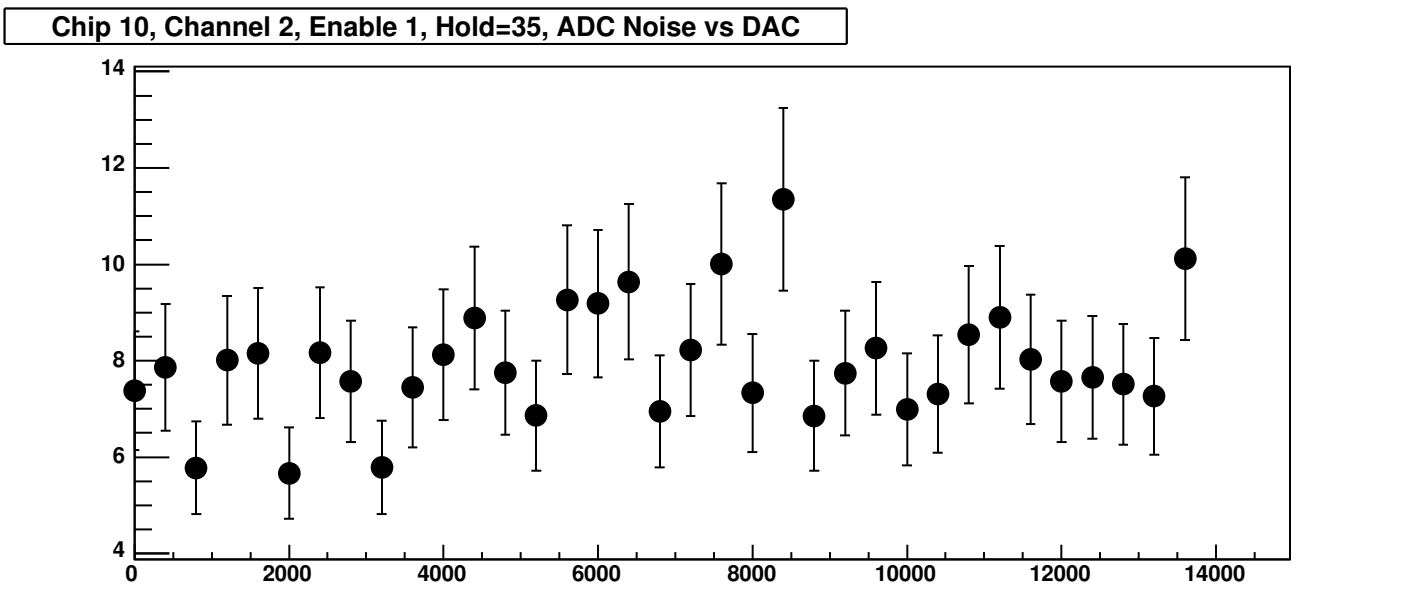

Chip 10, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

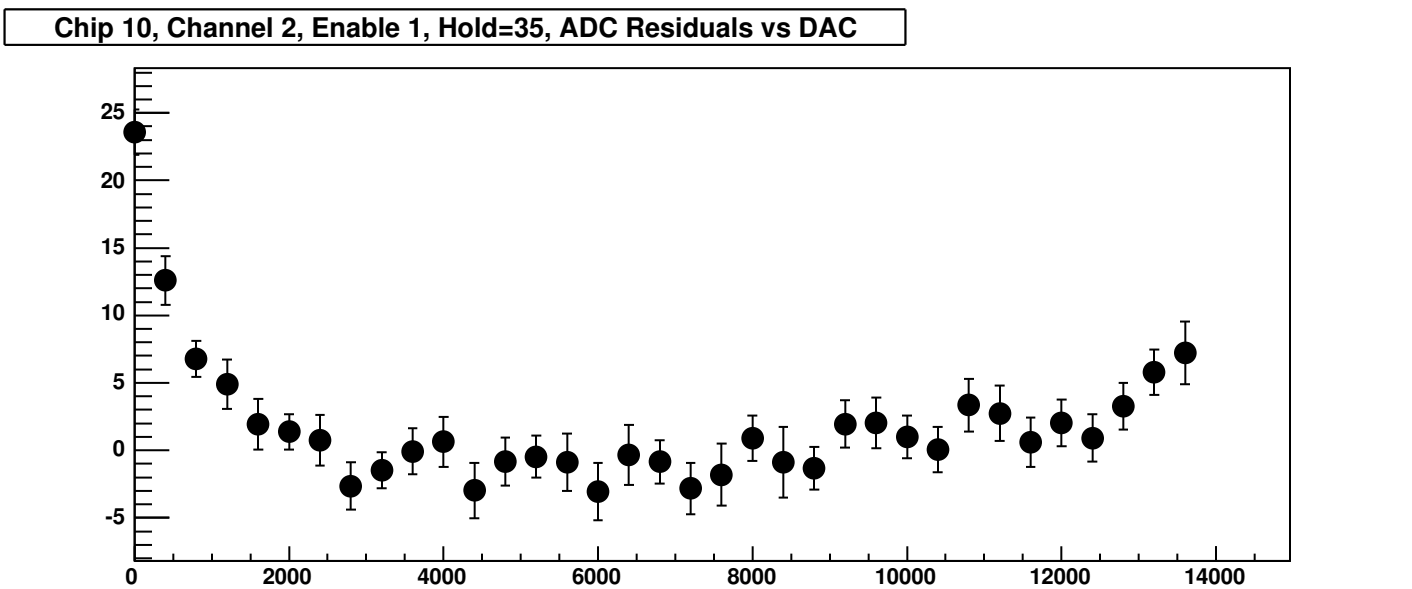

Chip 10, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 160.8 / 23
p0 3.014e+04 ± 36.59
p1 0.01838 ± 0.005294
30000
28000
26000
24000
22000
20000
18000
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

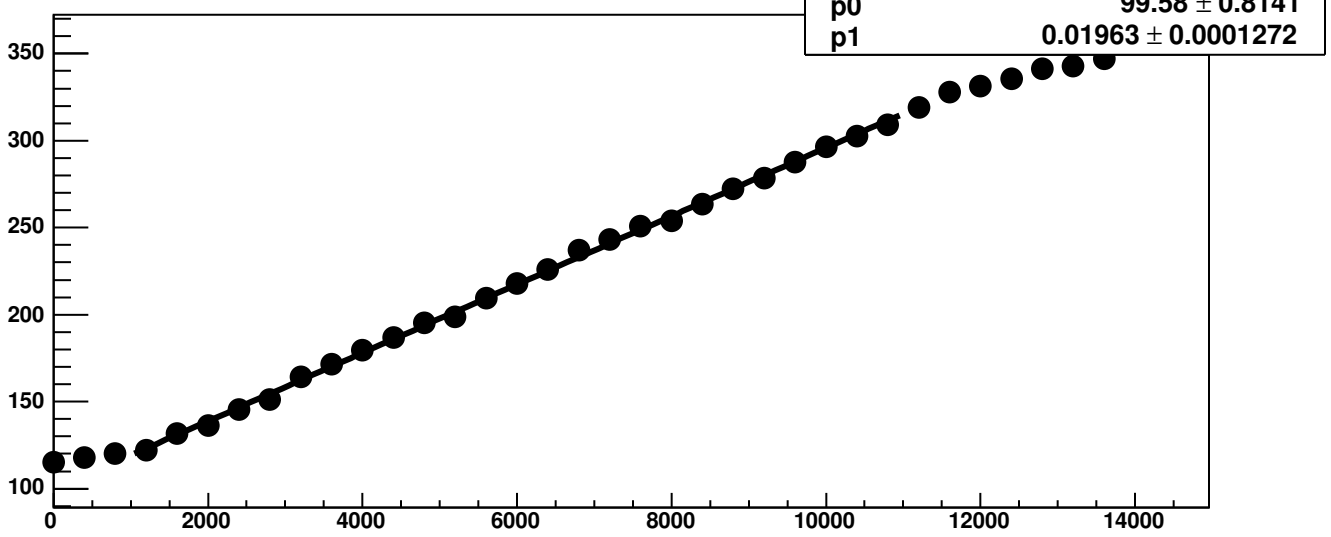

Chip 10, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 27.8 / 23
p0 94.68 ± 0.7771
p1 0.02982 ± 0.0001195
500
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

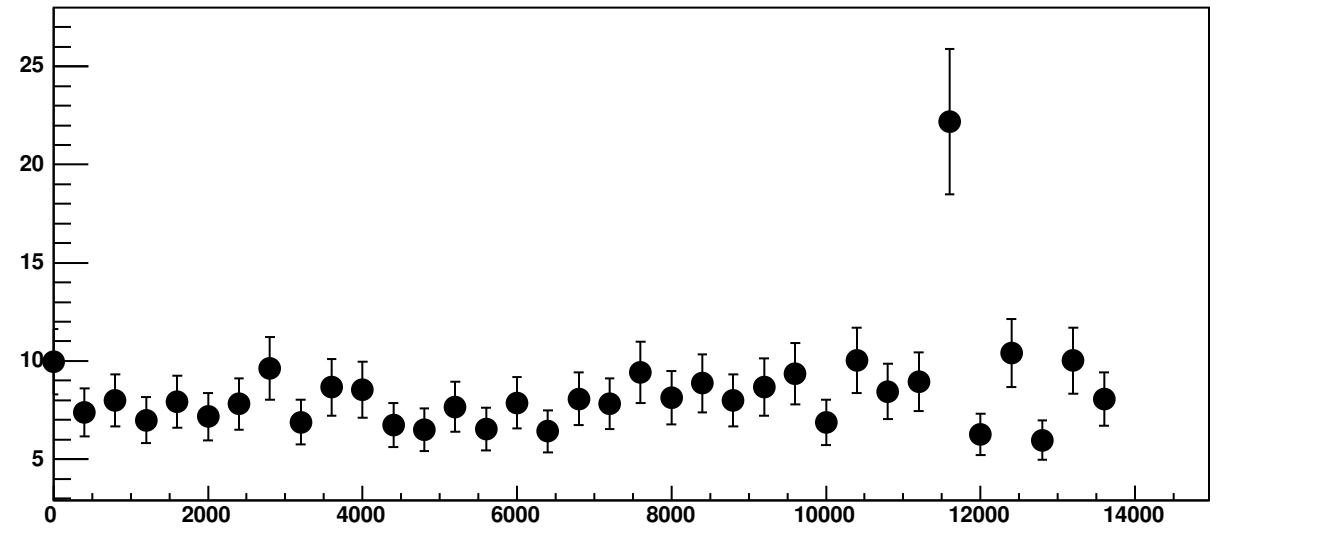

Chip 10, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

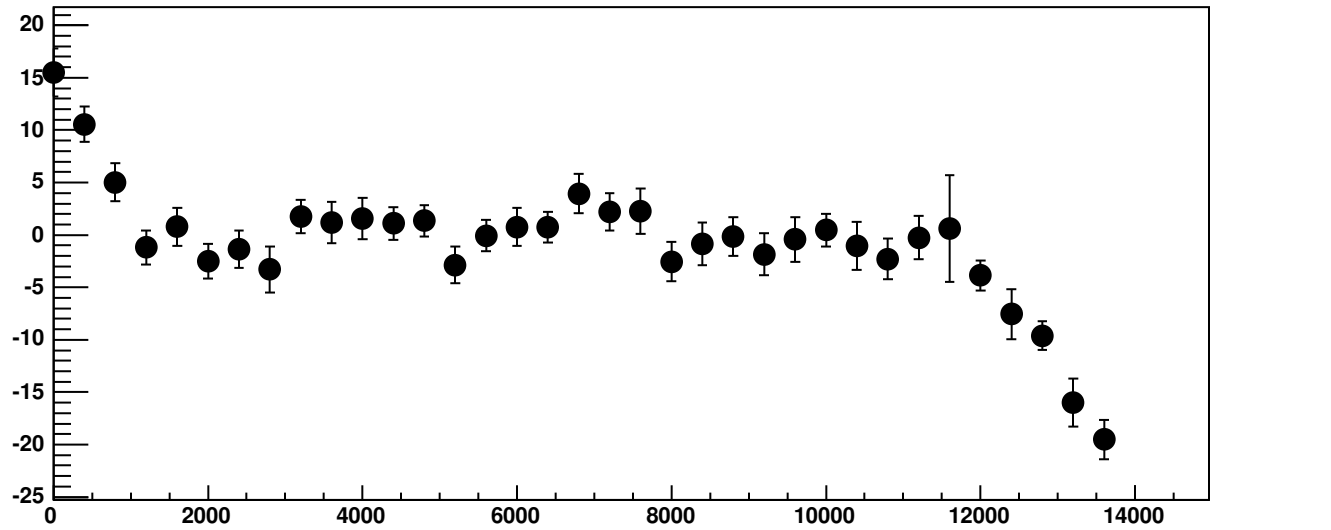
Chip 10, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

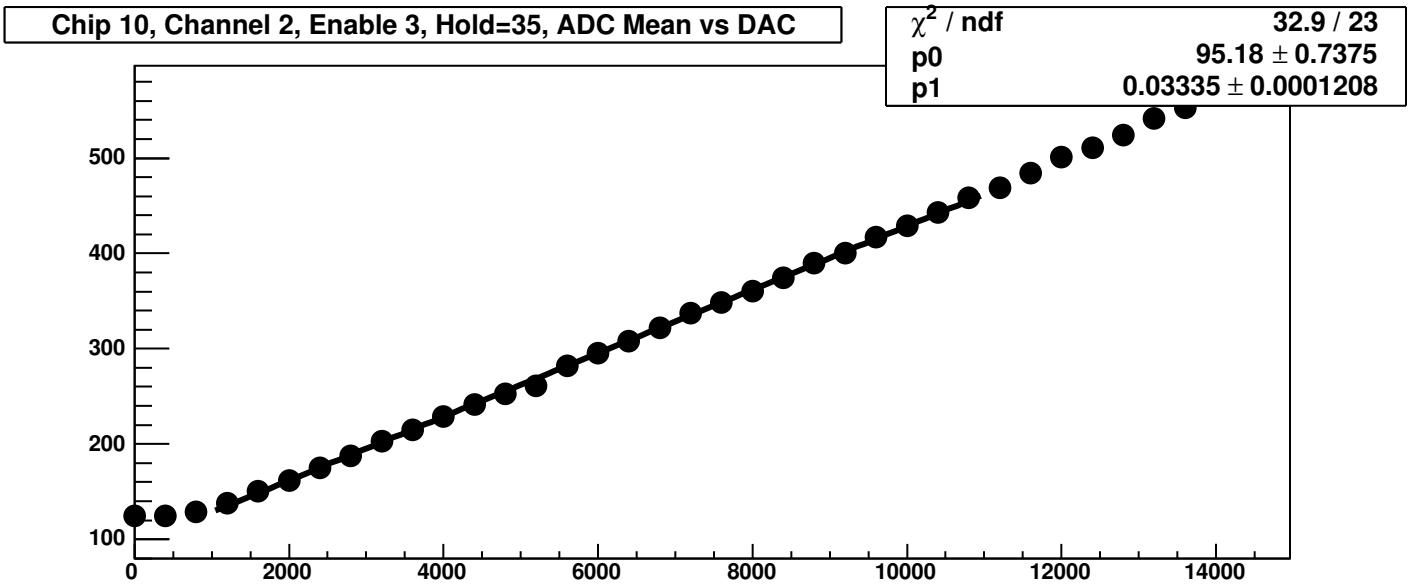


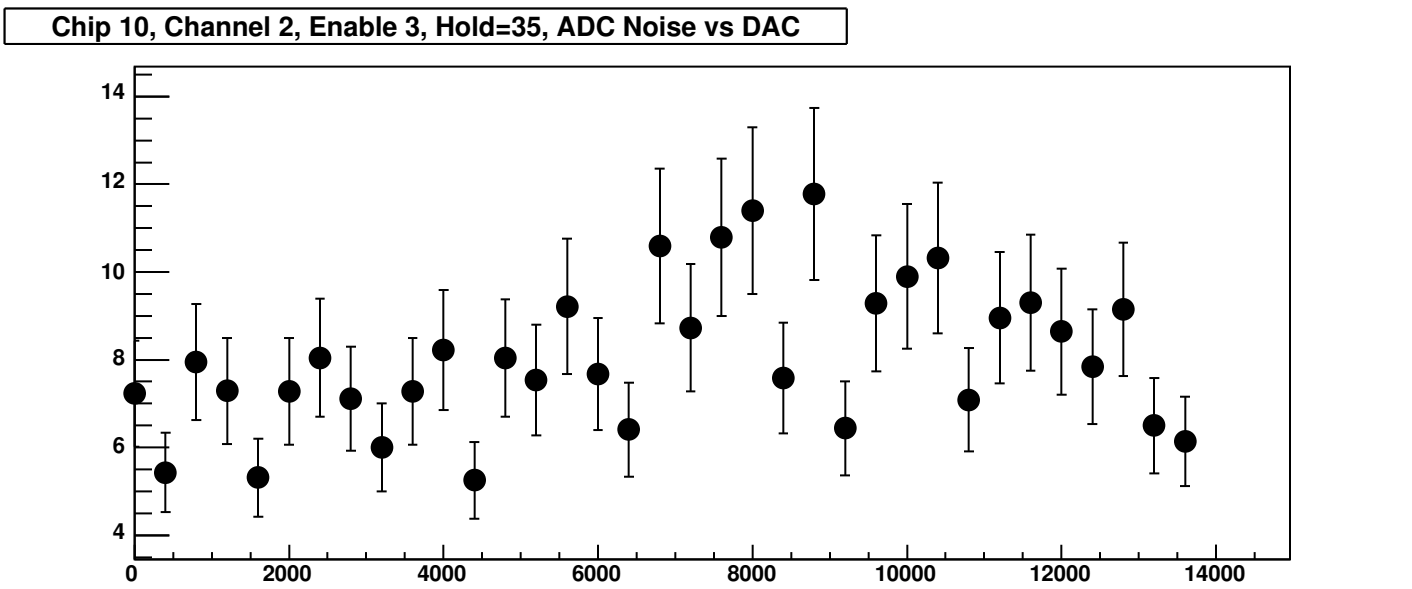
Chip 10, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

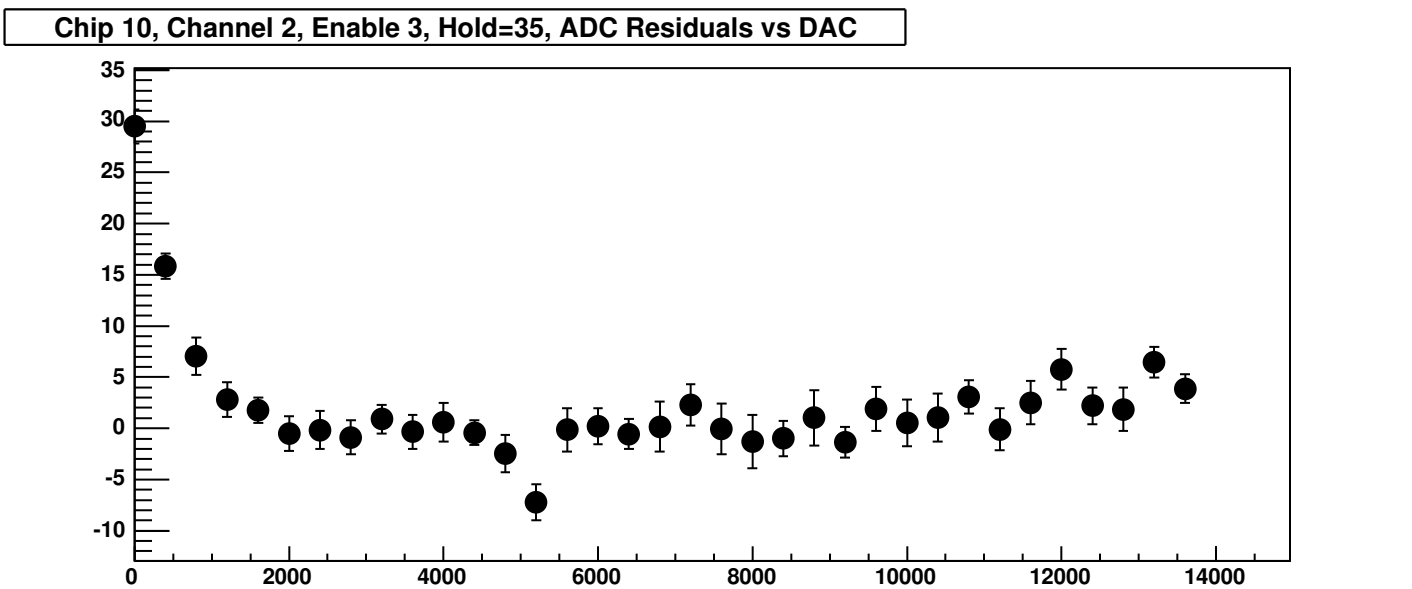


Chip 10, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

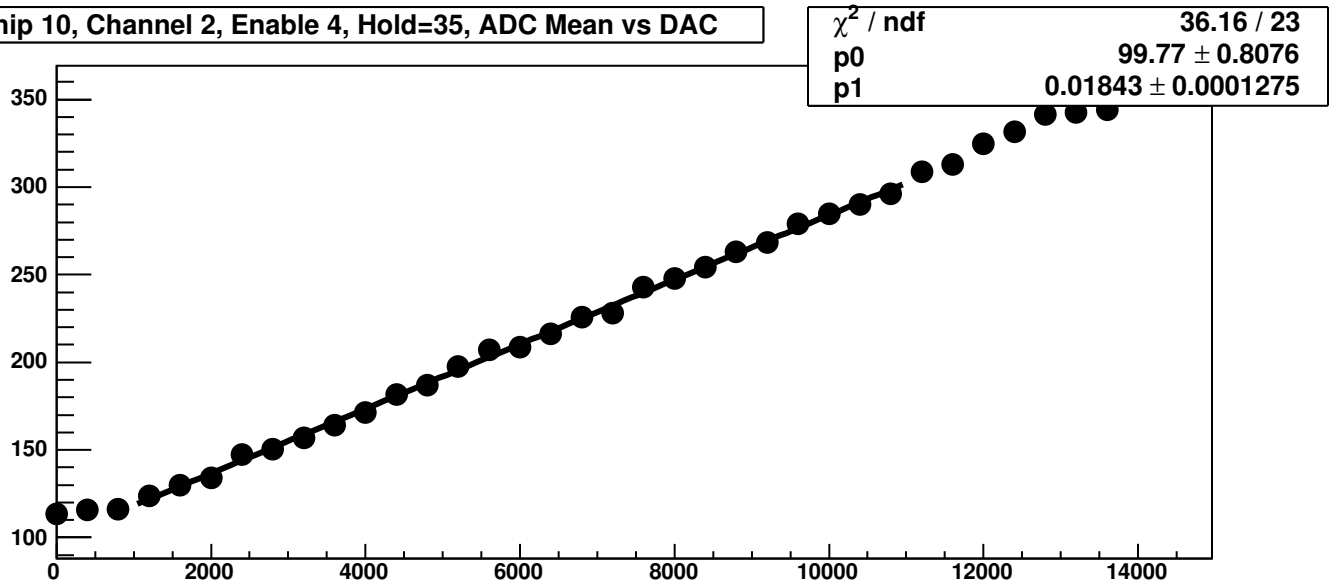



Chip 10, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 32.9 / 23
p0 95.18 ± 0.7375
p1 0.03335 ± 0.0001208
500
400
300
200
100
0
2000
4000
6000
8000
10000
12000
14000

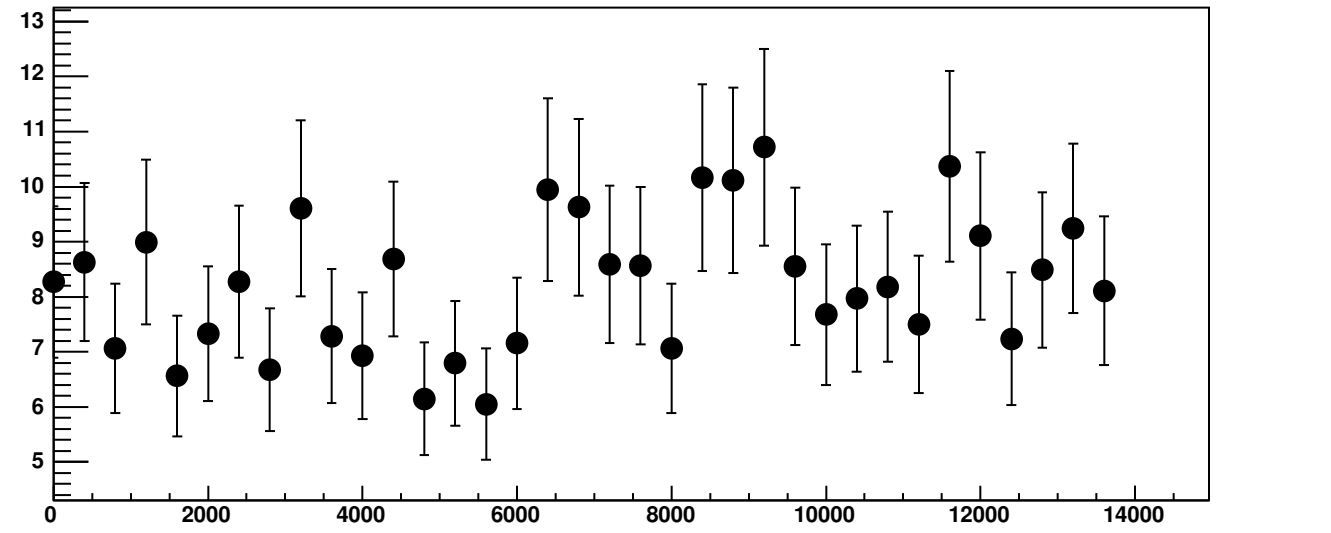

Chip 10, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC
35
30
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

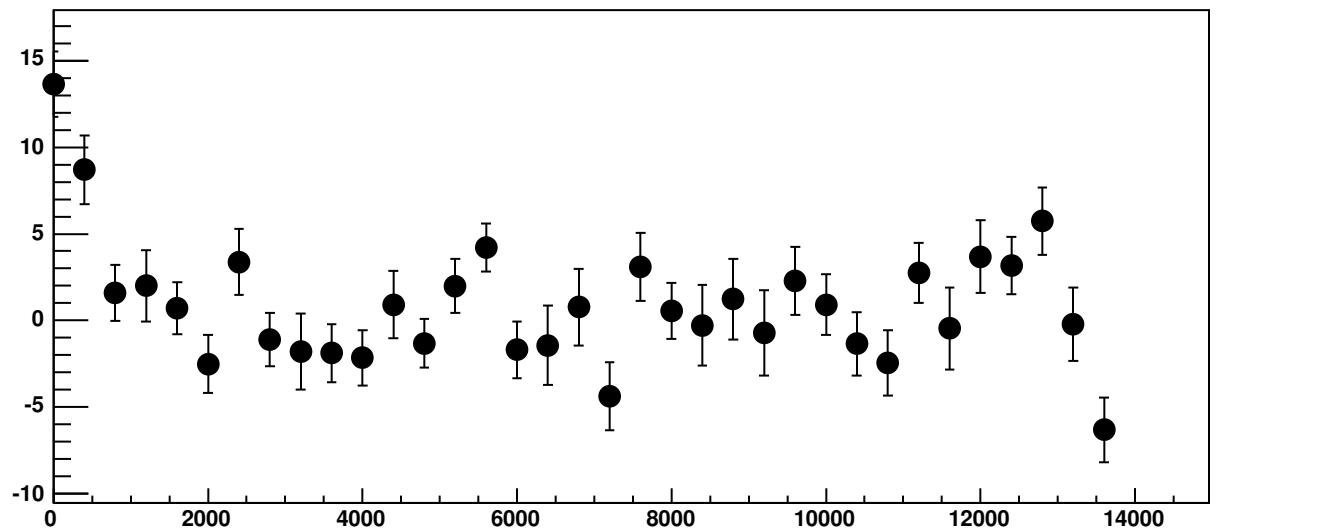
Chip 10, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

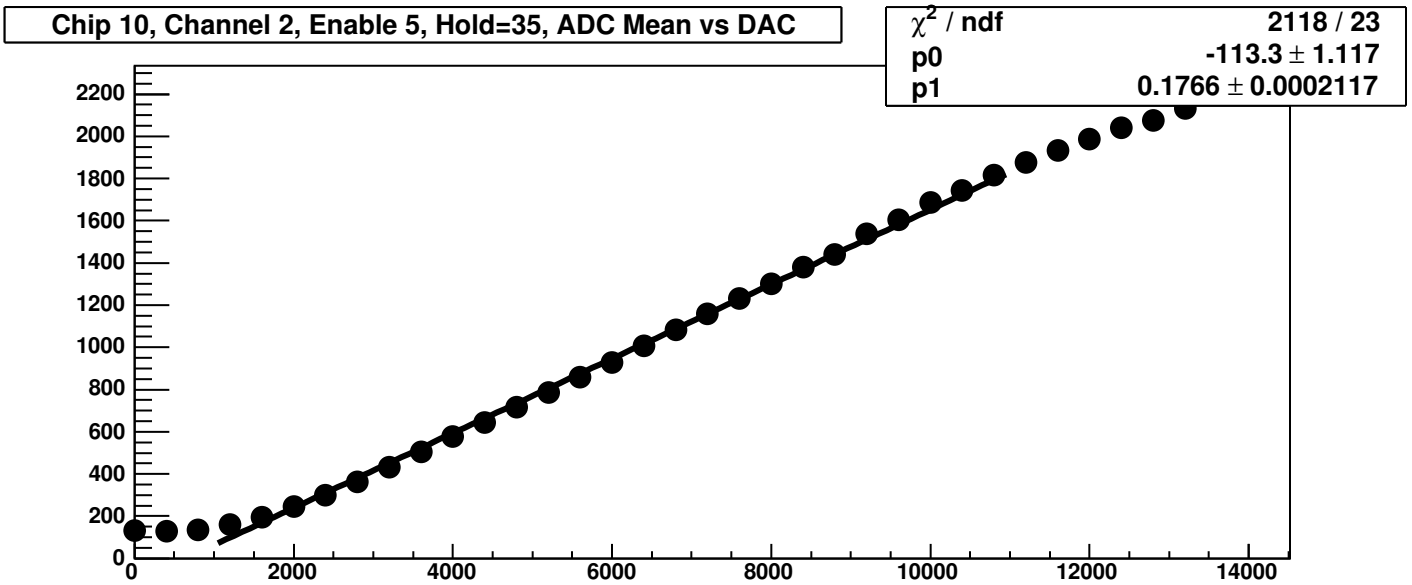


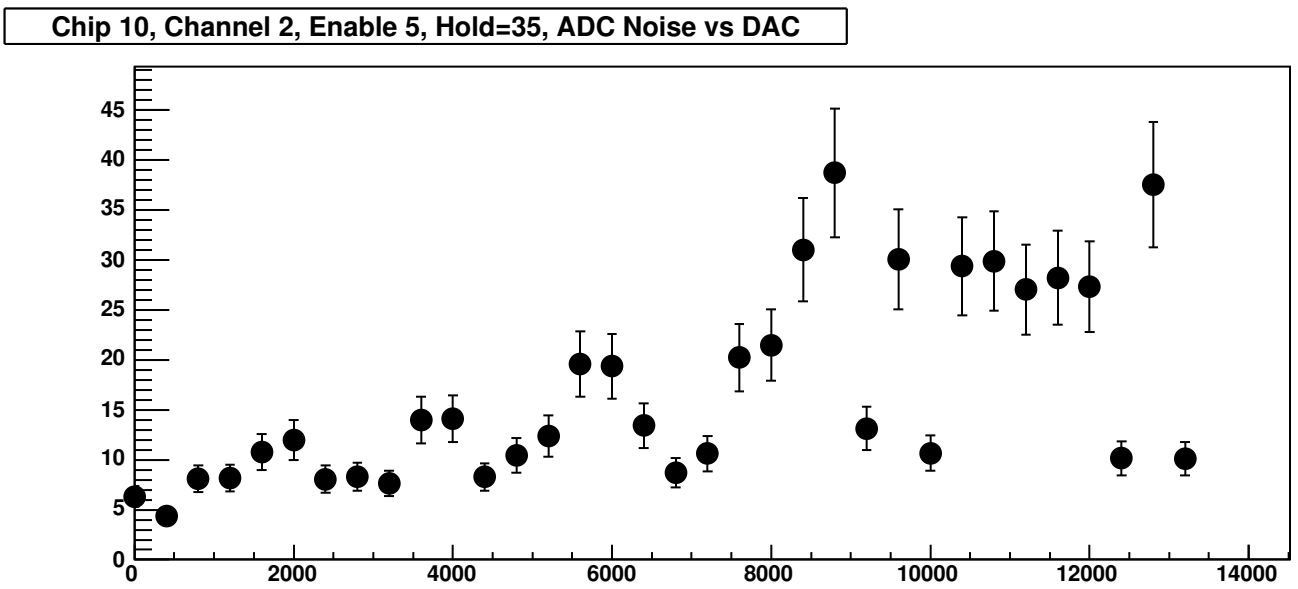
Chip 10, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

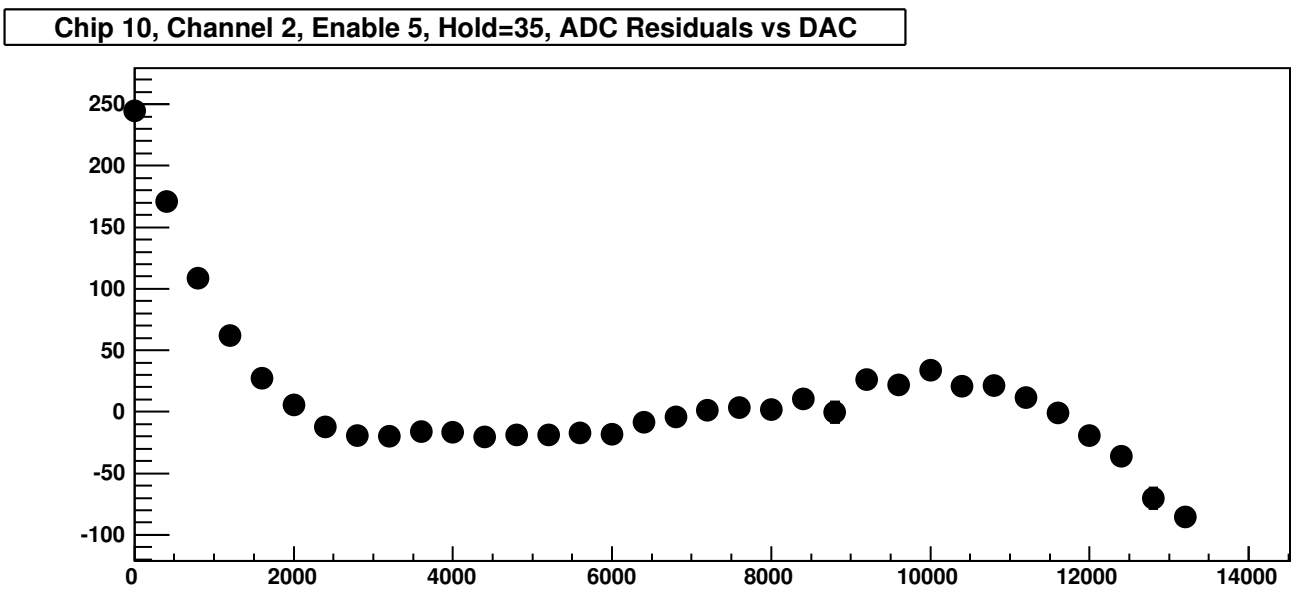


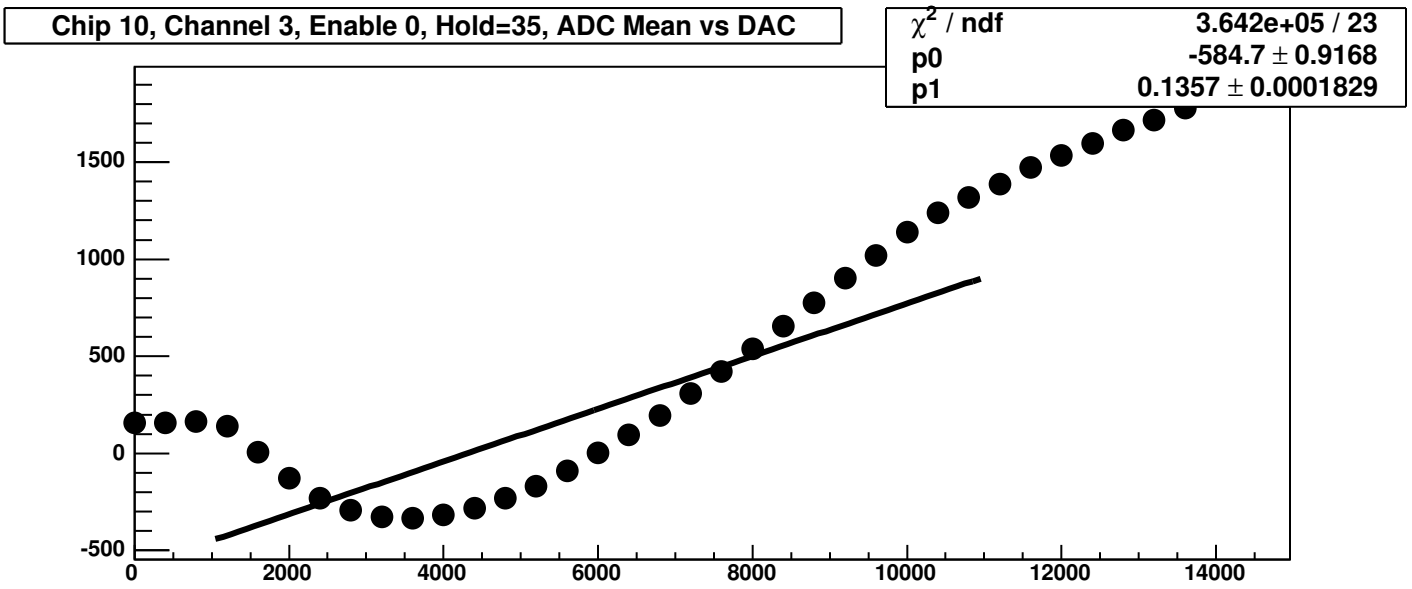
Chip 10, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

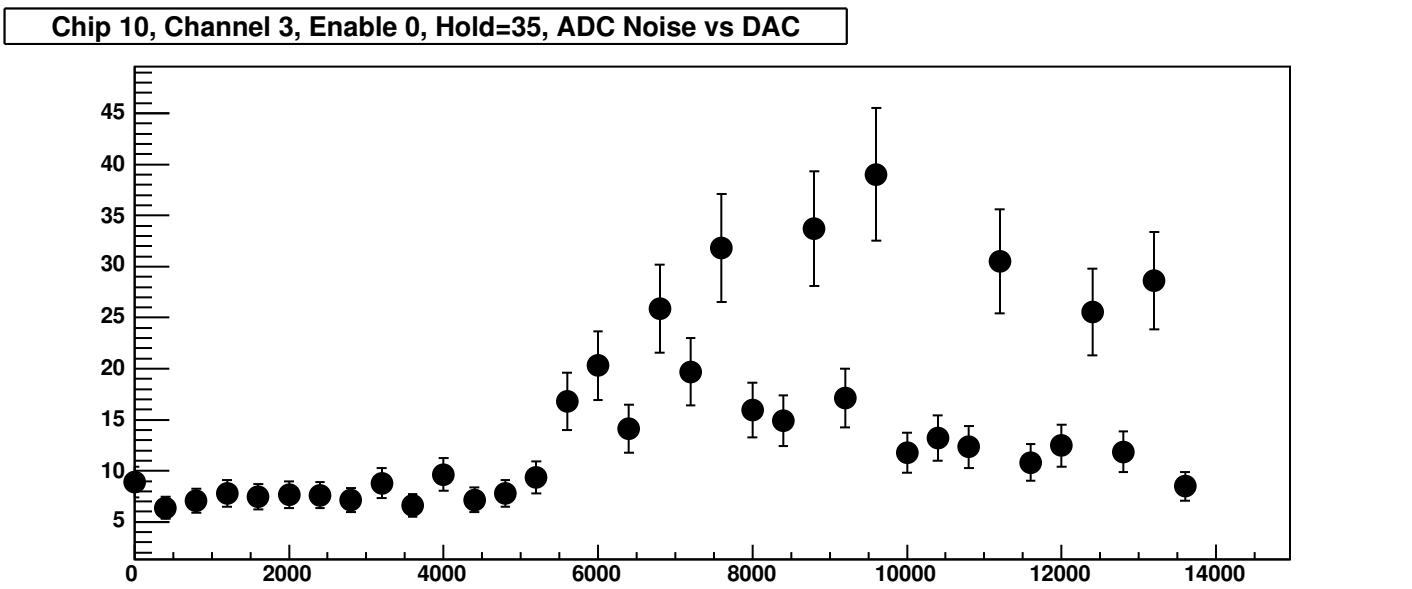


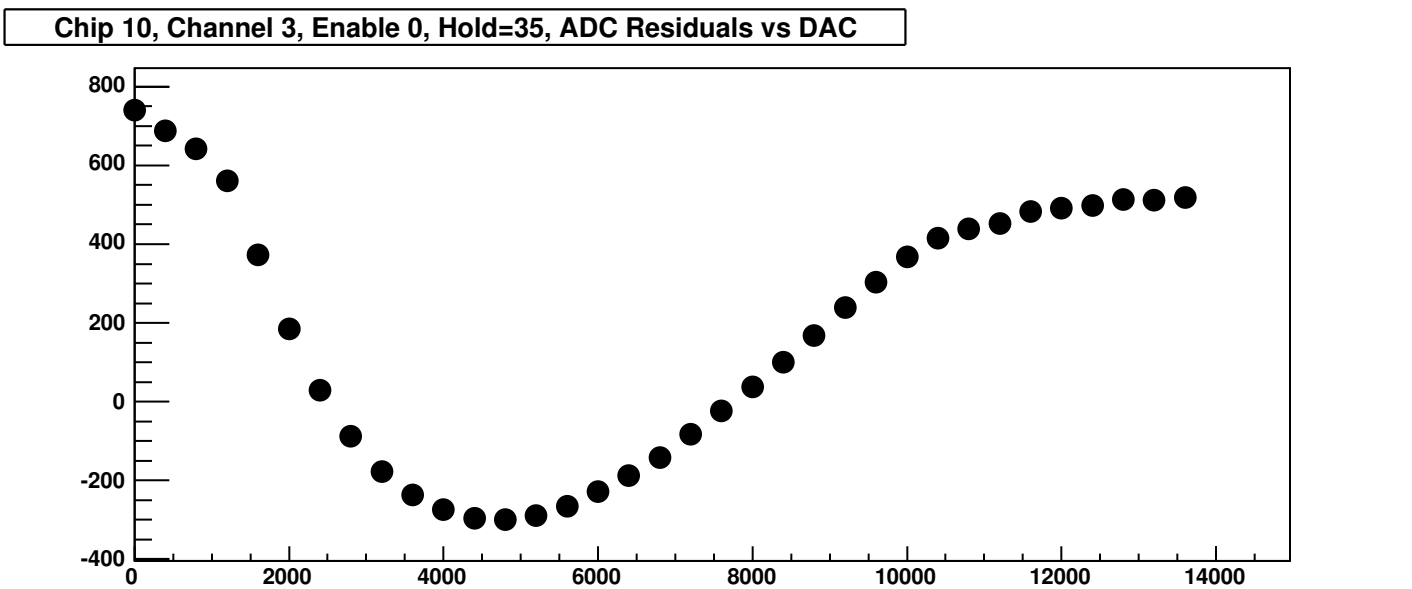

Chip 10, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2118 / 23
p0 -113.3 ± 1.117
p1 0.1766 ± 0.0002117
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

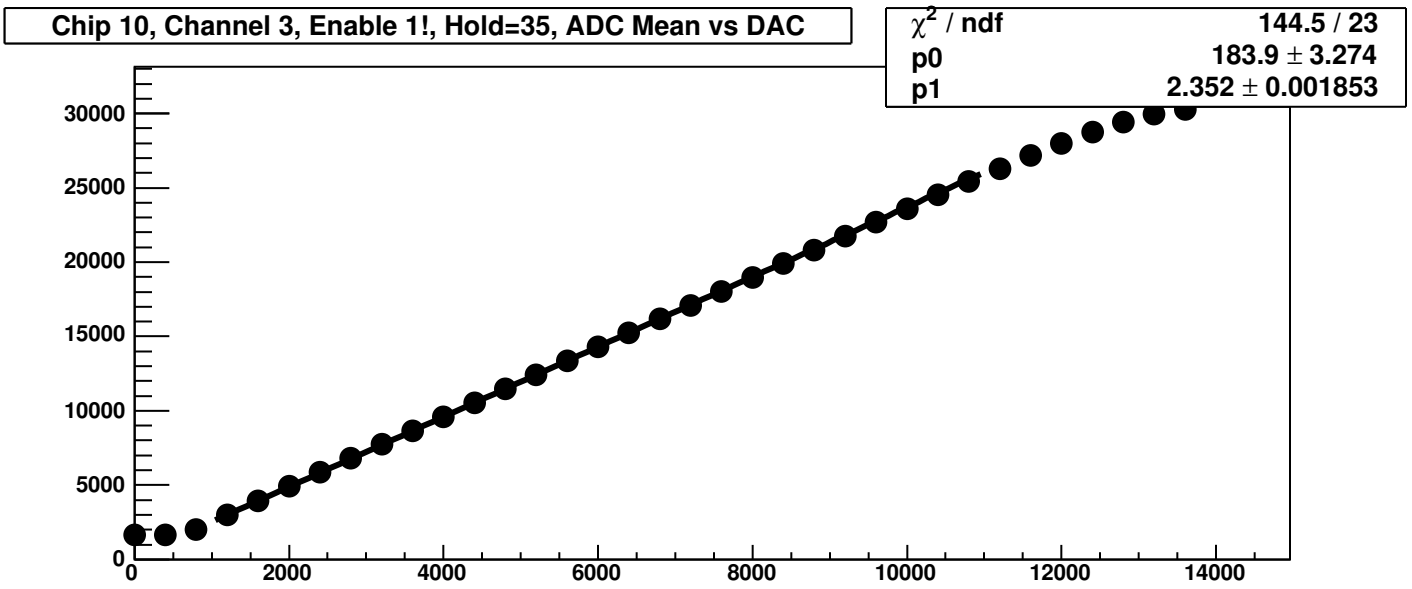

Chip 10, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
0
2000
4000
6000
8000
10000
12000
14000

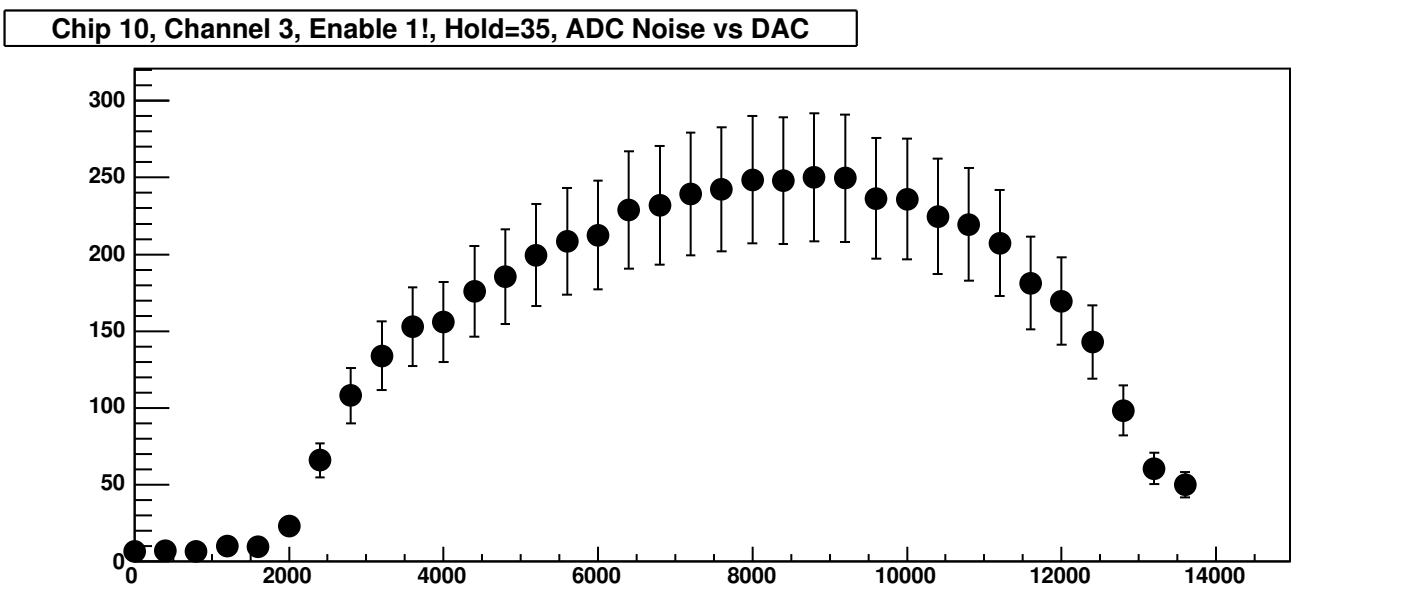

Chip 10, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

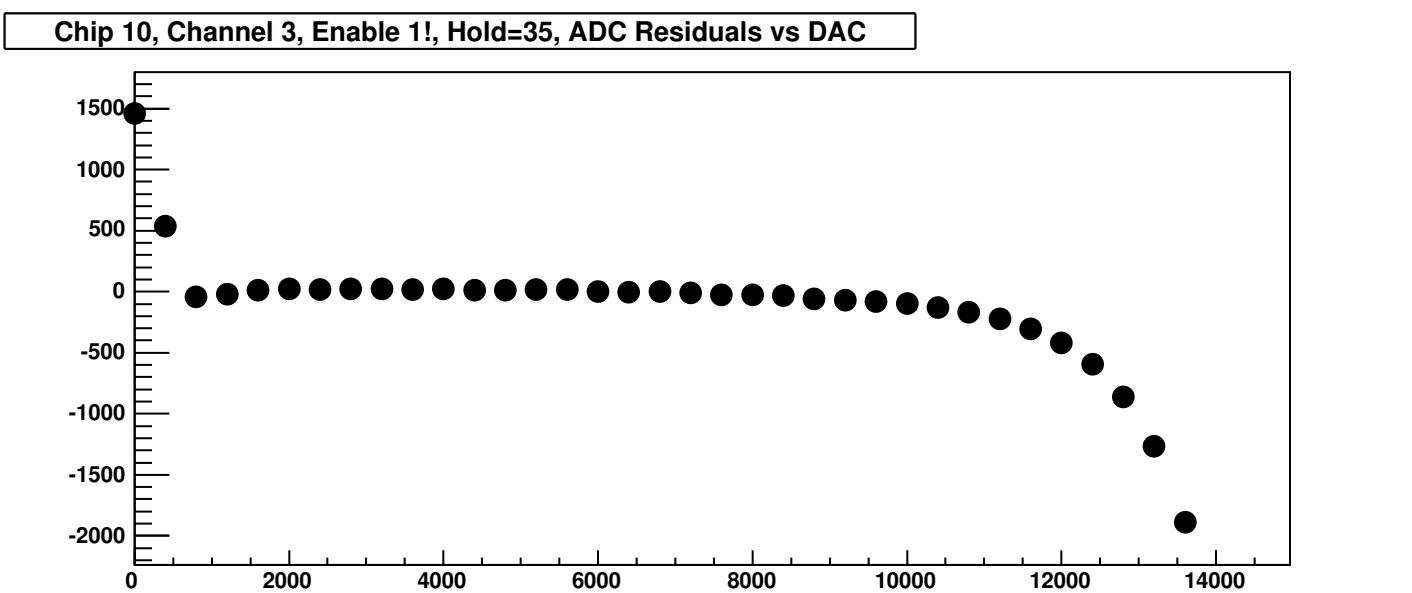

Chip 10, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.642e+05 / 23
p0 -584.7 ± 0.9168
p1 0.1357 ± 0.0001829
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

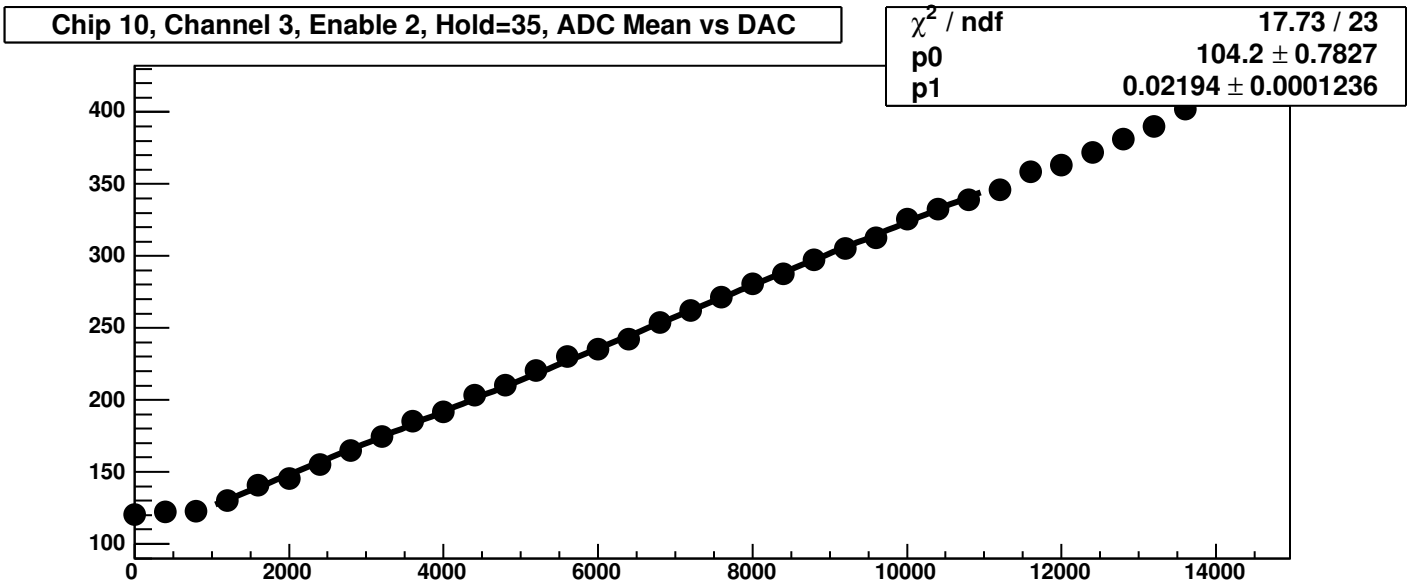

Chip 10, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

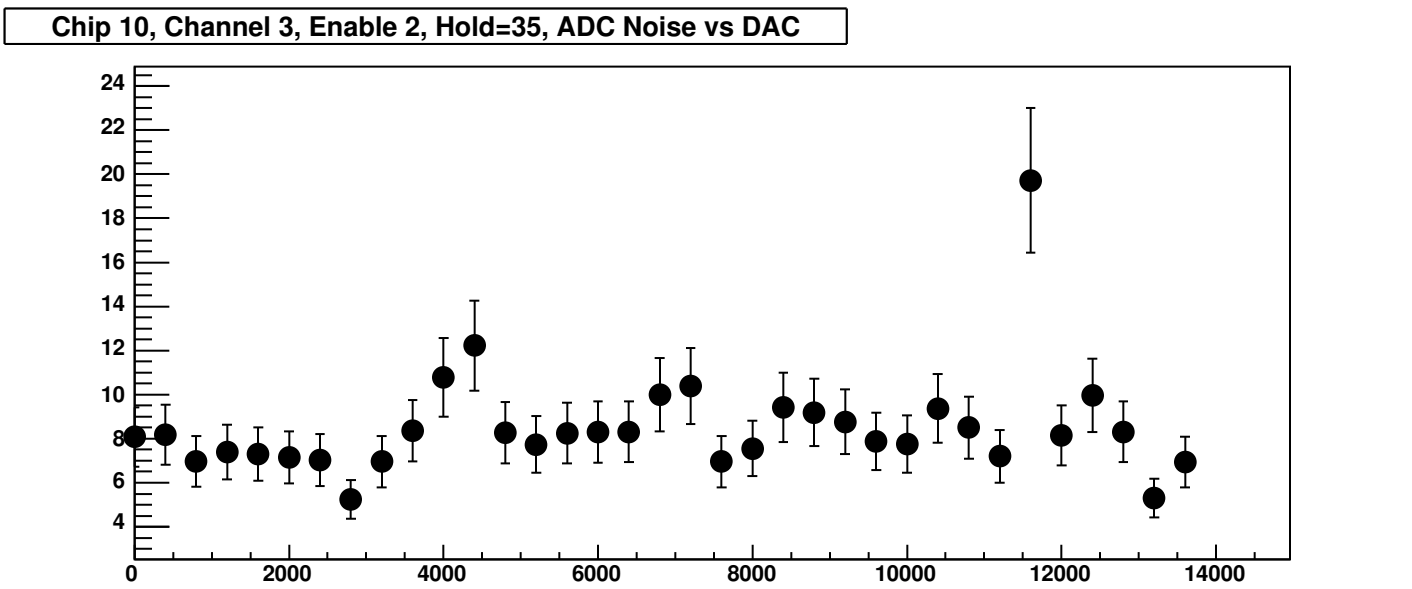

Chip 10, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

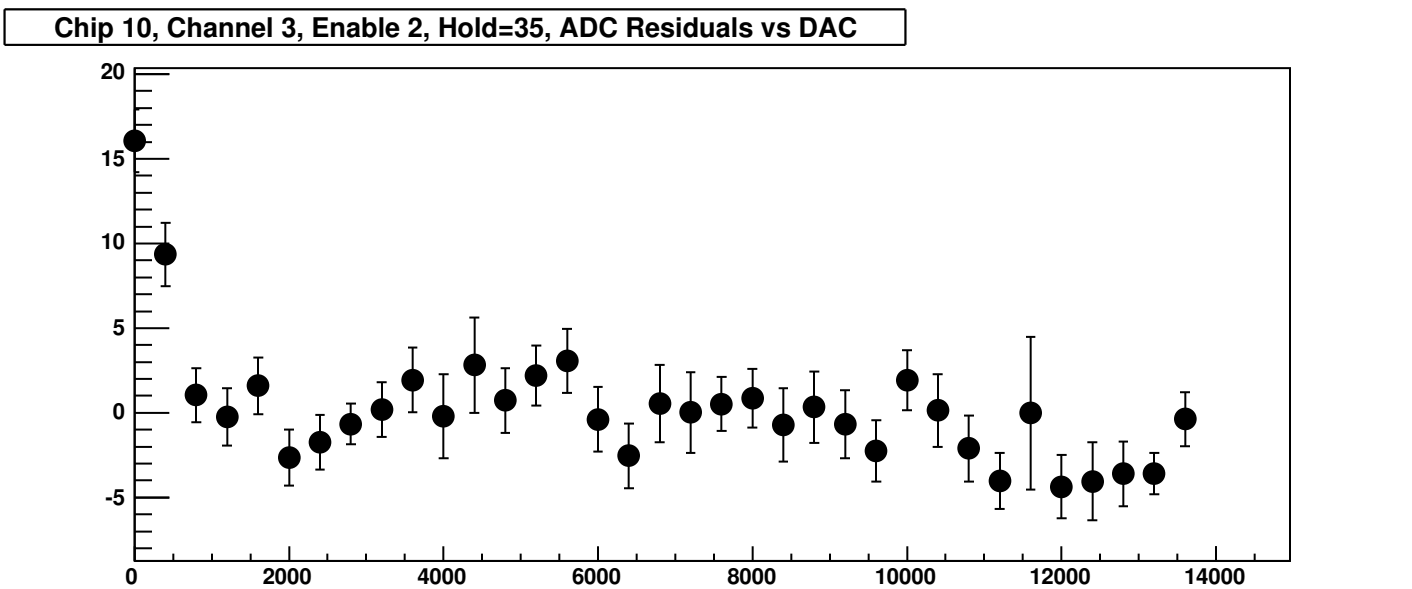

Chip 10, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 144.5 / 23
p0 183.9 ± 3.274
p1 2.352 ± 0.001853
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

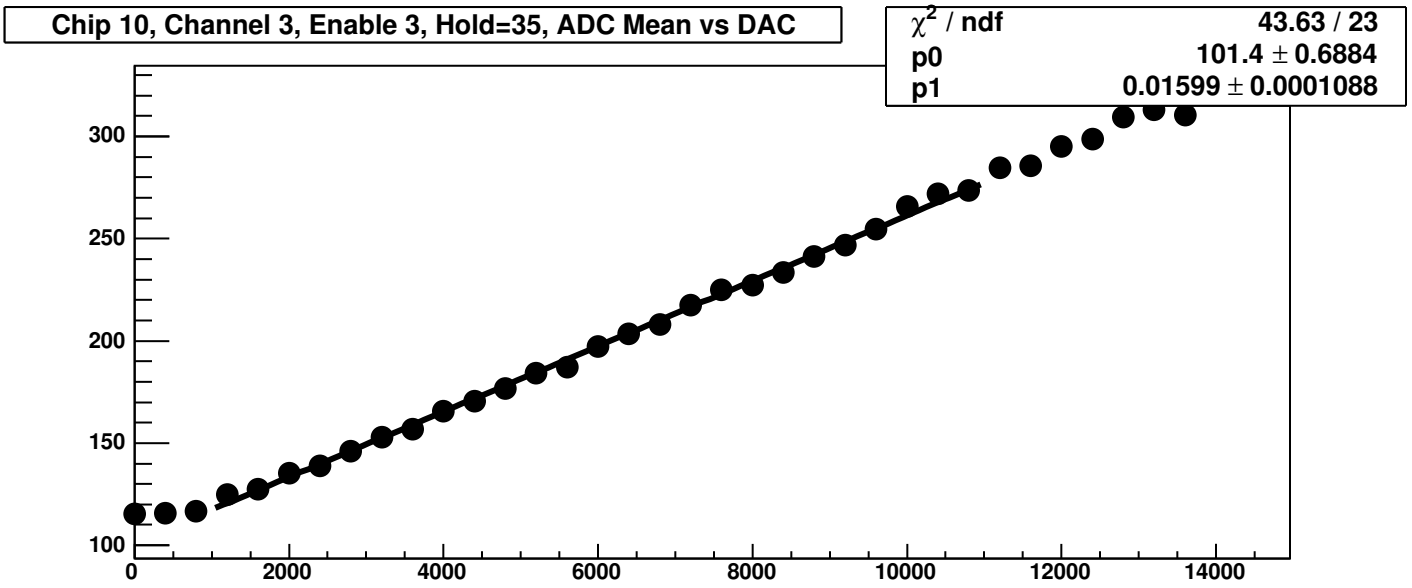

Chip 10, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

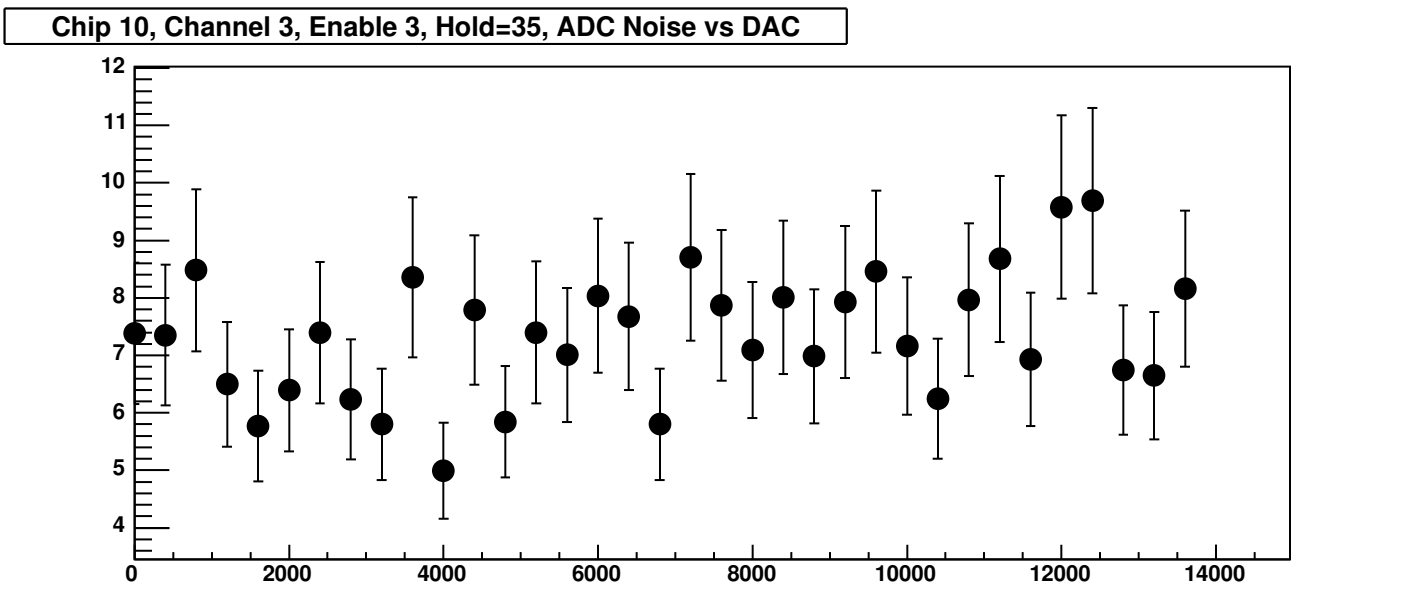

Chip 10, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

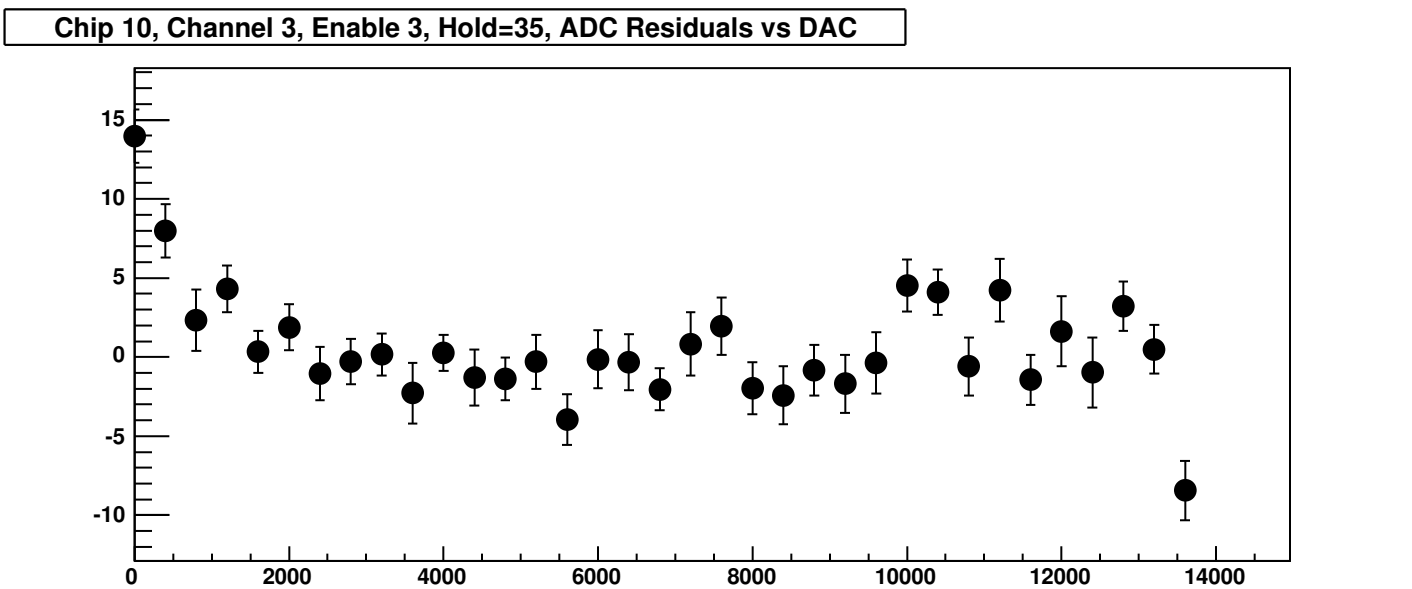

Chip 10, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 17.73 / 23
p0 104.2 ± 0.7827
p1 0.02194 ± 0.0001236
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

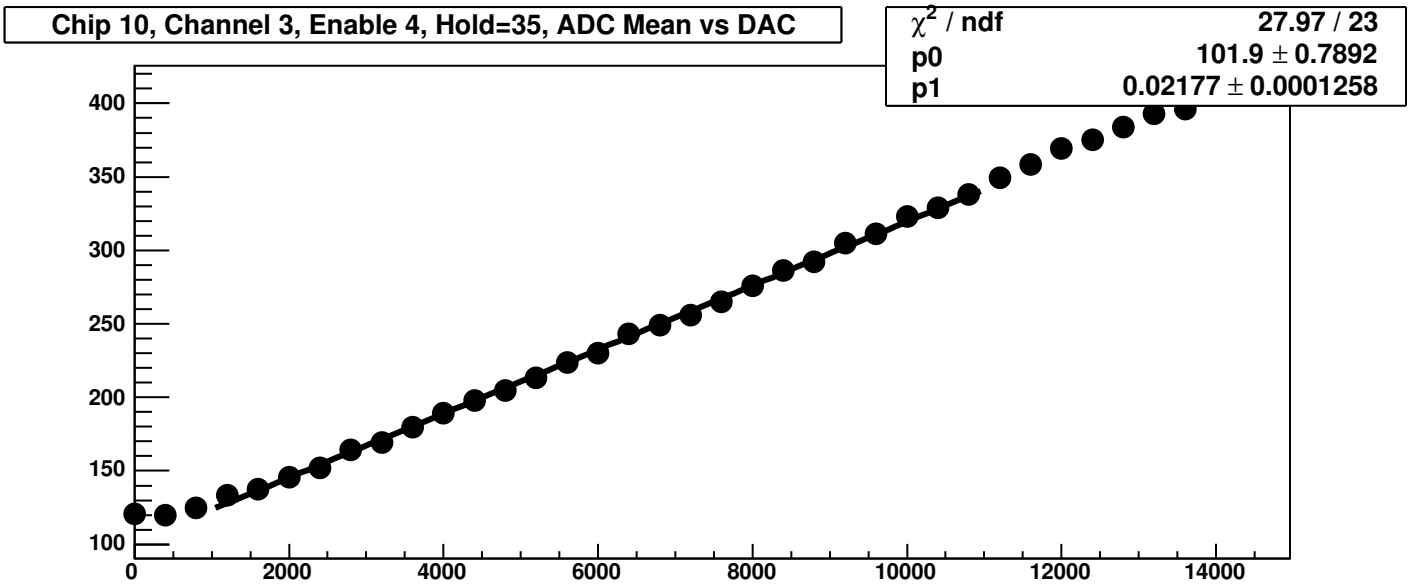

Chip 10, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
24
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

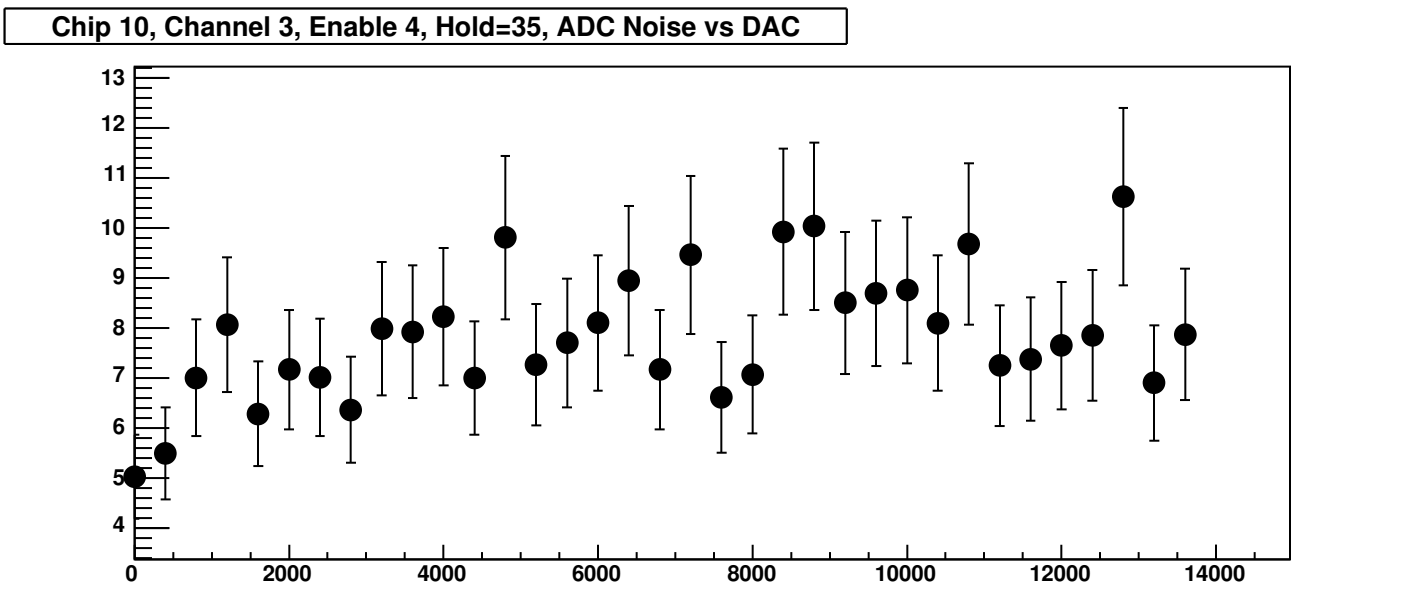

Chip 10, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

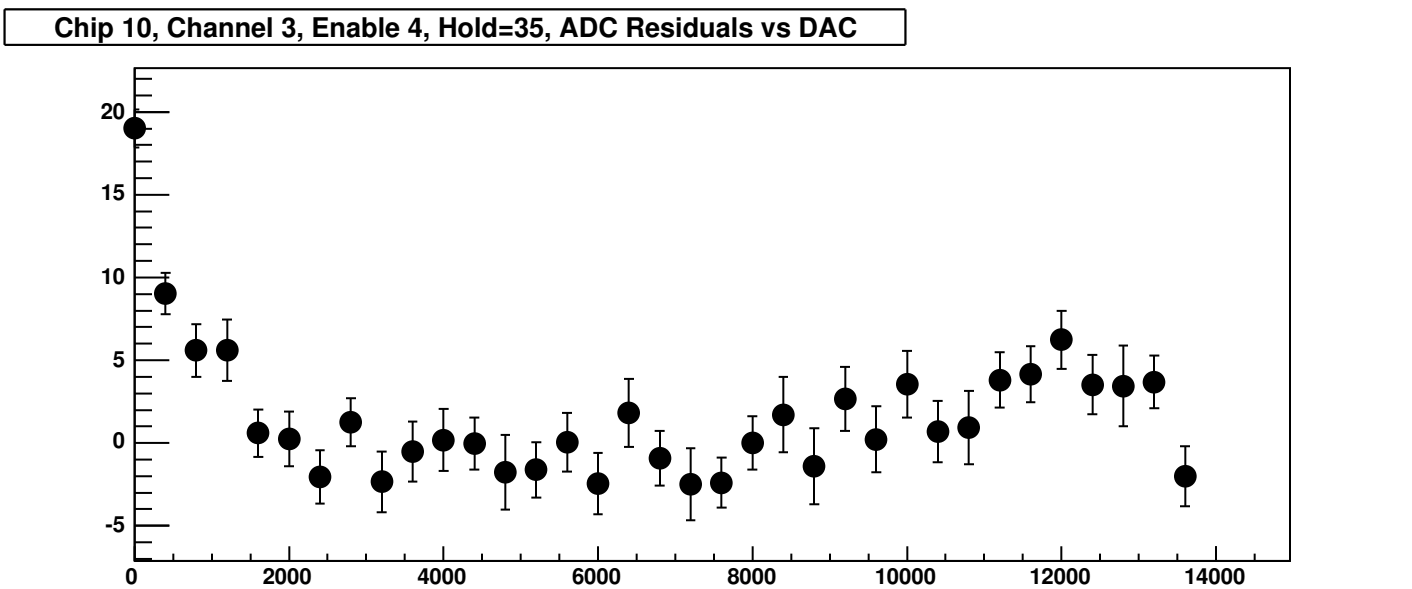

Chip 10, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 43.63 / 23
p0 101.4 ± 0.6884
p1 0.01599 ± 0.0001088
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000

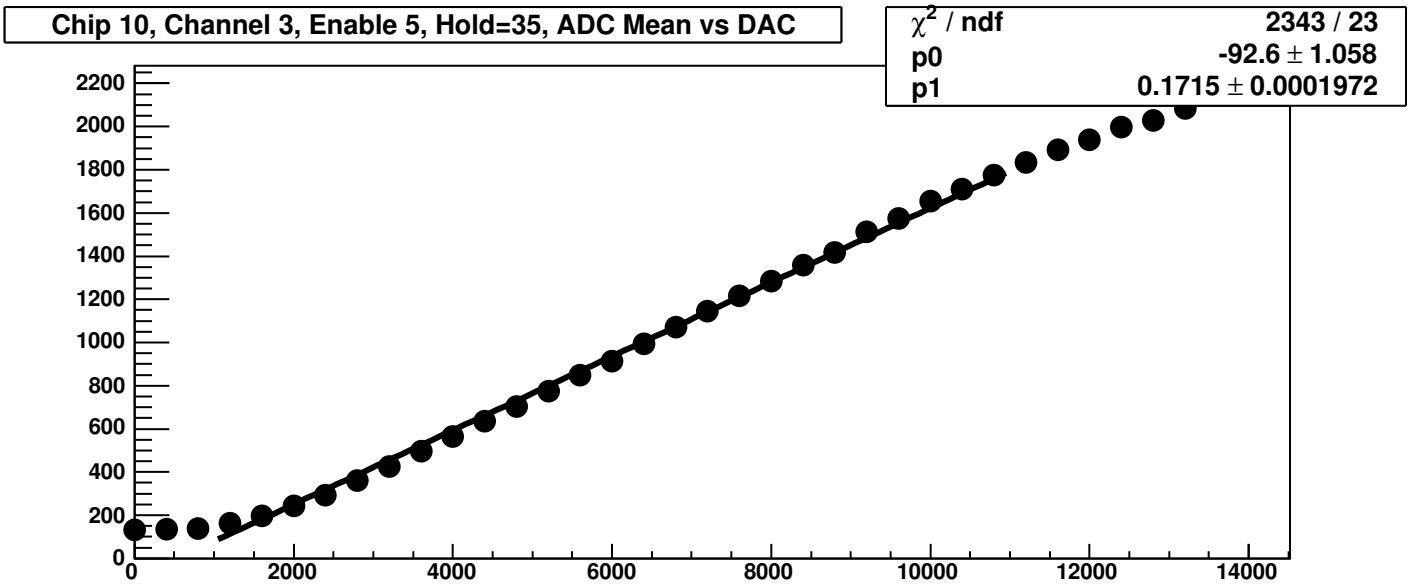

Chip 10, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

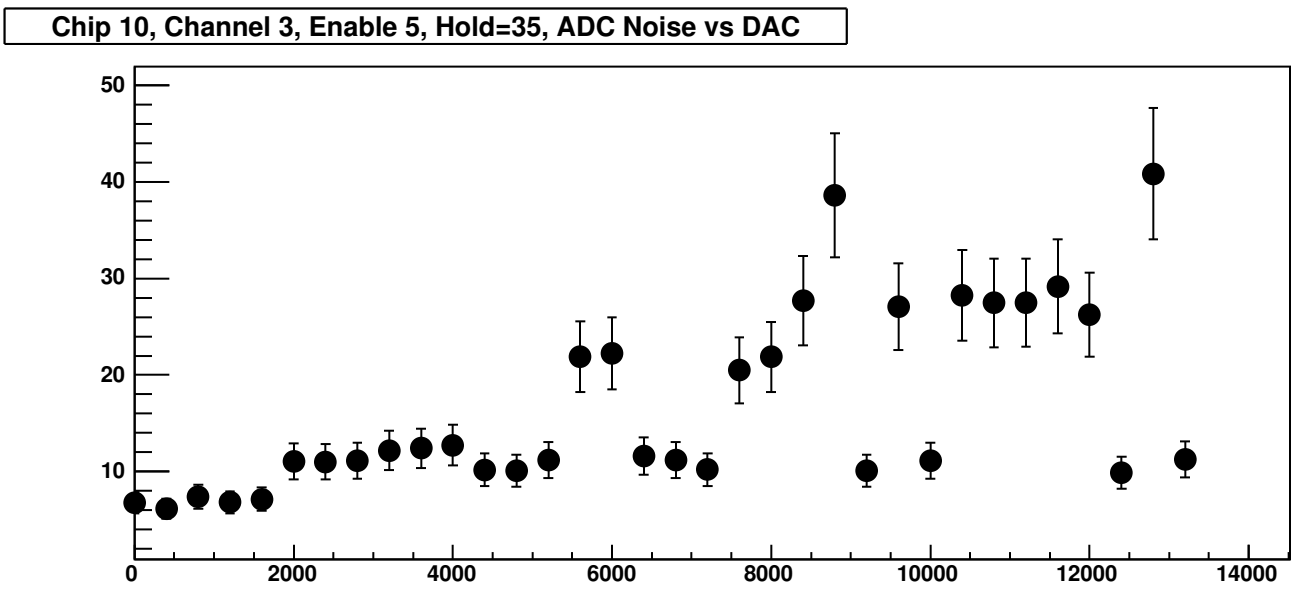

Chip 10, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

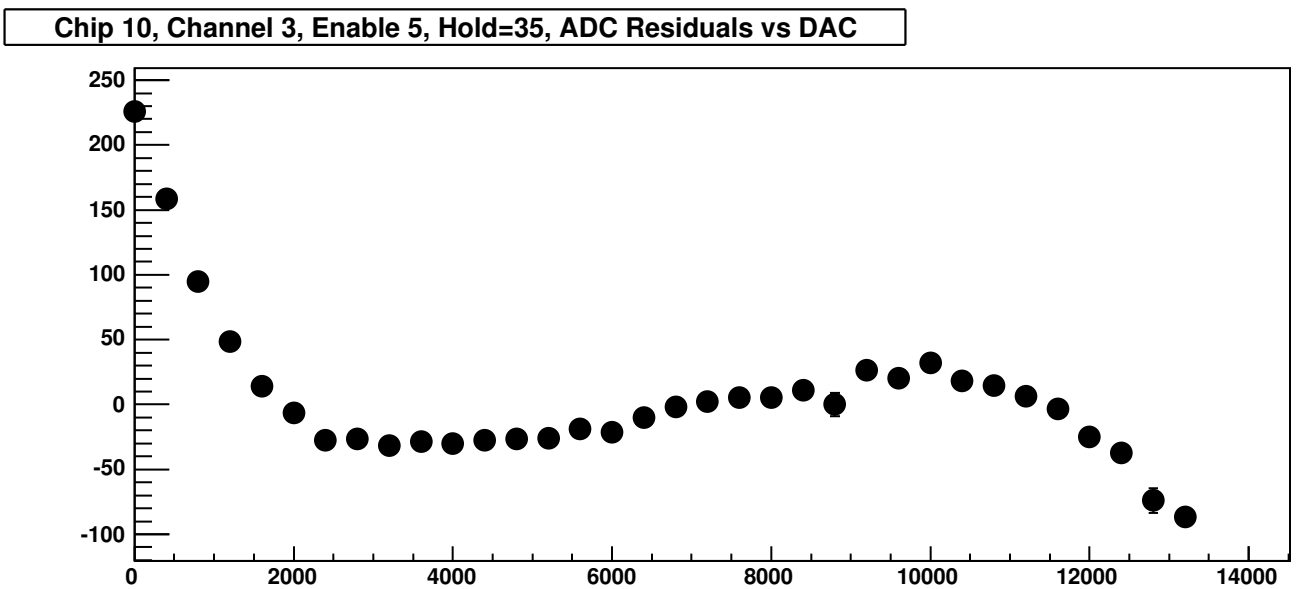

Chip 10, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.97 / 23
p0 101.9 ± 0.7892
p1 0.02177 ± 0.0001258
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

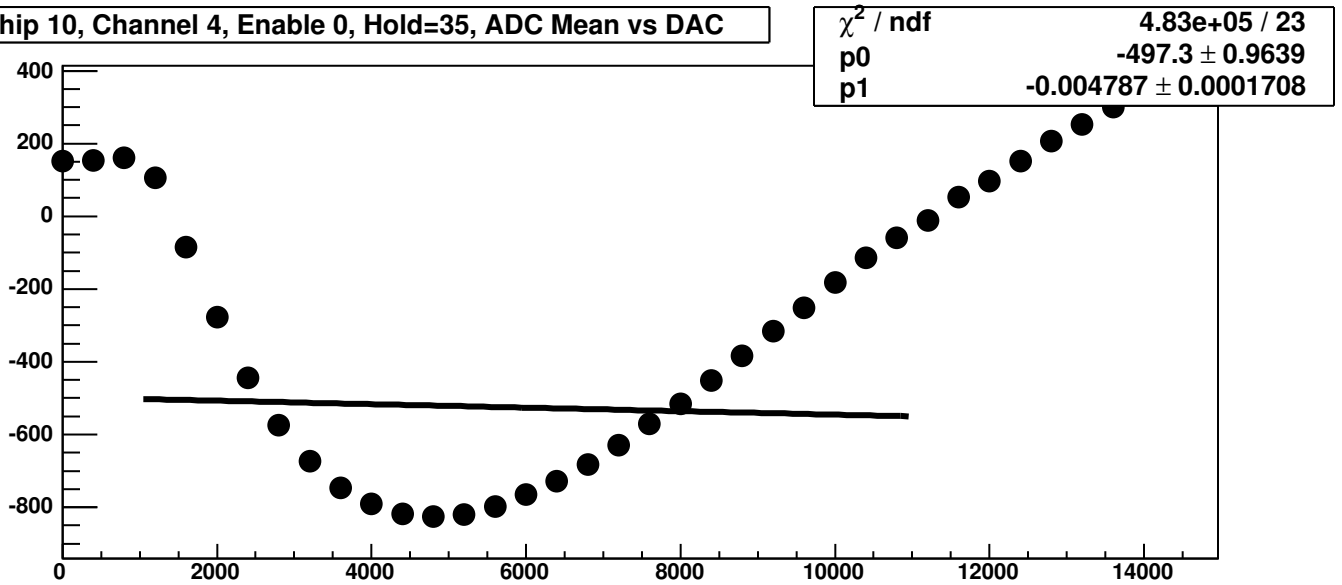

Chip 10, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2343 / 23
p0 -92.6 ± 1.058
p1 0.1715 ± 0.0001972
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

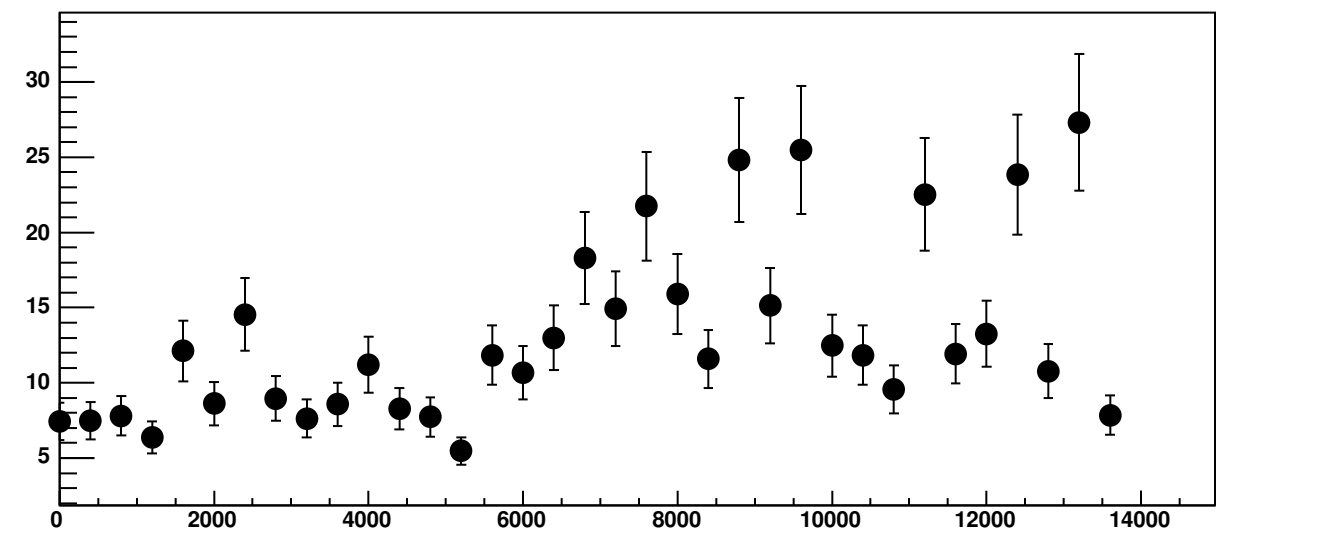

Chip 10, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

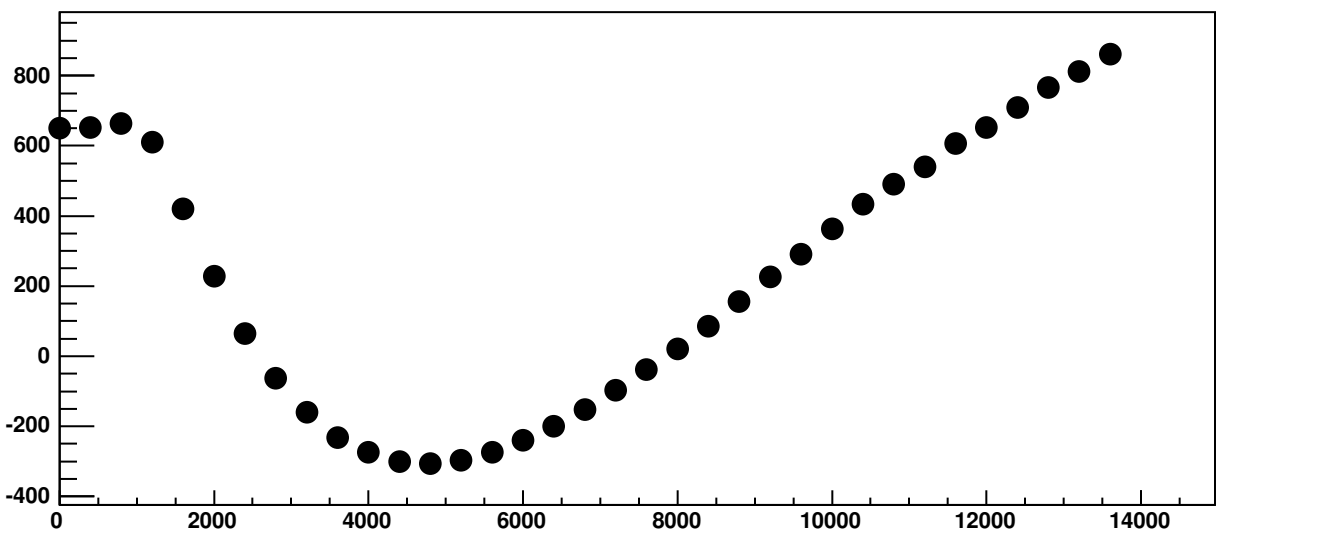
Chip 10, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC

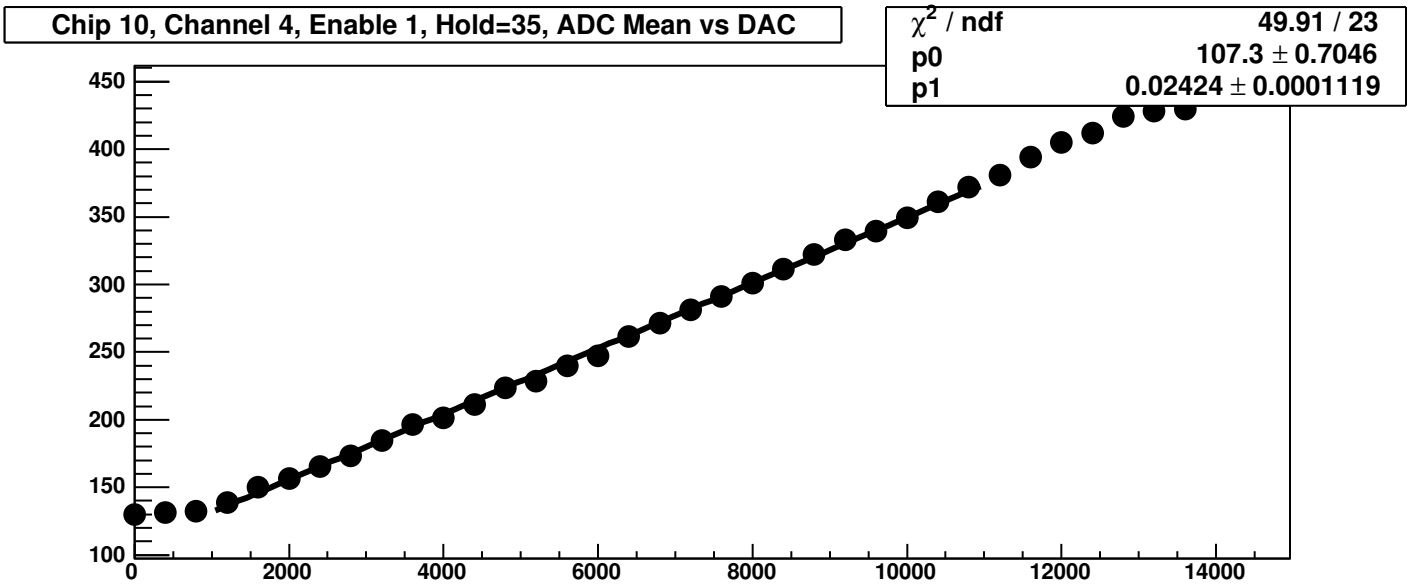


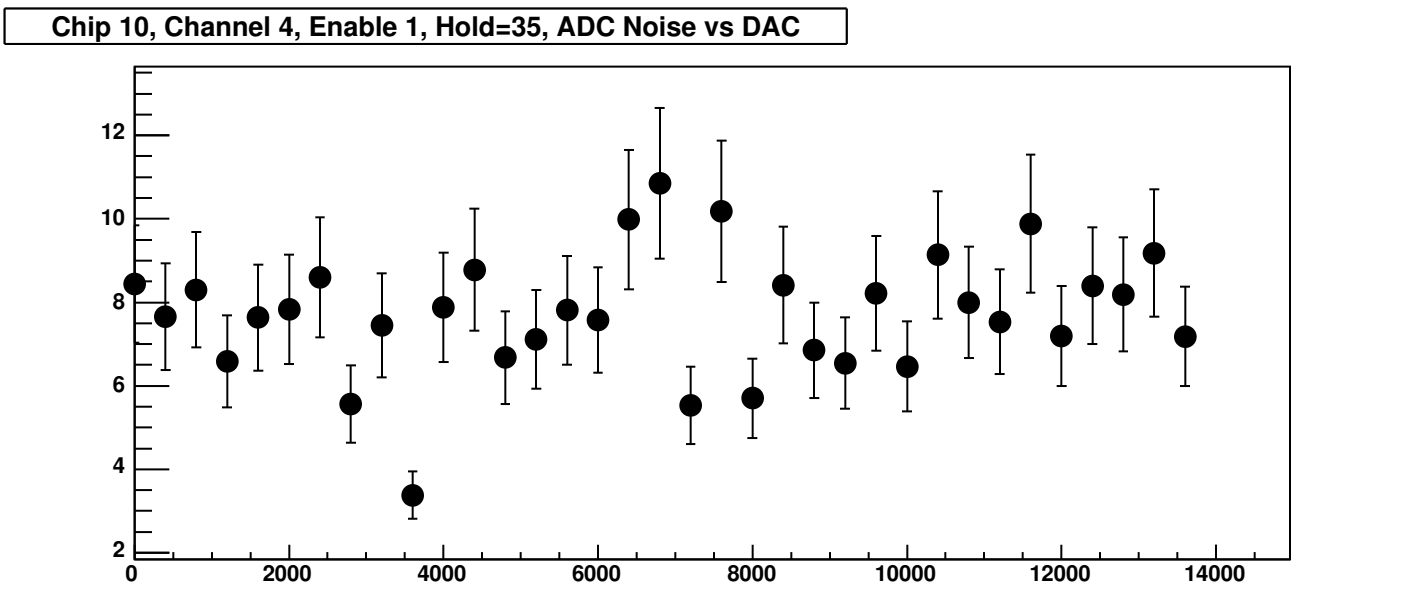
Chip 10, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC

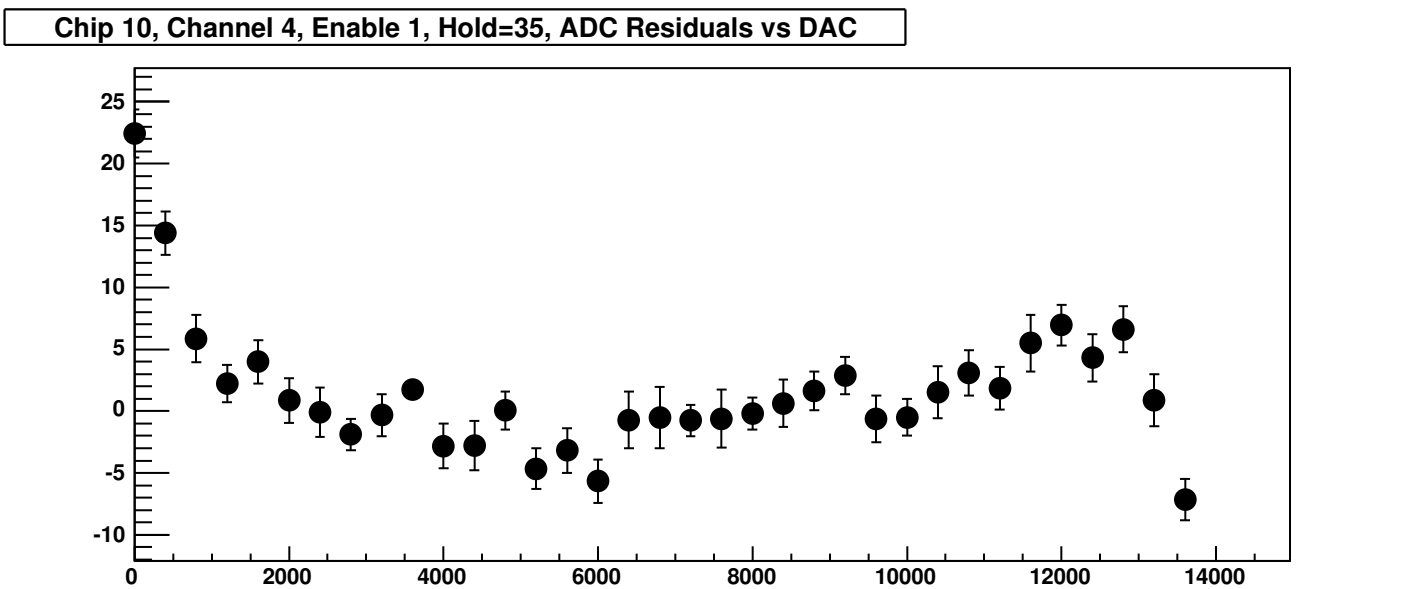


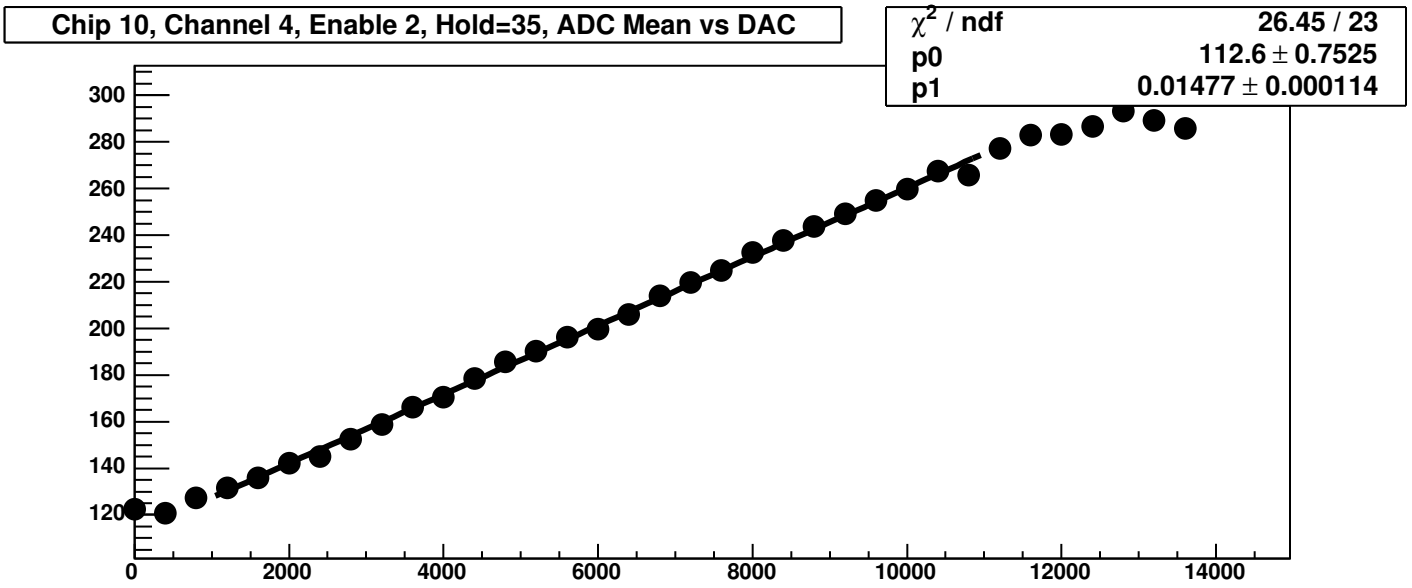
Chip 10, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC

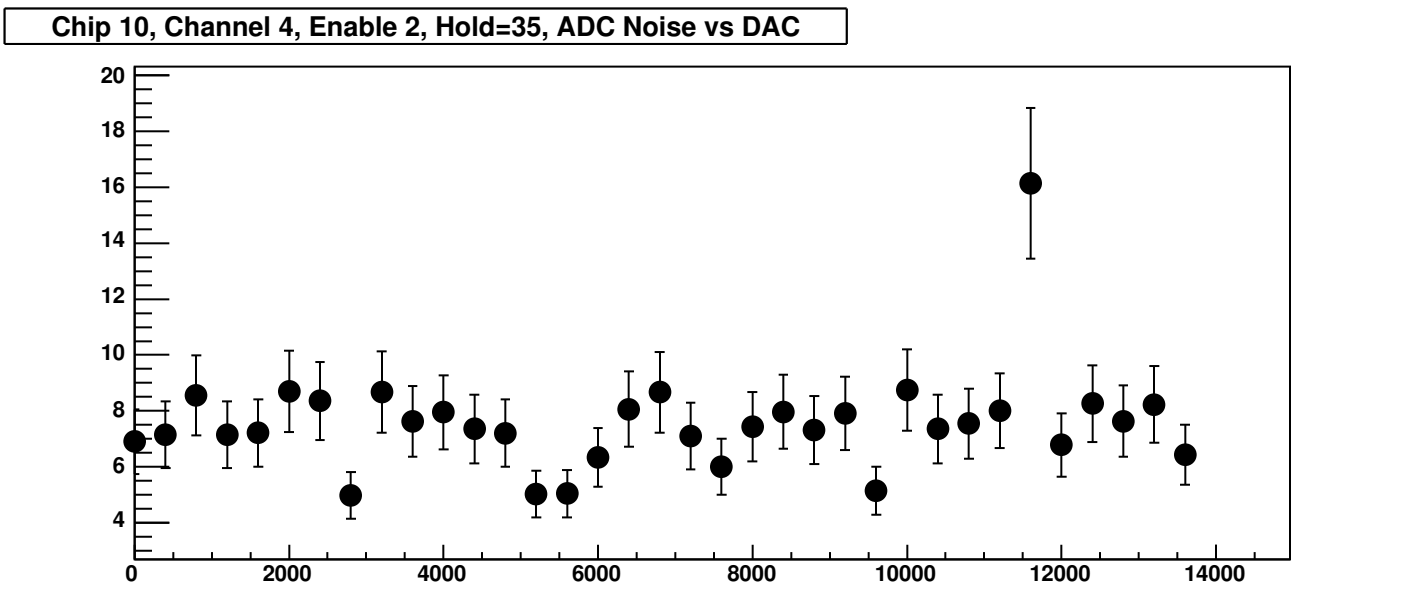


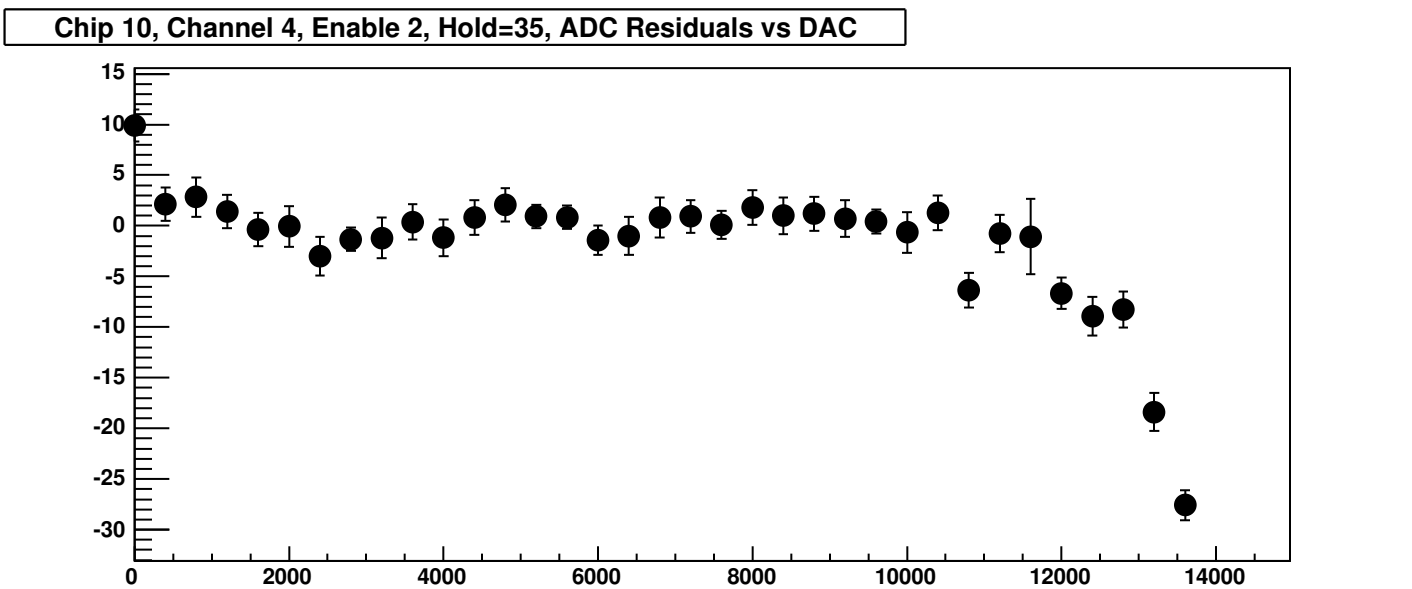

Chip 10, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 49.91 / 23
p0 107.3 ± 0.7046
p1 0.02424 ± 0.0001119
450
400
350
300
250
200
150
100
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

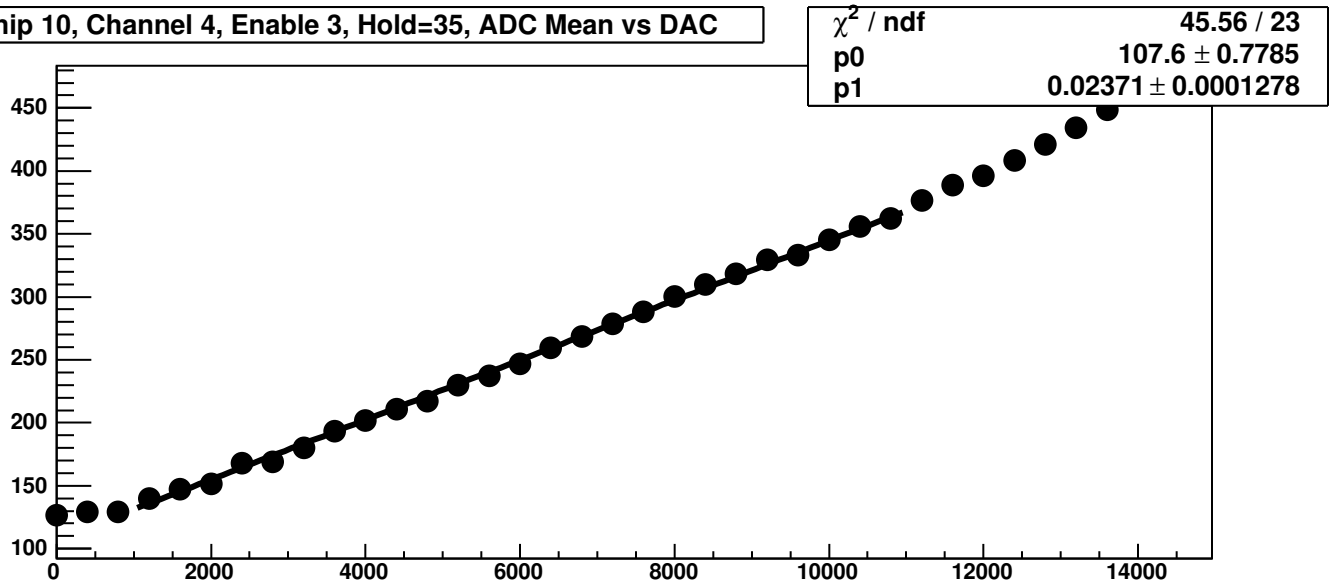

Chip 10, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 26.45 / 23
p0 112.6 ± 0.7525
p1 0.01477 ± 0.000114
300
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000

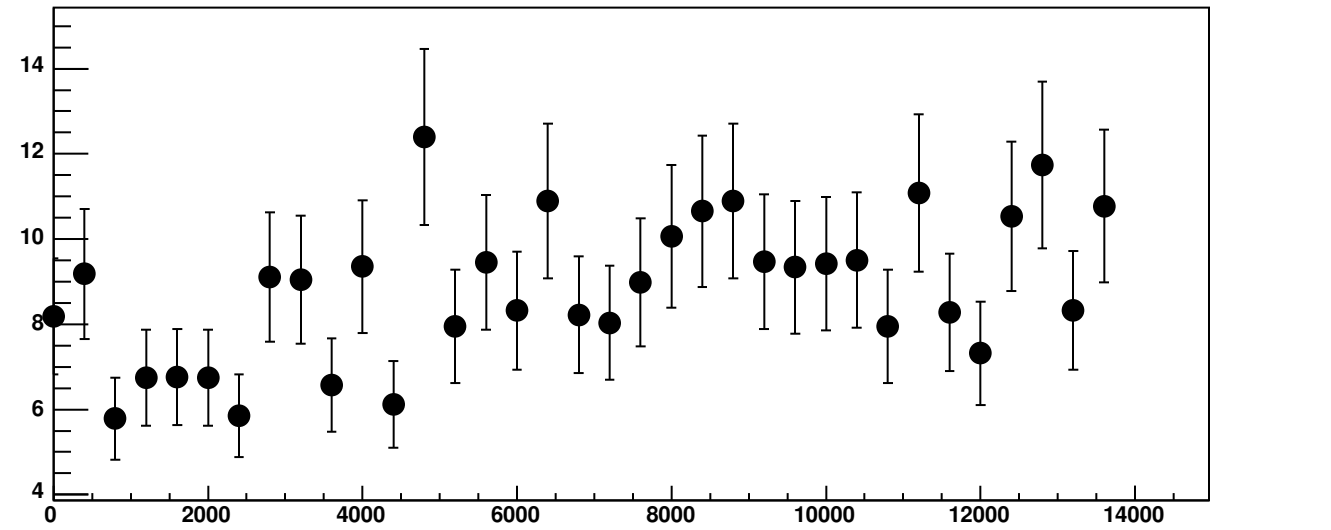

Chip 10, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
-25
-30
0
2000
4000
6000
8000
10000
12000
14000

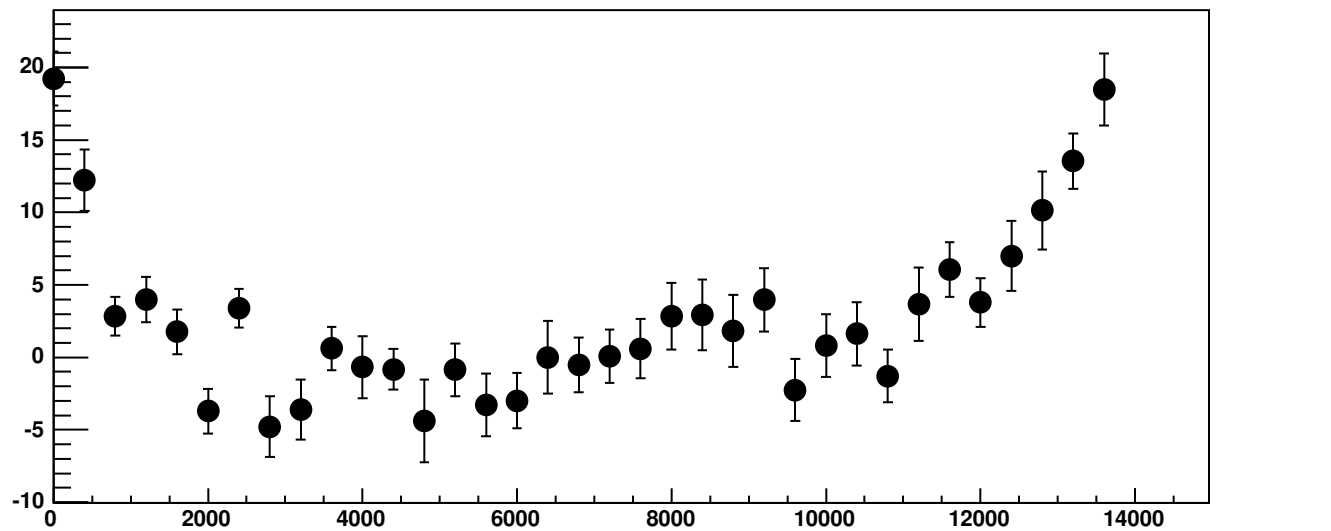
Chip 10, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

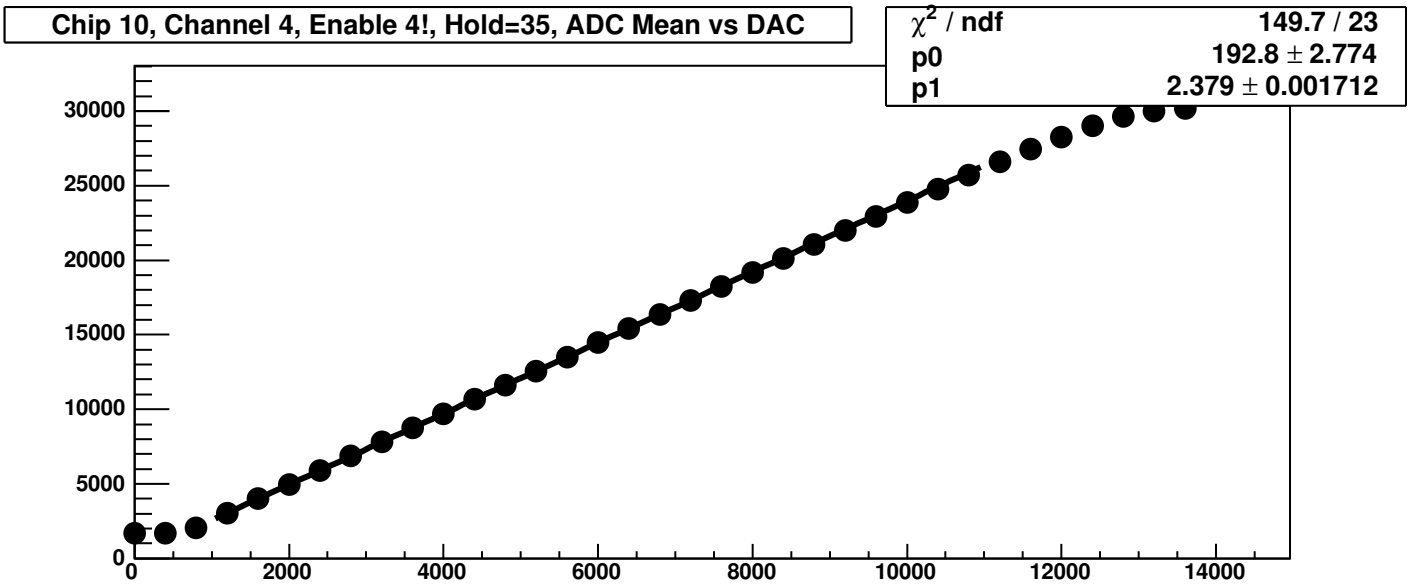


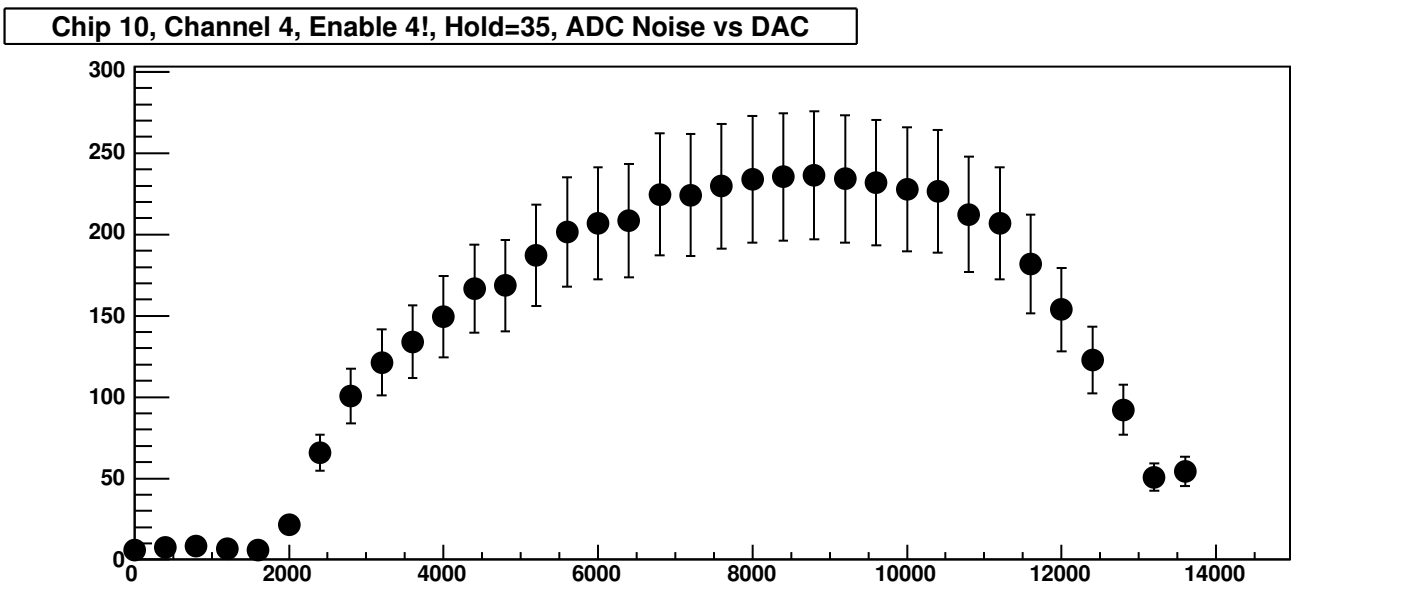
Chip 10, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC

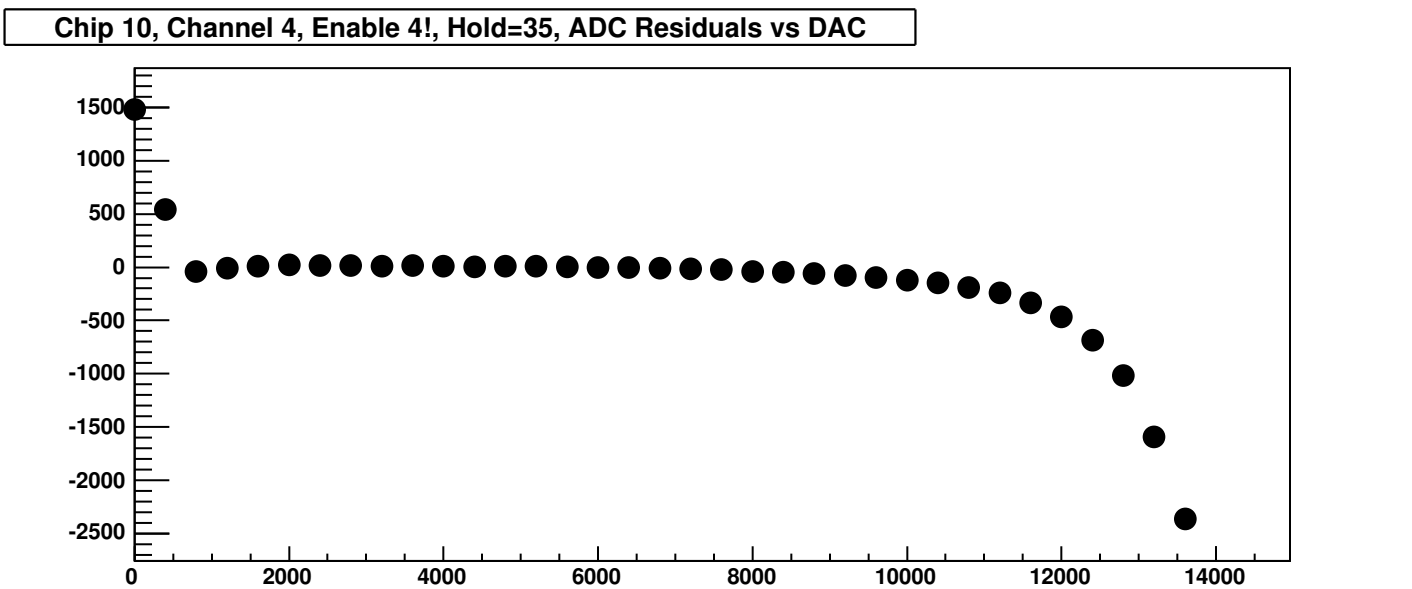


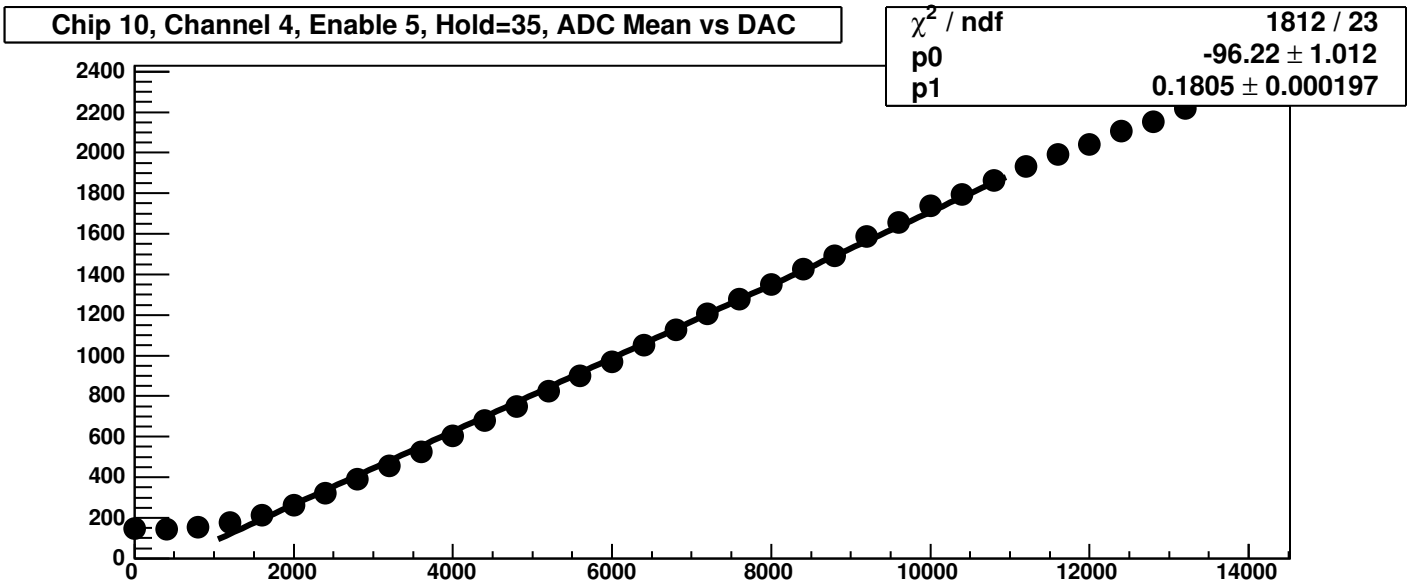
Chip 10, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

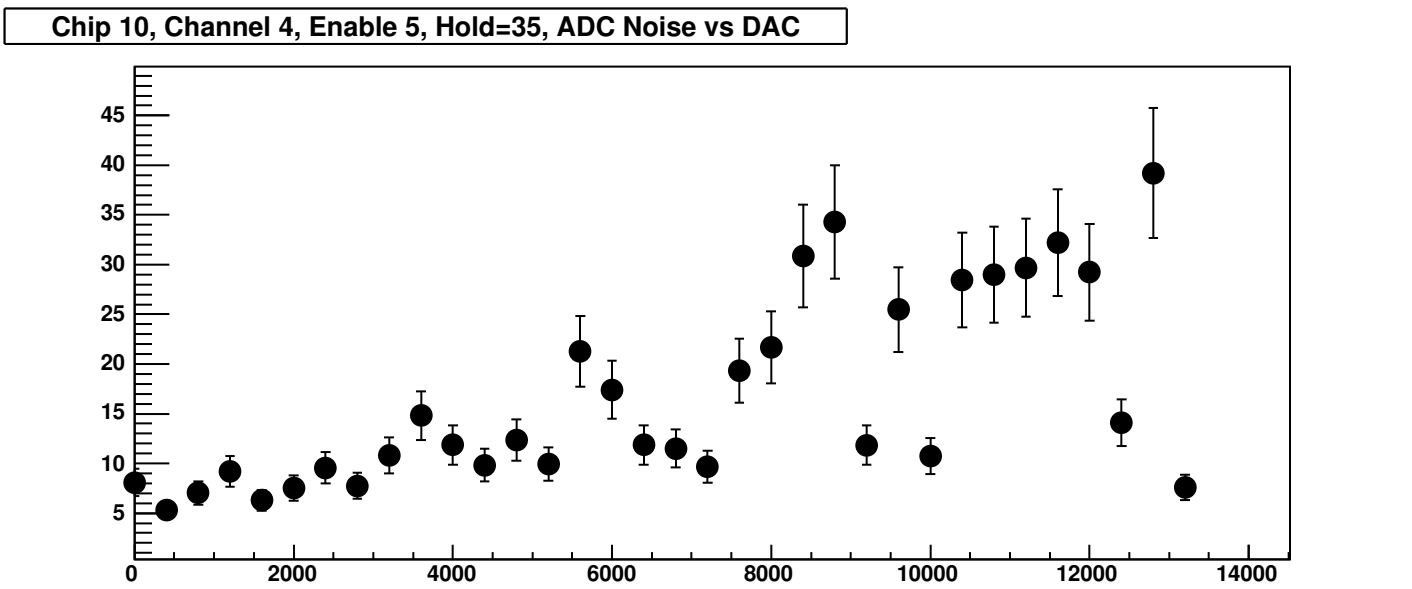


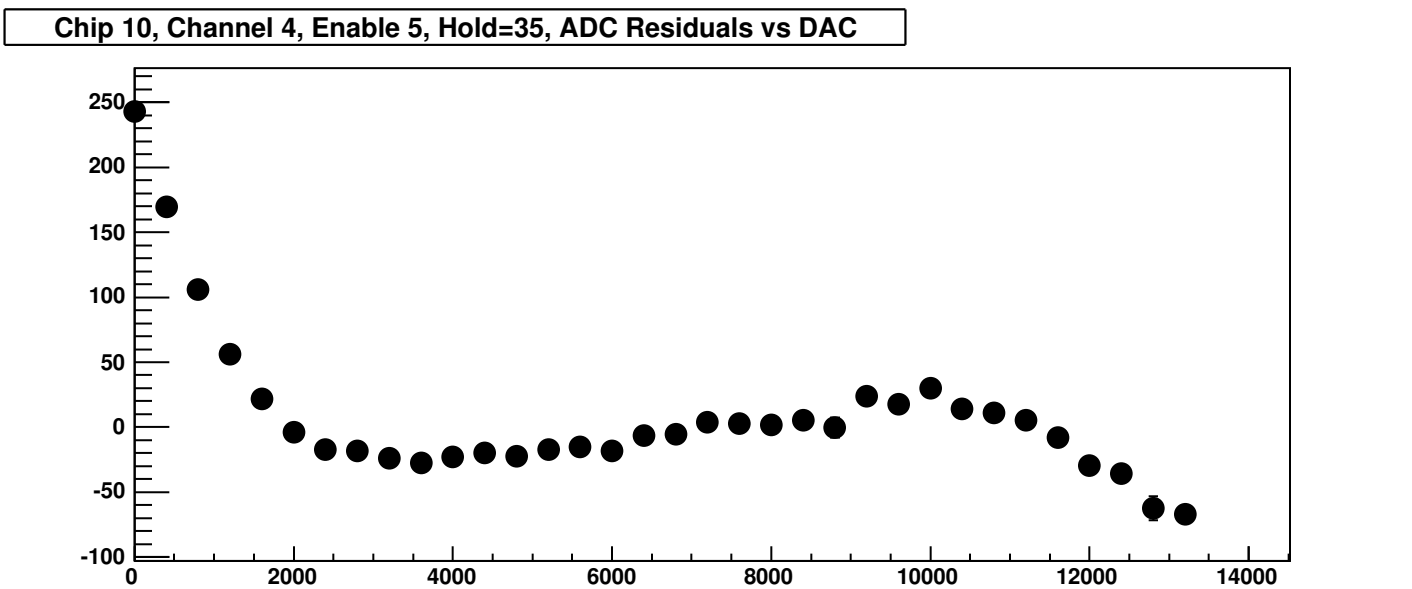

Chip 10, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 149.7 / 23
p0 192.8 ± 2.774
p1 2.379 ± 0.001712
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

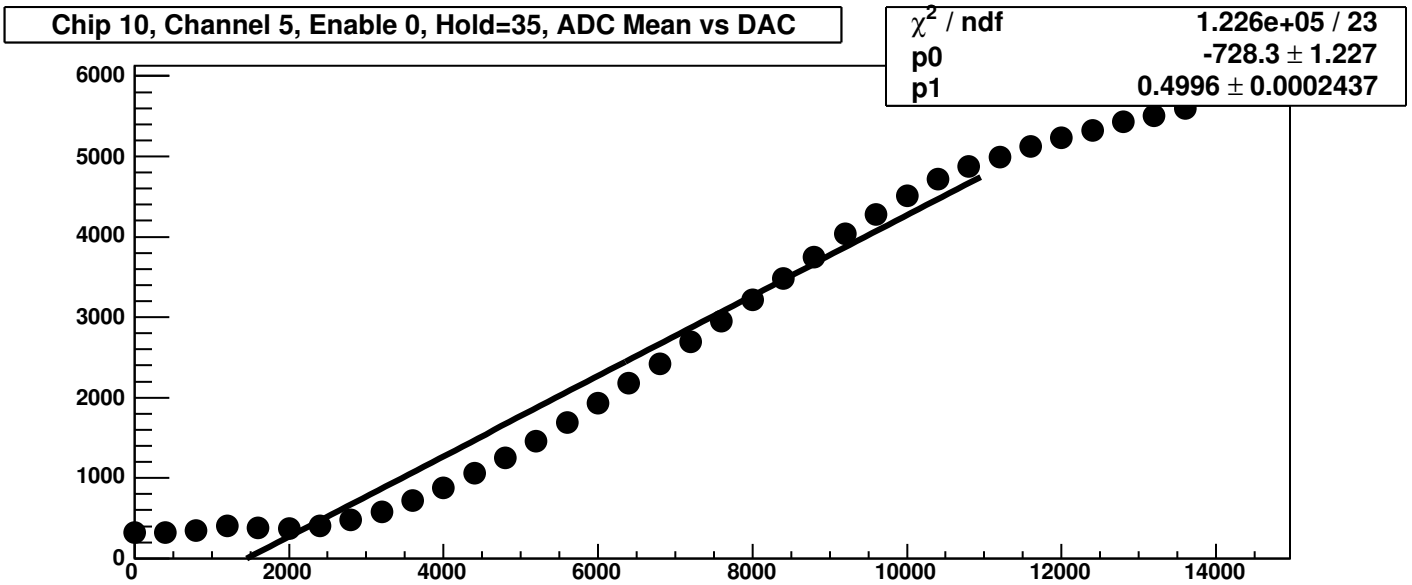

Chip 10, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

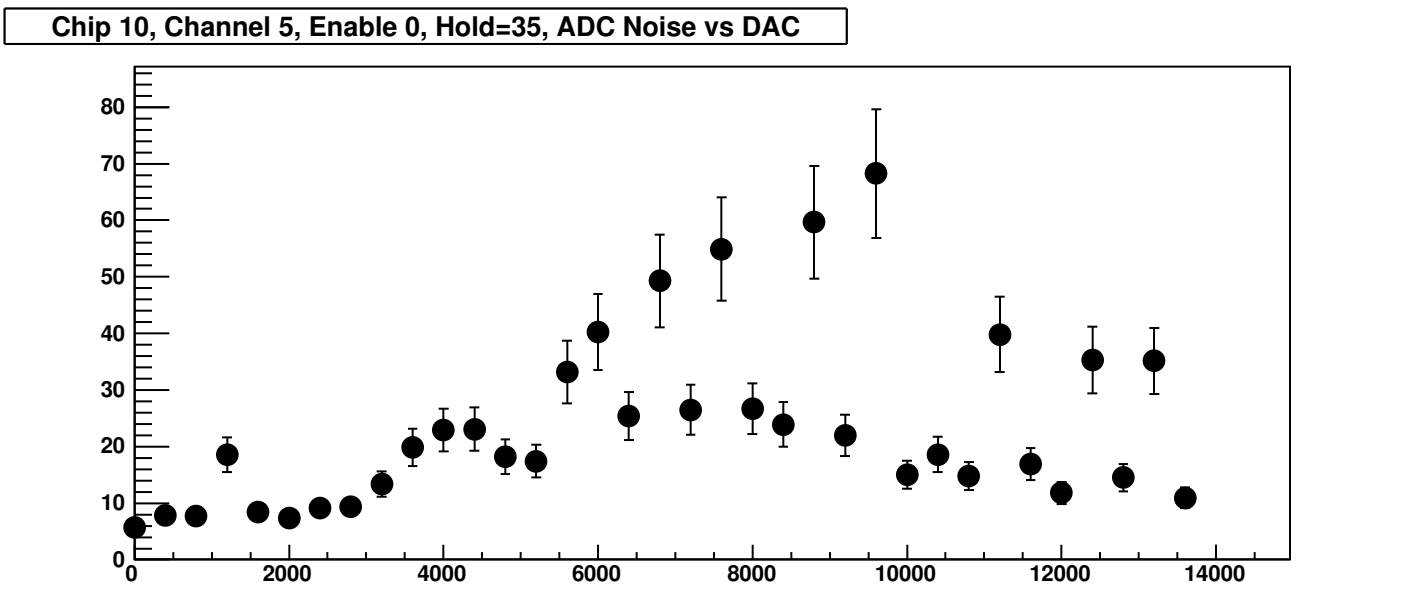

Chip 10, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

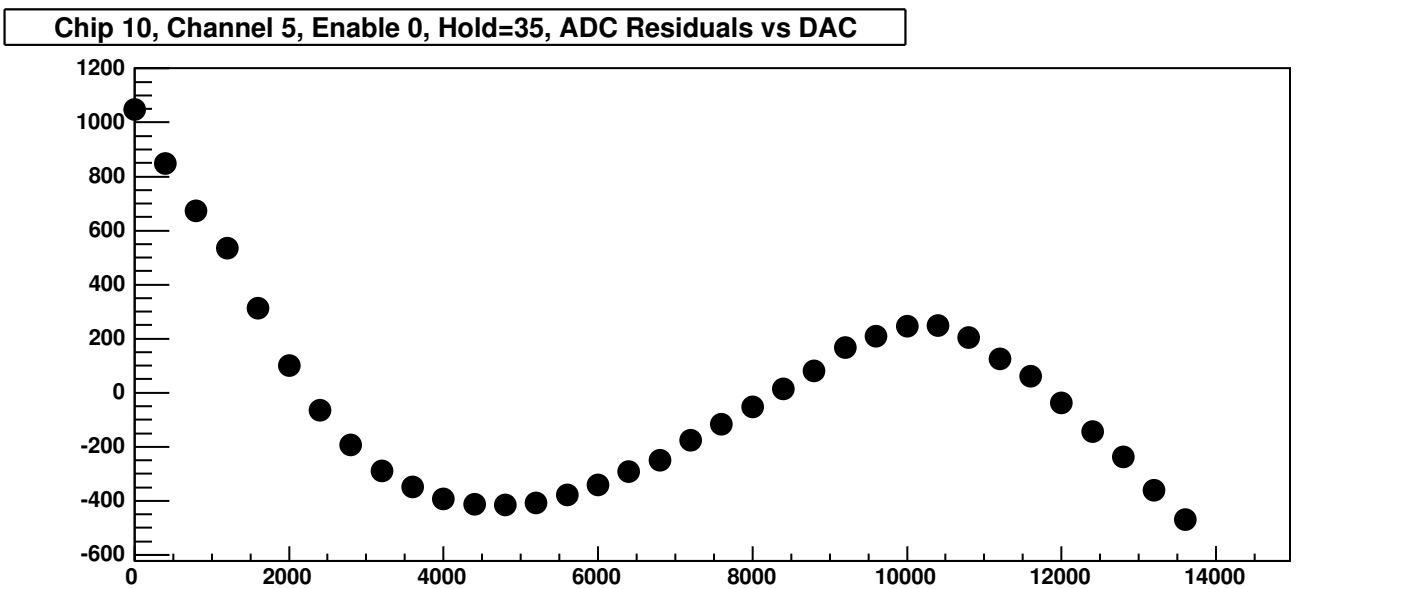

Chip 10, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1812 / 23
p0 -96.22 ± 1.012
p1 0.1805 ± 0.000197
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

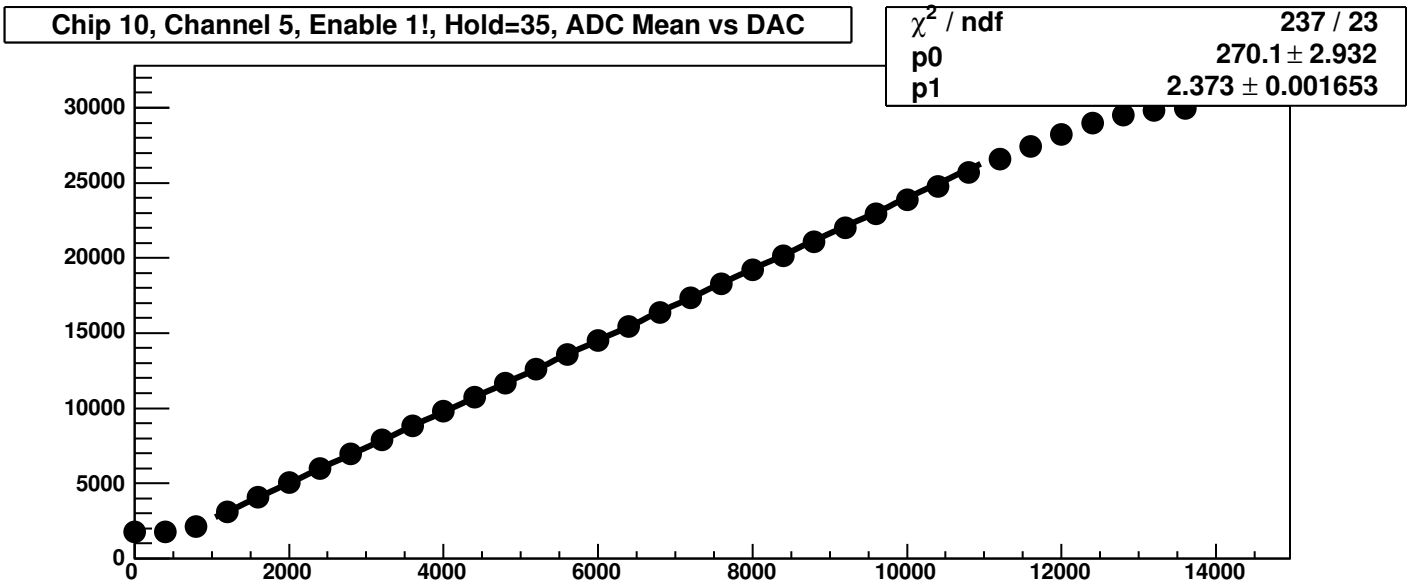

Chip 10, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

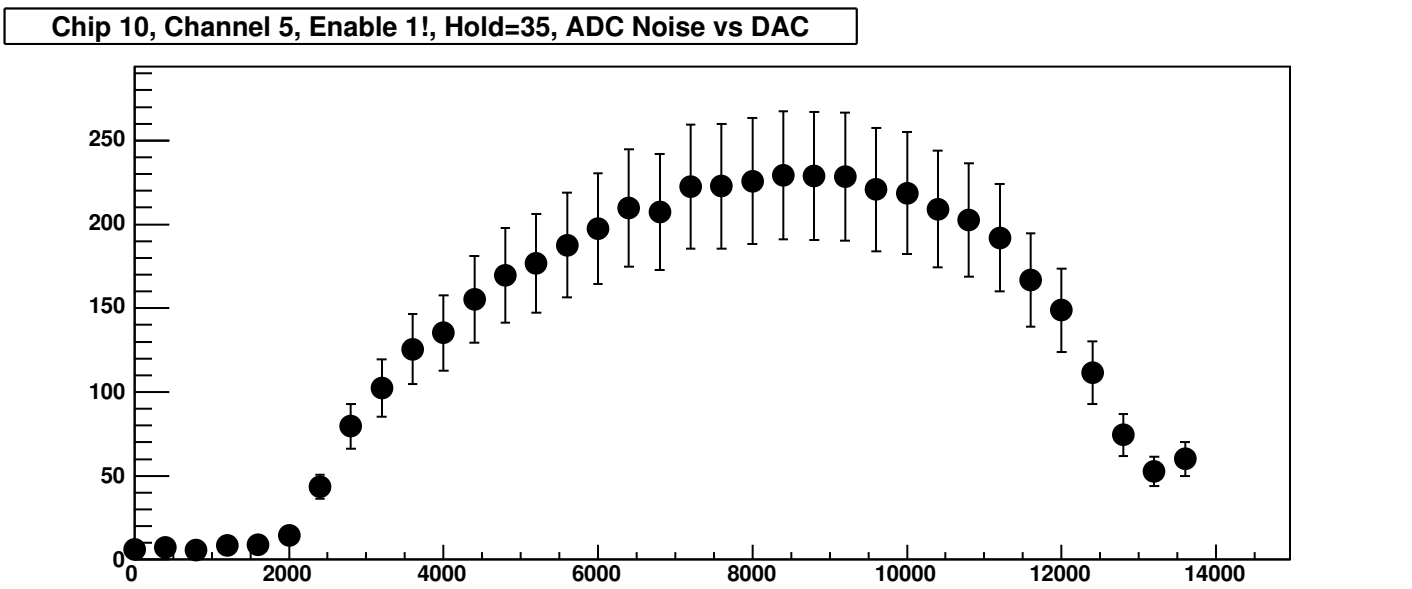

Chip 10, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

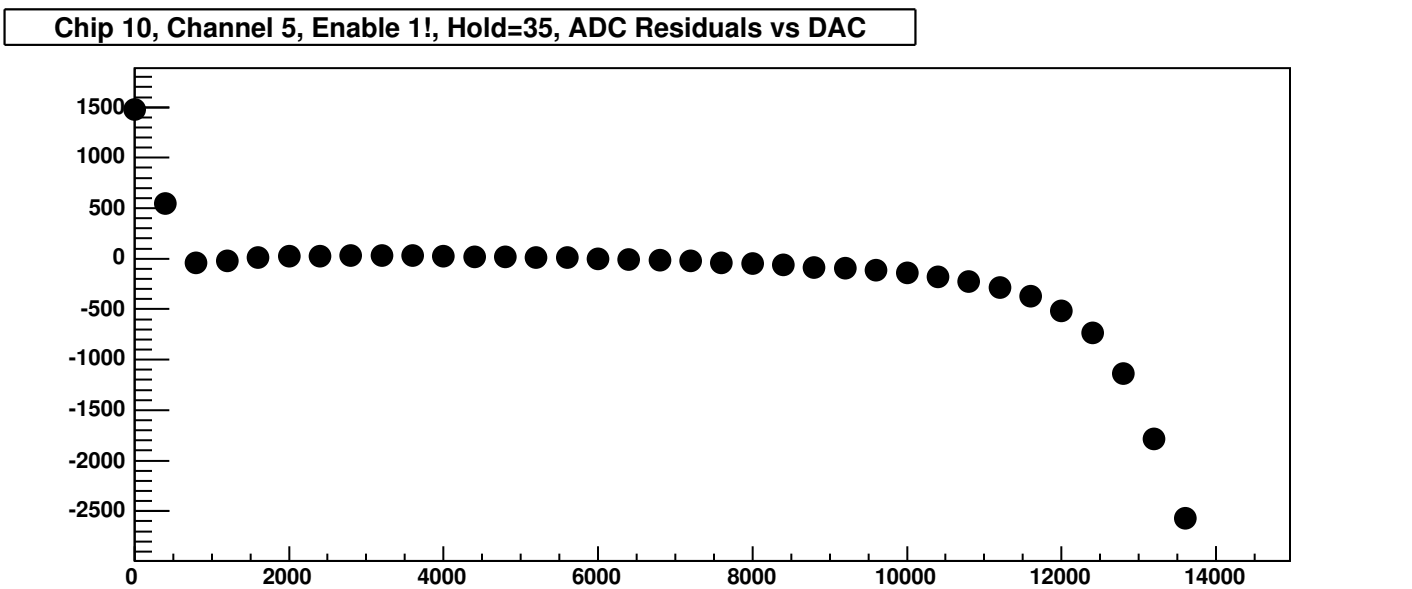

Chip 10, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.226e+05 / 23
p0 -728.3 ± 1.227
p1 0.4996 ± 0.0002437
6000
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

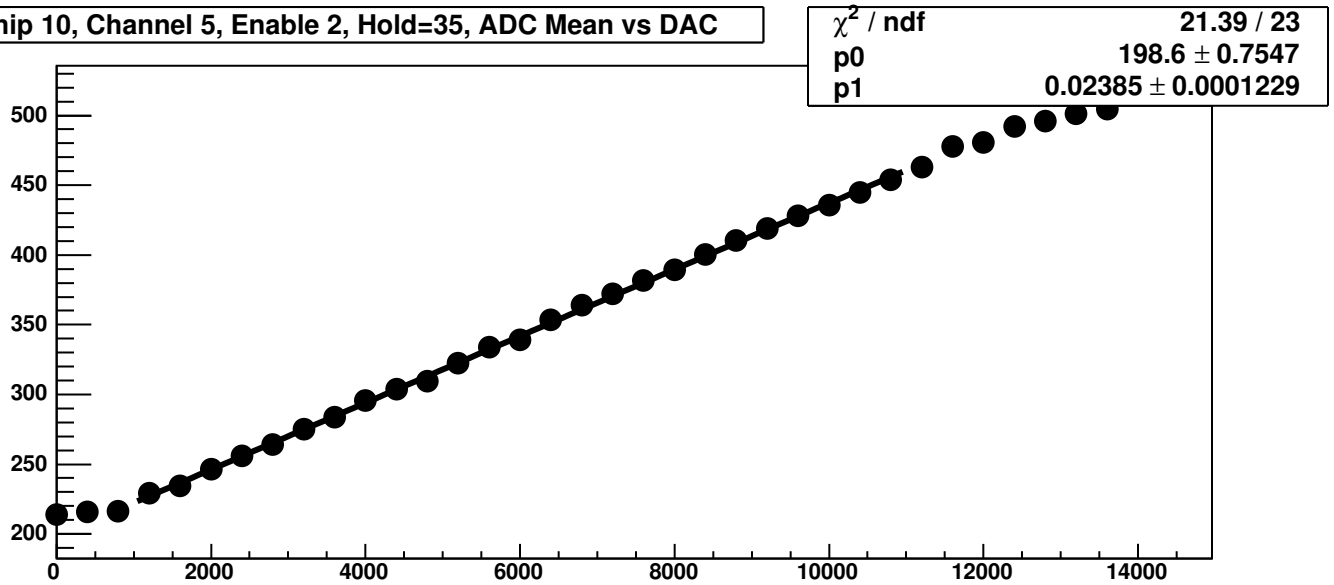

Chip 10, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 237 / 23
p0 270.1 ± 2.932
p1 2.373 ± 0.001653
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

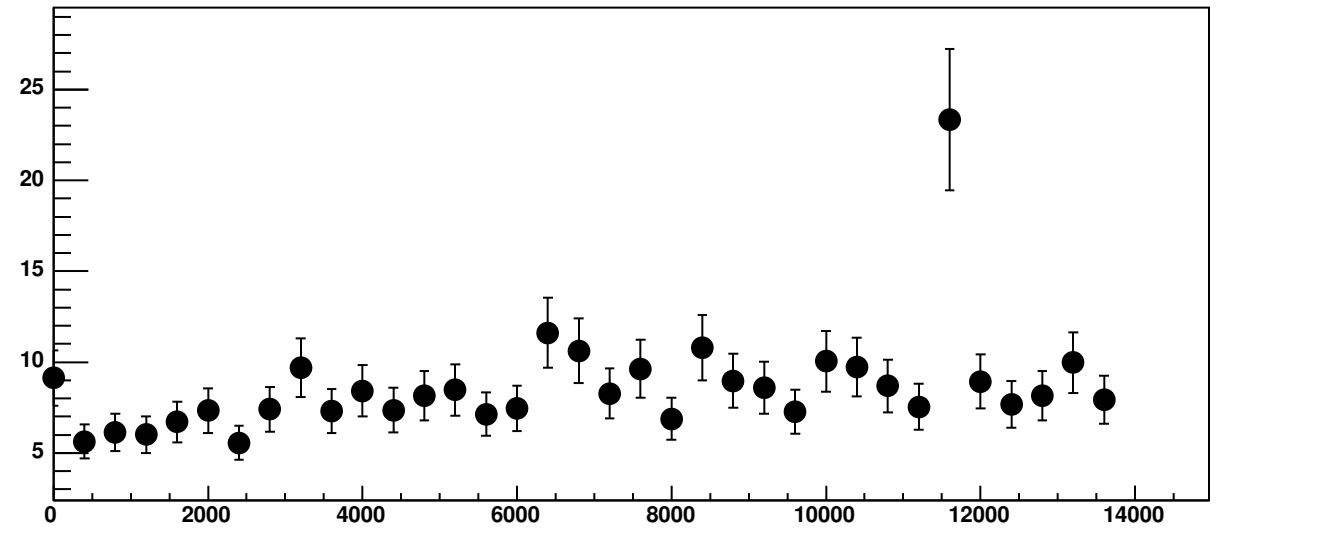

Chip 10, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

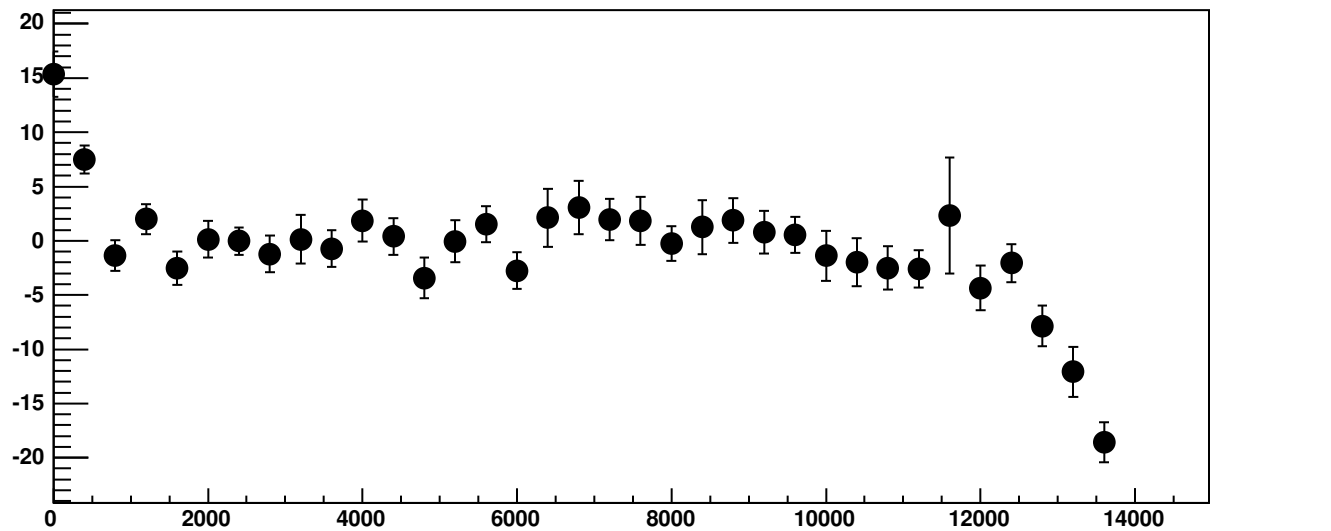
Chip 10, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



Chip 10, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC

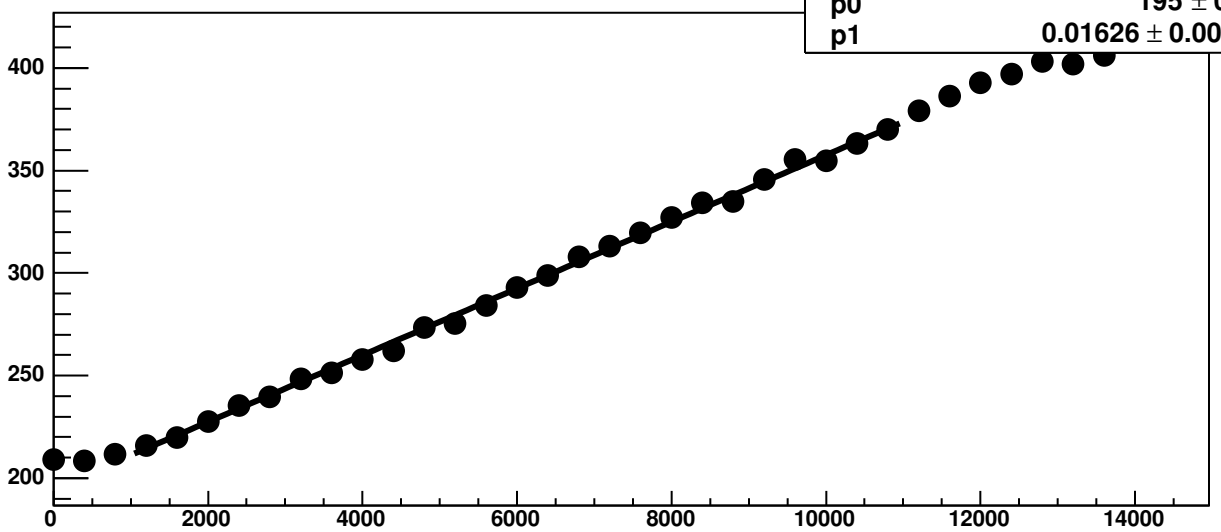


Chip 10, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

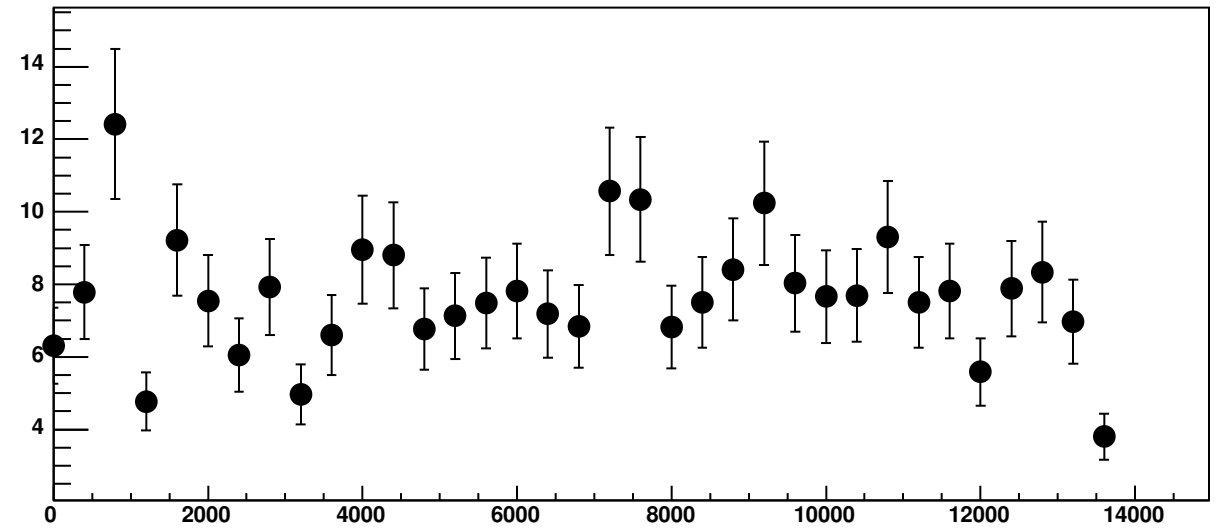


Chip 10, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC

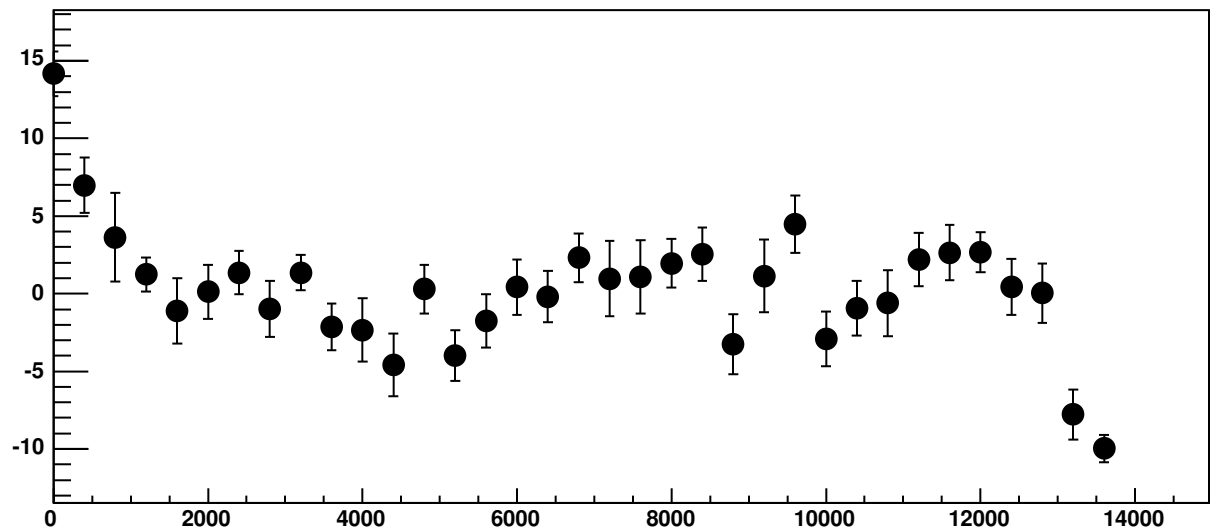
χ^2 / ndf	38.21 / 23
p0	195 ± 0.7065
p1	0.01626 ± 0.0001153

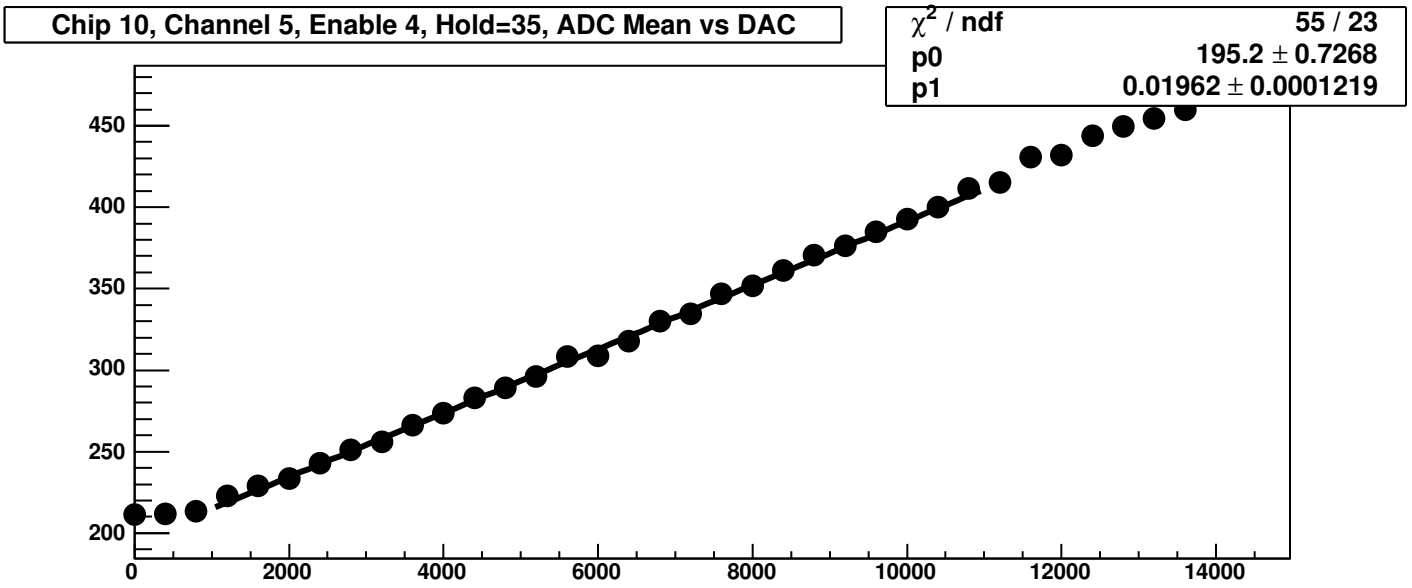


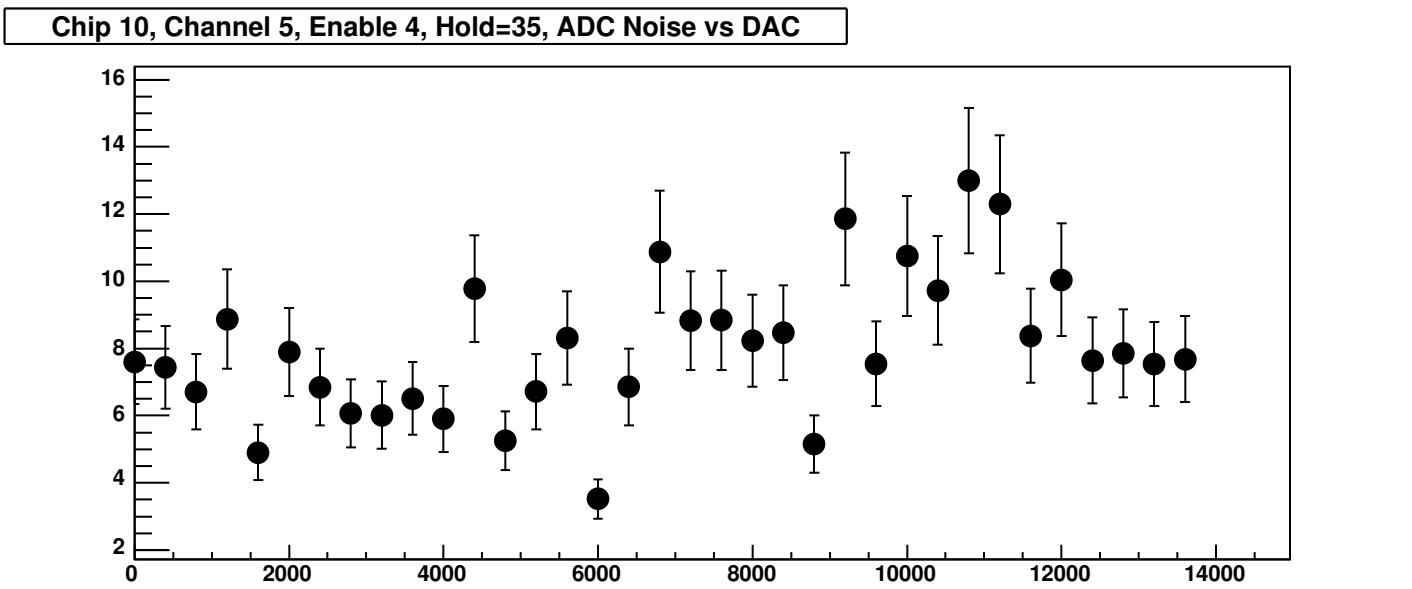
Chip 10, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC

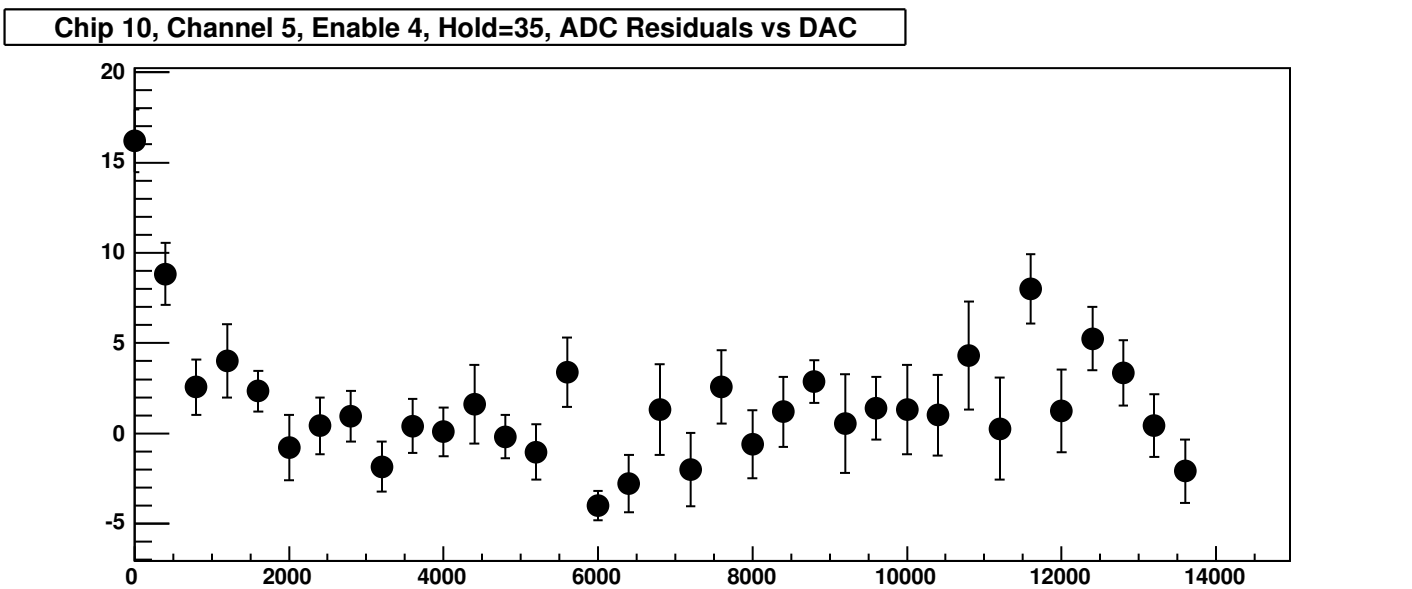


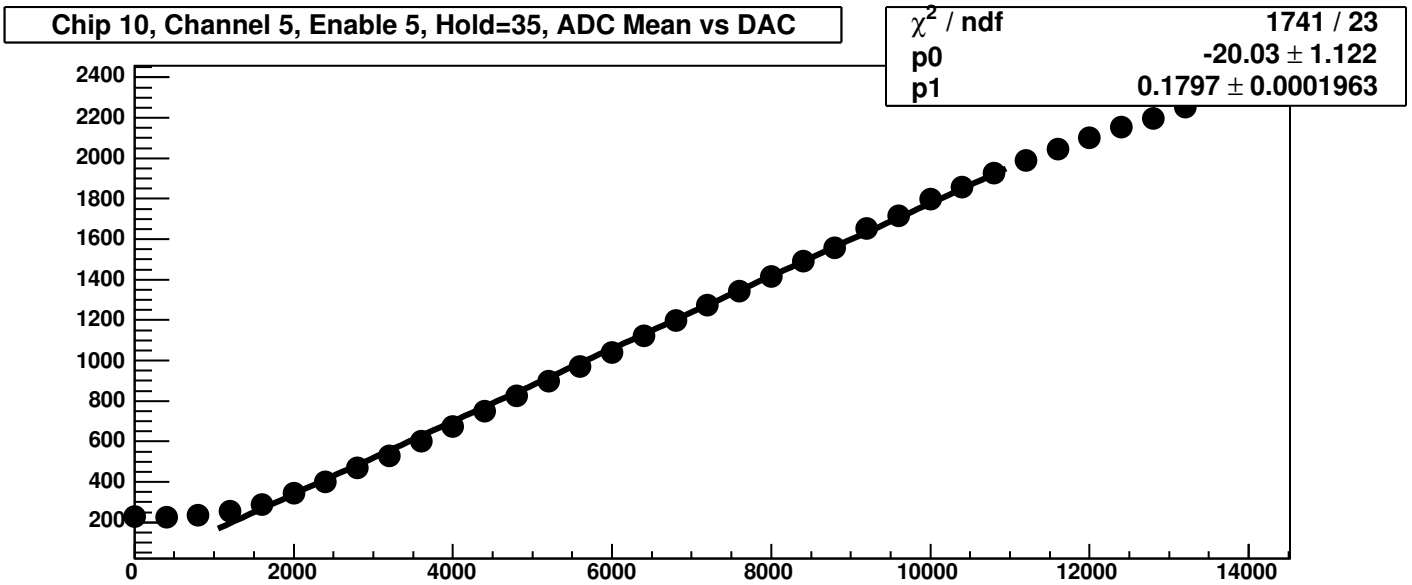
Chip 10, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC

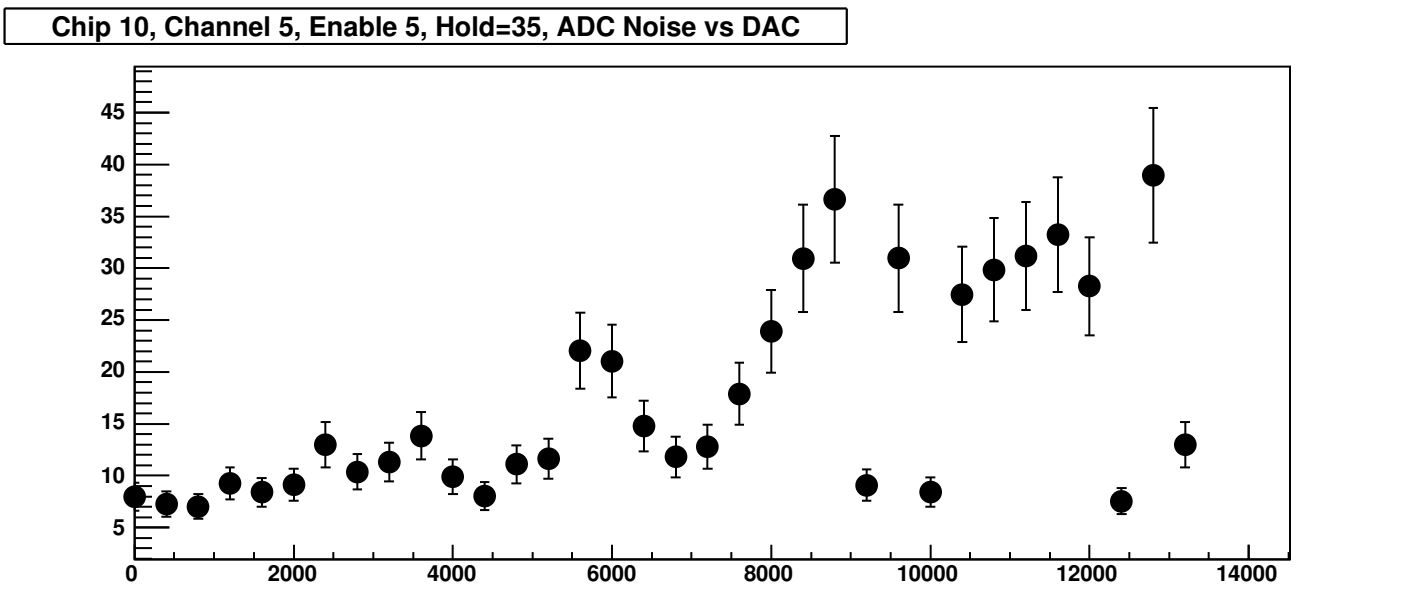


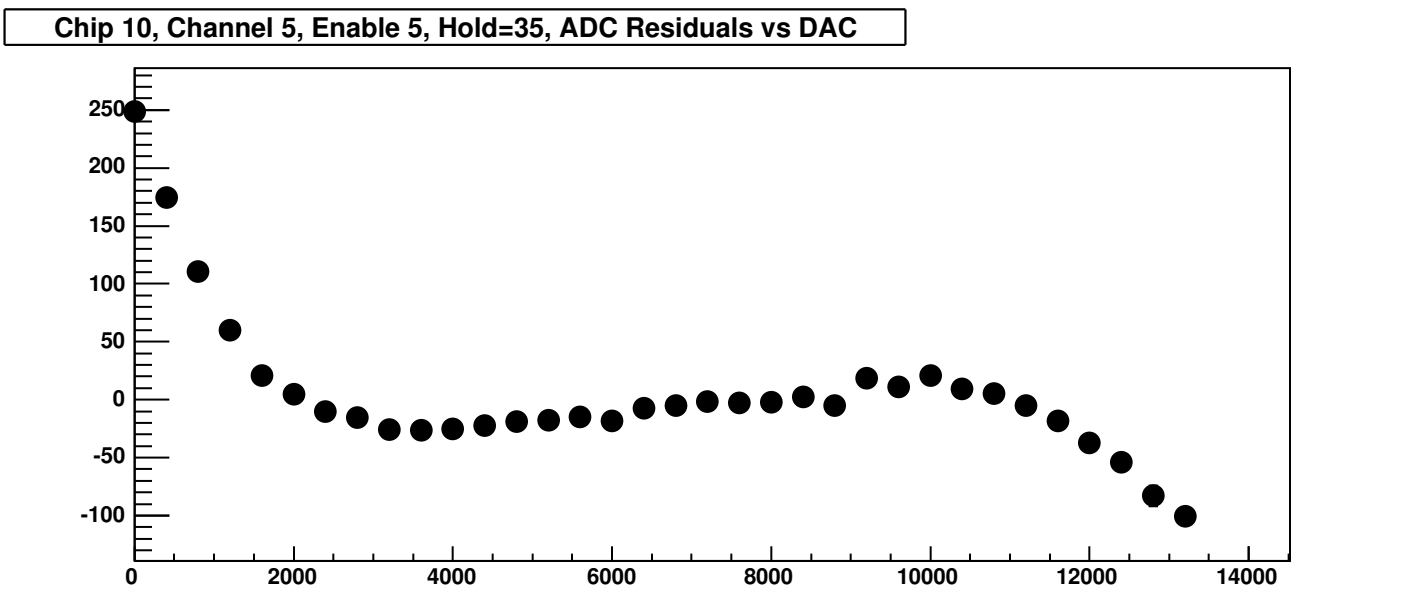

Chip 10, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 55 / 23
p0 195.2 ± 0.7268
p1 0.01962 ± 0.0001219
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

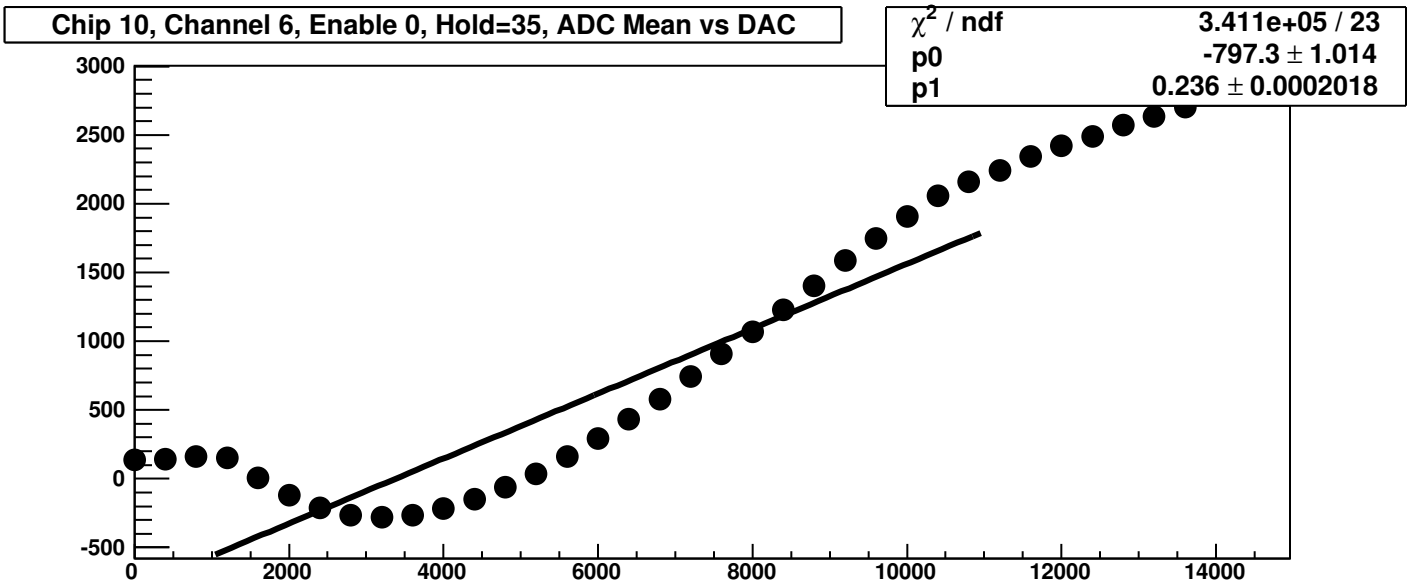

Chip 10, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

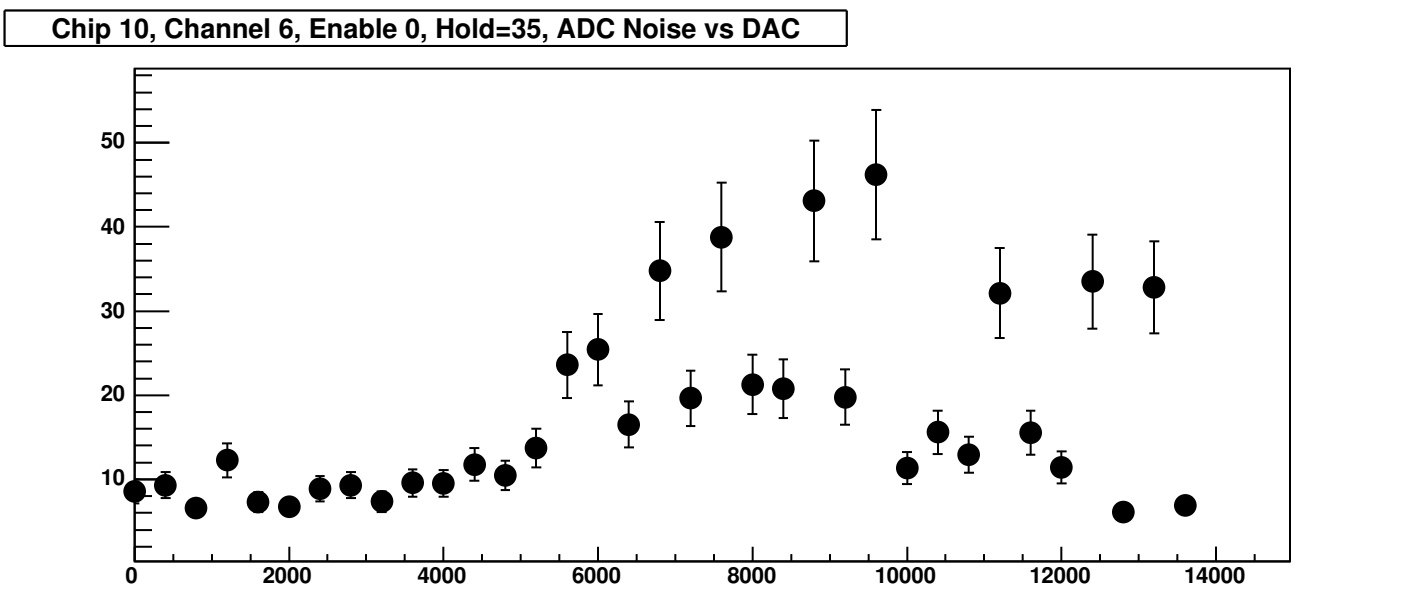

Chip 10, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

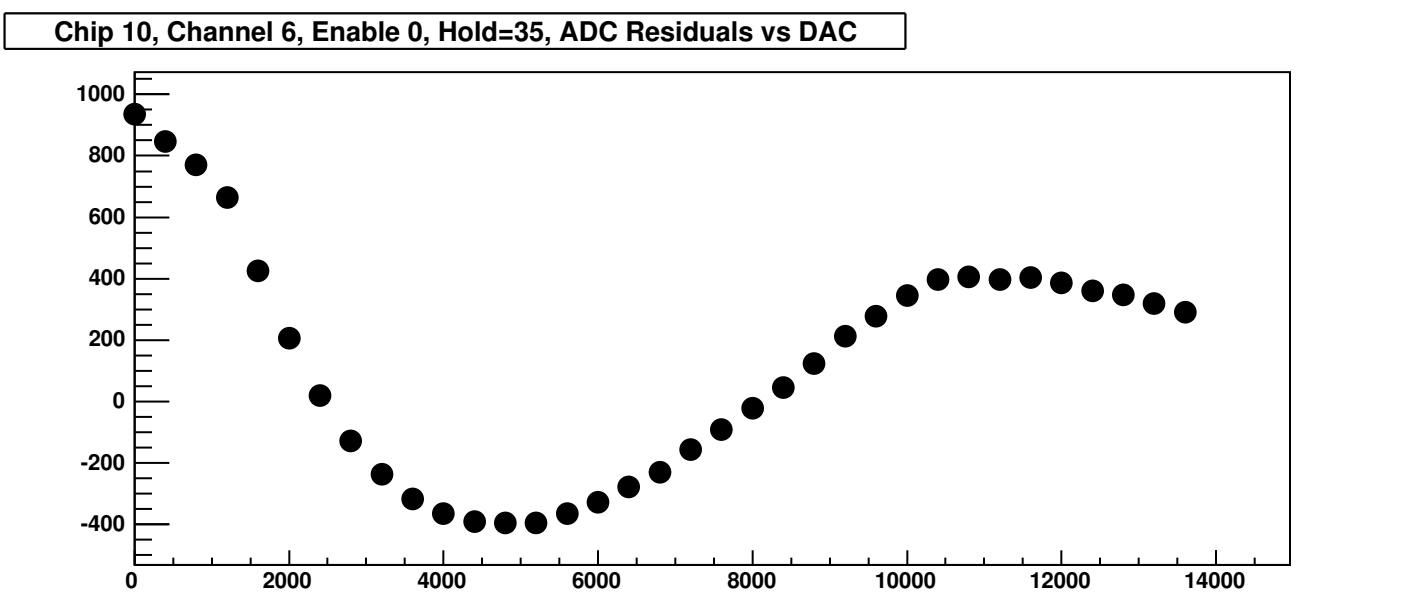

Chip 10, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1741 / 23
p0 -20.03 ± 1.122
p1 0.1797 ± 0.0001963
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

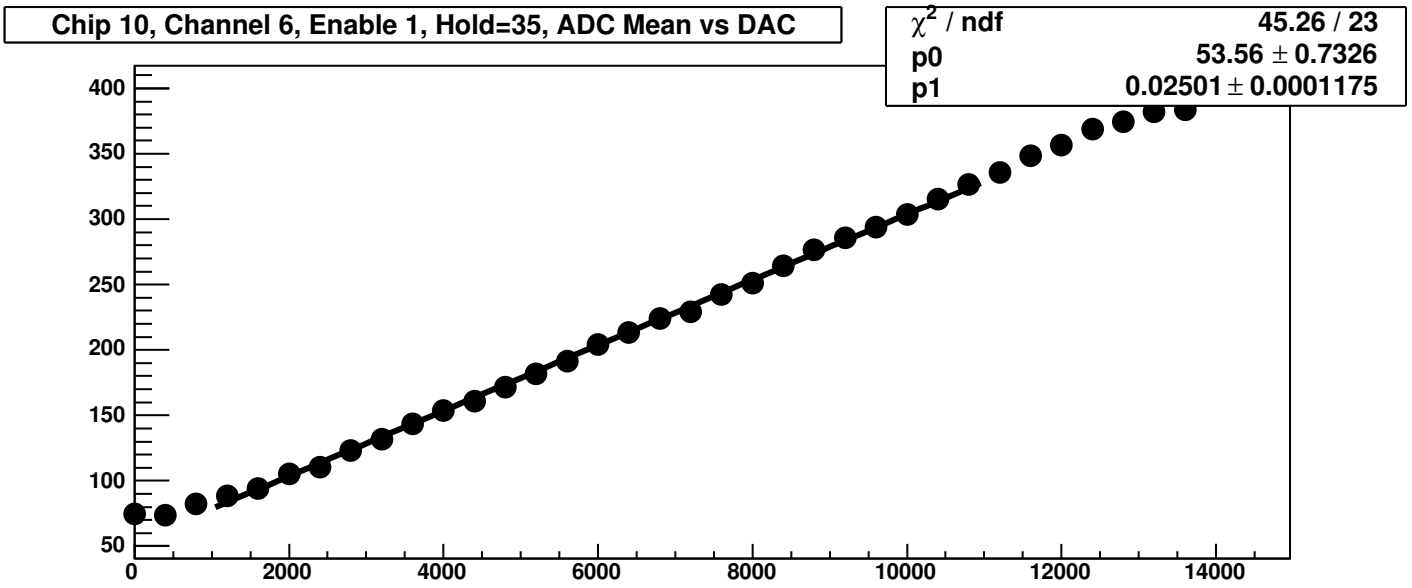

Chip 10, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

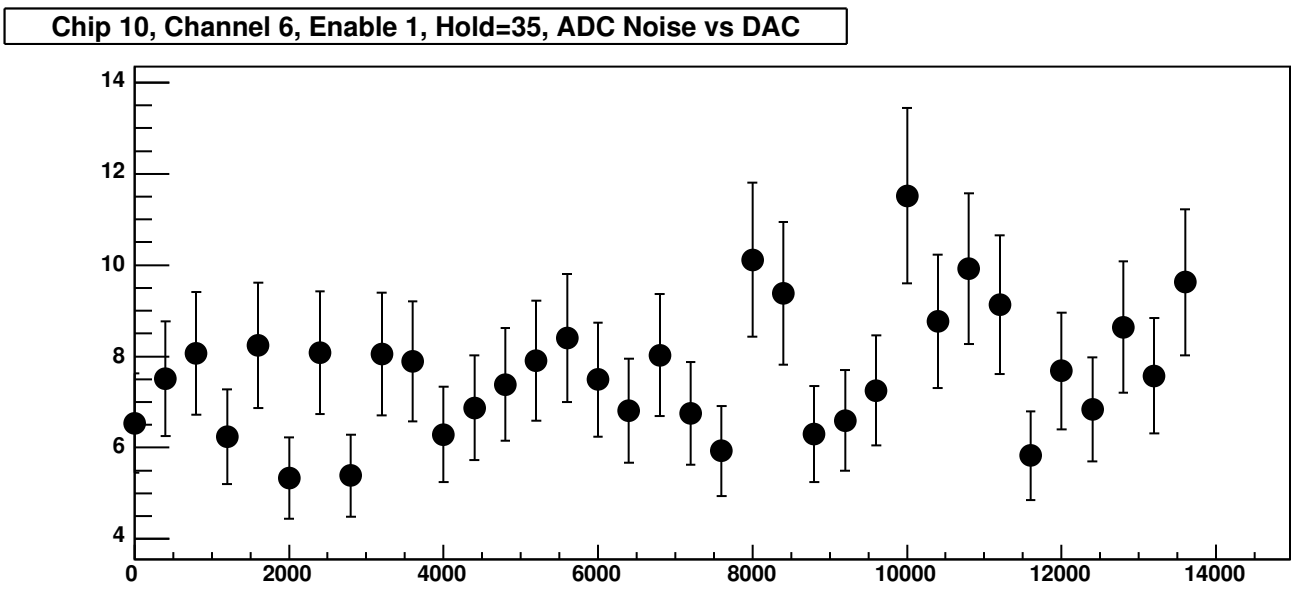

Chip 10, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

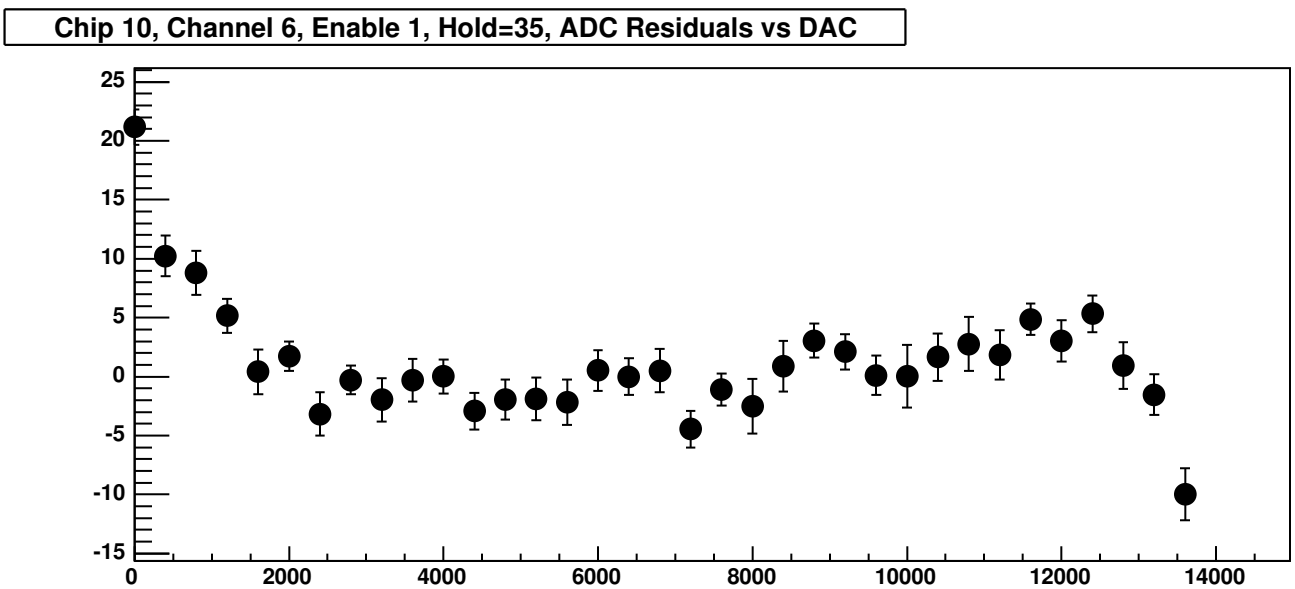

Chip 10, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.411e+05 / 23
p0 -797.3 ± 1.014
p1 0.236 ± 0.0002018
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

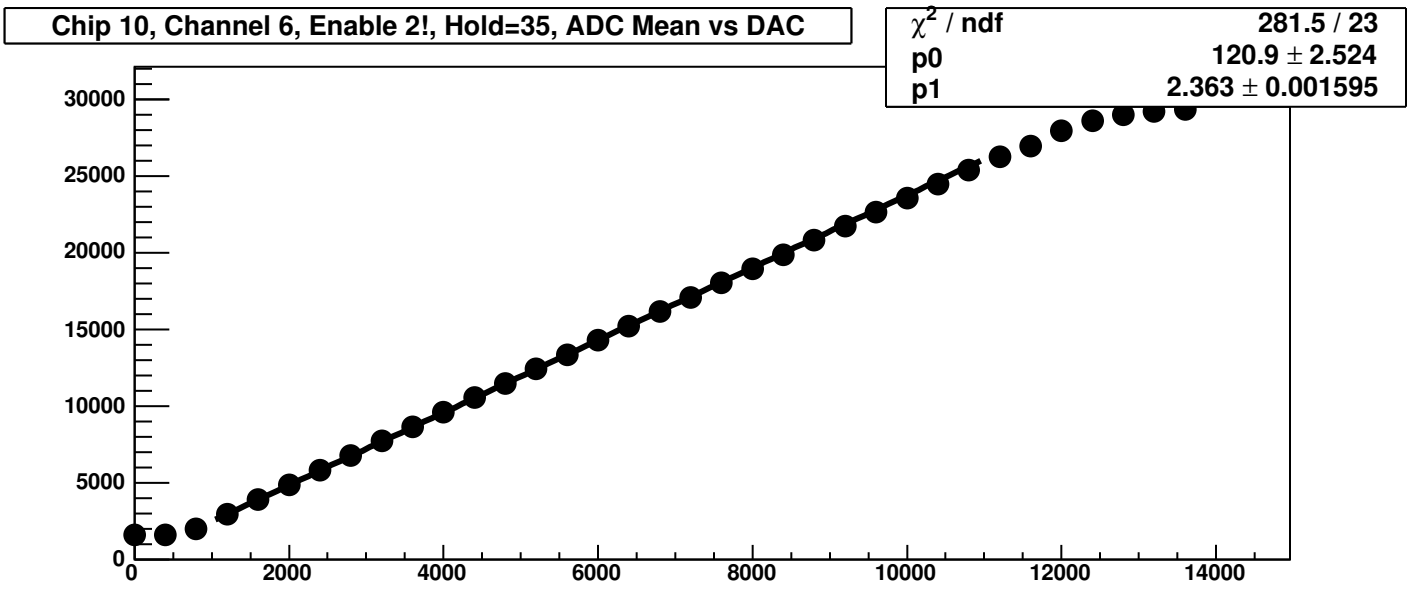

Chip 10, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

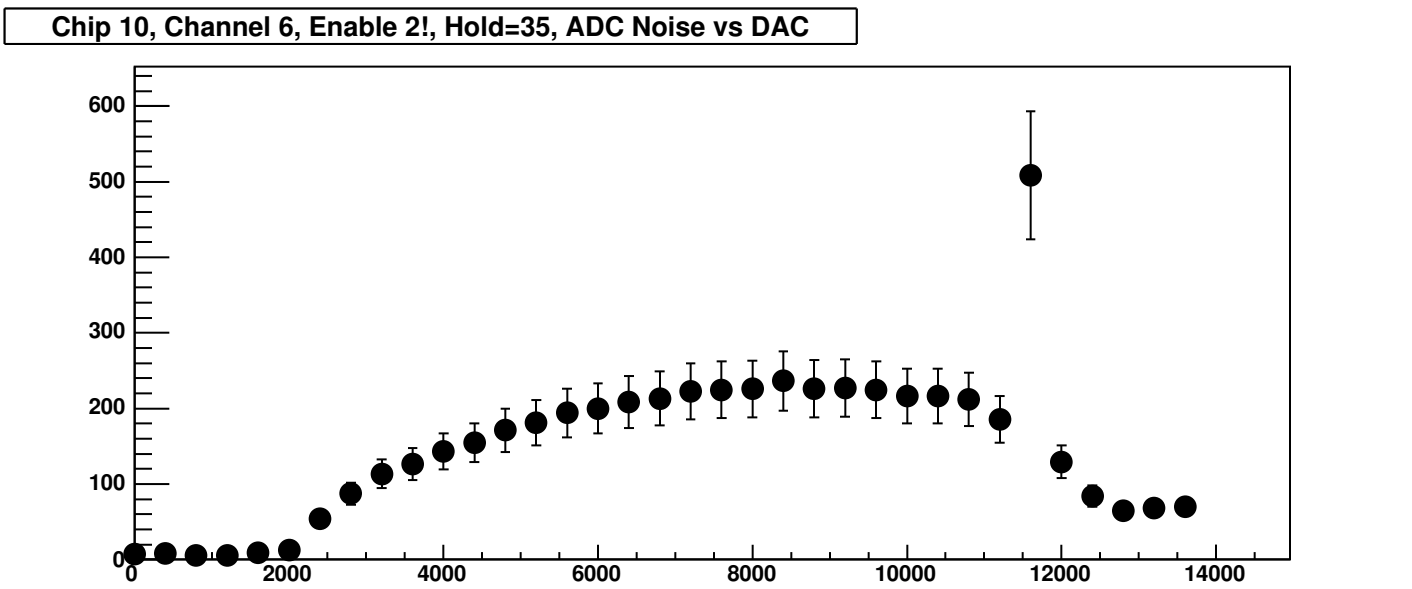

Chip 10, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

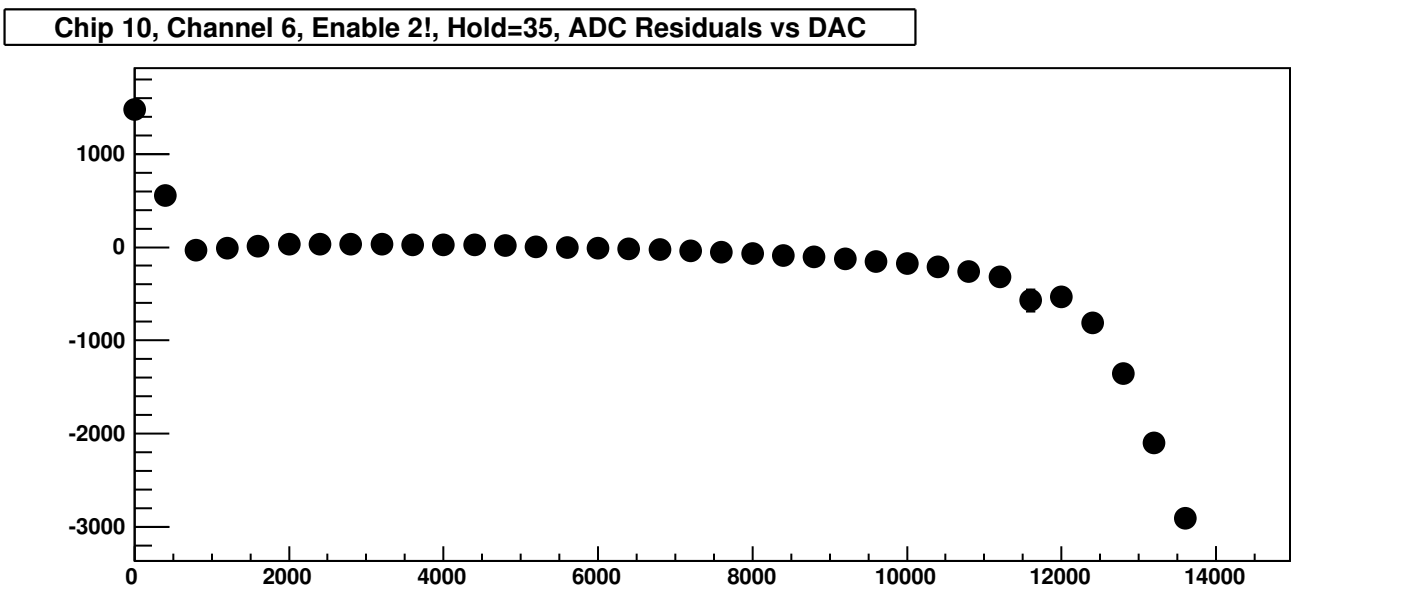

Chip 10, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 45.26 / 23
p0 53.56 ± 0.7326
p1 0.02501 ± 0.0001175
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

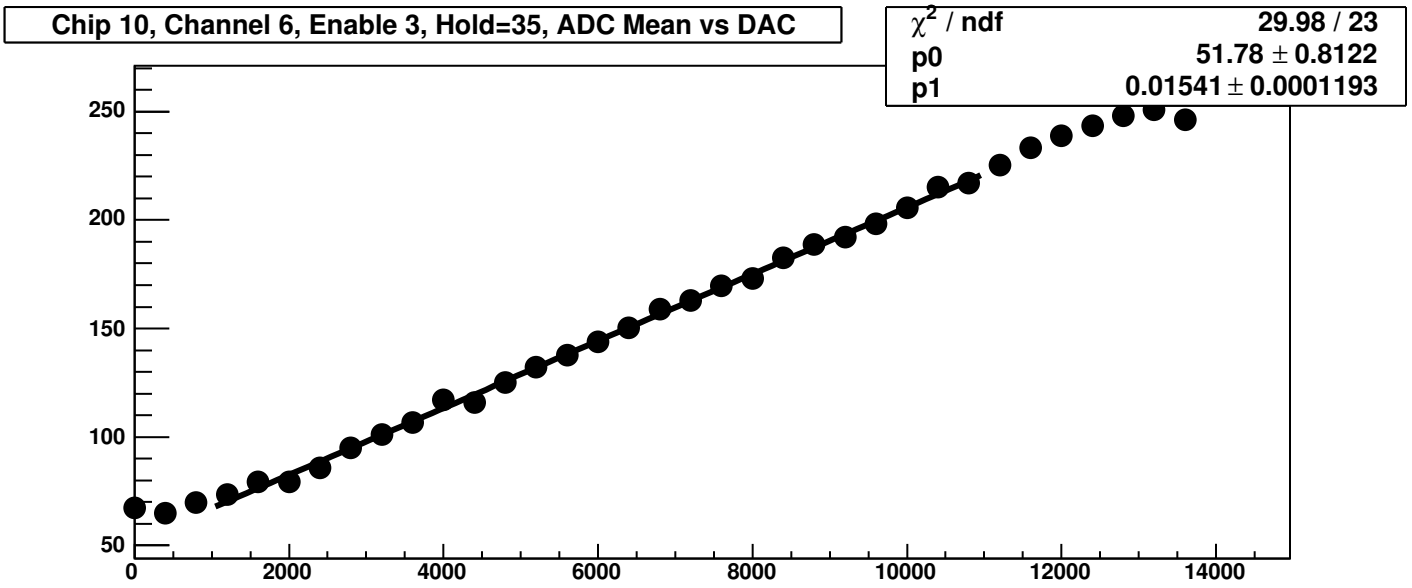

Chip 10, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

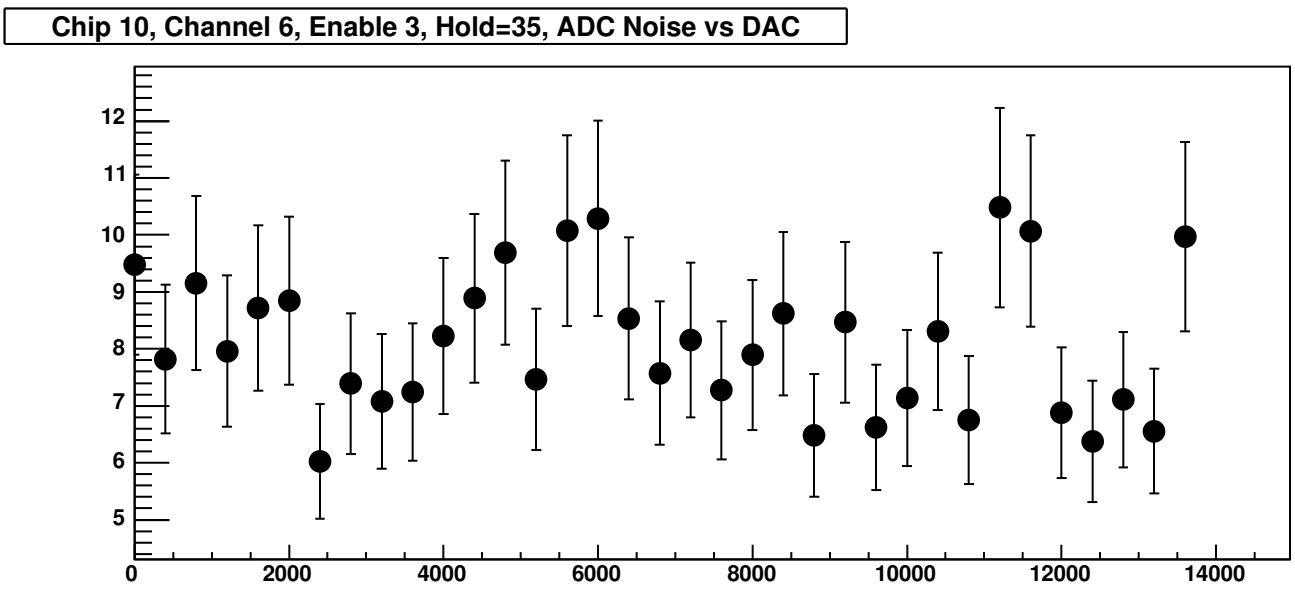

Chip 10, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

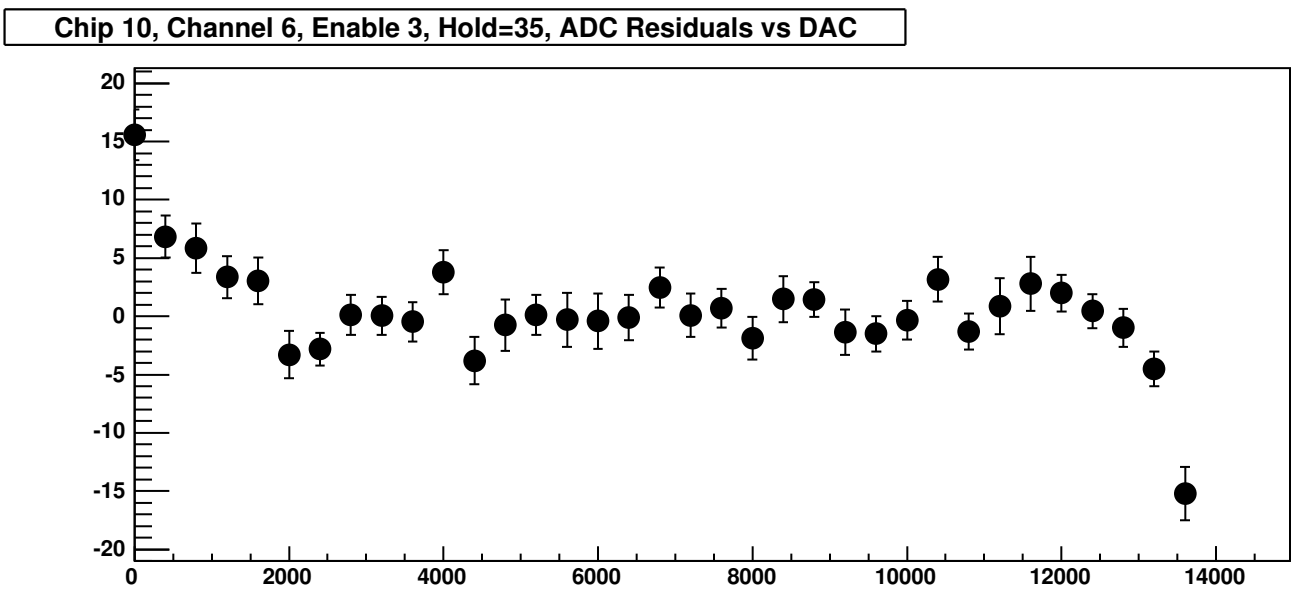

Chip 10, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 281.5 / 23
p0 120.9 ± 2.524
p1 2.363 ± 0.001595
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

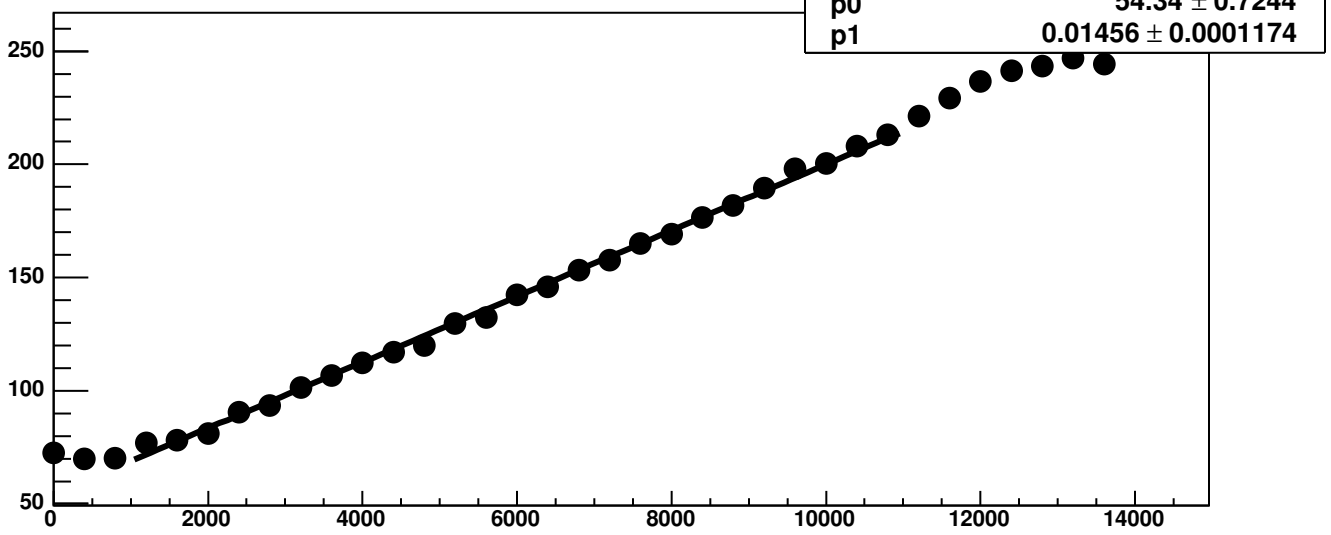

Chip 10, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 29.98 / 23
p0 51.78 ± 0.8122
p1 0.01541 ± 0.0001193
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

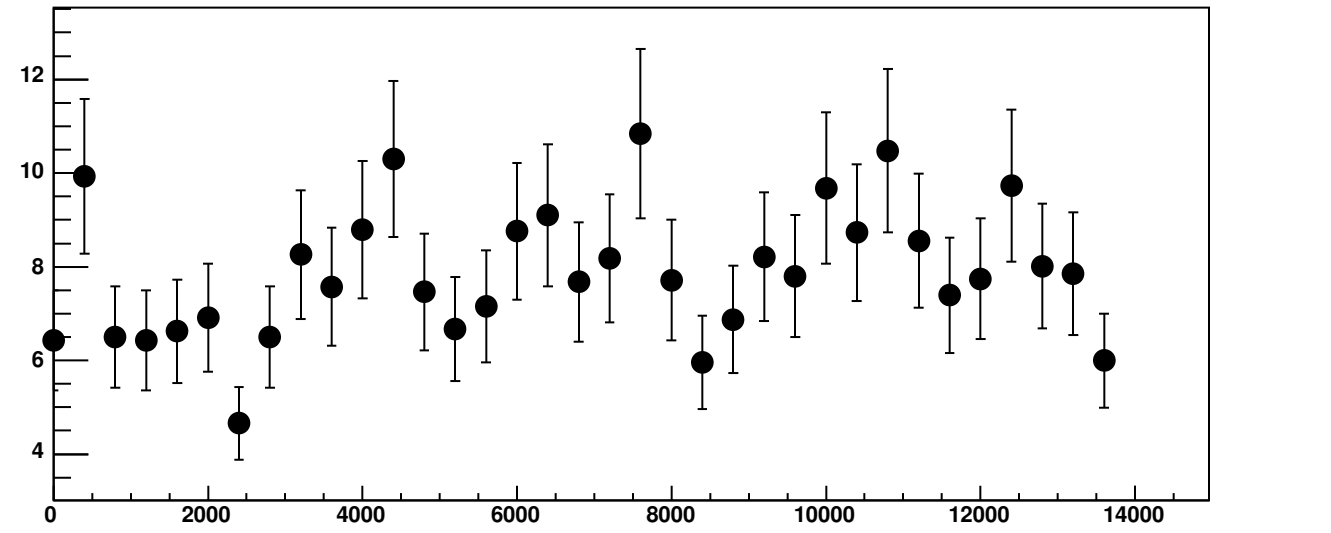

Chip 10, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

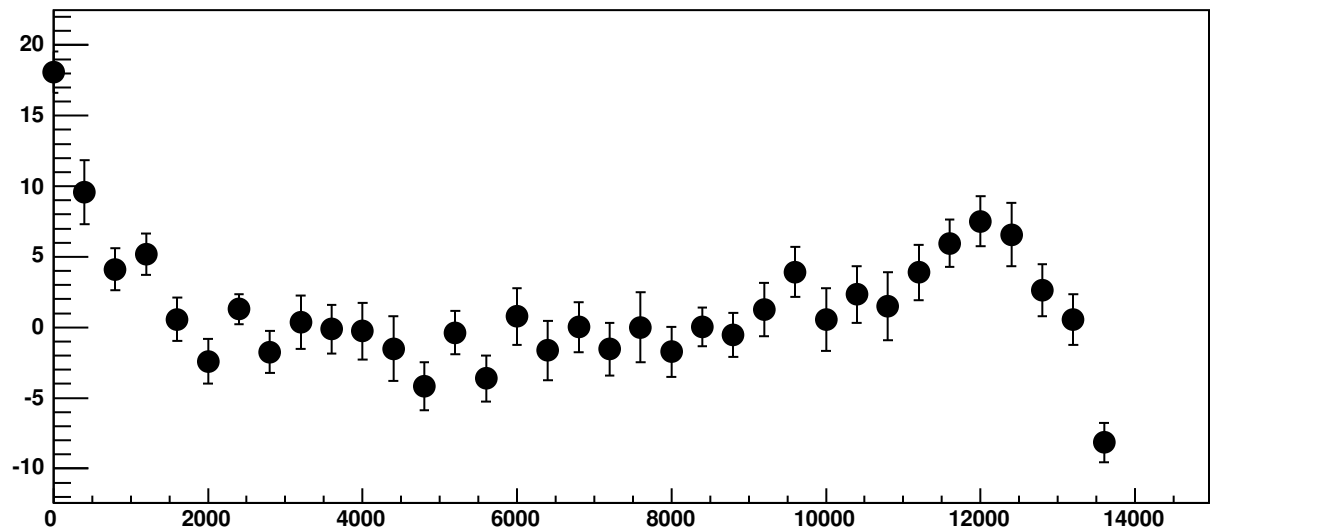
Chip 10, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

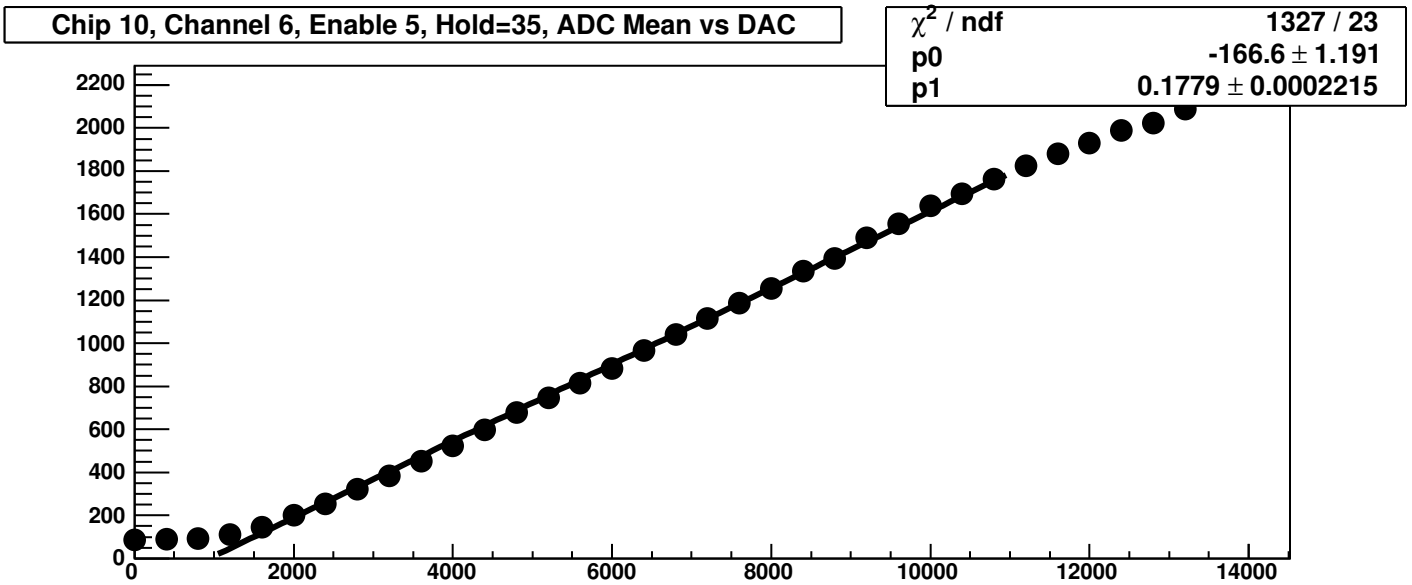


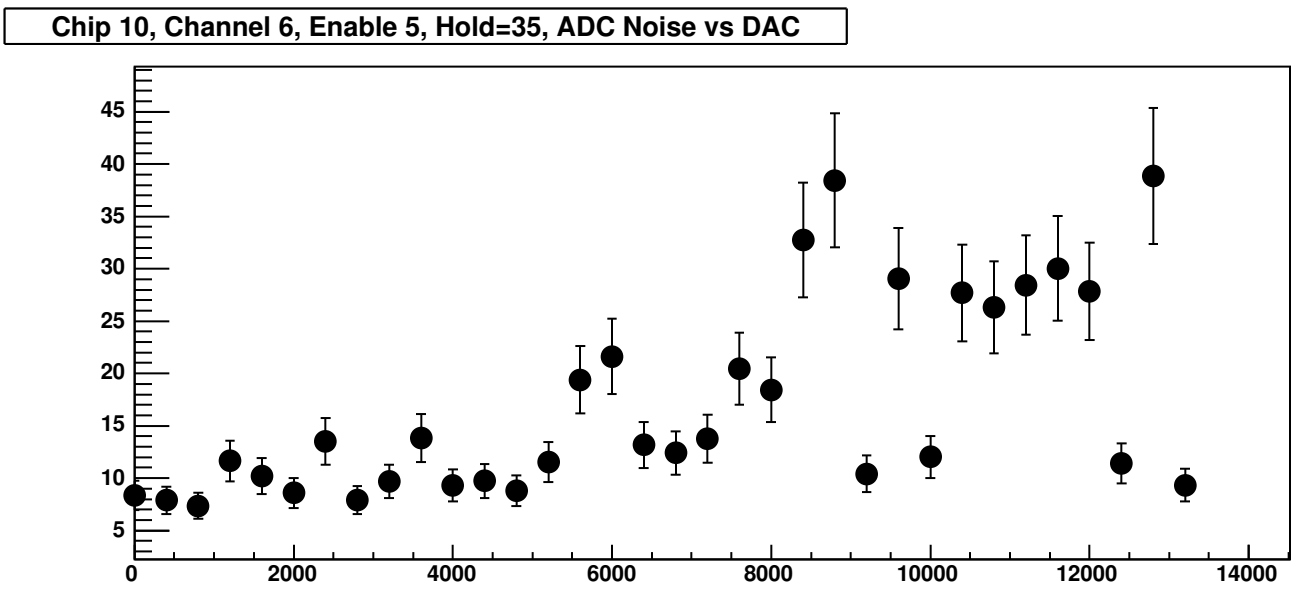
Chip 10, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

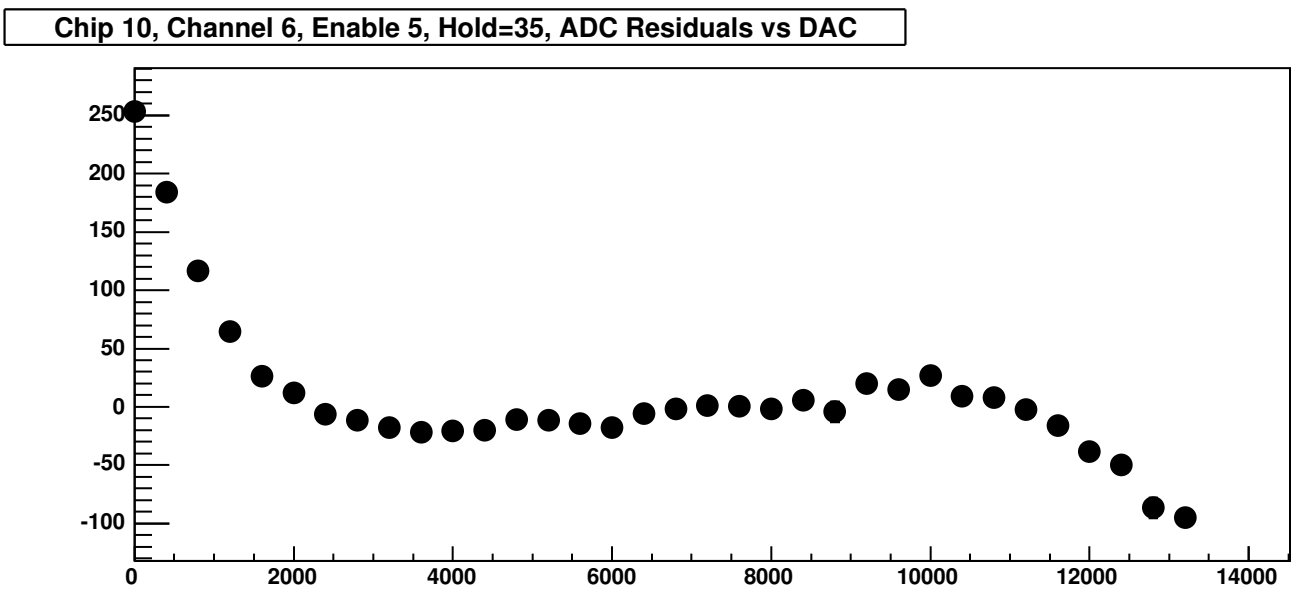


Chip 10, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

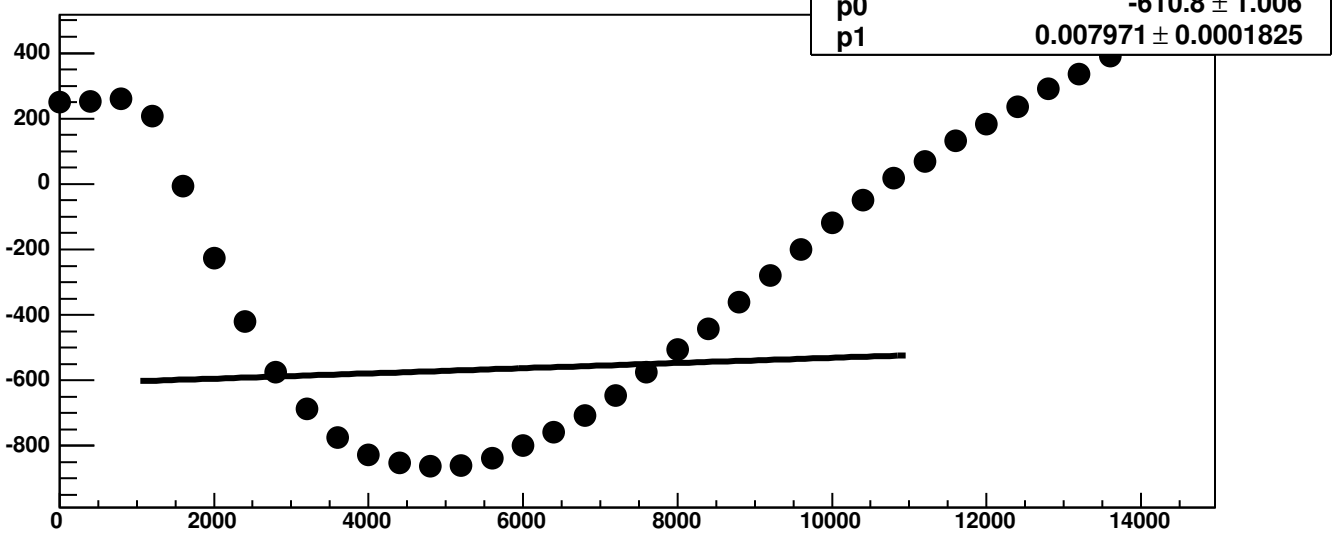



Chip 10, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1327 / 23
p0 -166.6 ± 1.191
p1 0.1779 ± 0.0002215
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

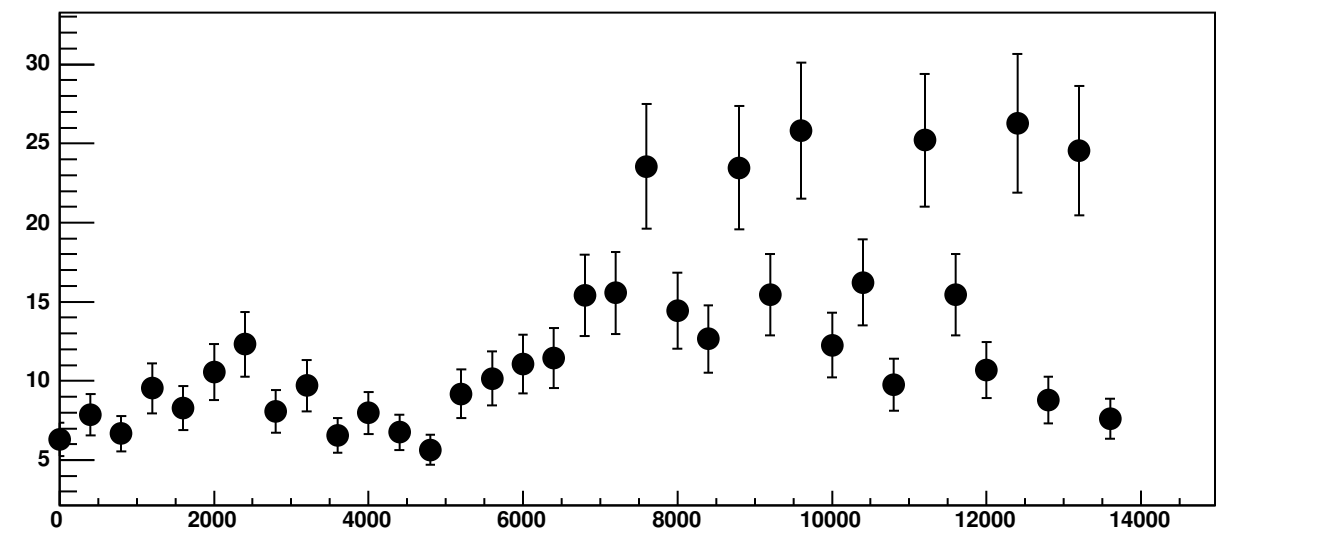

Chip 10, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

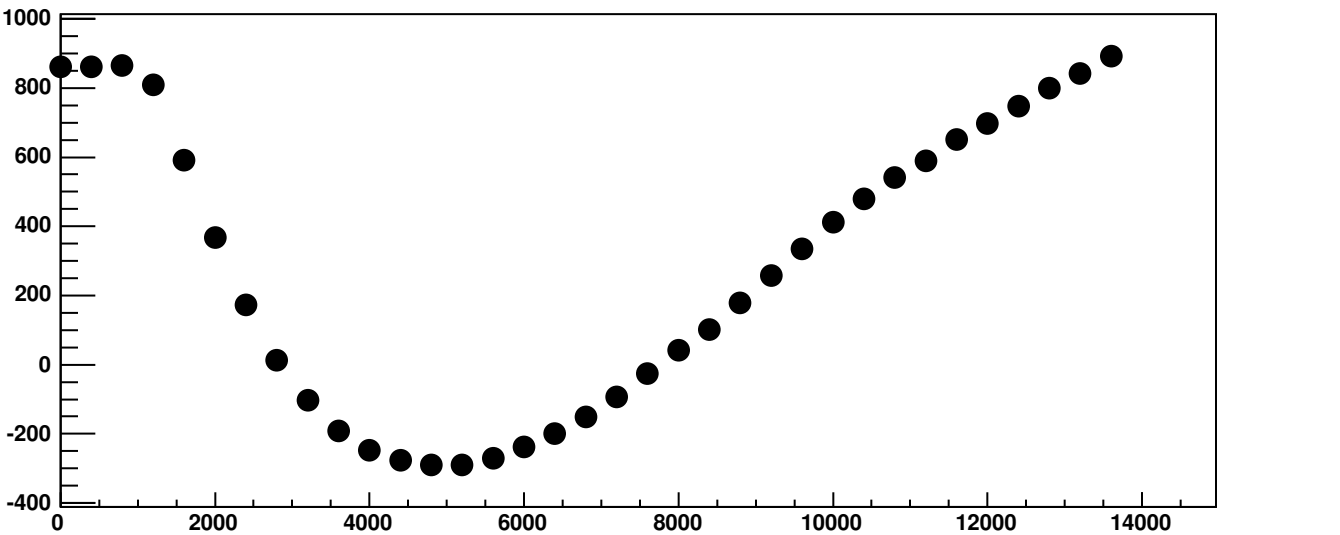
Chip 10, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC



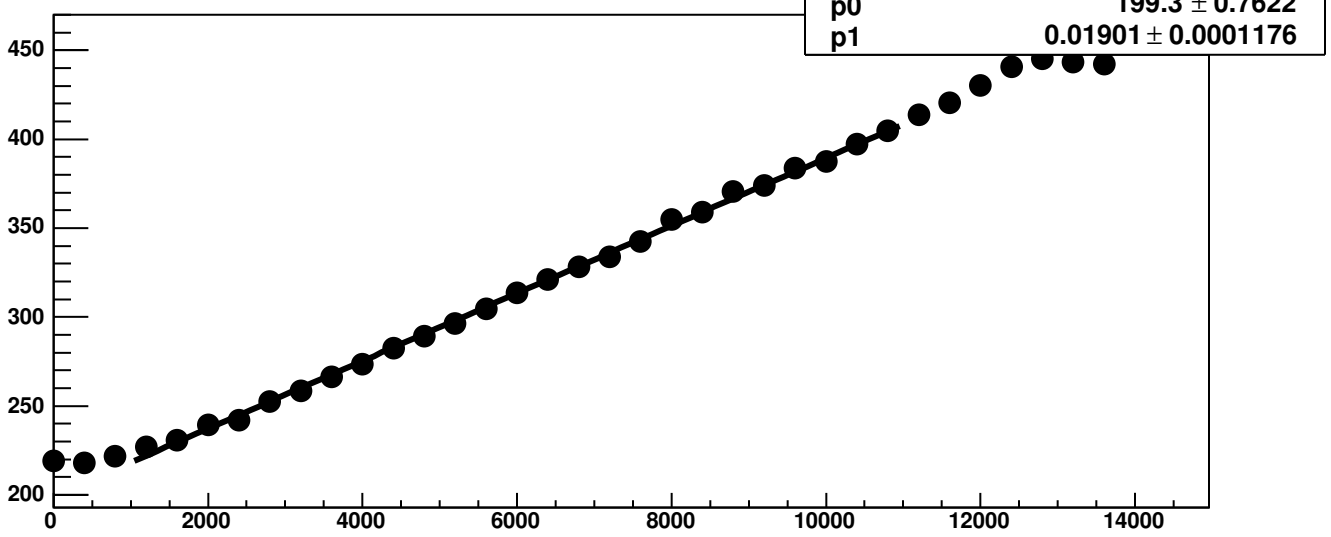
Chip 10, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC



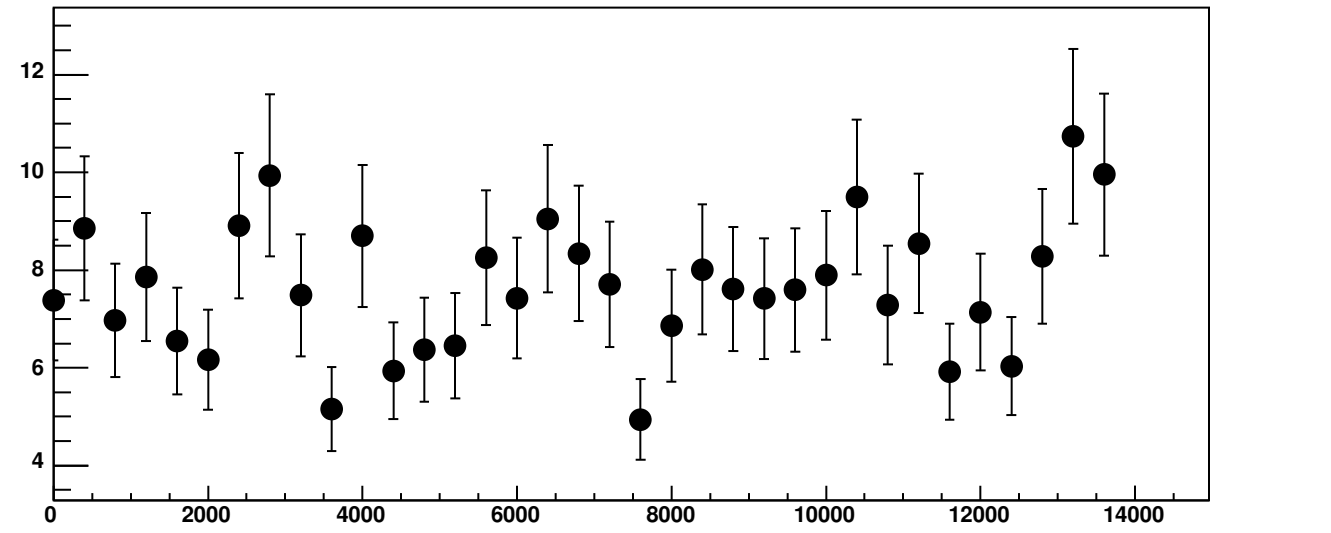
Chip 10, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC



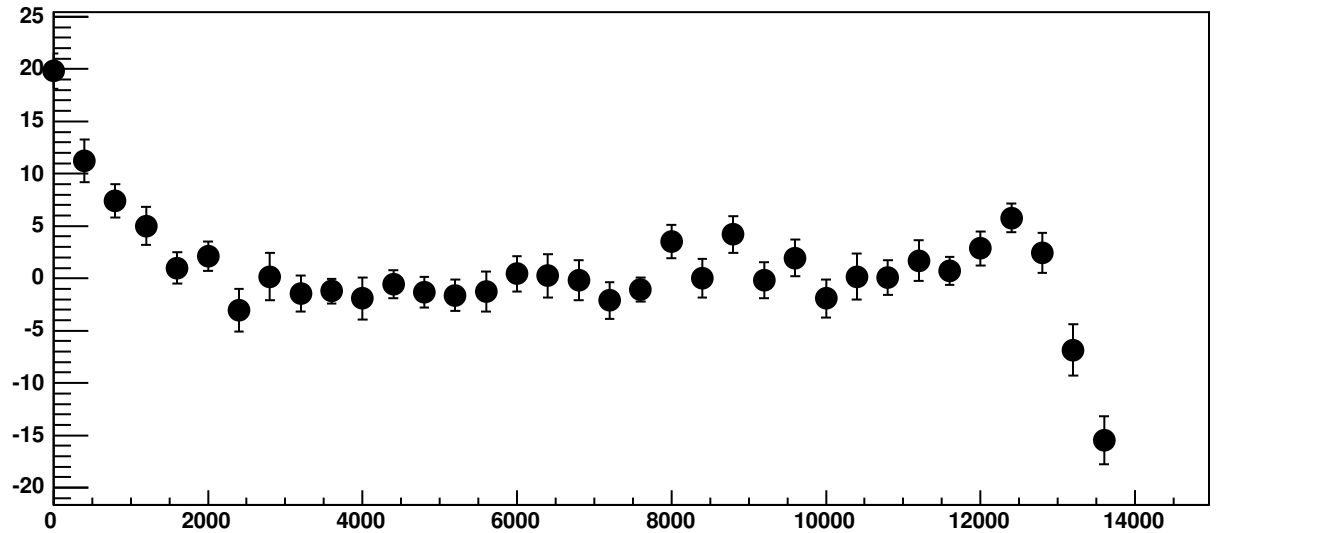
Chip 10, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC



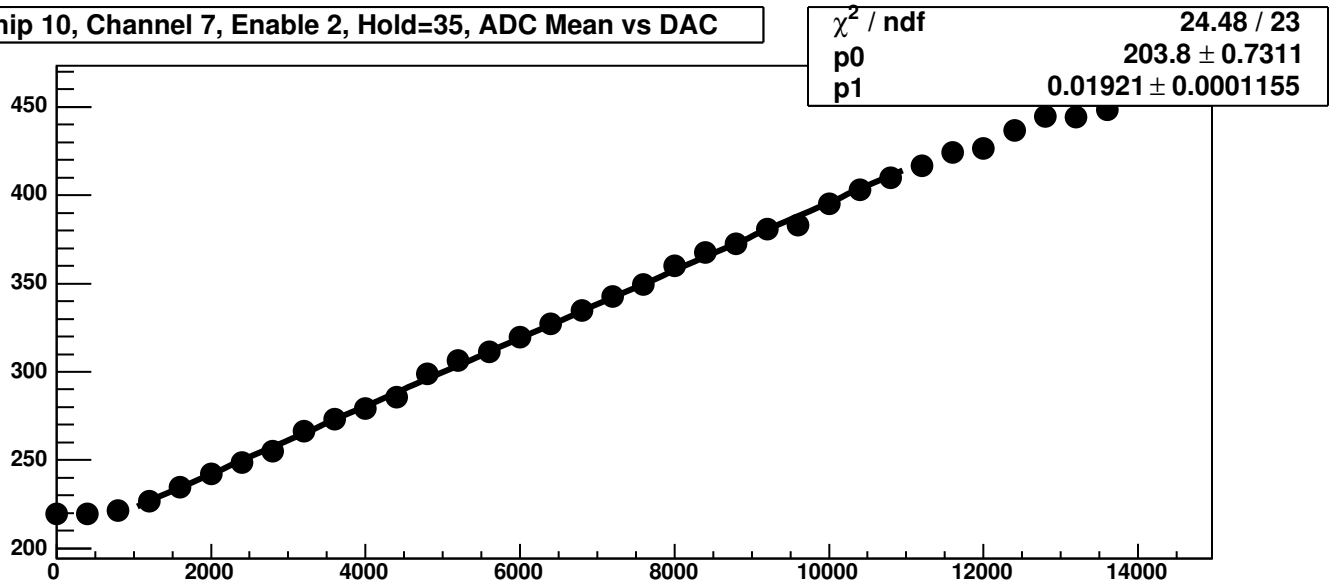
Chip 10, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC



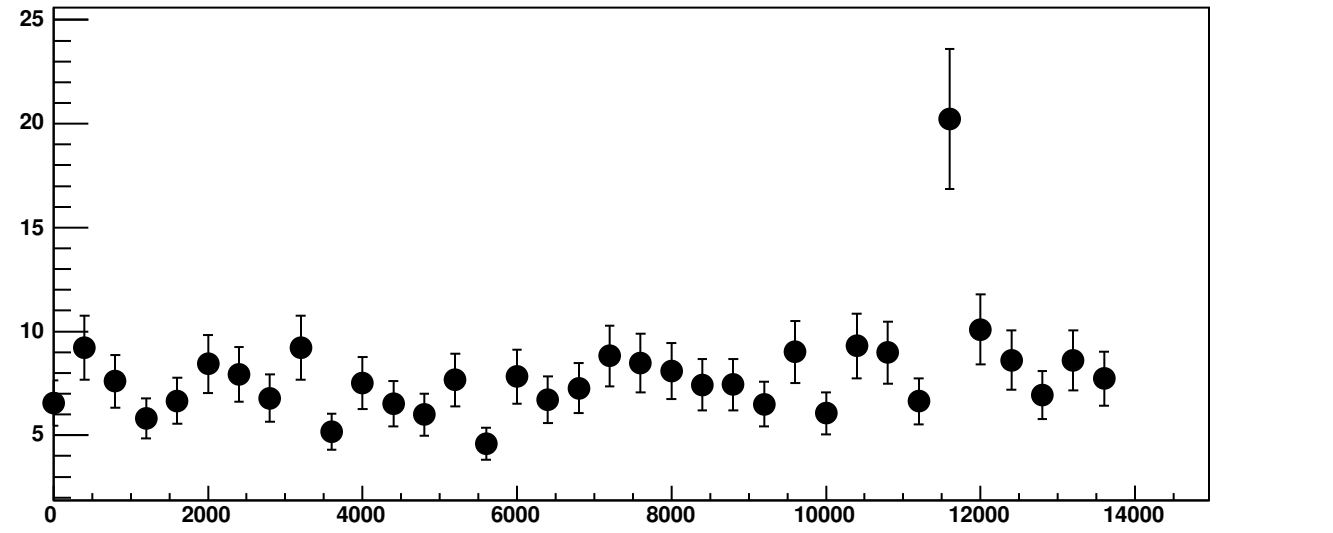
Chip 10, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC



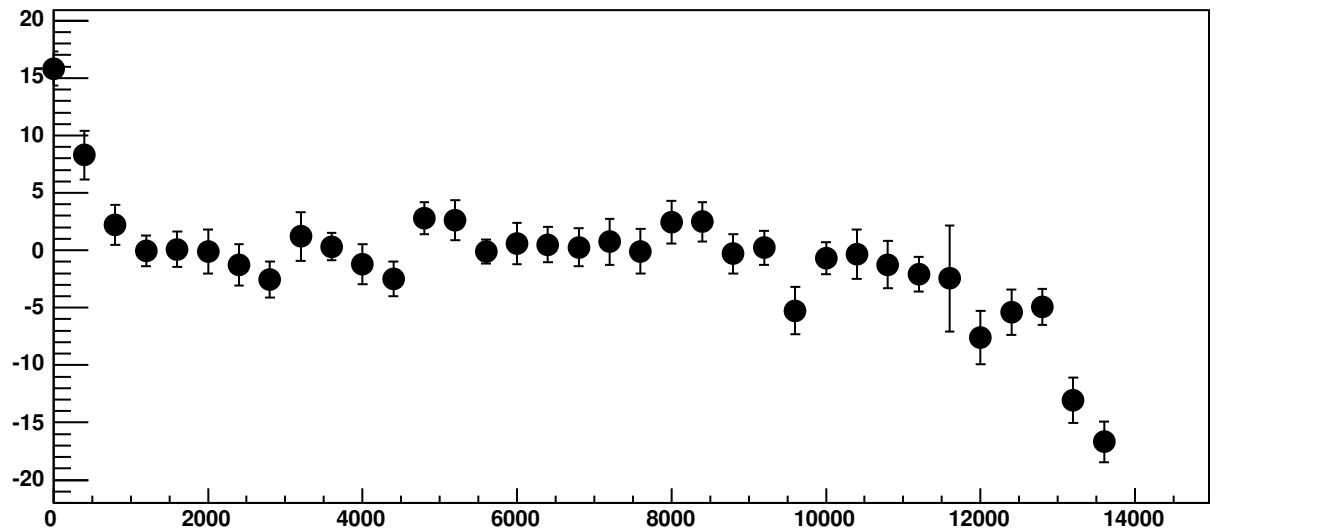
Chip 10, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC

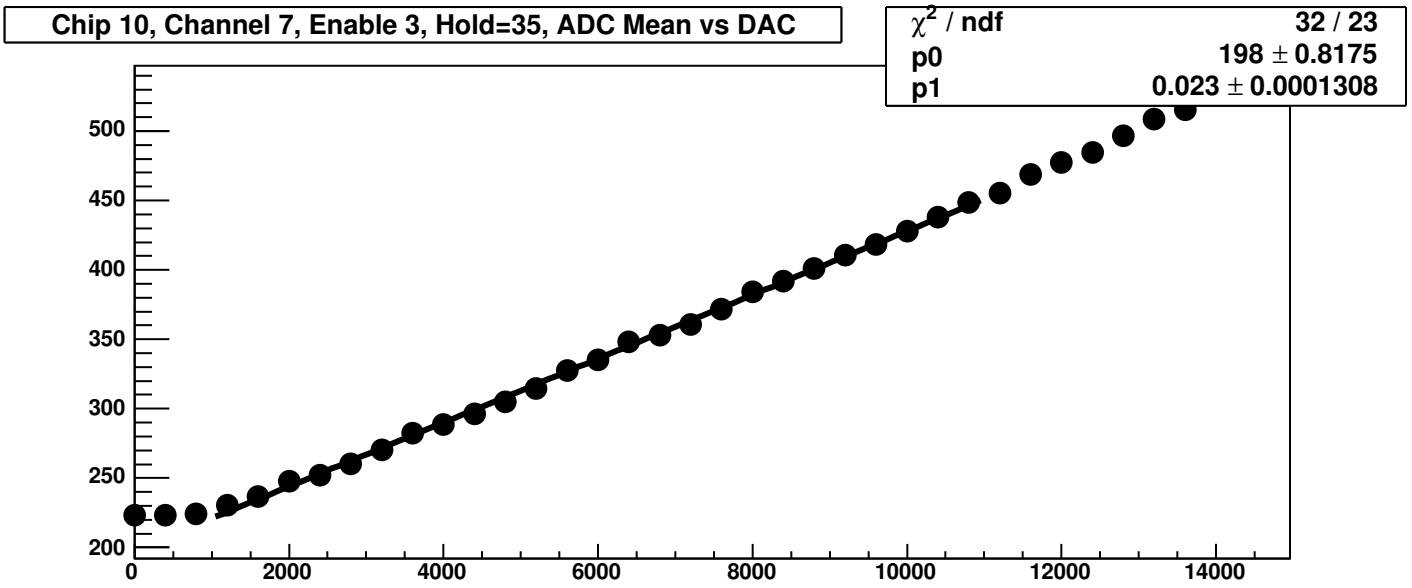


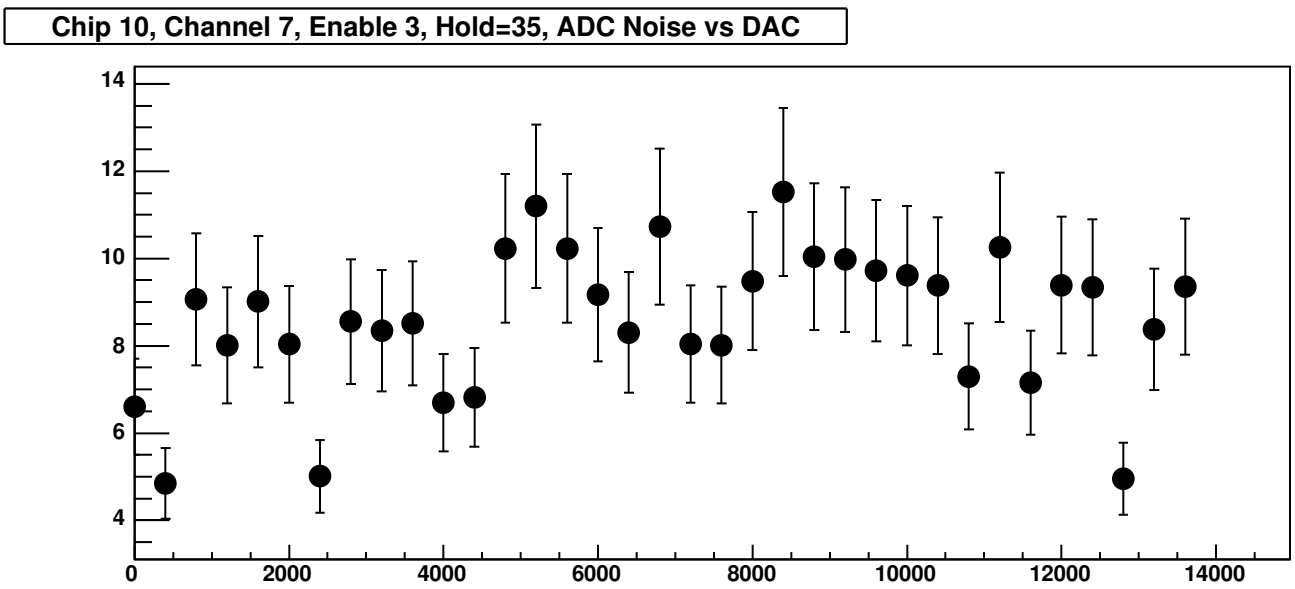
Chip 10, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC

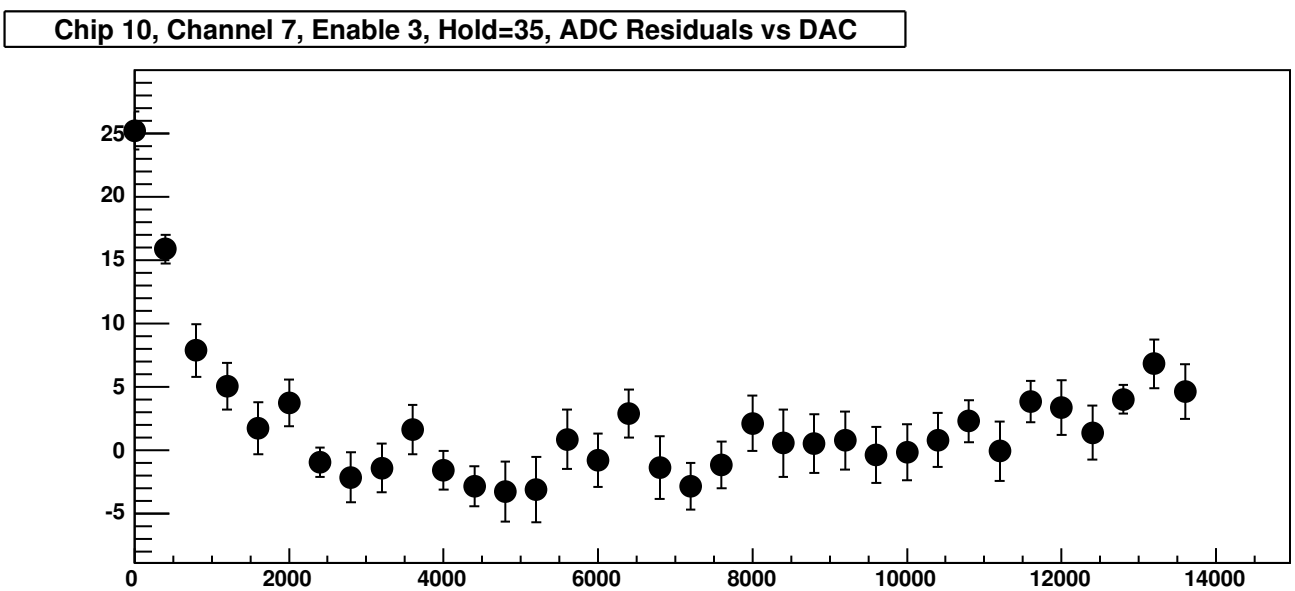


Chip 10, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC



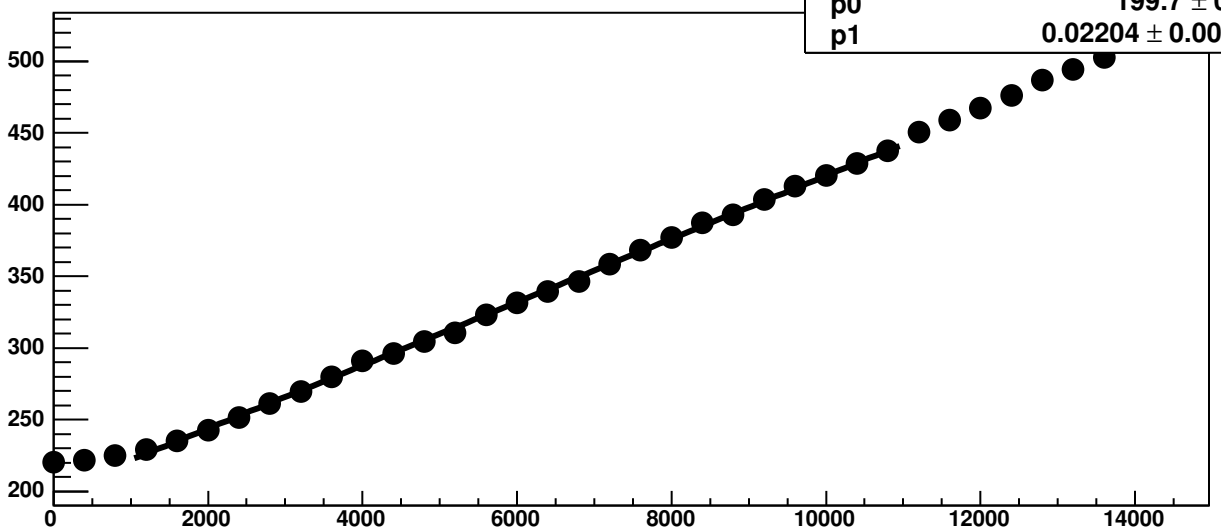

Chip 10, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 32 / 23
p0 198 ± 0.8175
p1 0.023 ± 0.0001308
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

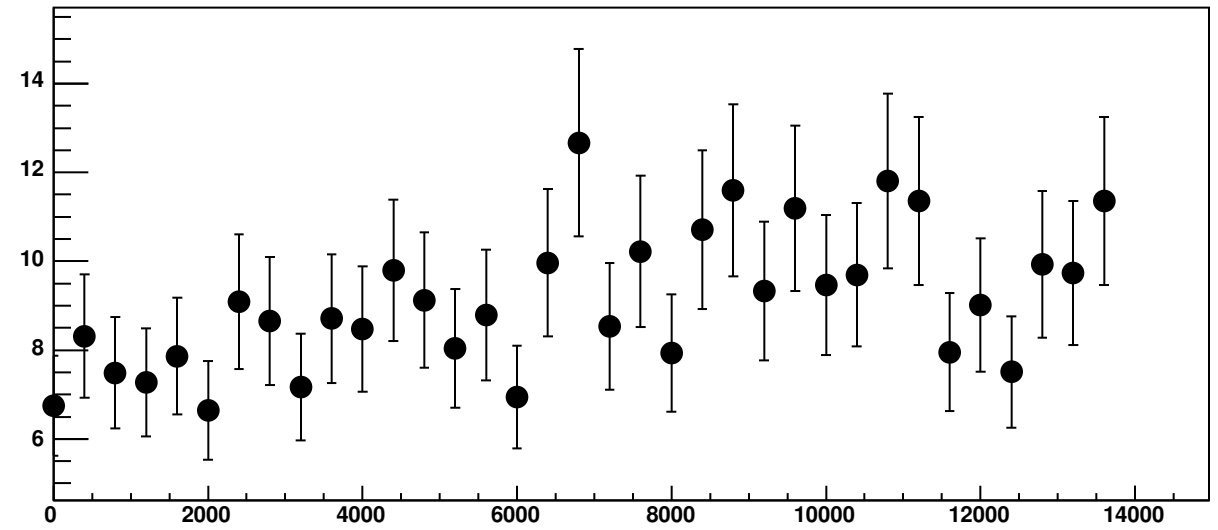

Chip 10, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

Chip 10, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

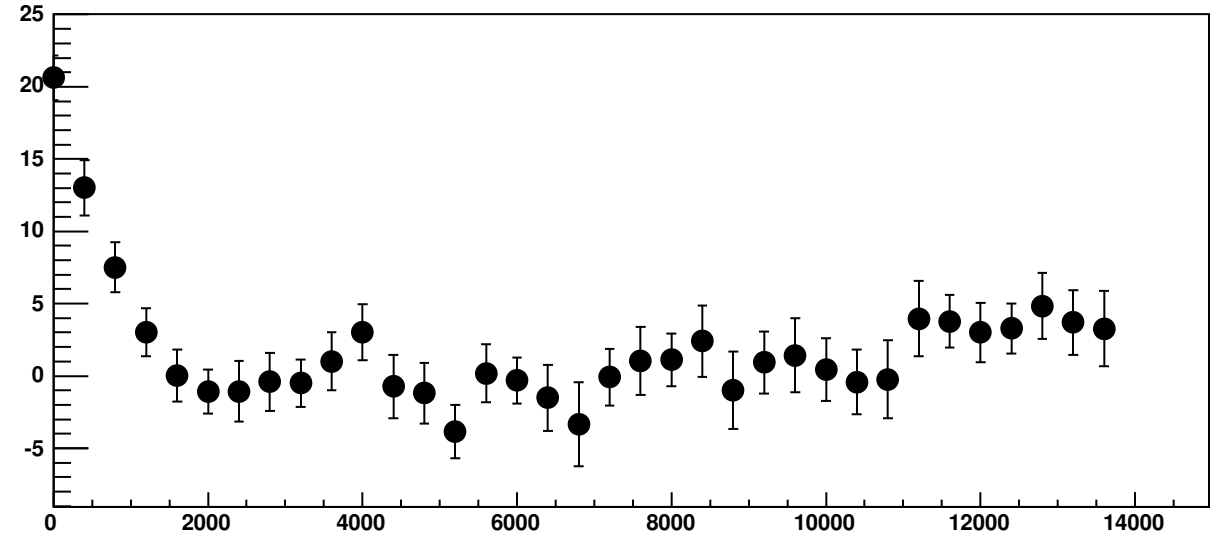
χ^2 / ndf	15.68 / 23
p0	199.7 ± 0.8644
p1	0.02204 ± 0.0001418

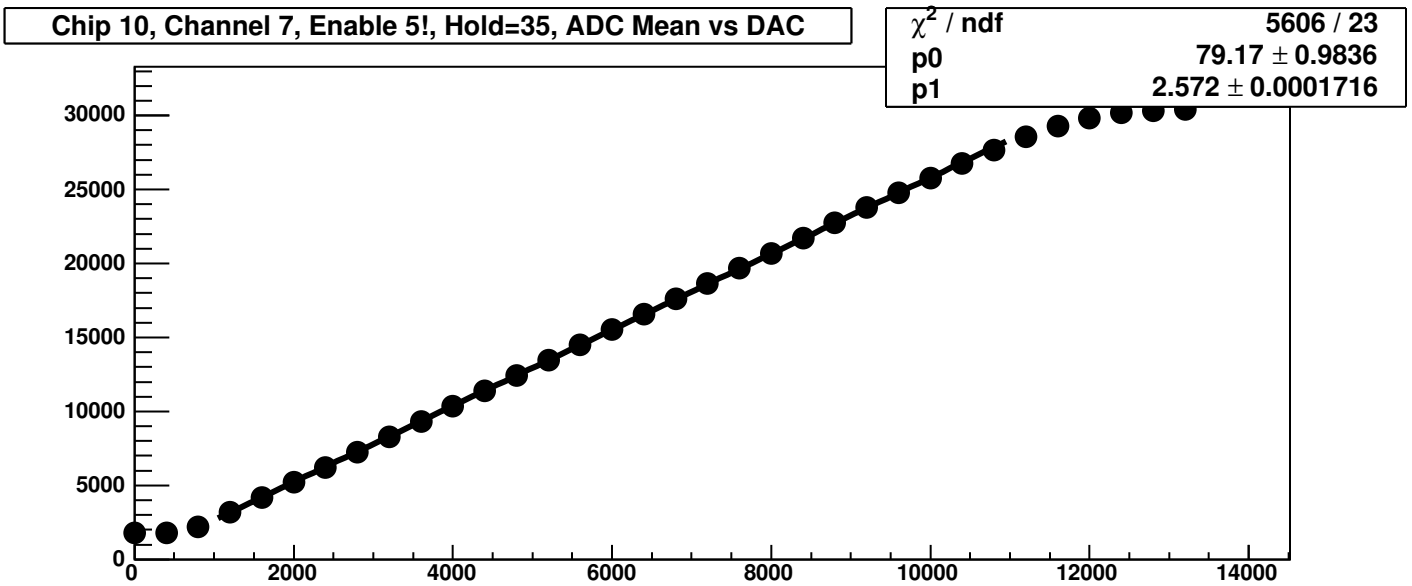


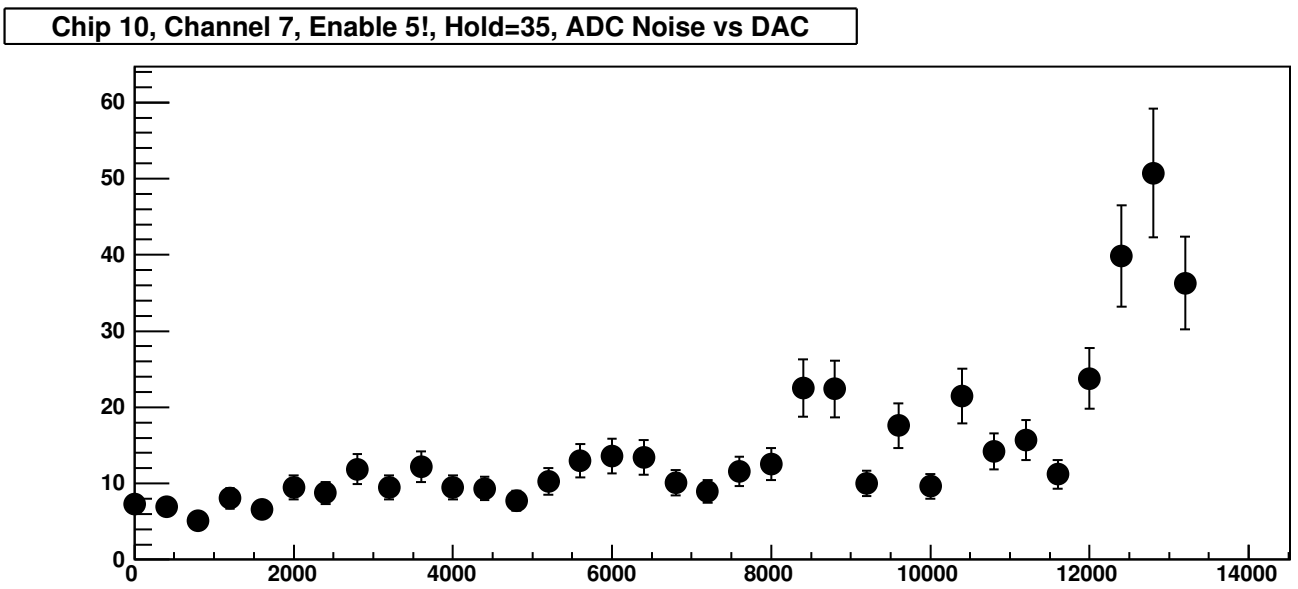
Chip 10, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

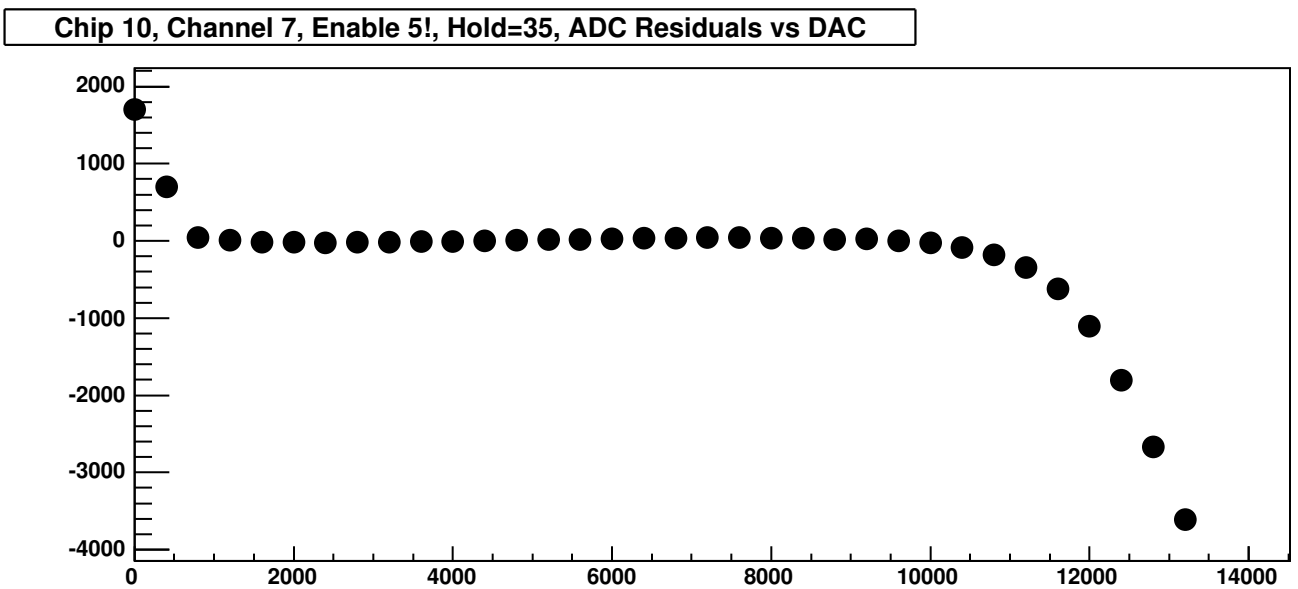


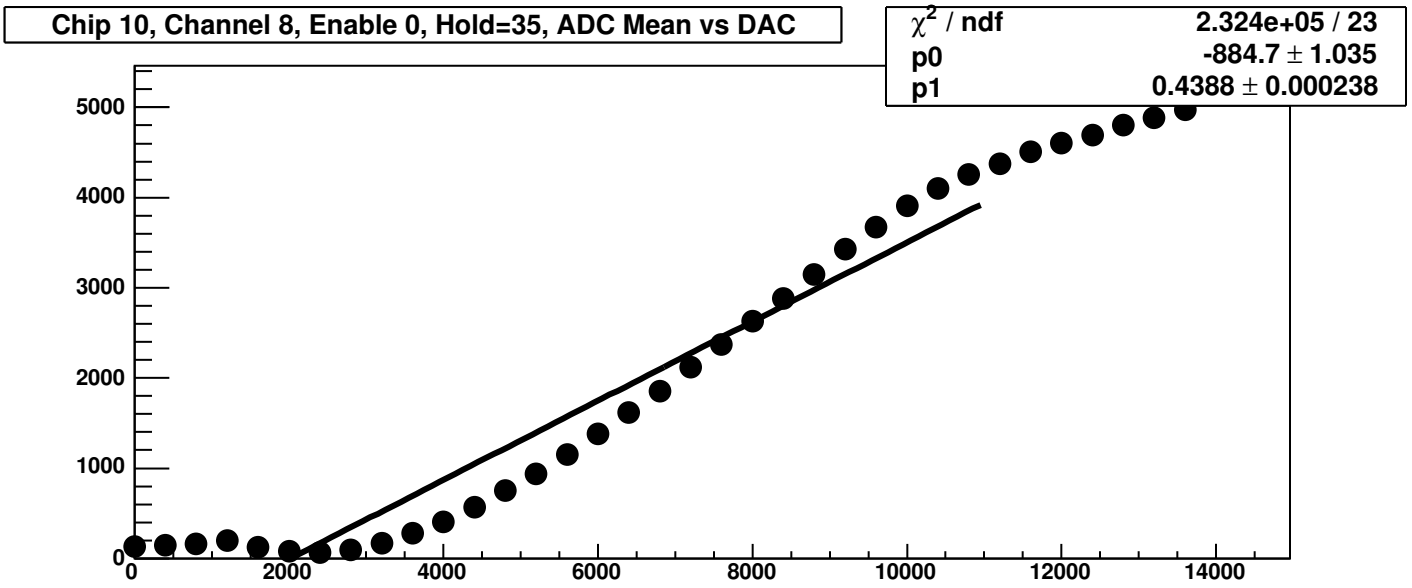
Chip 10, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

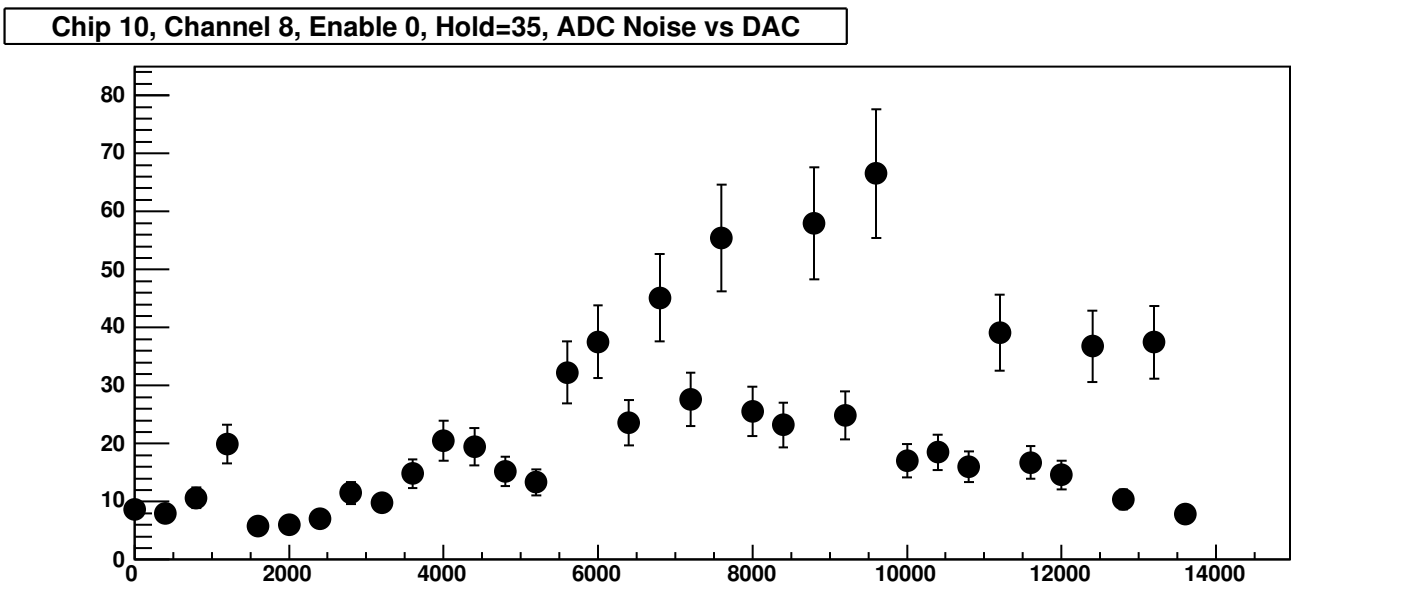


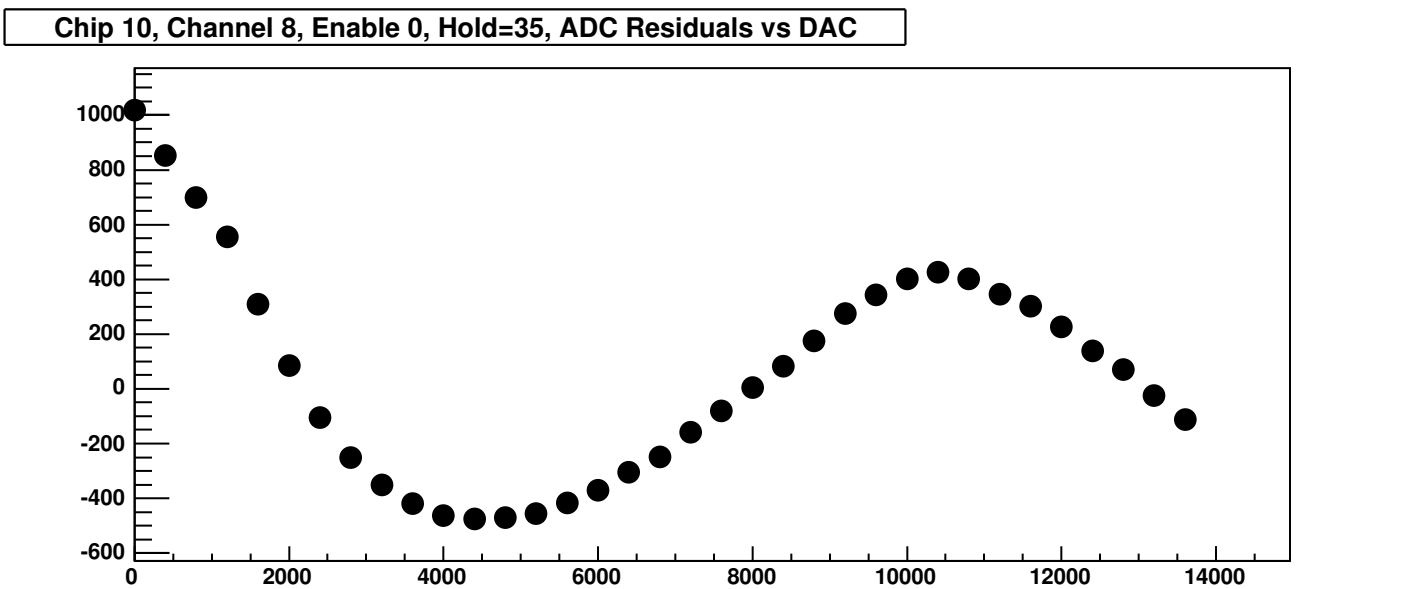

Chip 10, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 5606 / 23
p0 79.17 ± 0.9836
p1 2.572 ± 0.0001716
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

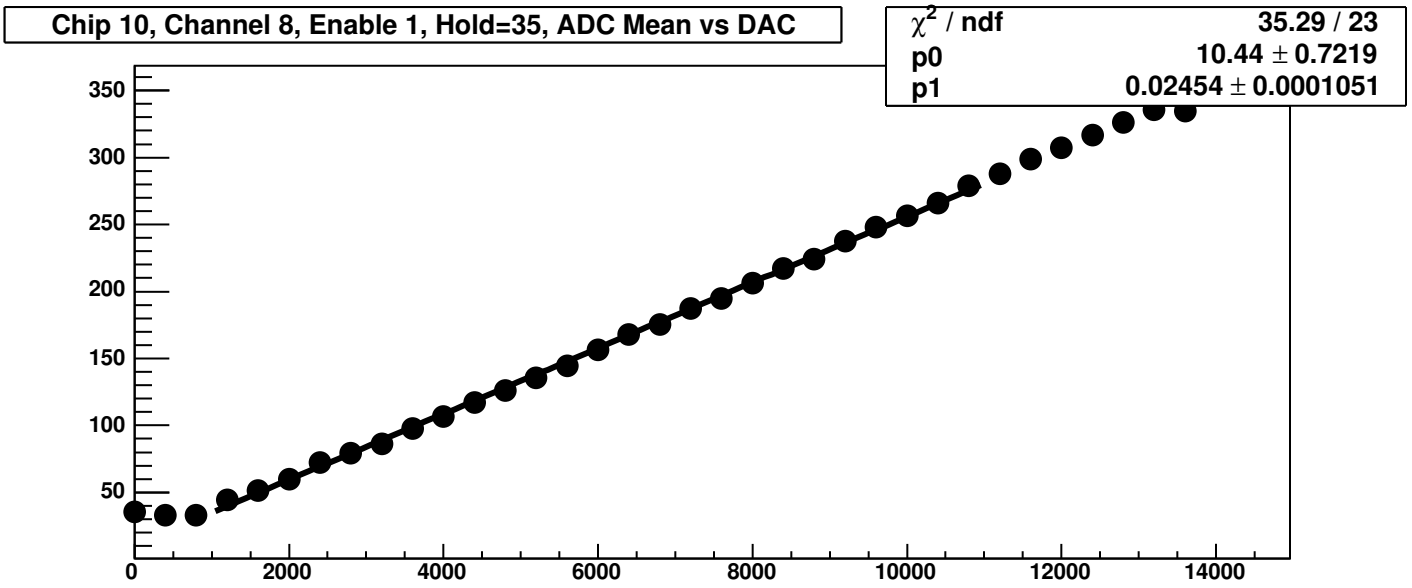

Chip 10, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

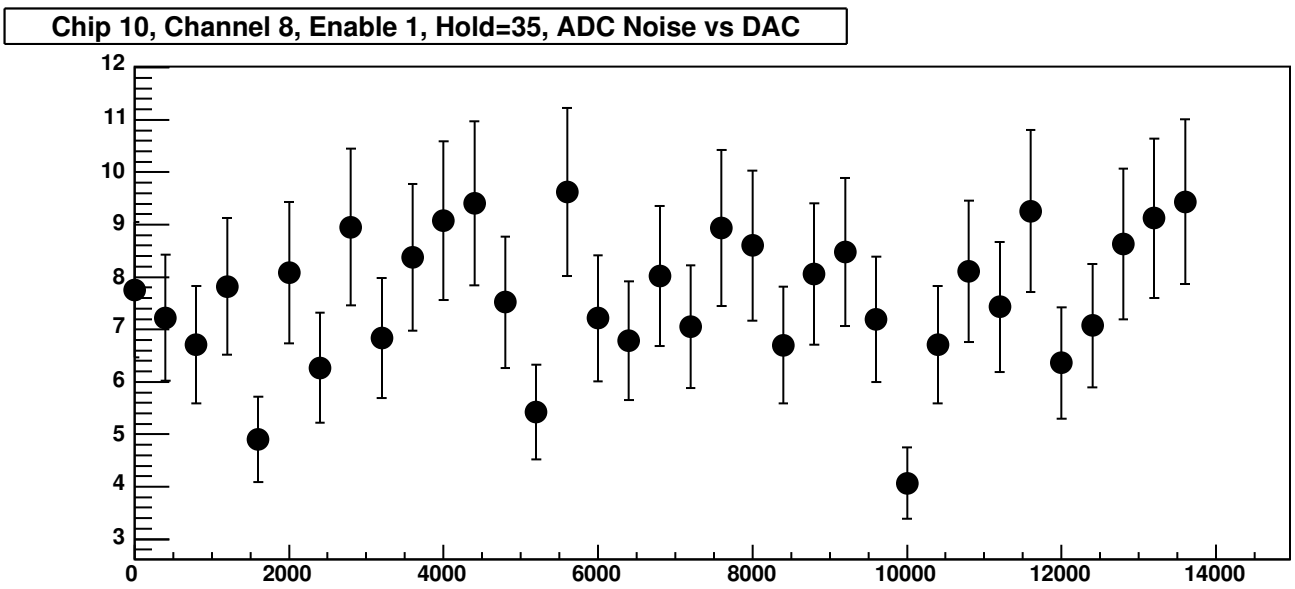

Chip 10, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000

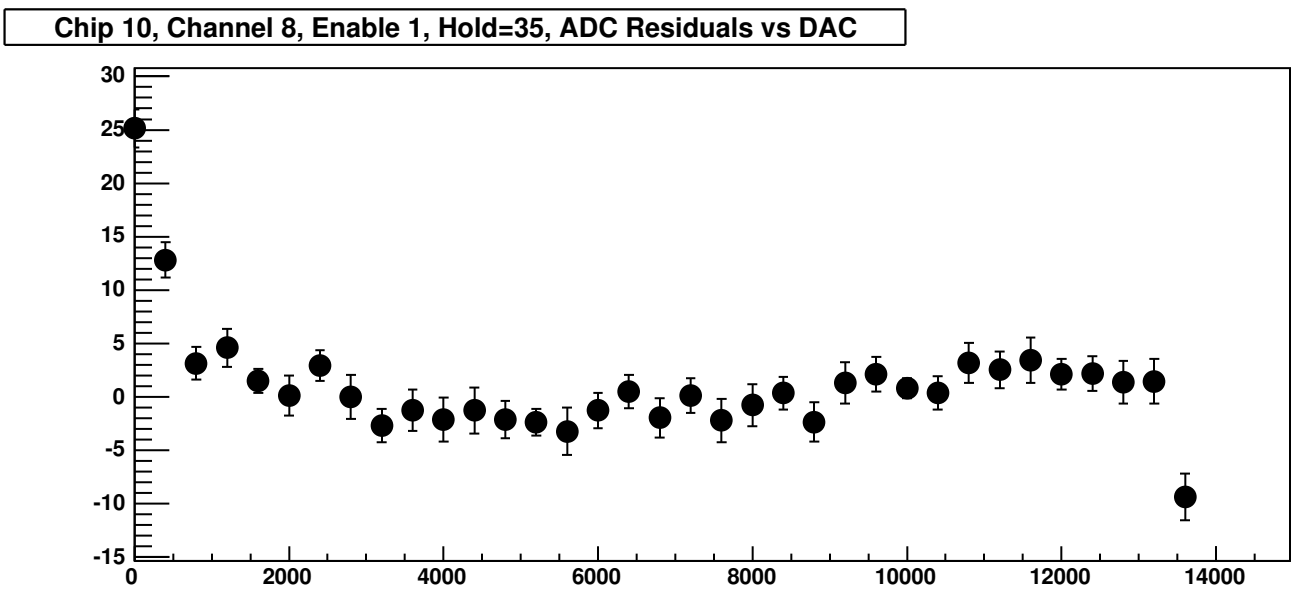

Chip 10, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.324e+05 / 23
p0 -884.7 ± 1.035
p1 0.4388 ± 0.000238
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

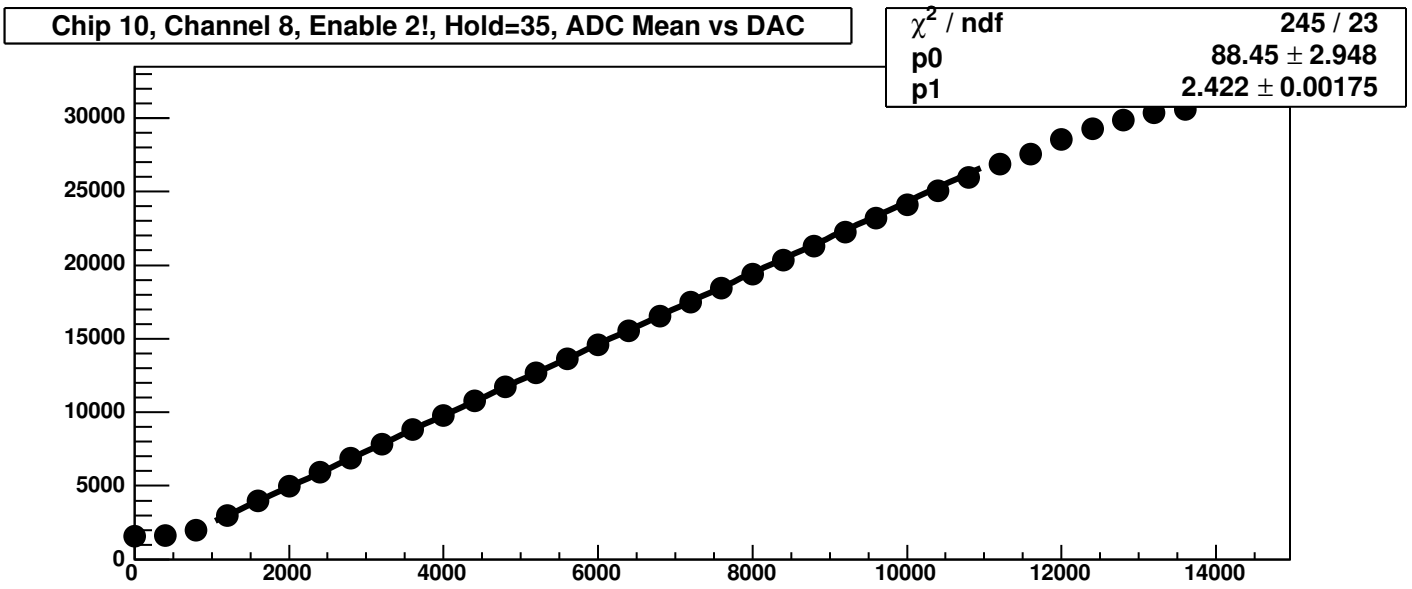

Chip 10, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

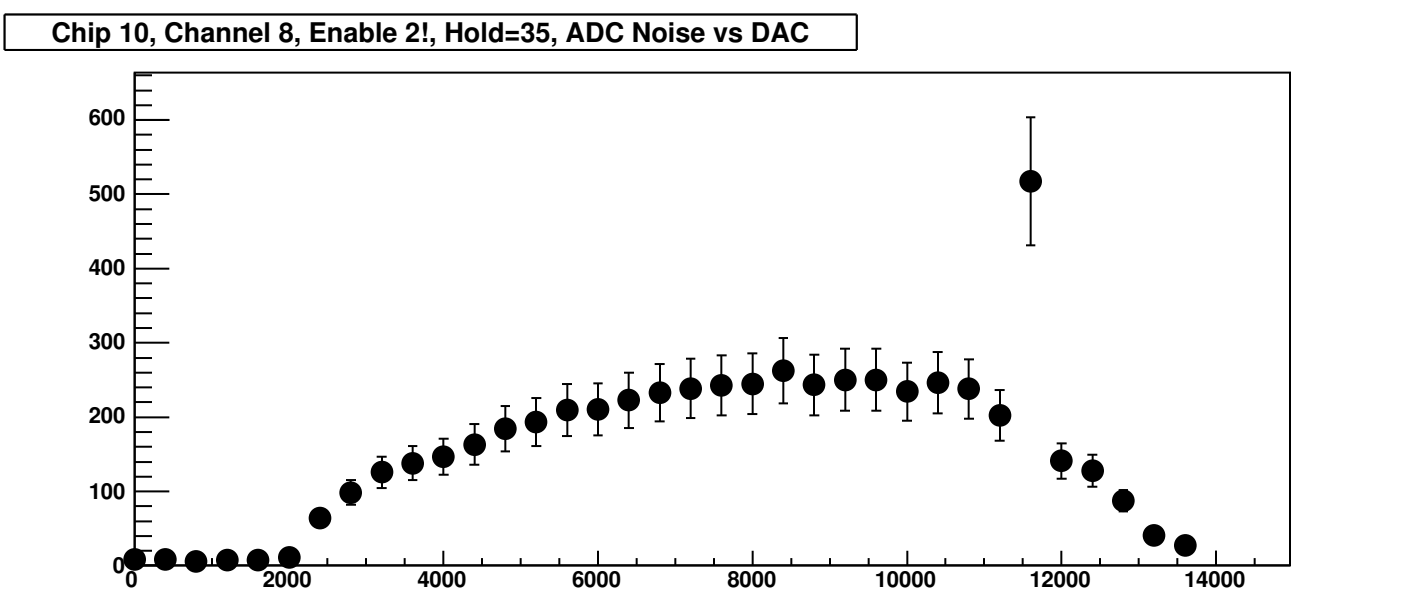

Chip 10, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

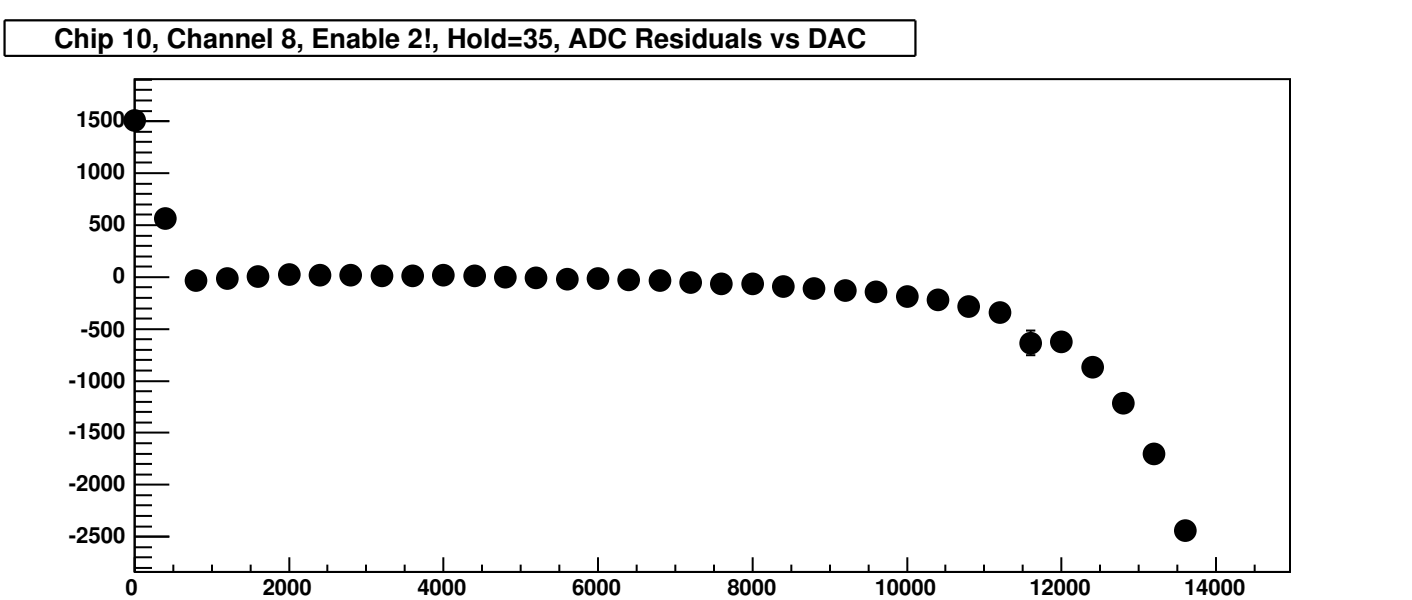

Chip 10, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 35.29 / 23
p0 10.44 ± 0.7219
p1 0.02454 ± 0.0001051
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

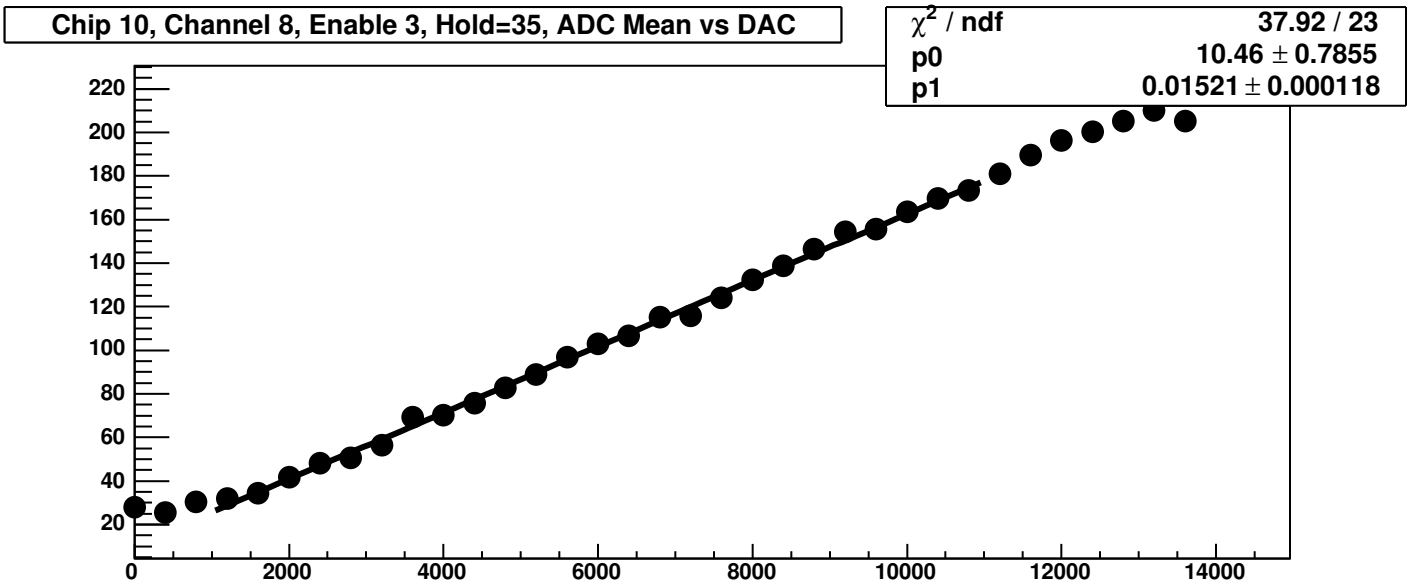

Chip 10, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

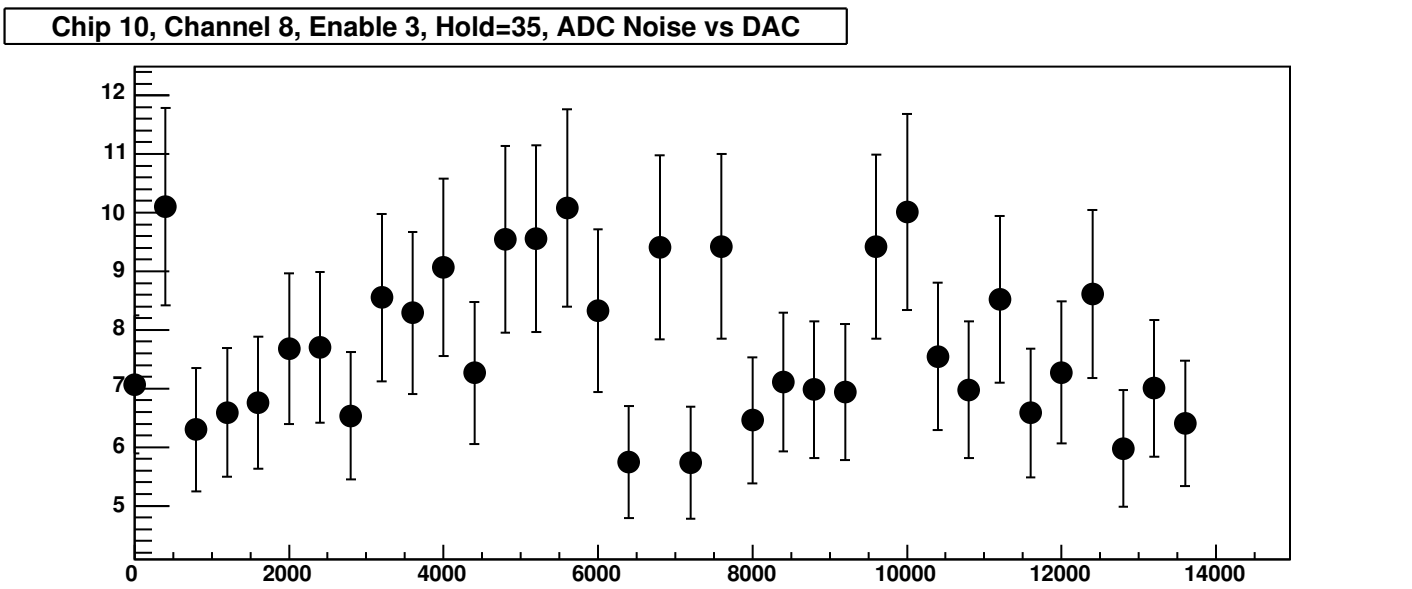

Chip 10, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

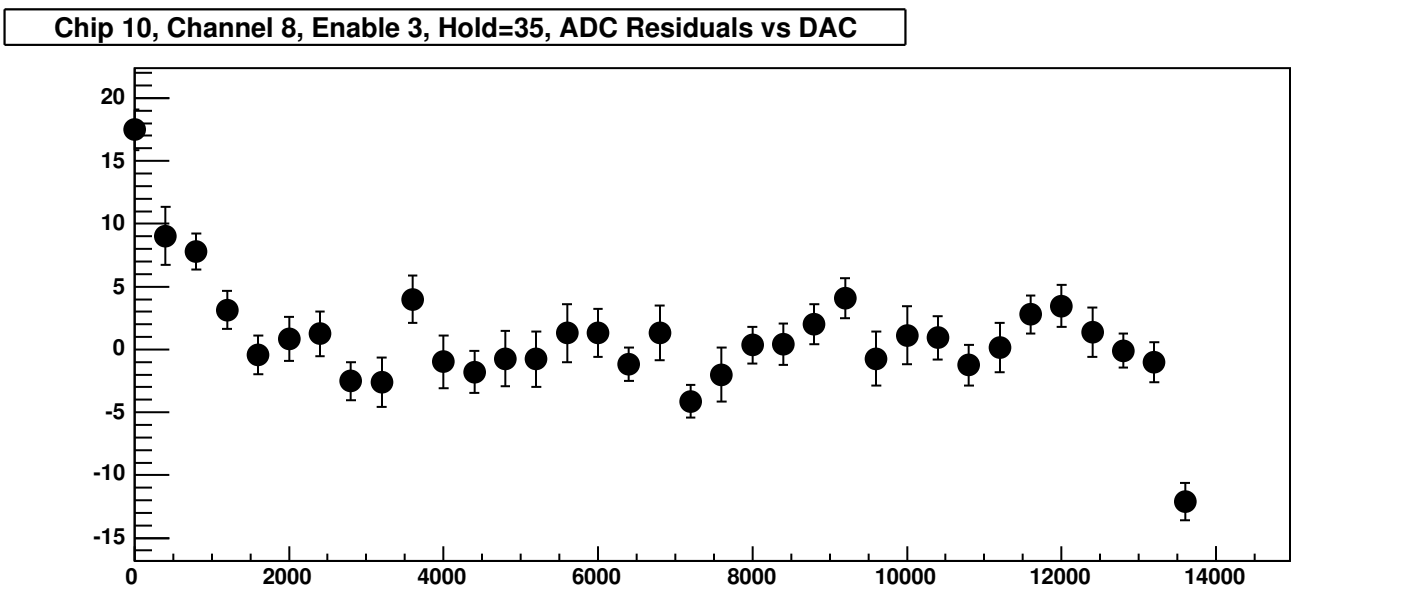

Chip 10, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 245 / 23
p0 88.45 ± 2.948
p1 2.422 ± 0.00175
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

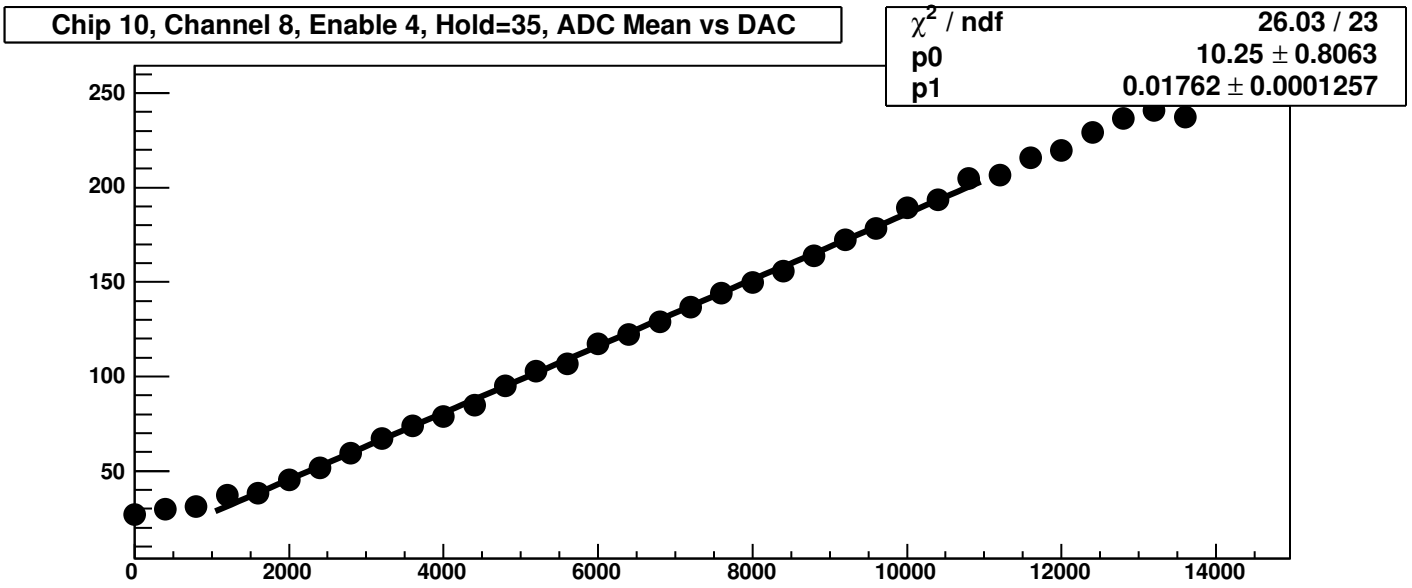

Chip 10, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

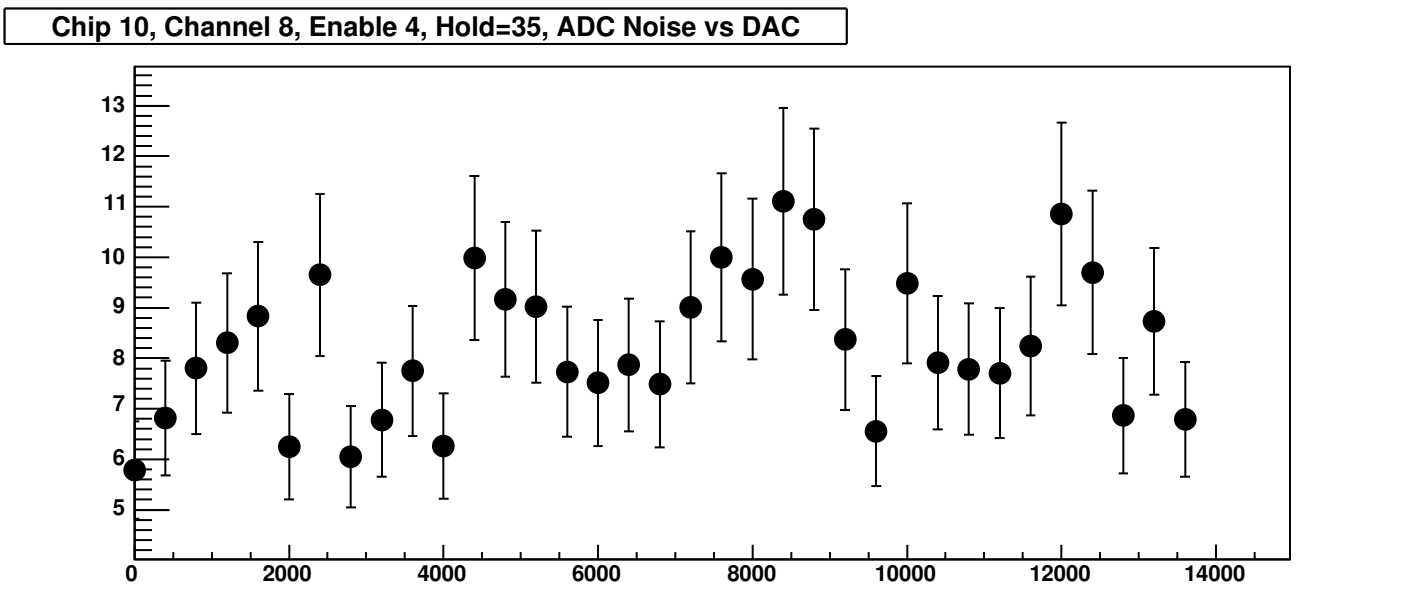

Chip 10, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000

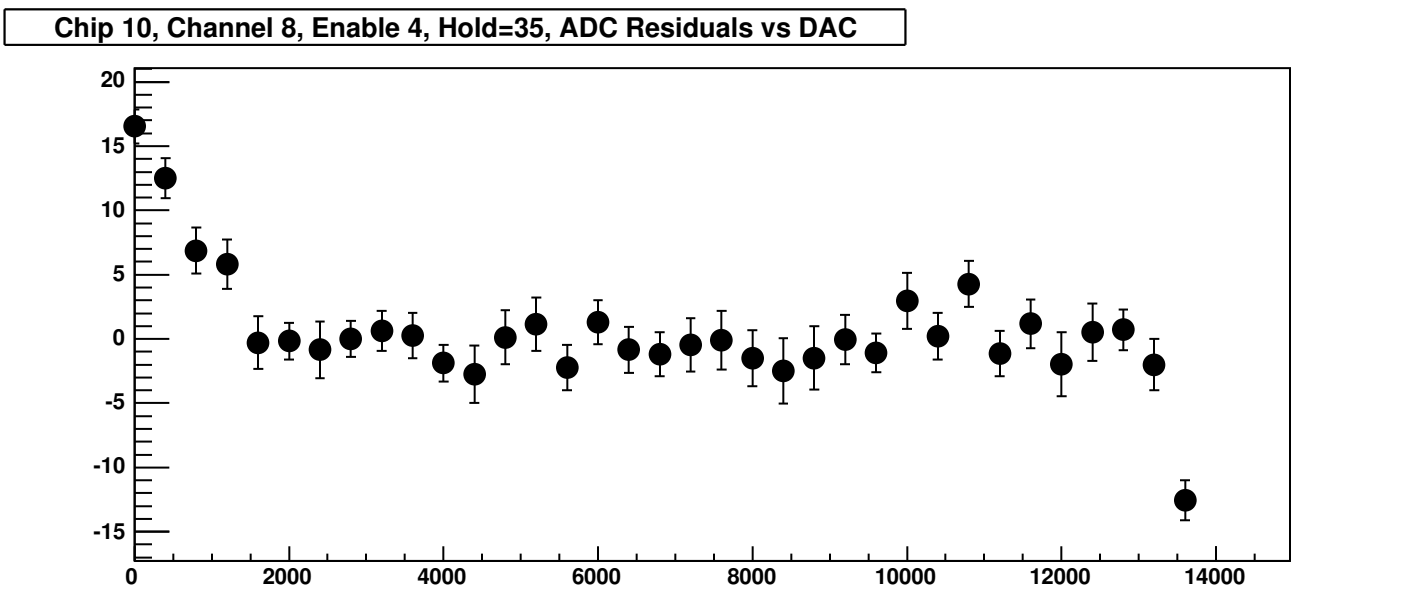

Chip 10, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 37.92 / 23
p0 10.46 ± 0.7855
p1 0.01521 ± 0.000118
220
200
180
160
140
120
100
80
60
40
20
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

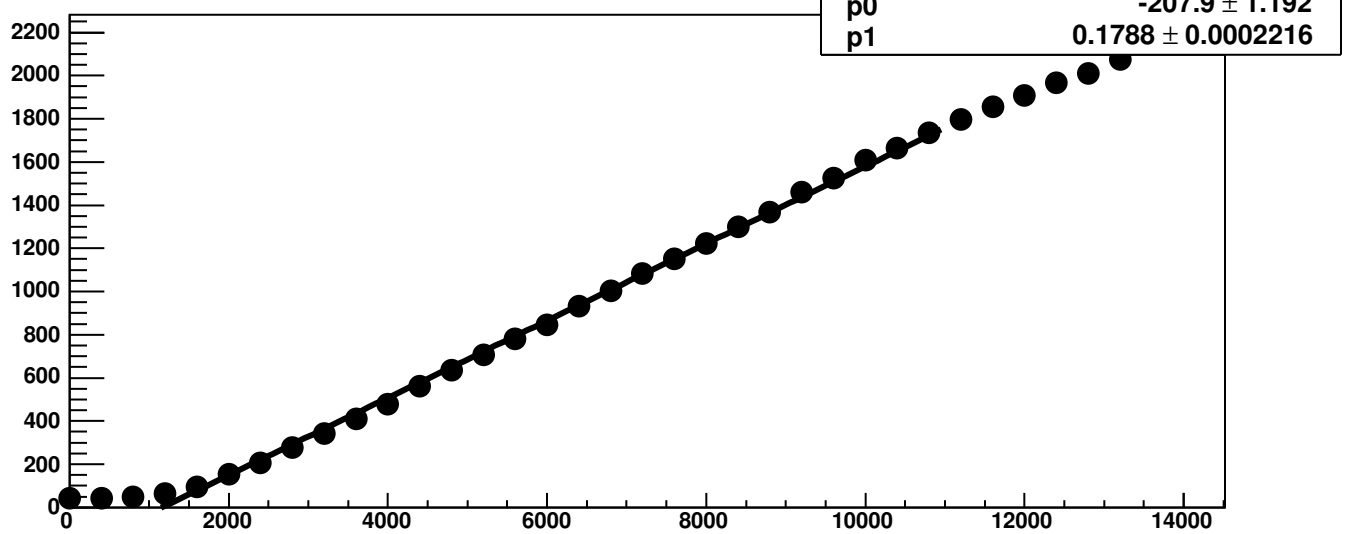

Chip 10, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.03 / 23
p0 10.25 ± 0.8063
p1 0.01762 ± 0.0001257
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

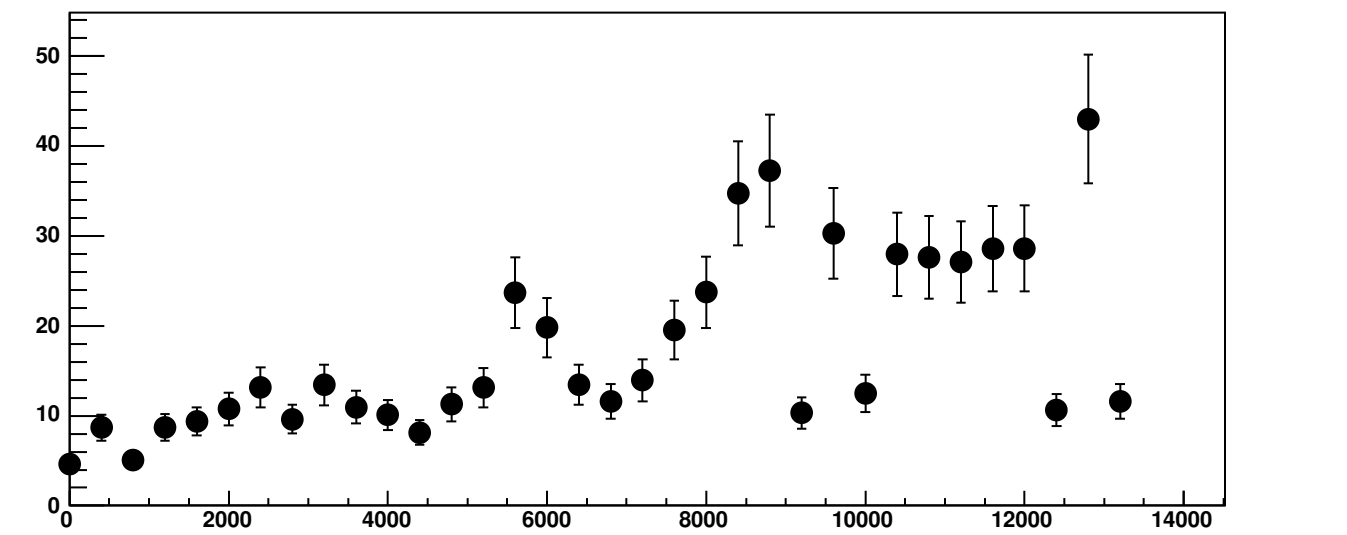

Chip 10, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

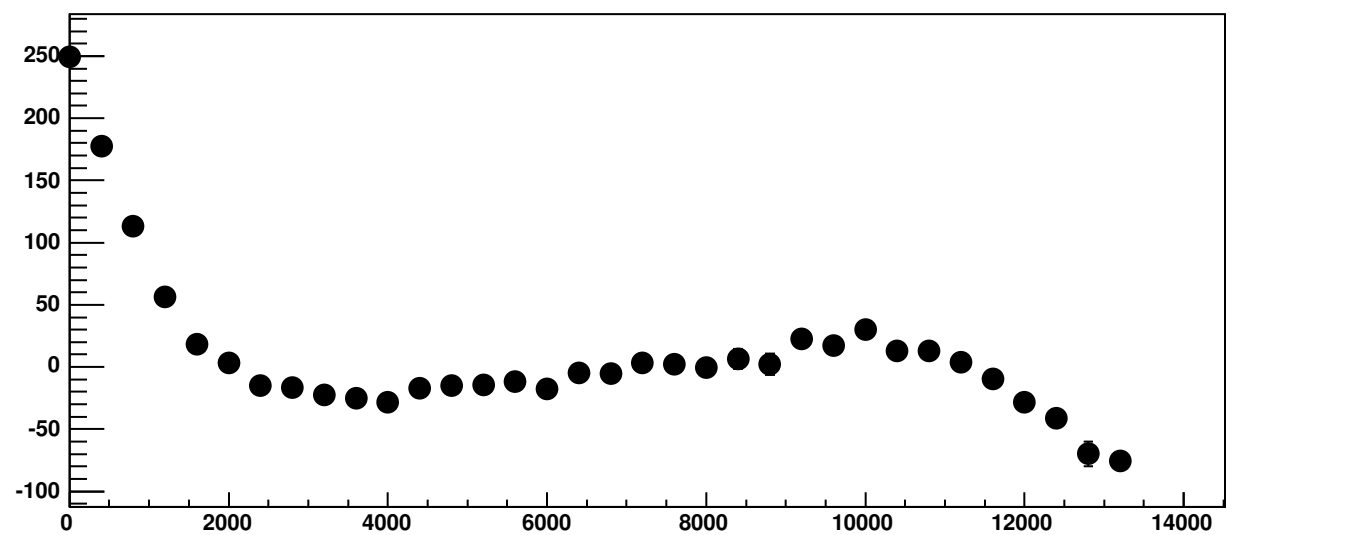
Chip 10, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC

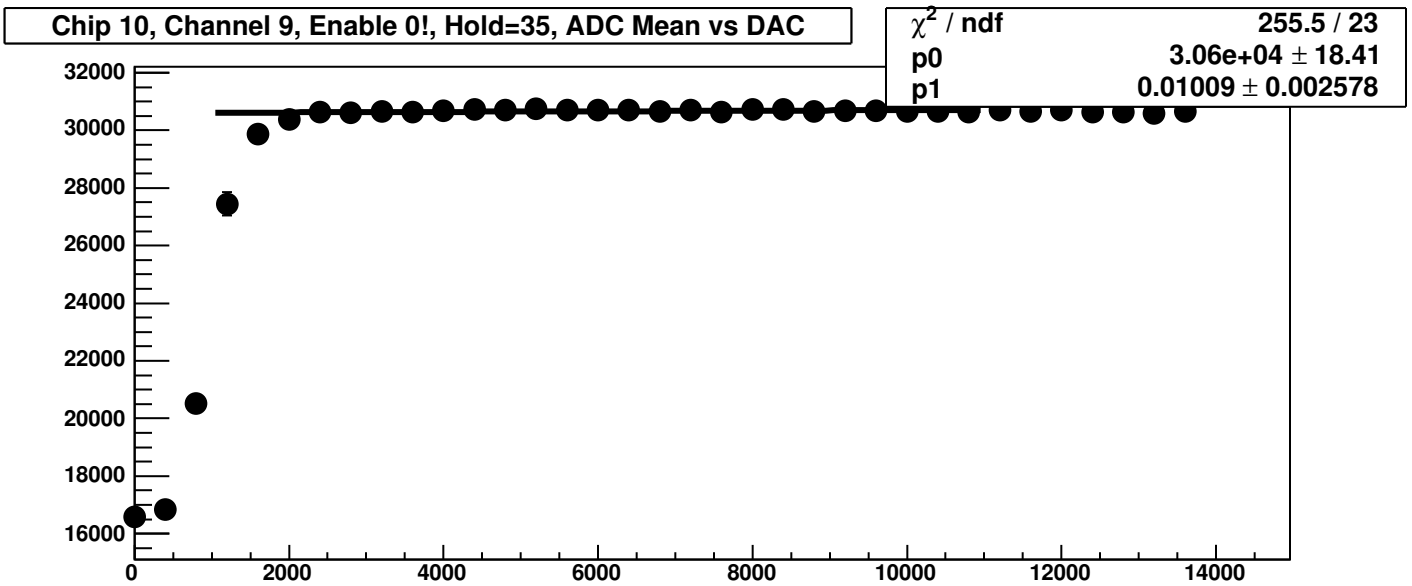


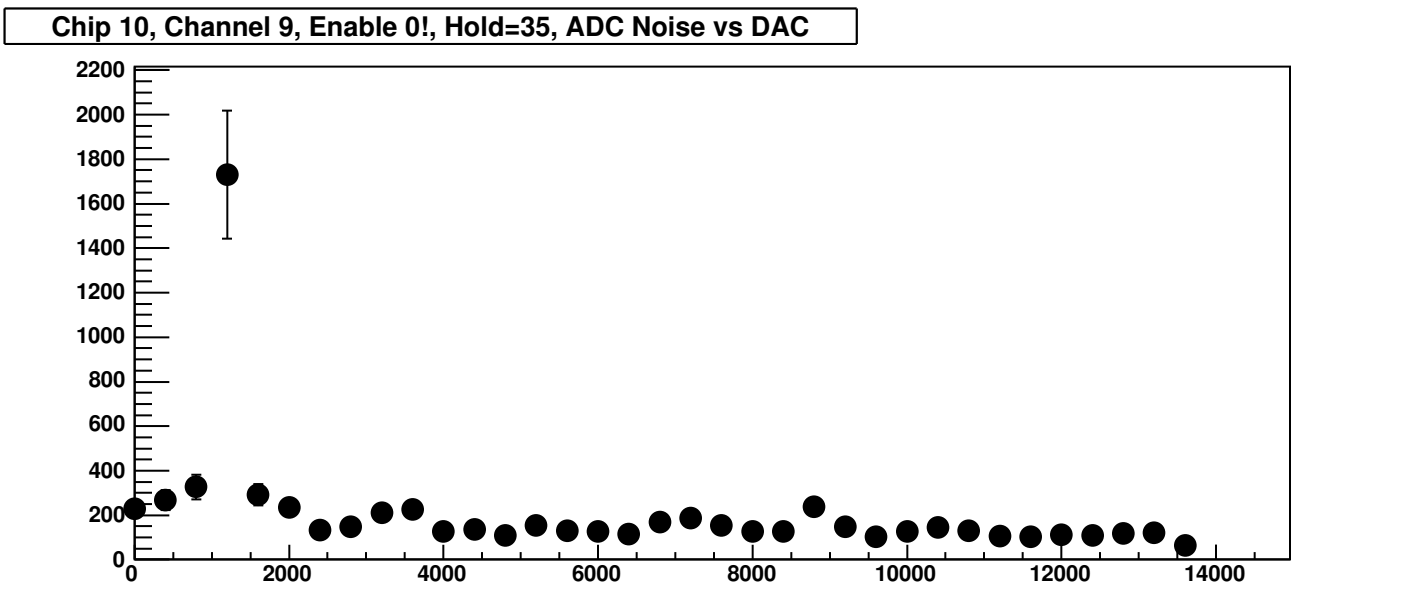
Chip 10, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC

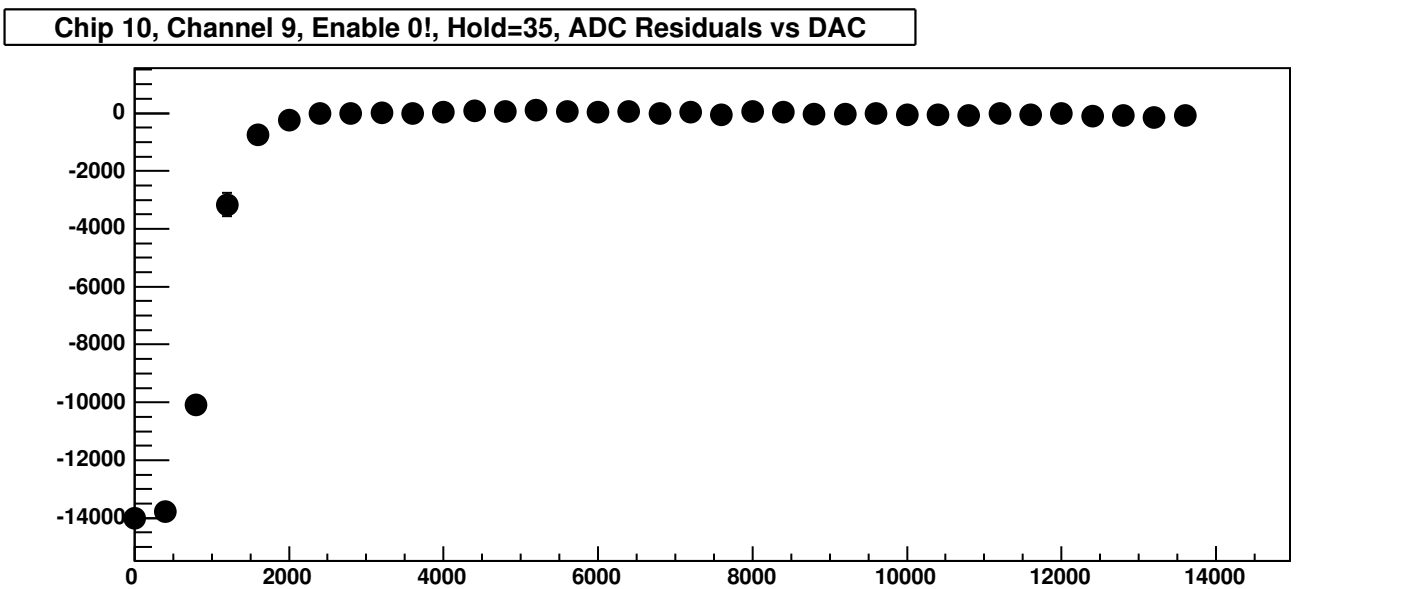


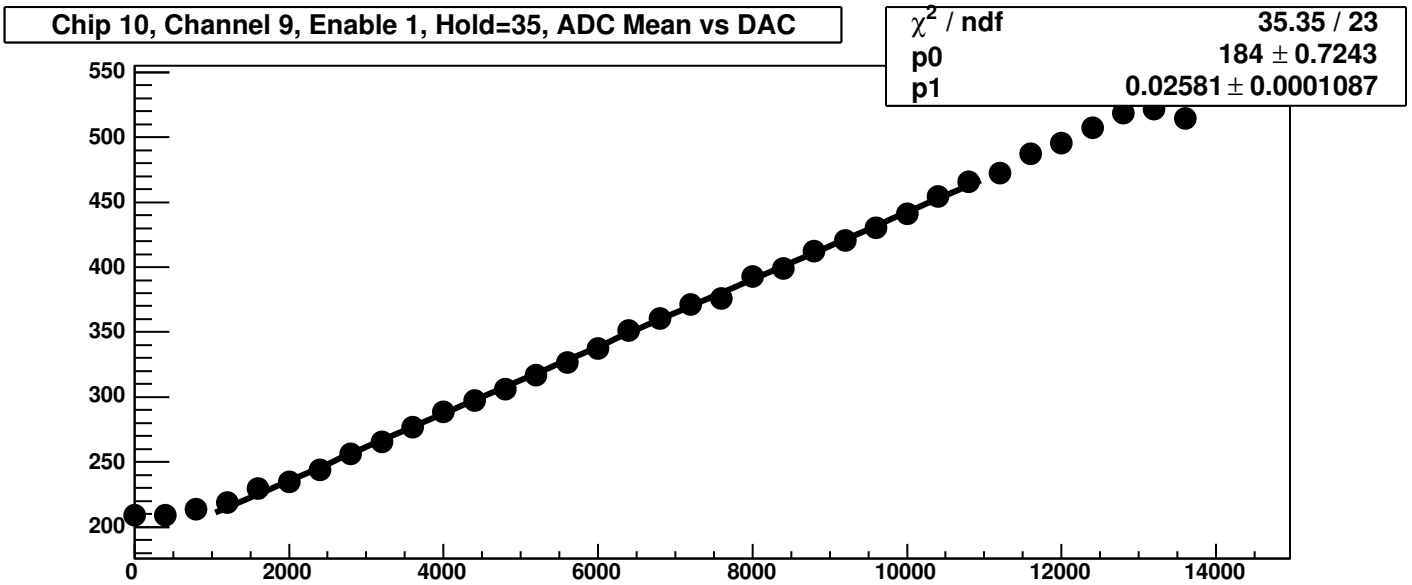
Chip 10, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC

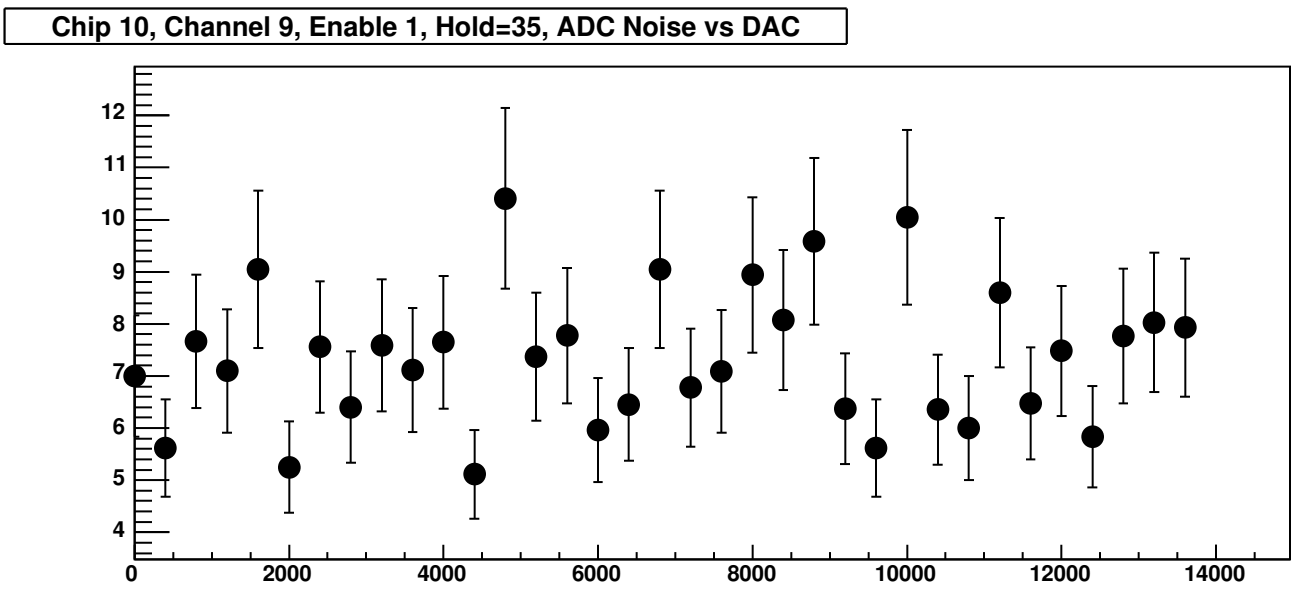


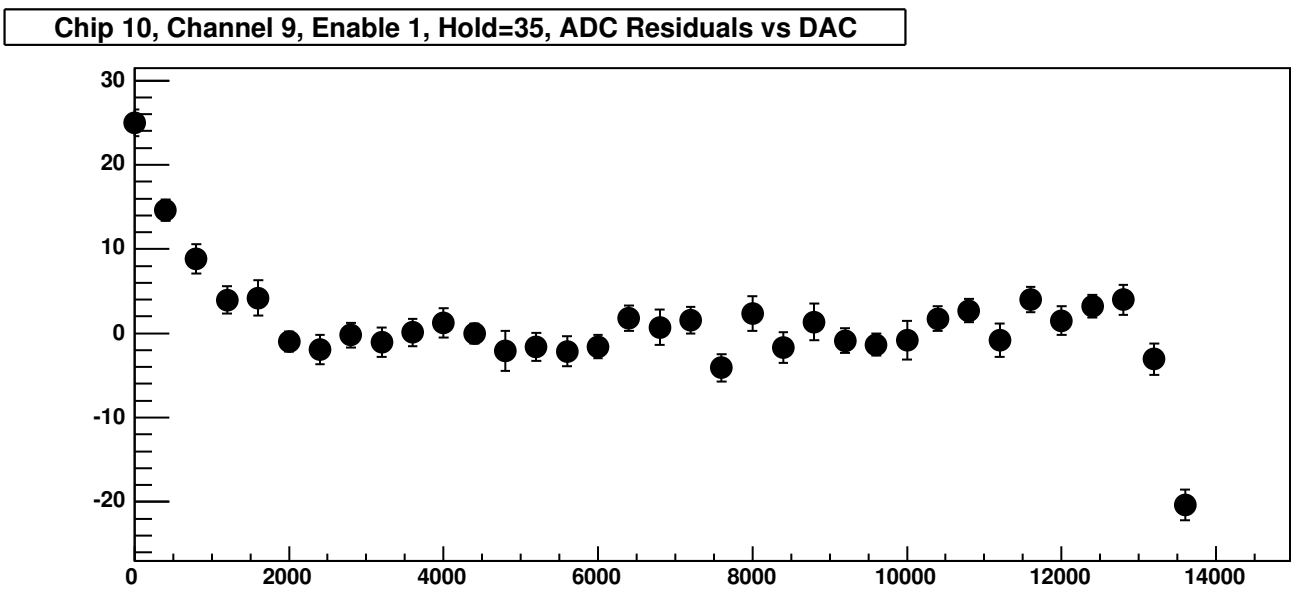

Chip 10, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 255.5 / 23
p0 3.06e+04 ± 18.41
p1 0.01009 ± 0.002578
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

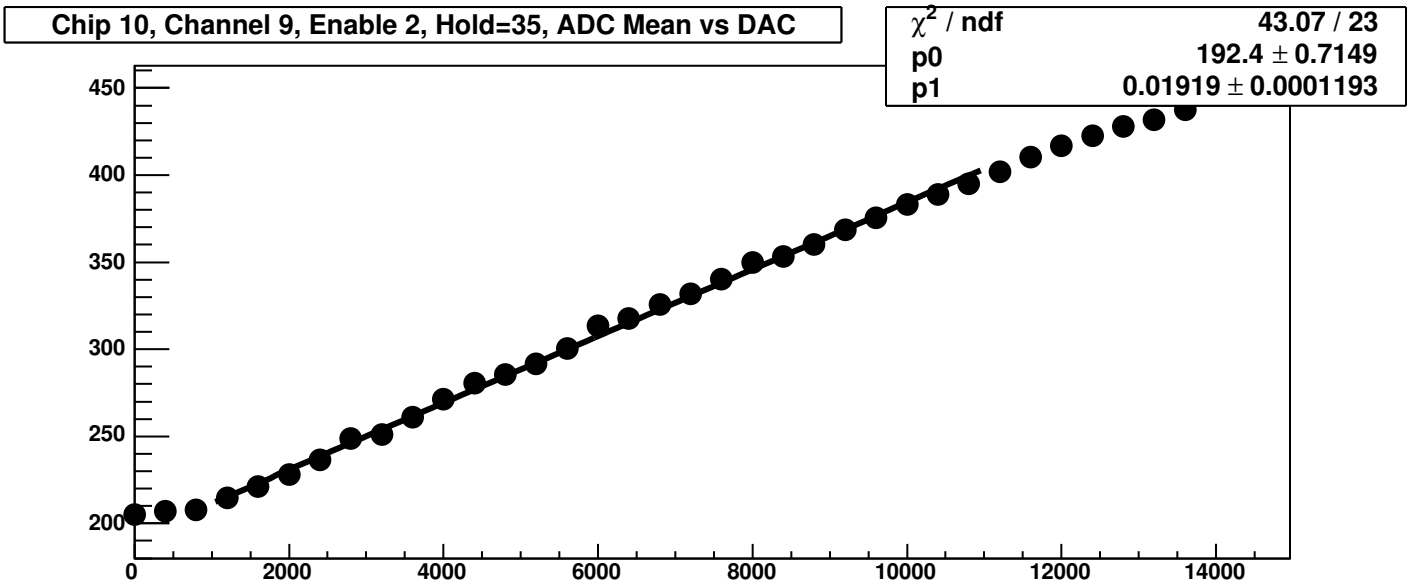

Chip 10, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

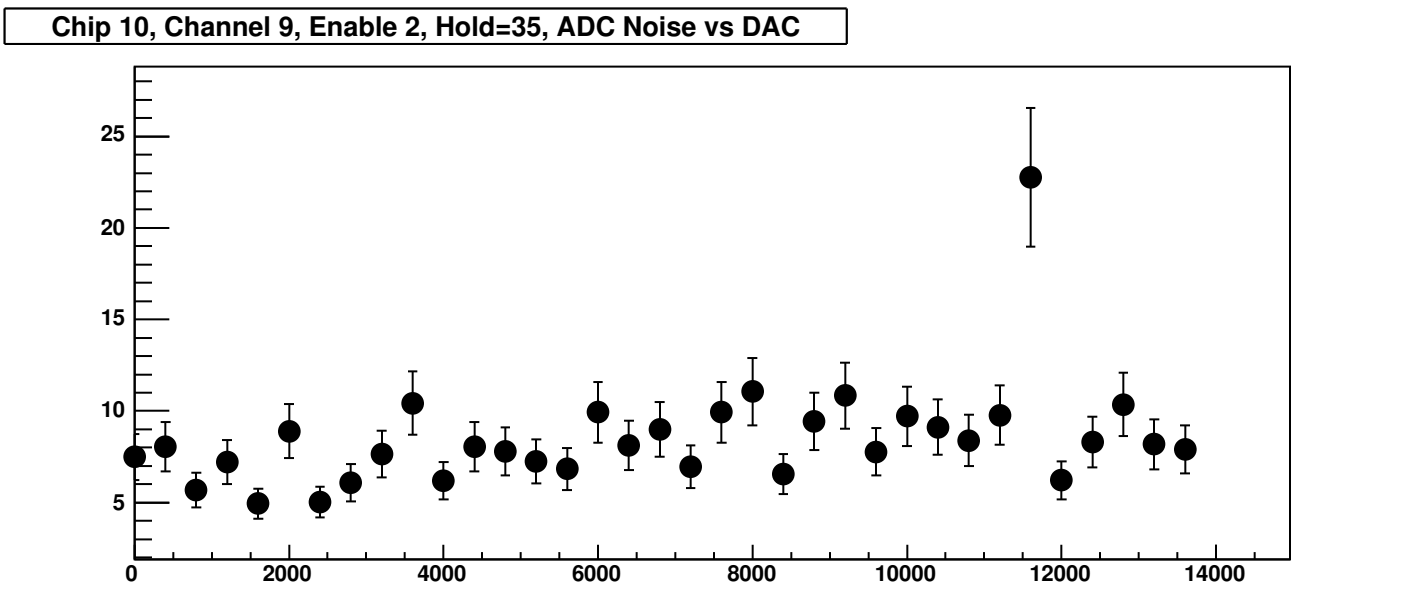

Chip 10, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

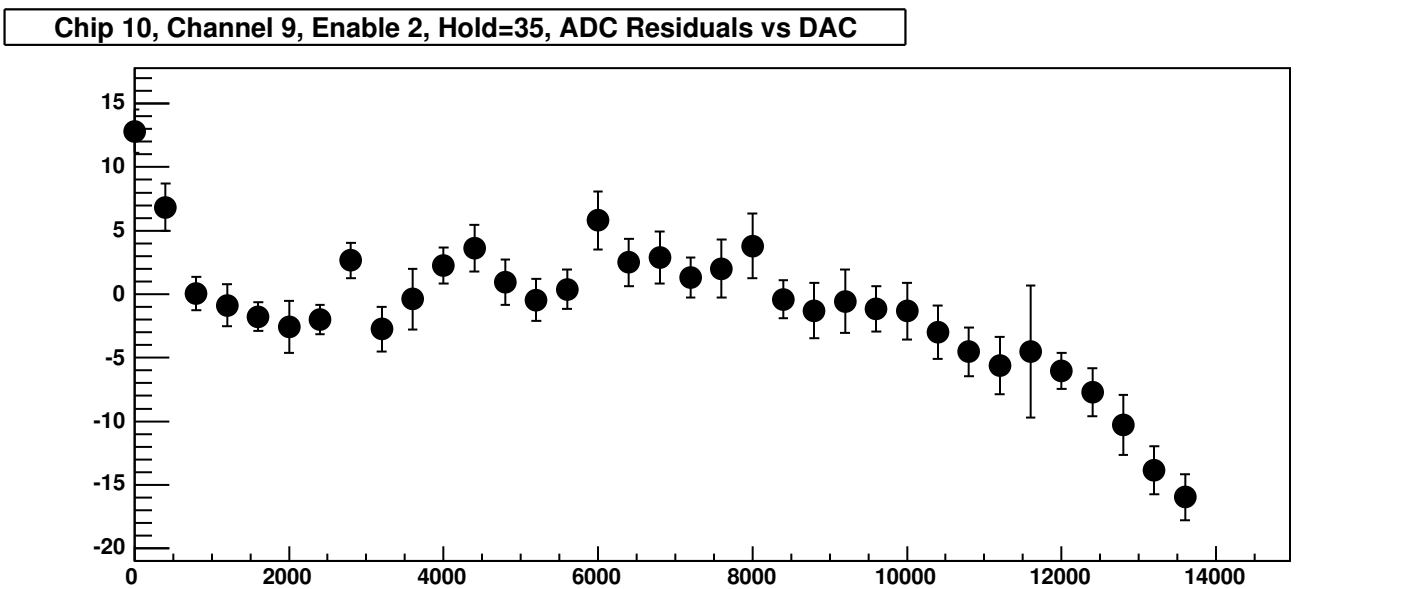

Chip 10, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 35.35 / 23
p0 184 ± 0.7243
p1 0.02581 ± 0.0001087
550
500
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

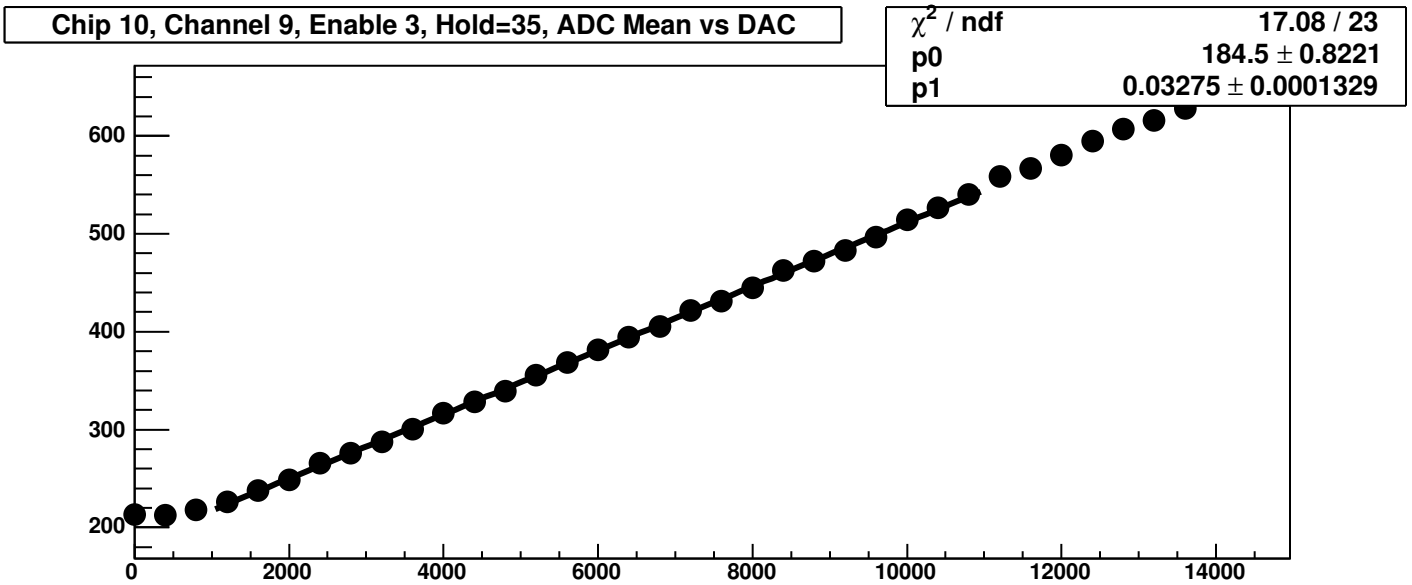

Chip 10, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

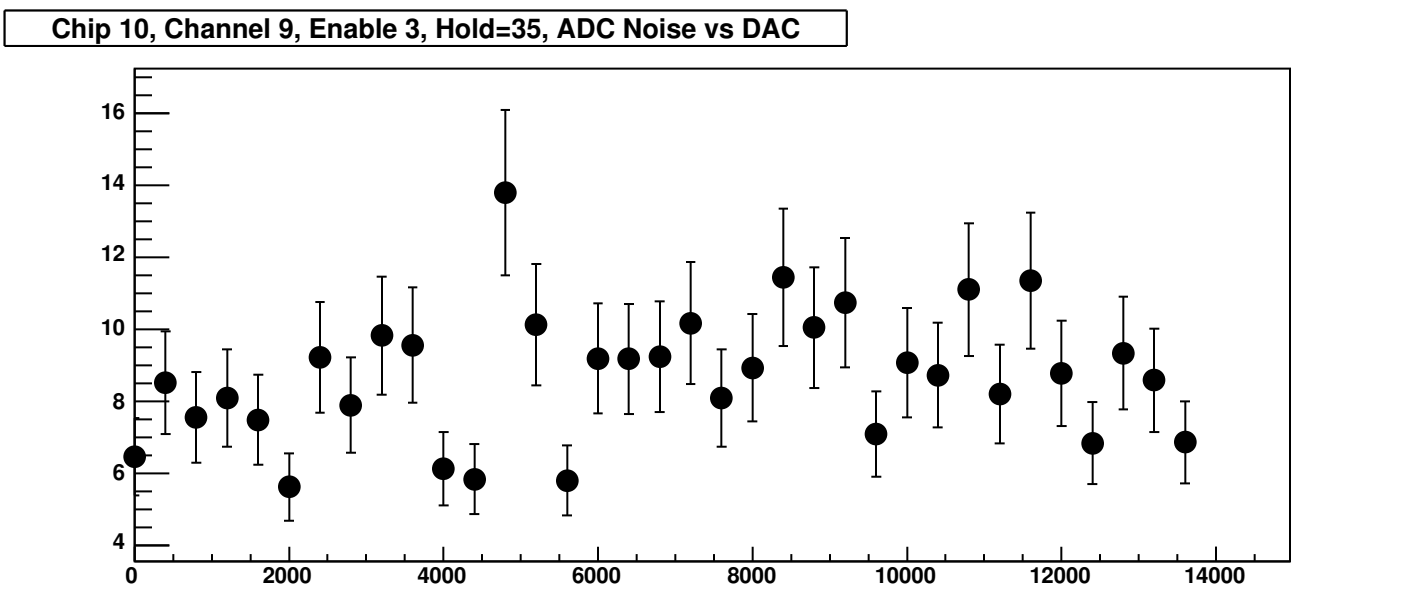

Chip 10, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

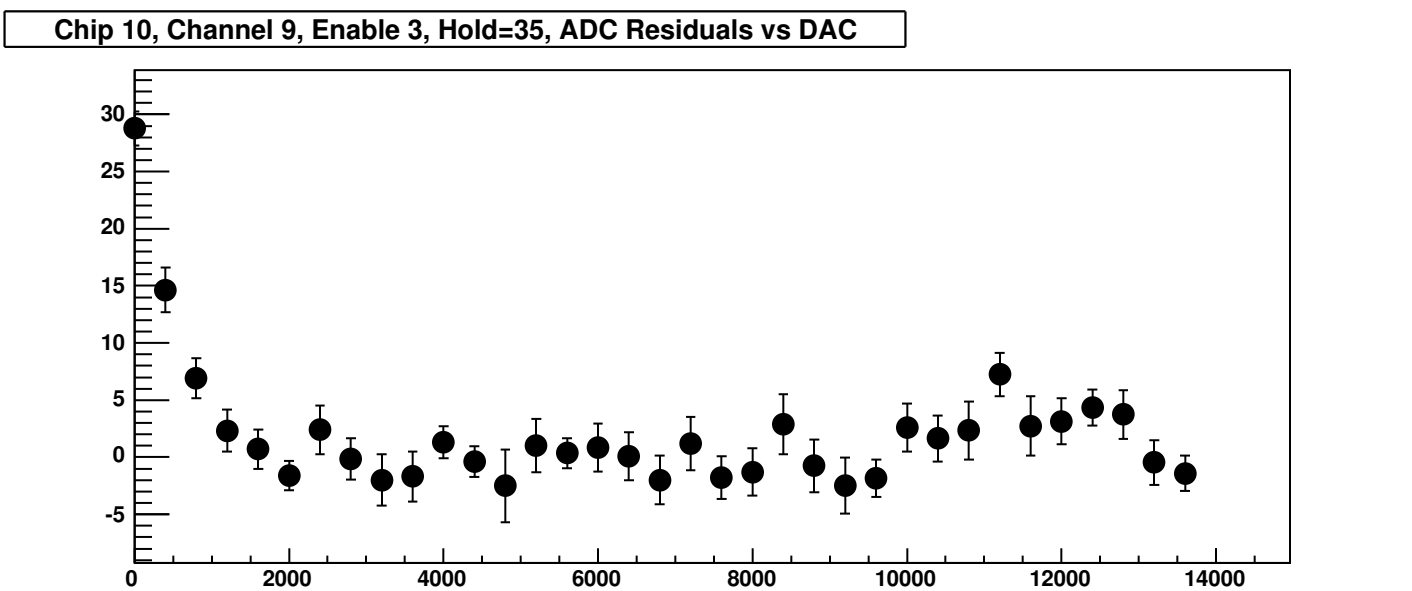

Chip 10, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 43.07 / 23
p0 192.4 ± 0.7149
p1 0.01919 ± 0.0001193
450
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

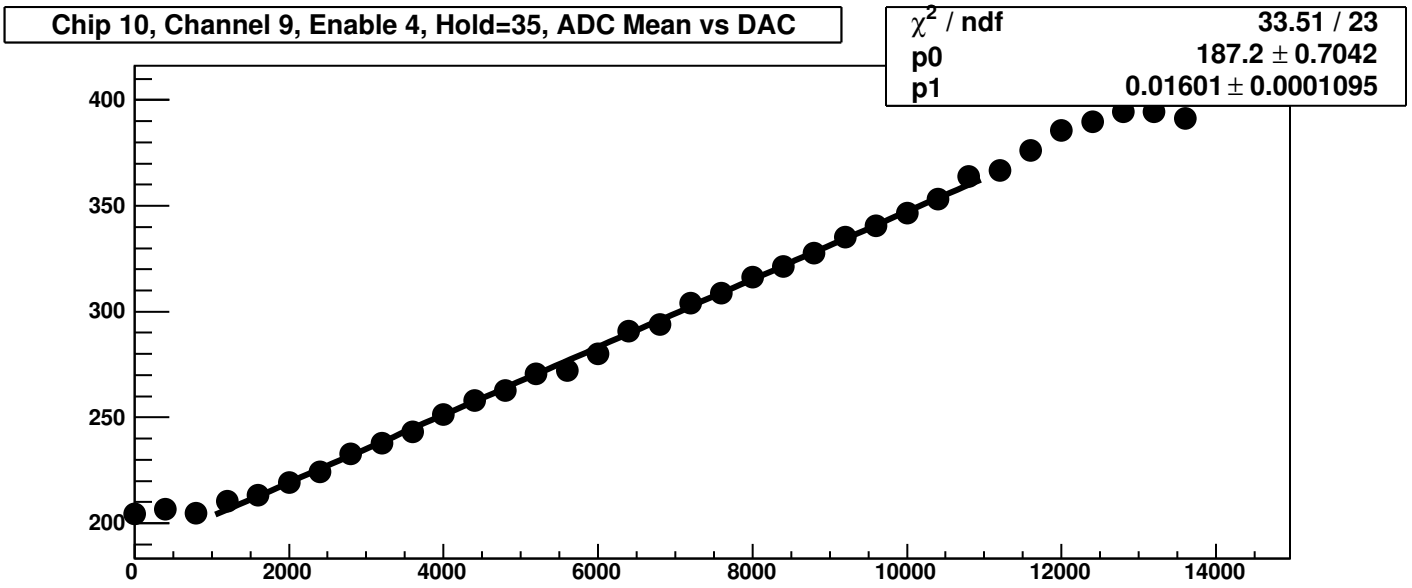

Chip 10, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

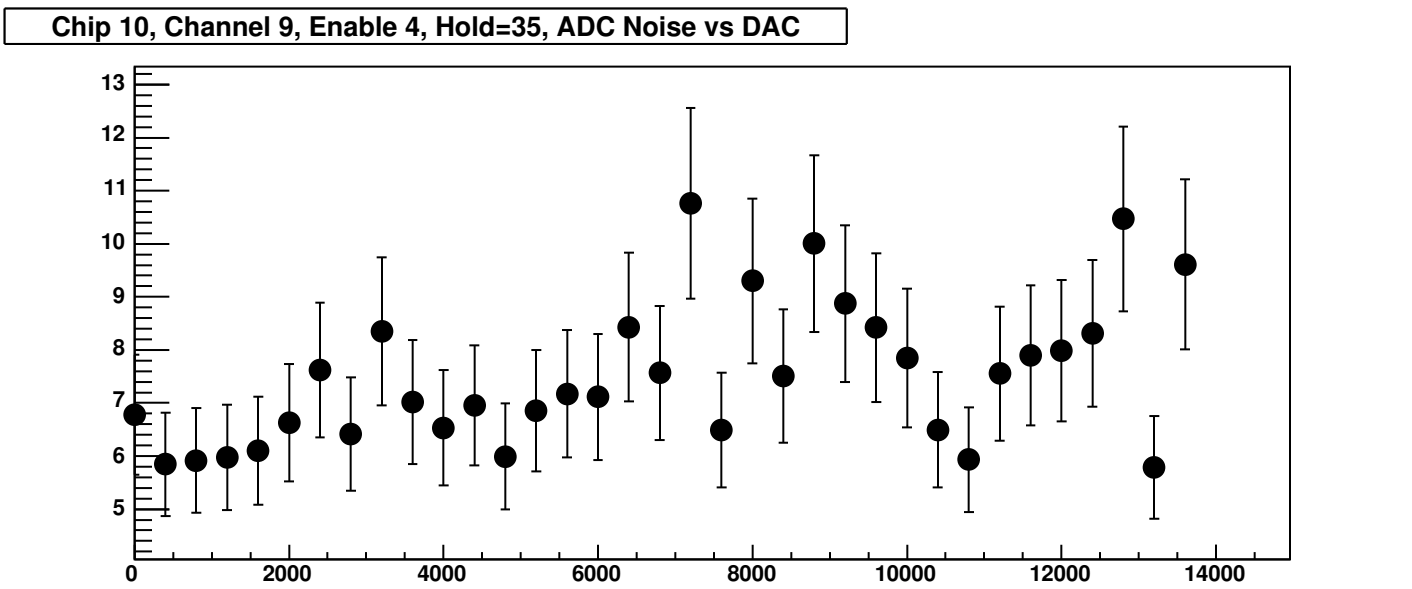

Chip 10, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

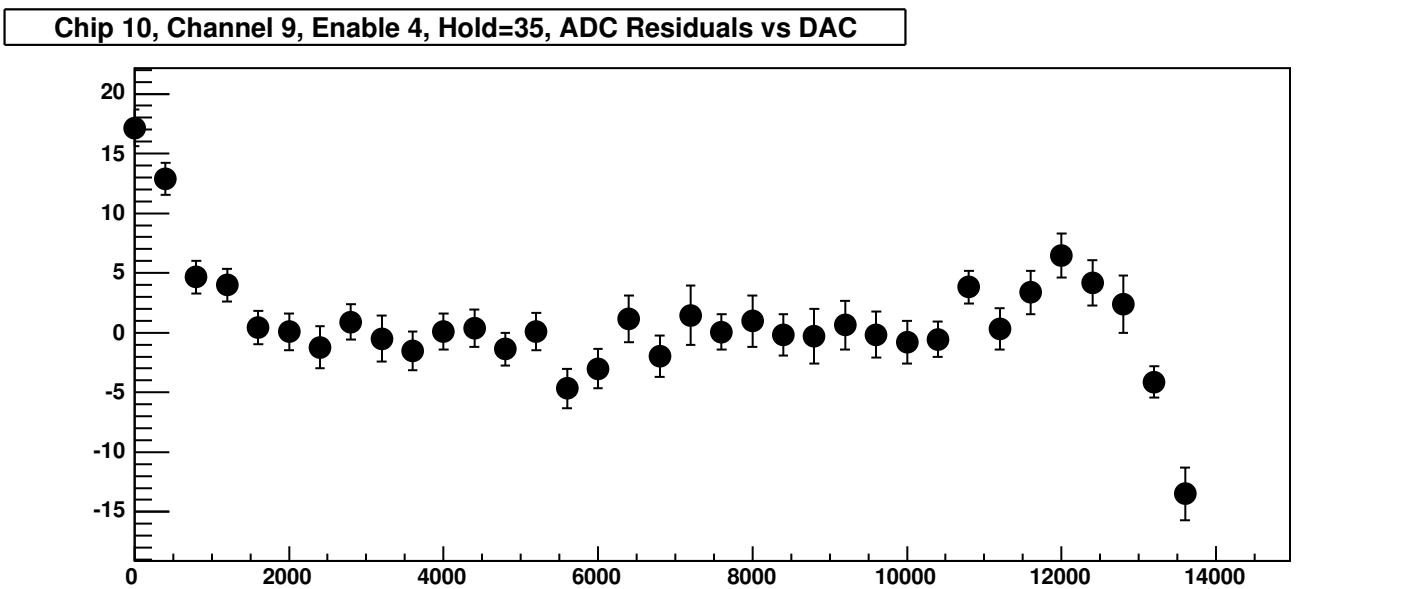

Chip 10, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 17.08 / 23
p0 184.5 ± 0.8221
p1 0.03275 ± 0.0001329
600
500
400
300
200
0
2000
4000
6000
8000
10000
12000
14000

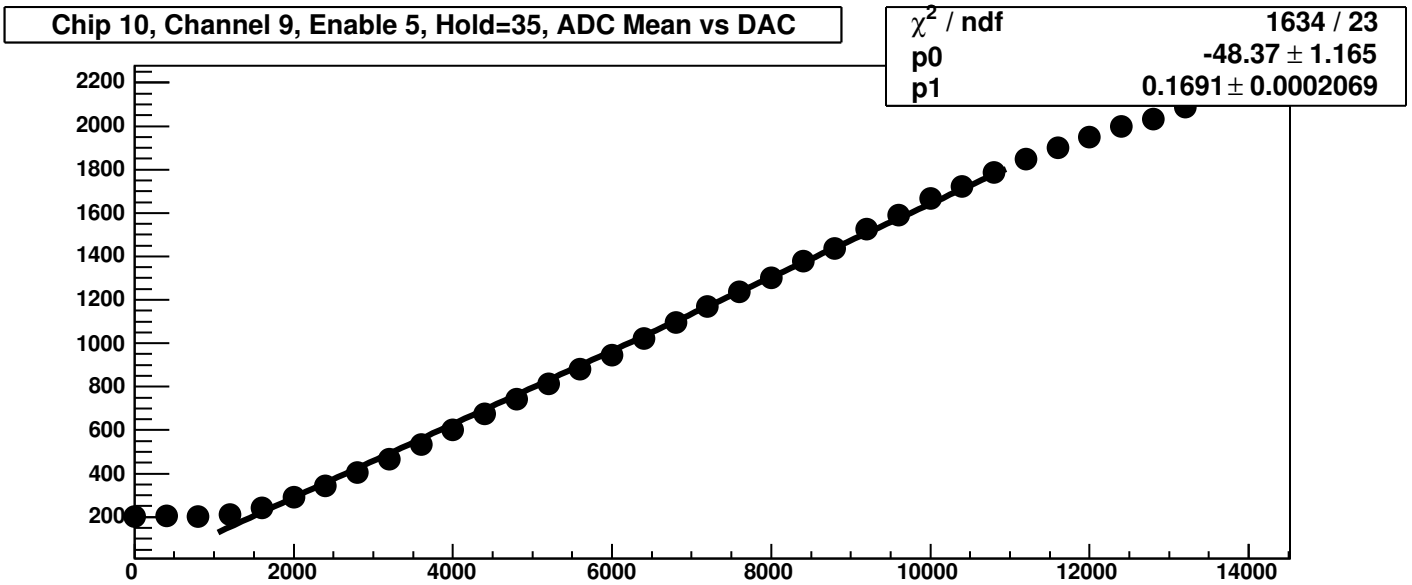

Chip 10, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

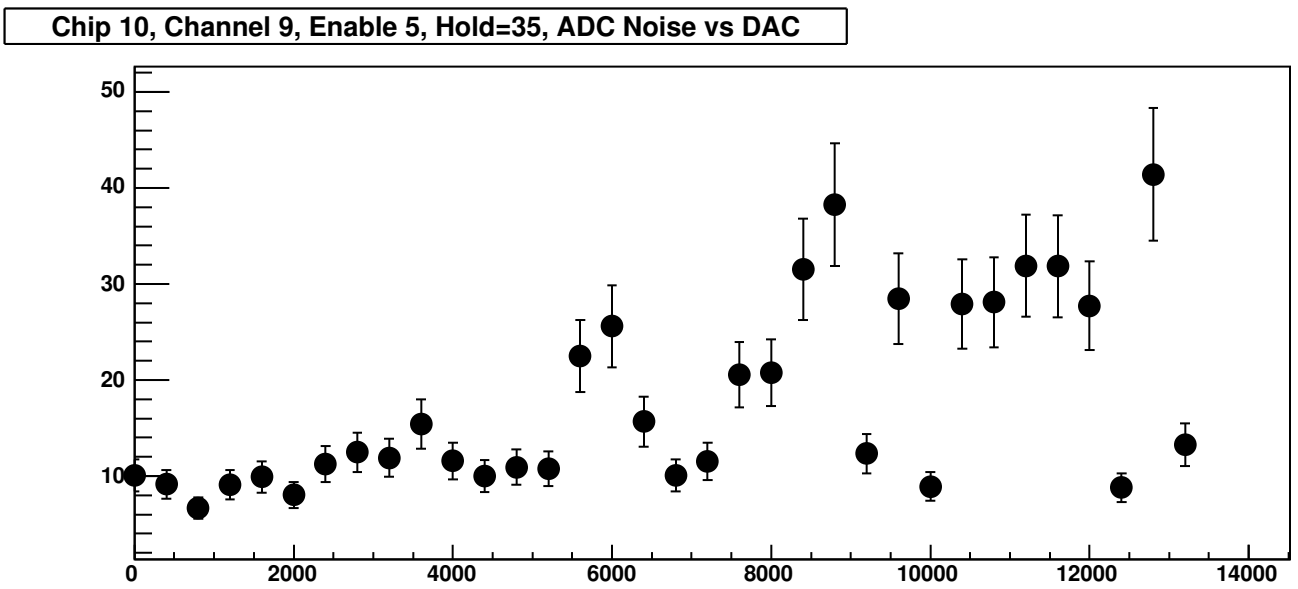

Chip 10, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
30
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

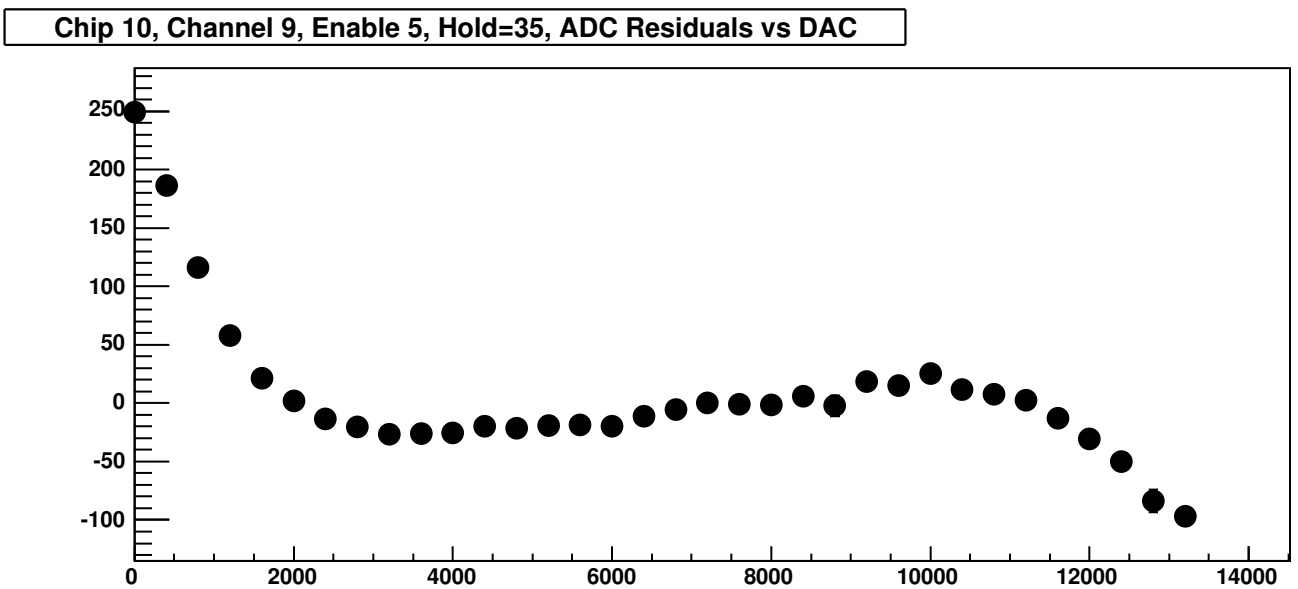

Chip 10, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 33.51 / 23
p0 187.2 ± 0.7042
p1 0.01601 ± 0.0001095
400
350
300
250
200
0
2000
4000
6000
8000
10000
12000
14000

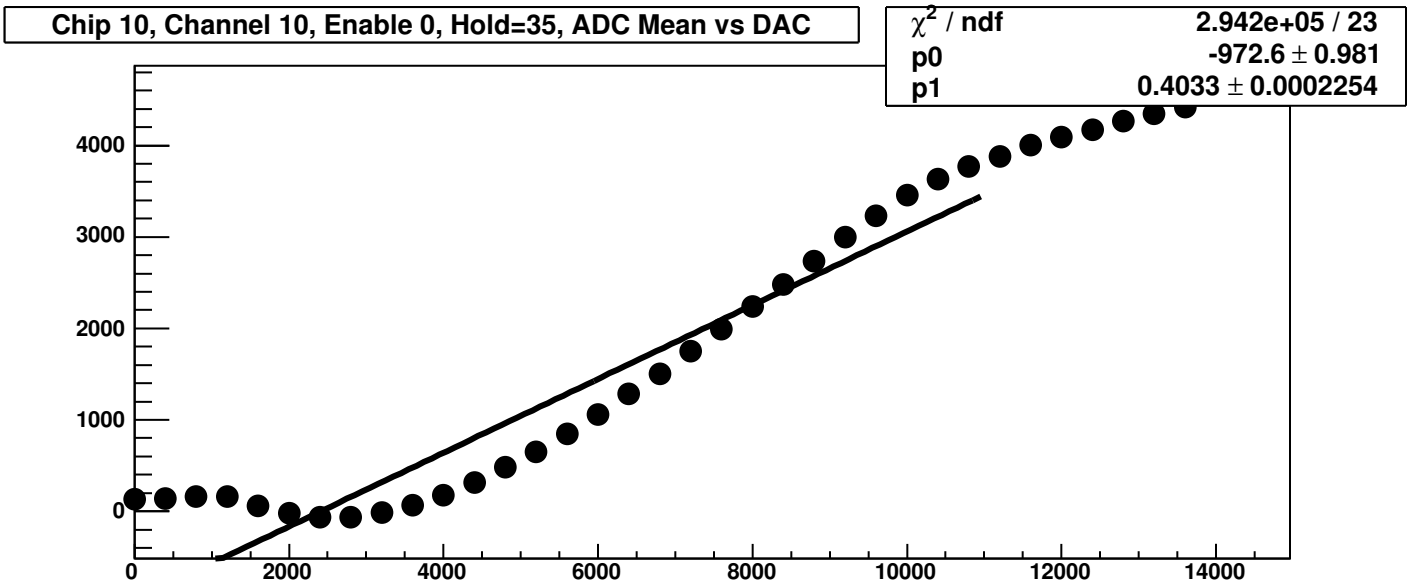

Chip 10, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

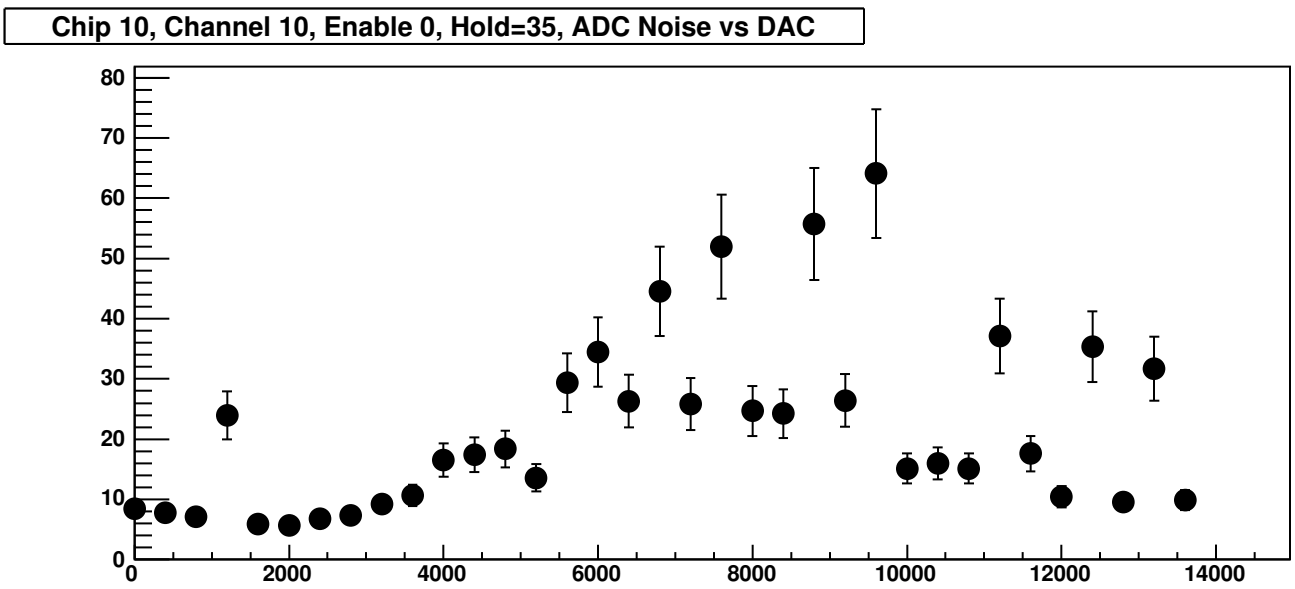

Chip 10, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

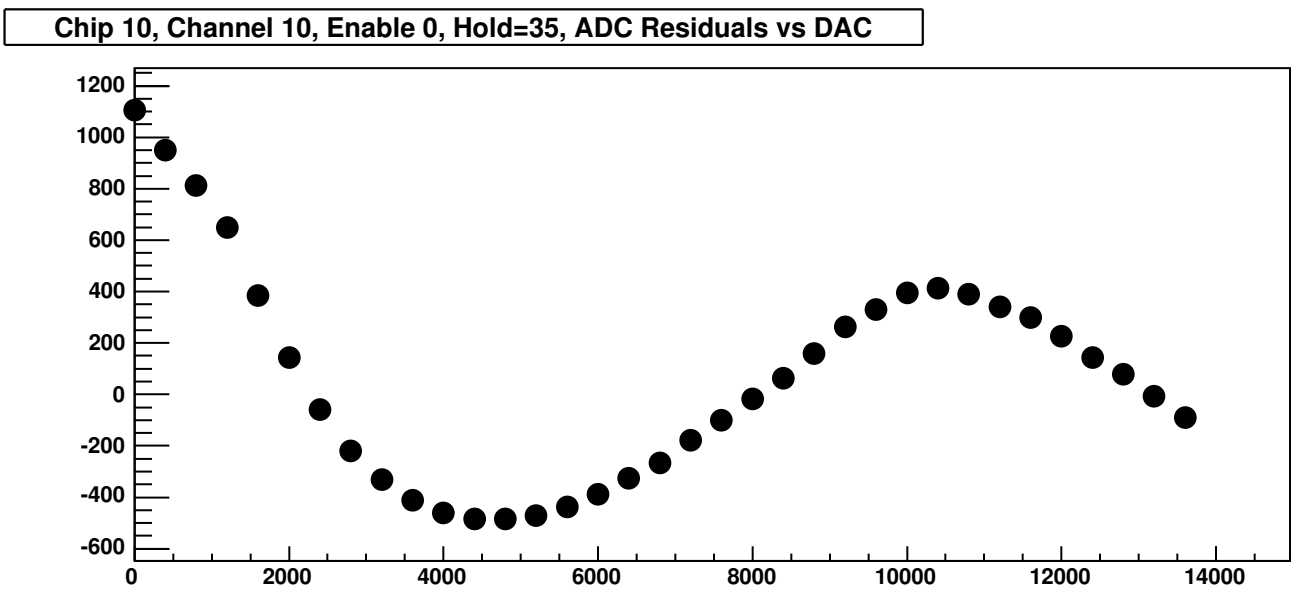

Chip 10, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1634 / 23
p0 -48.37 ± 1.165
p1 0.1691 ± 0.0002069
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
2000
4000
6000
8000
10000
12000
14000

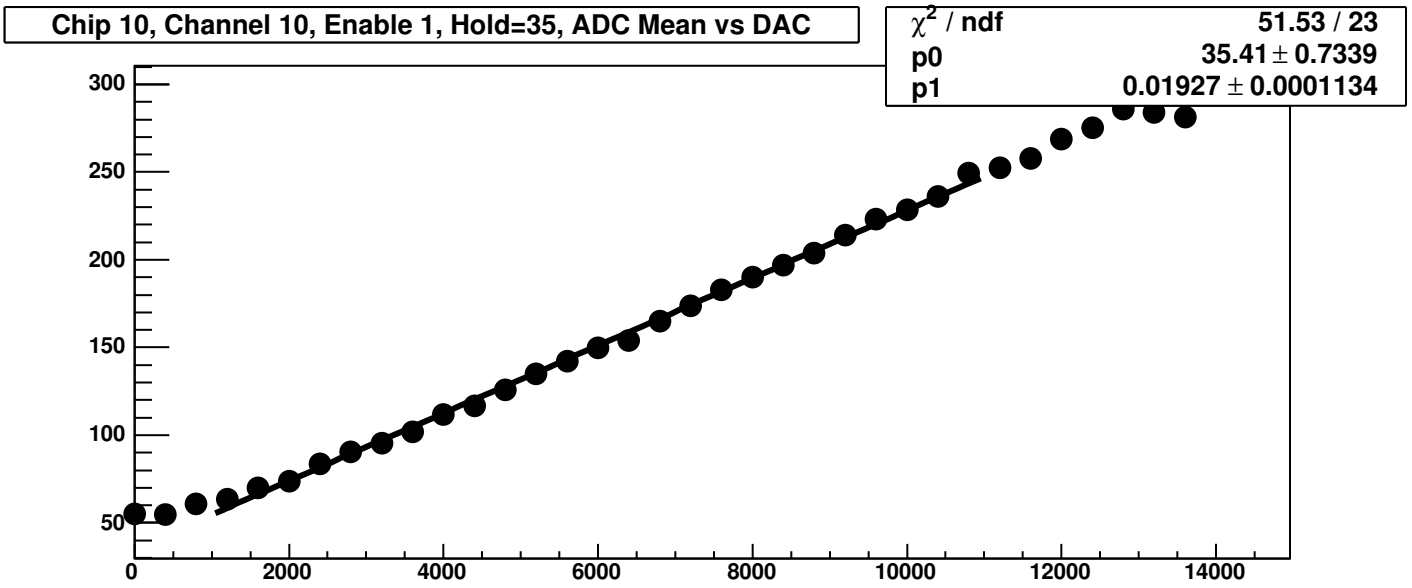

Chip 10, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

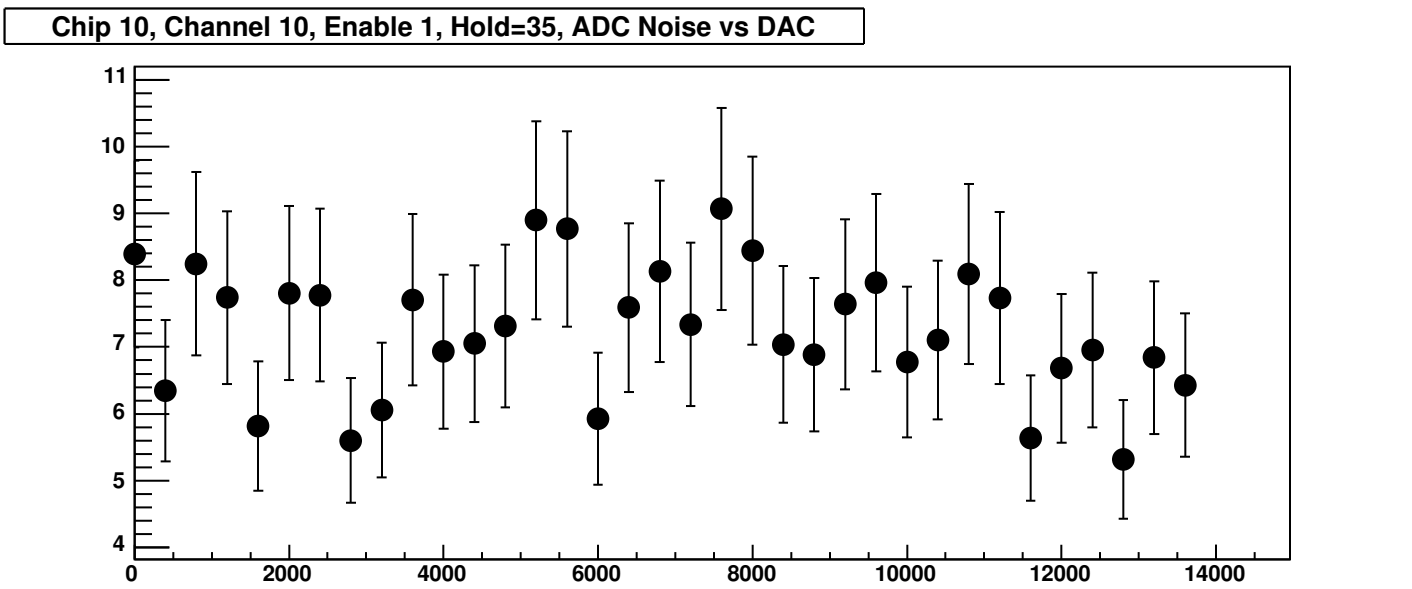

Chip 10, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

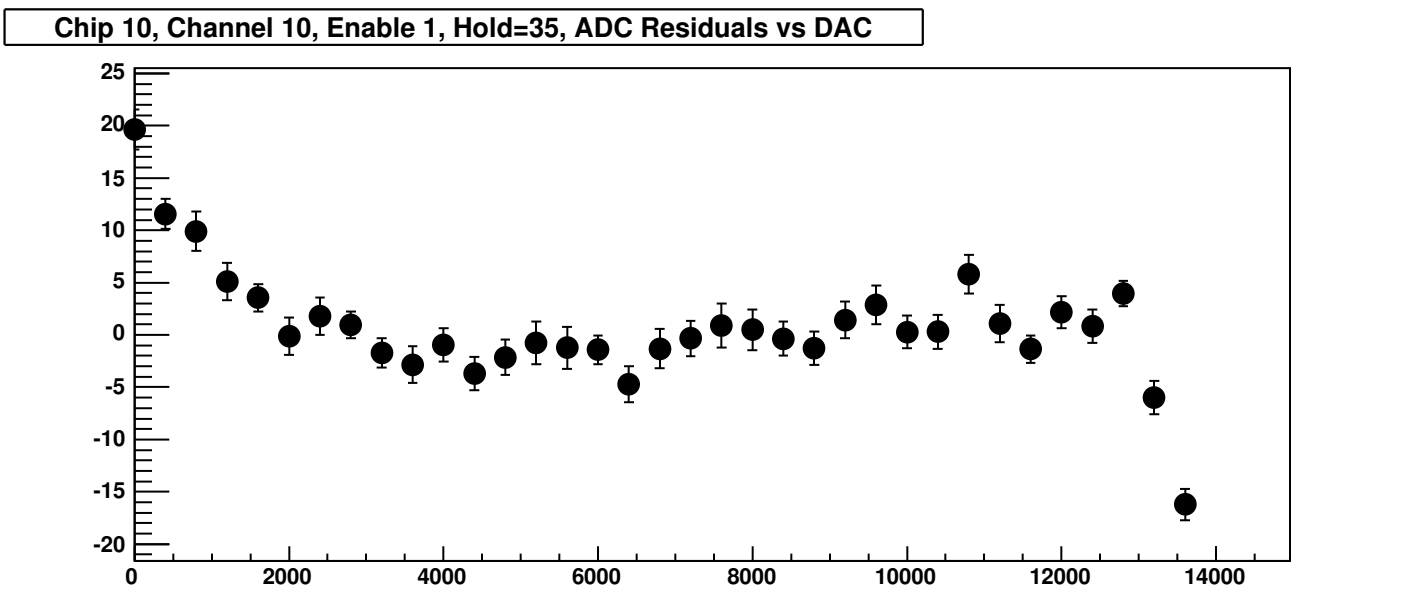

Chip 10, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.942e+05 / 23
p0 -972.6 ± 0.981
p1 0.4033 ± 0.0002254
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

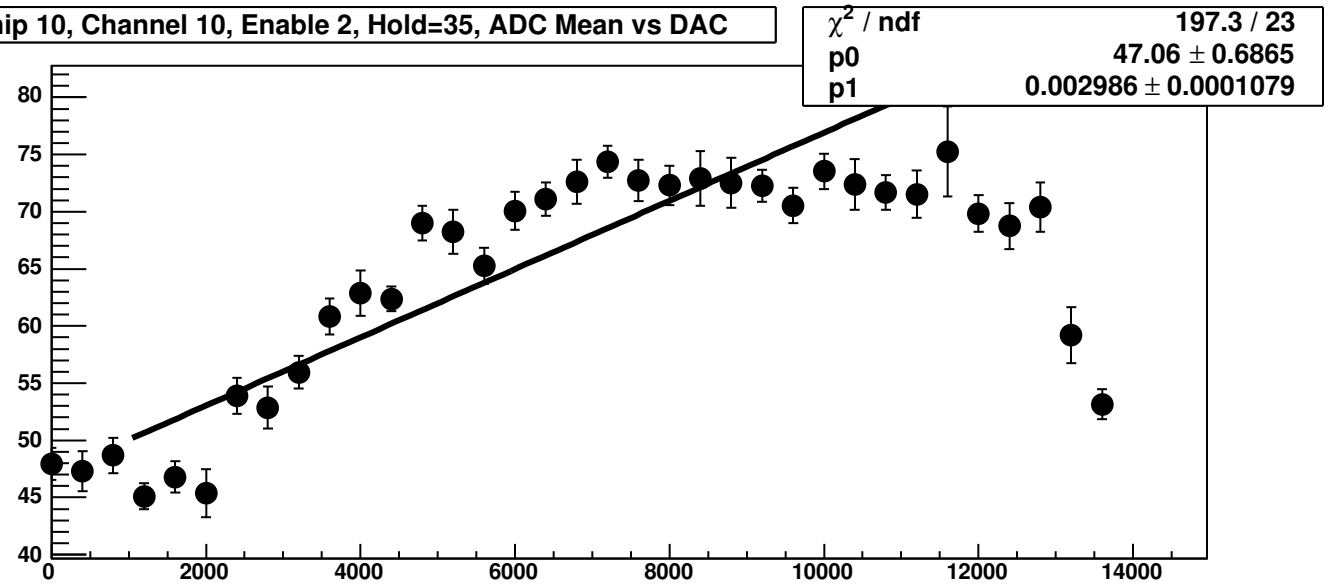

Chip 10, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 51.53 / 23
p0 35.41 ± 0.7339
p1 0.01927 ± 0.0001134
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

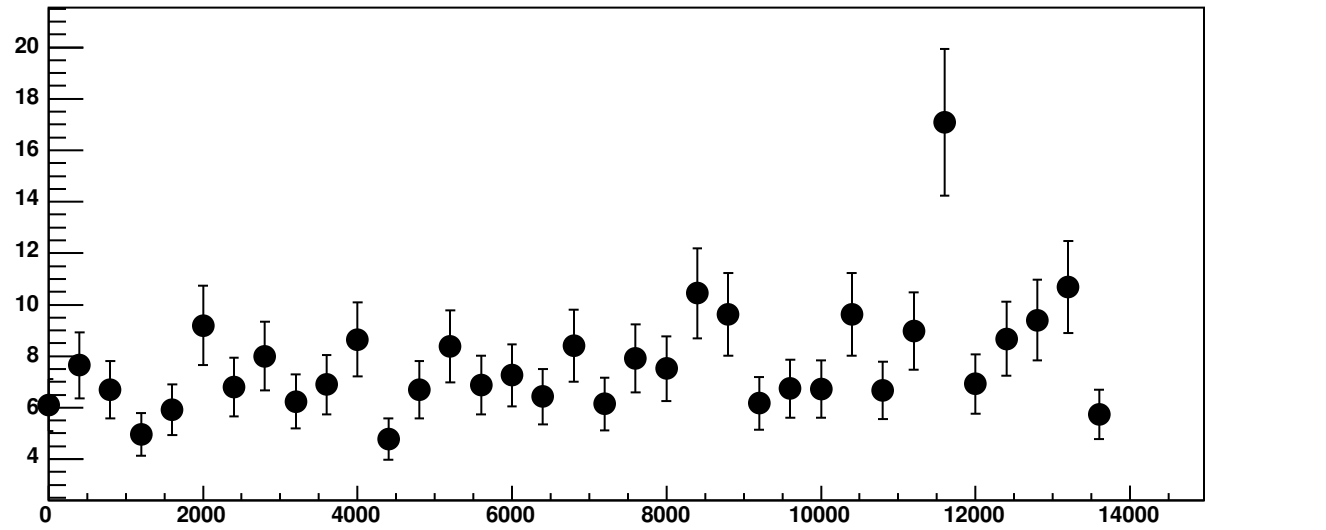

Chip 10, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

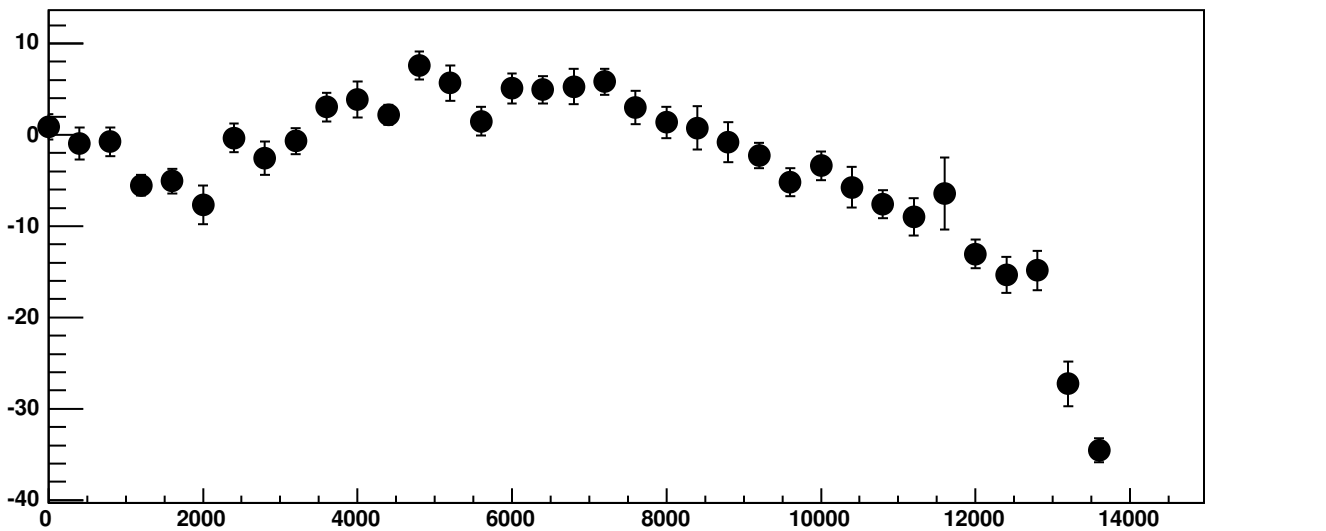
Chip 10, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

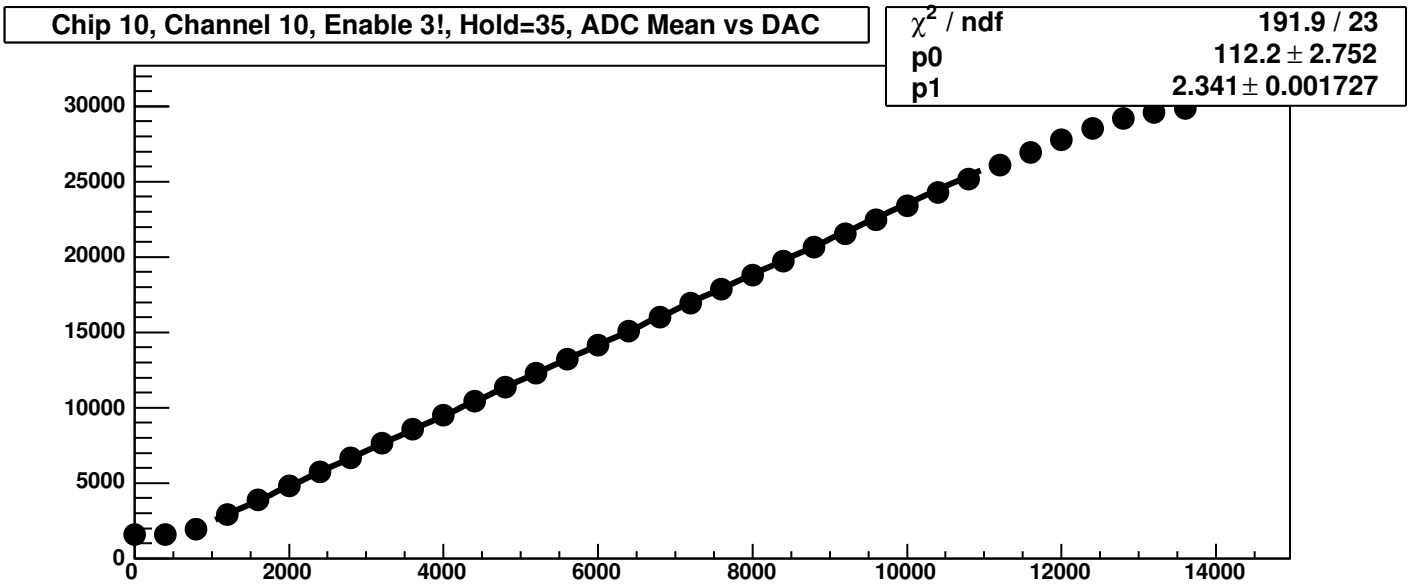


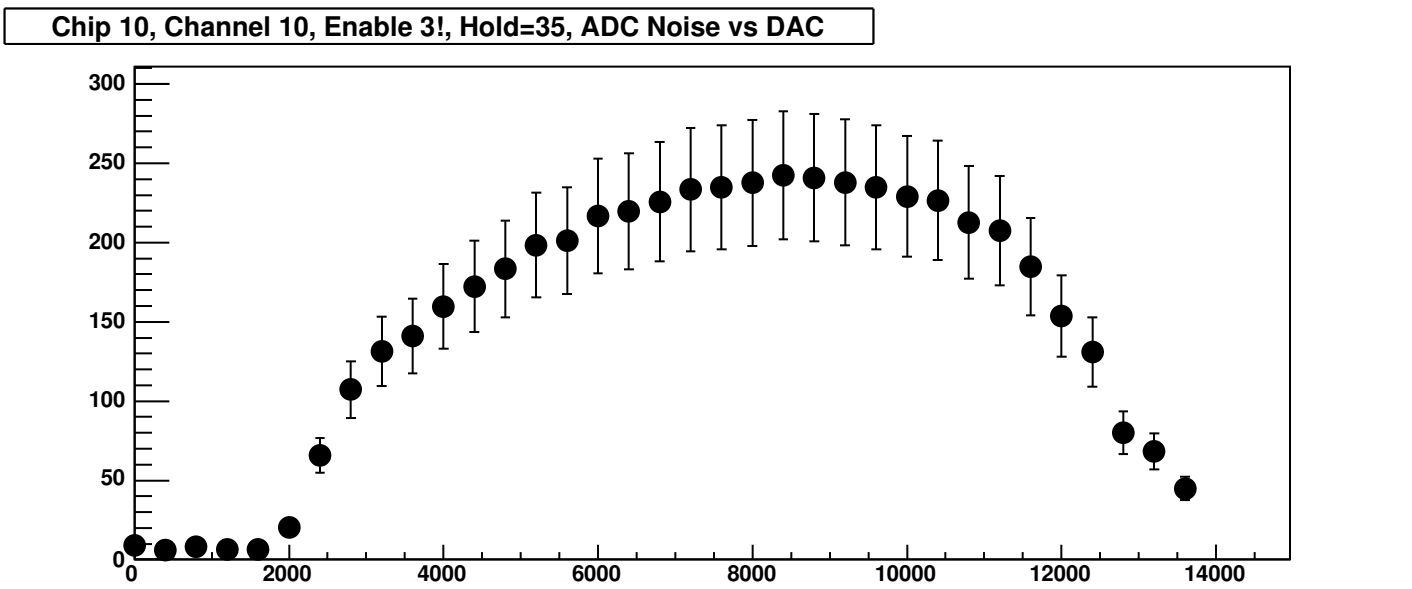
Chip 10, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

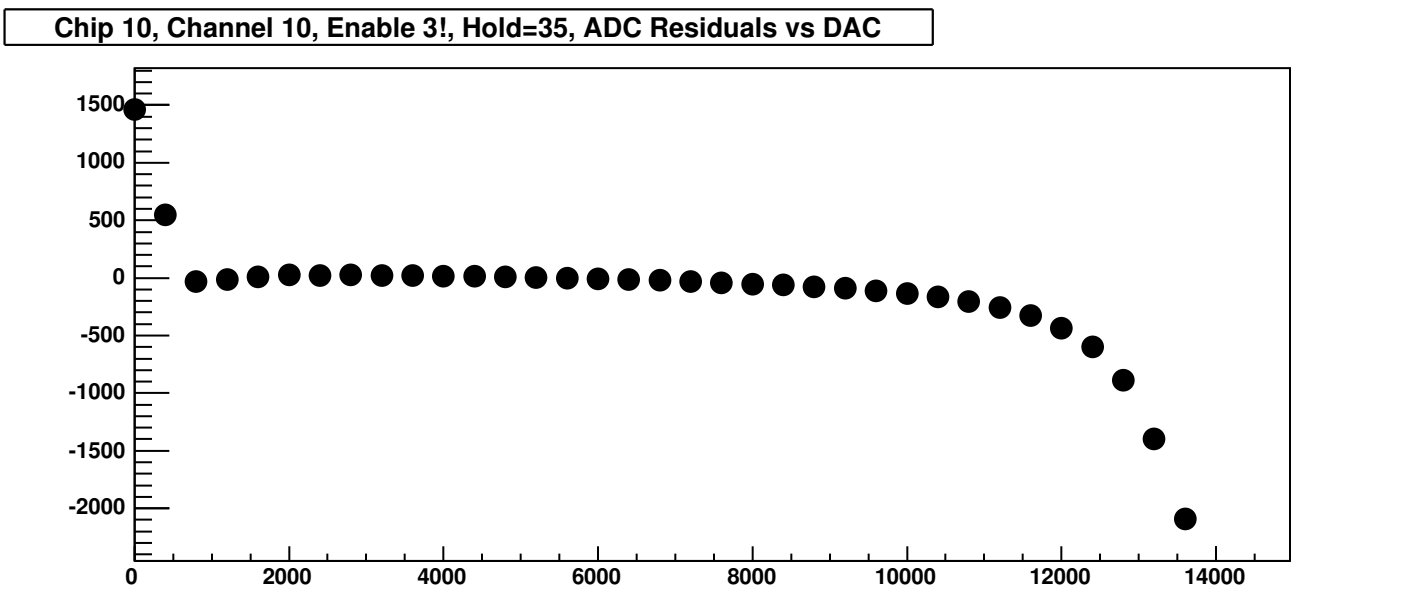


Chip 10, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

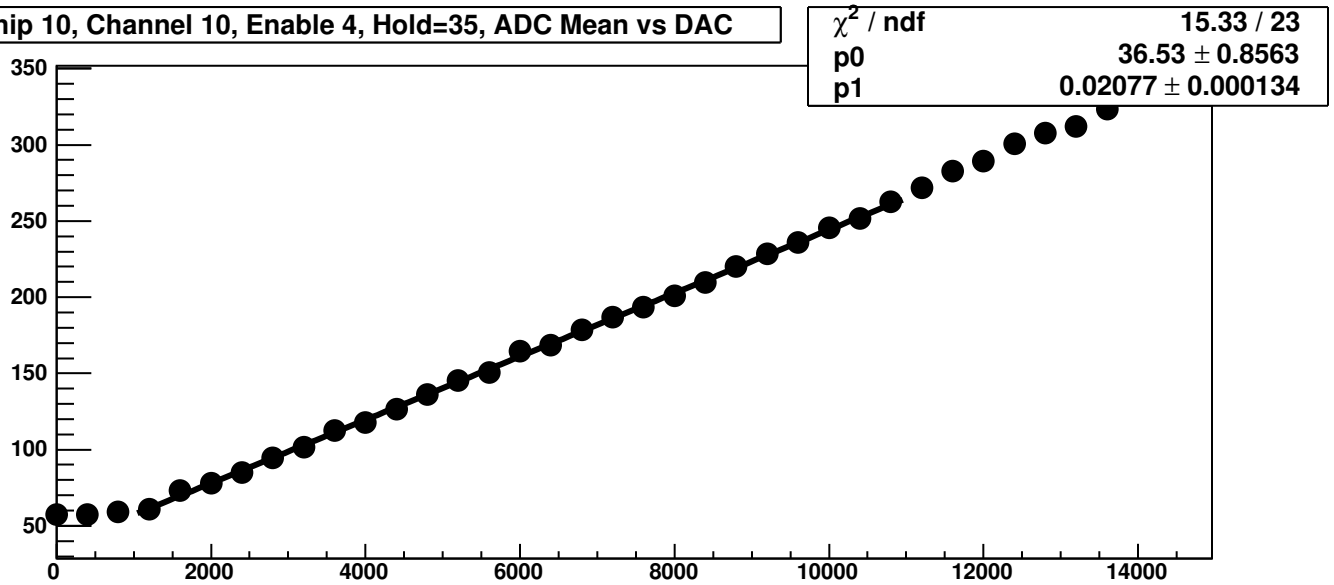



Chip 10, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 191.9 / 23
p0 112.2 ± 2.752
p1 2.341 ± 0.001727
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

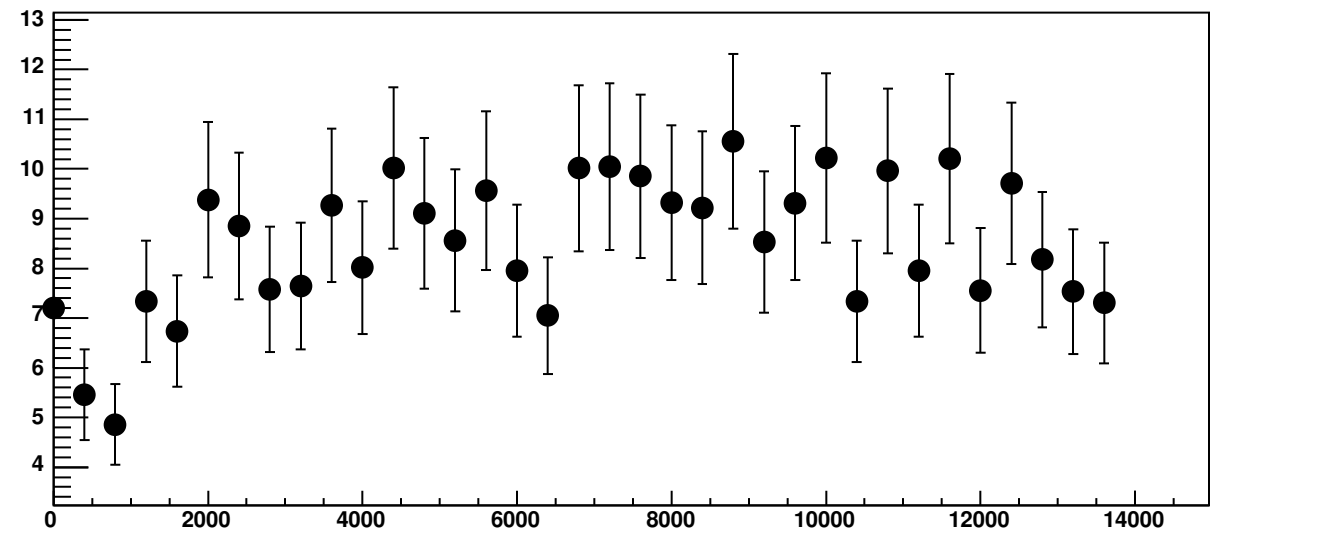

Chip 10, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

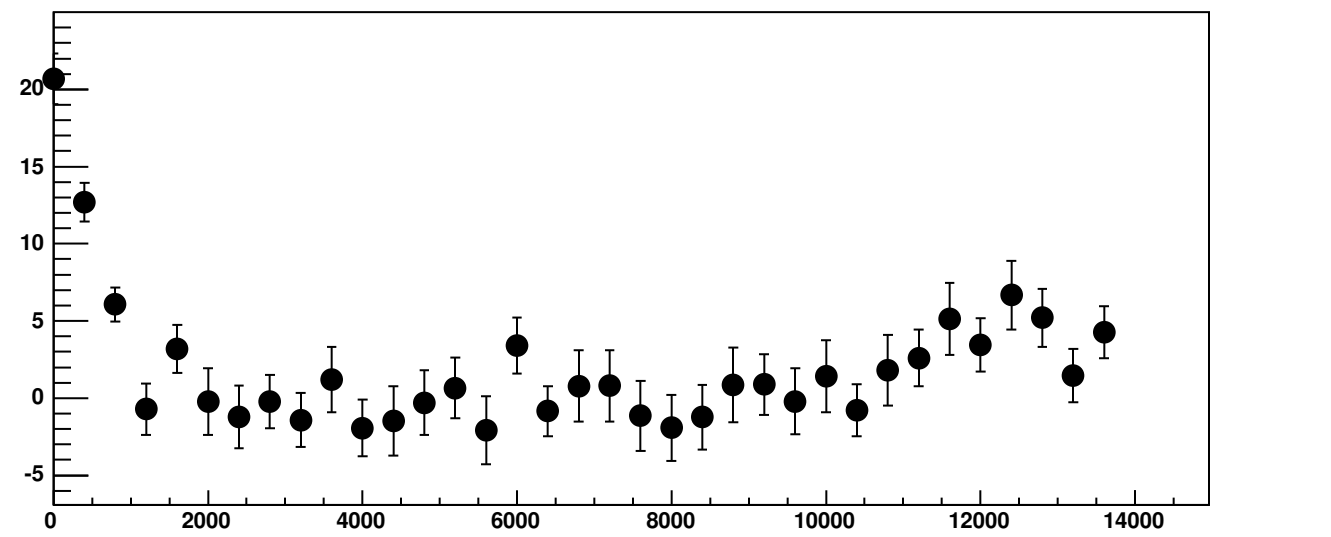
Chip 10, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC

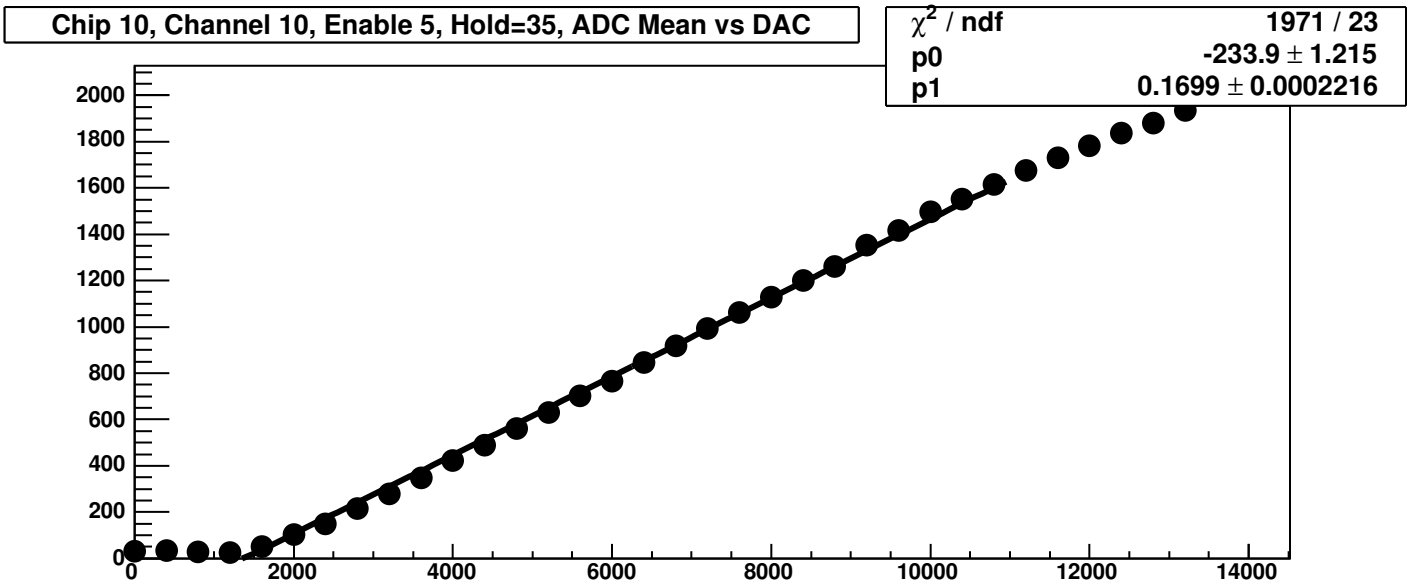


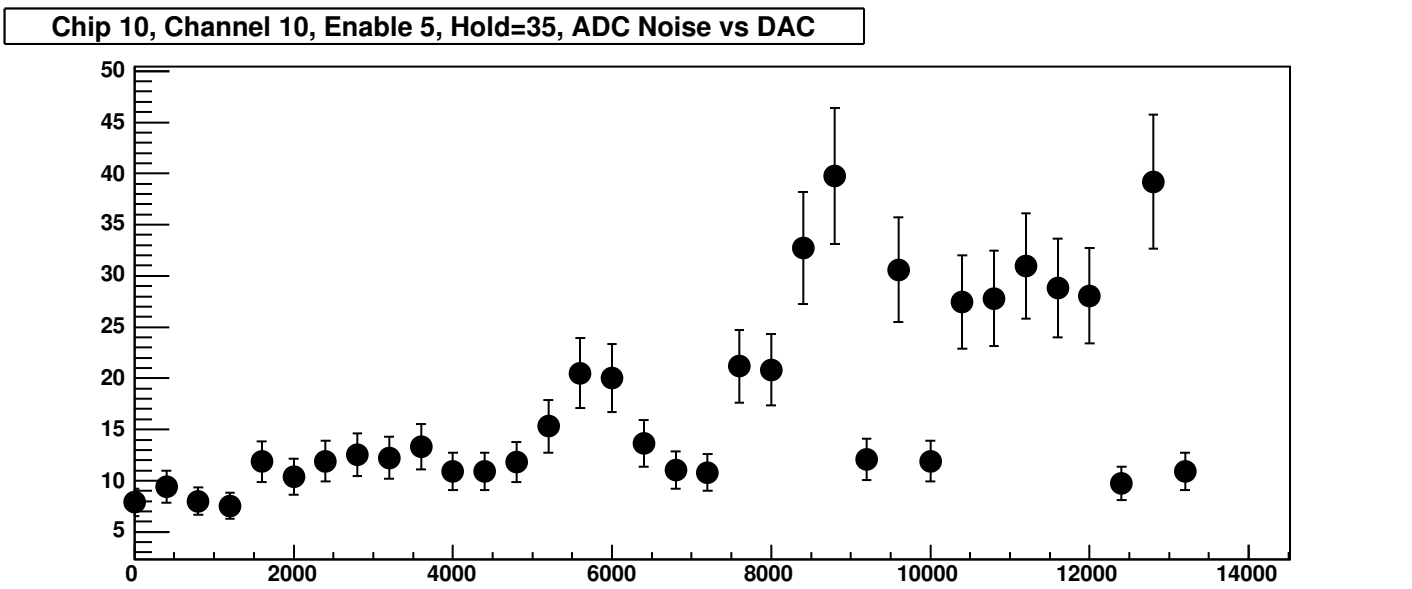
Chip 10, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC

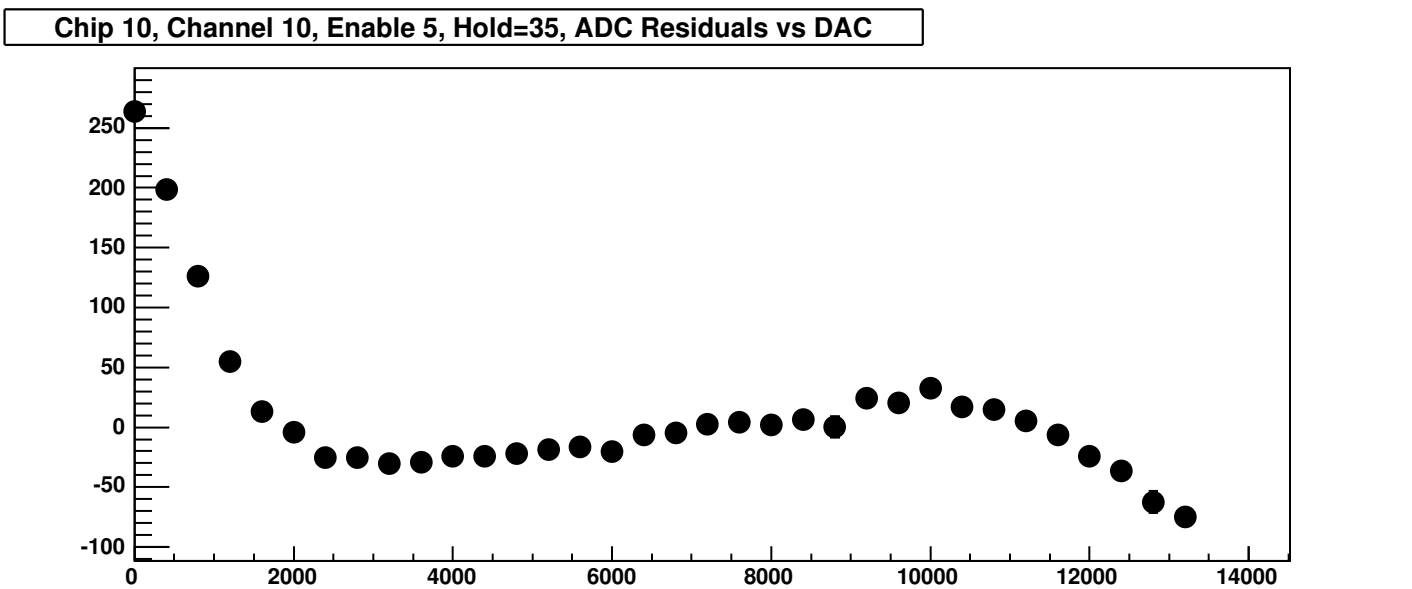


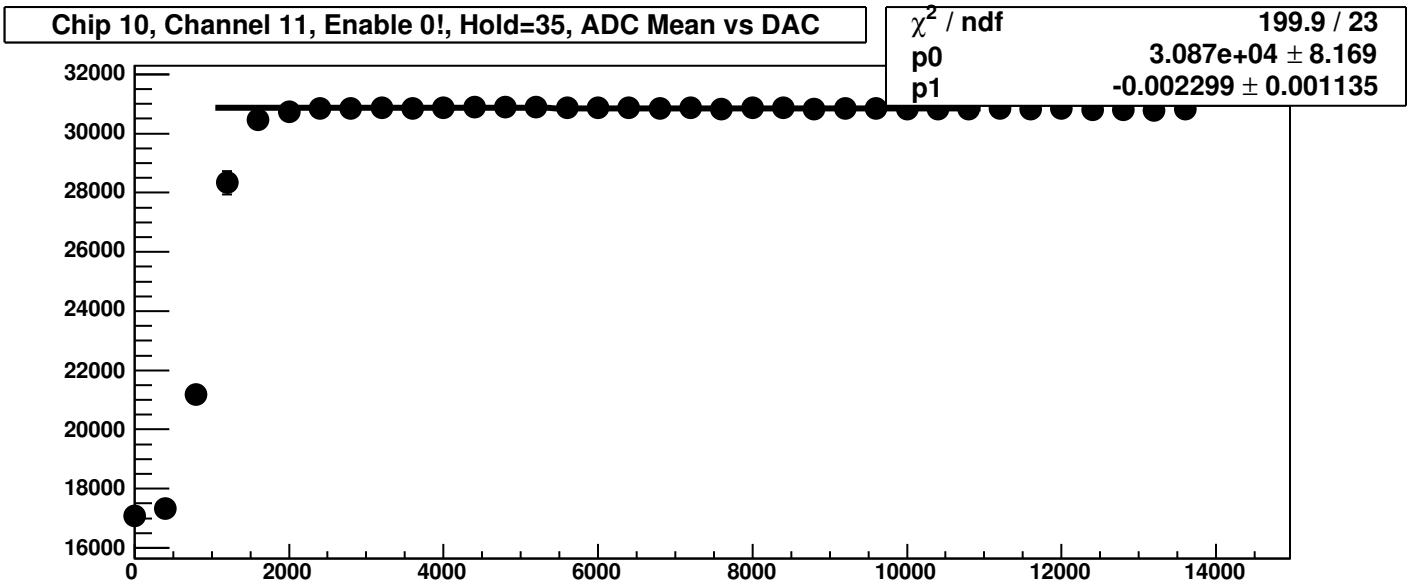
Chip 10, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC

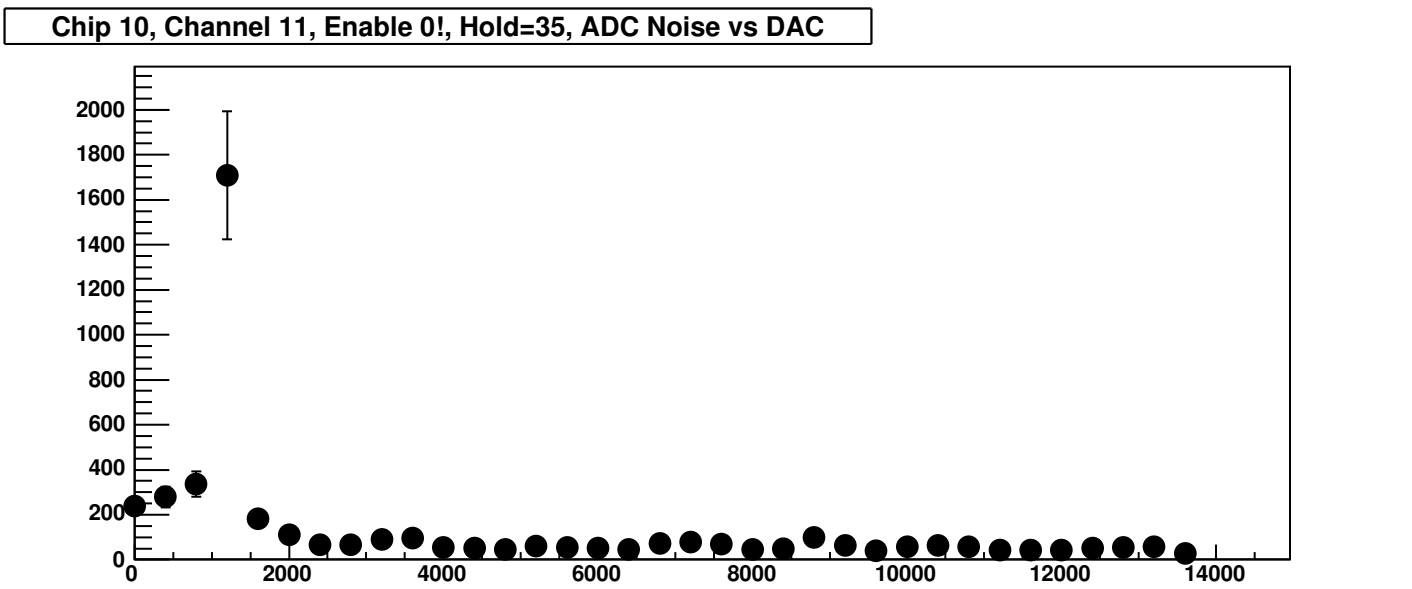


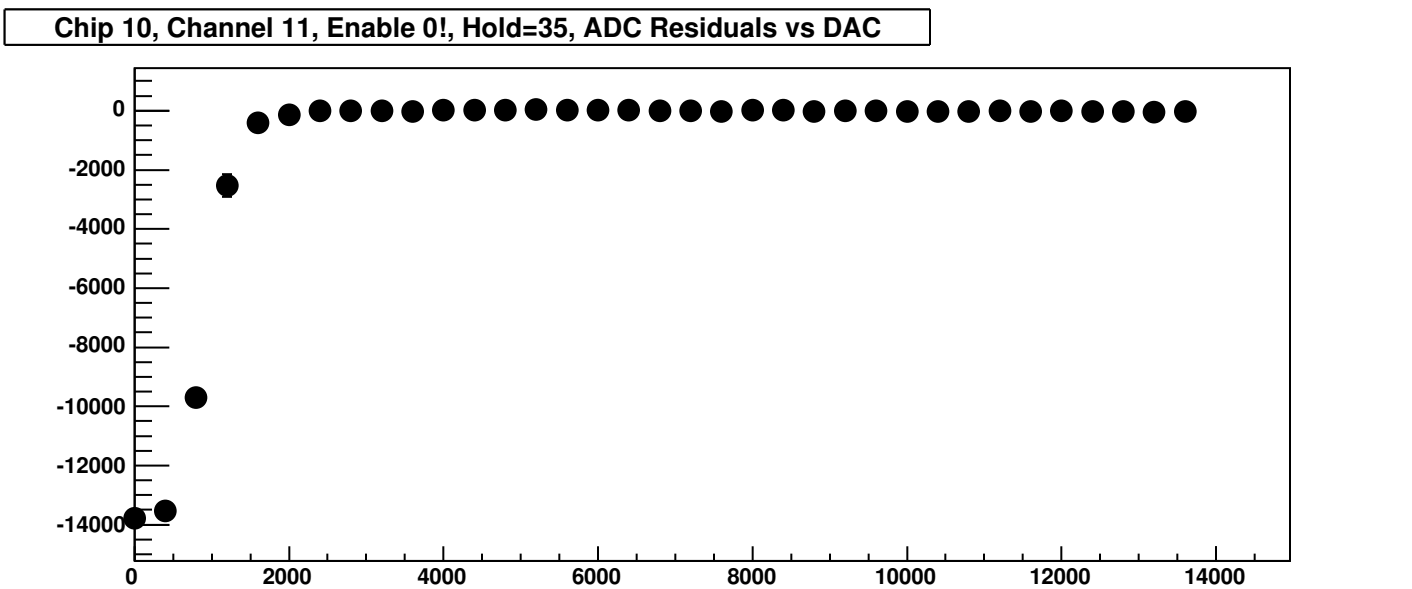

Chip 10, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1971 / 23
p0 -233.9 ± 1.215
p1 0.1699 ± 0.0002216
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

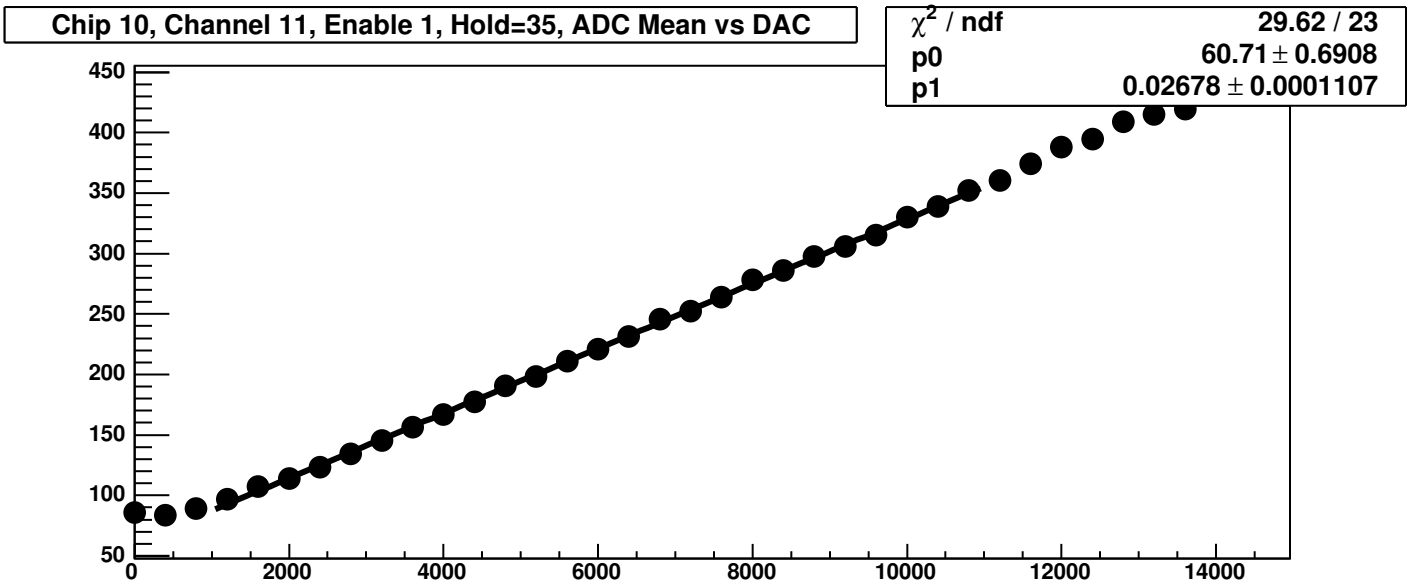

Chip 10, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
50
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

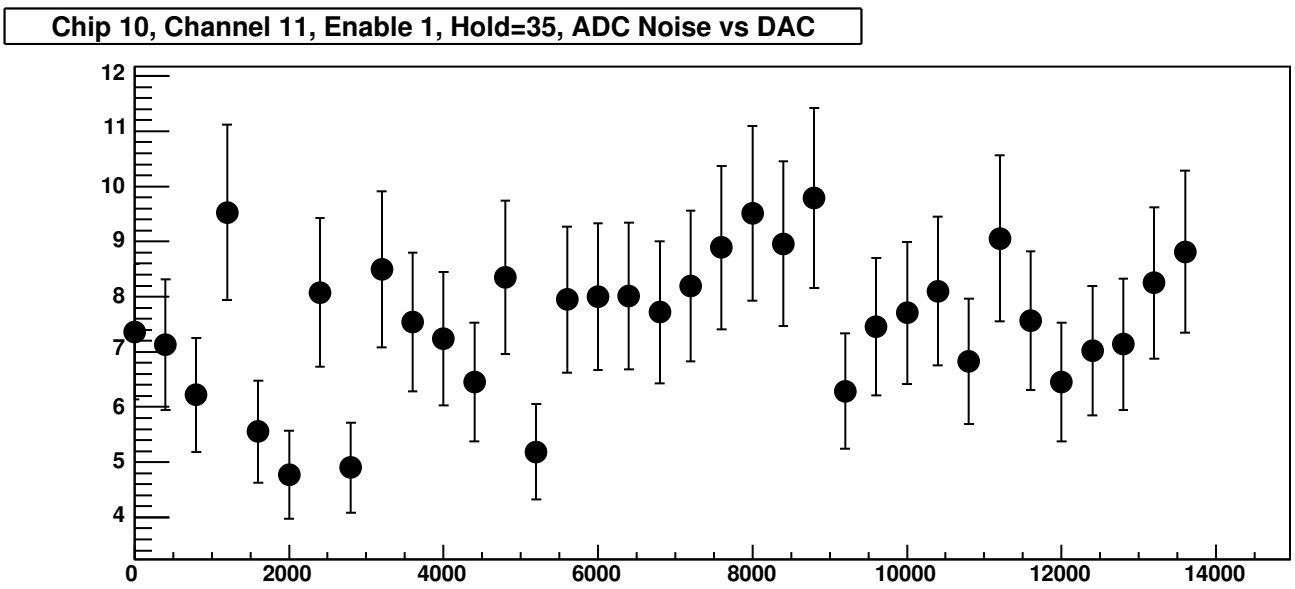

Chip 10, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

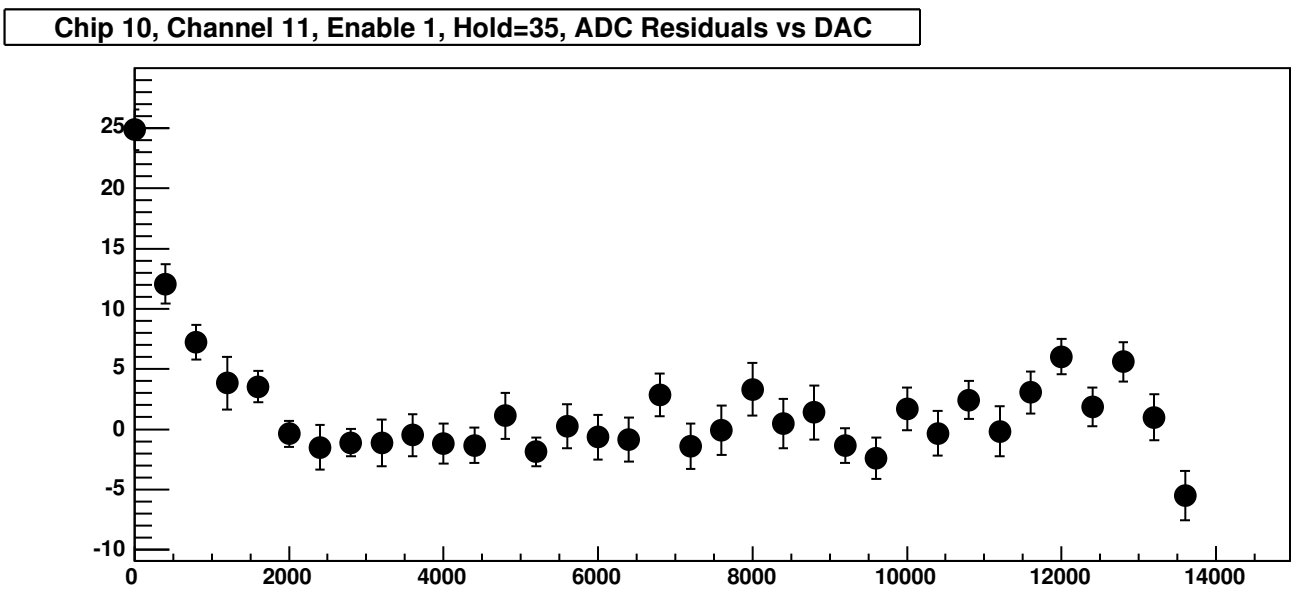

Chip 10, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 199.9 / 23
p0 3.087e+04 ± 8.169
p1 -0.002299 ± 0.001135
32000
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

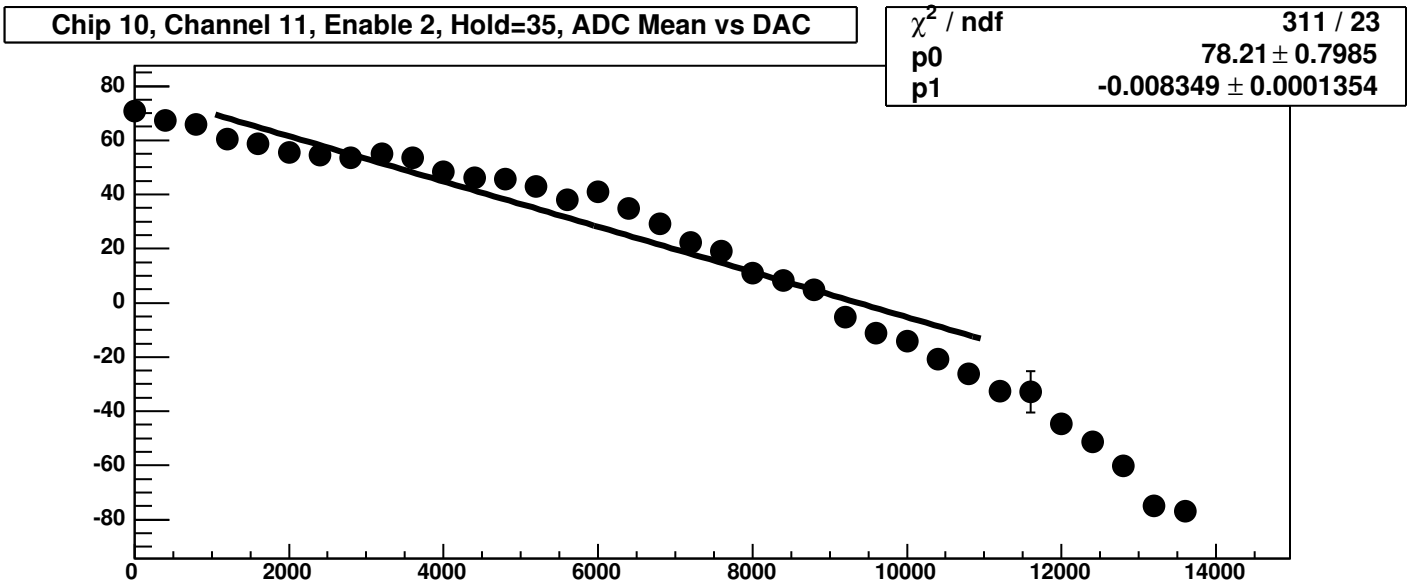

Chip 10, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

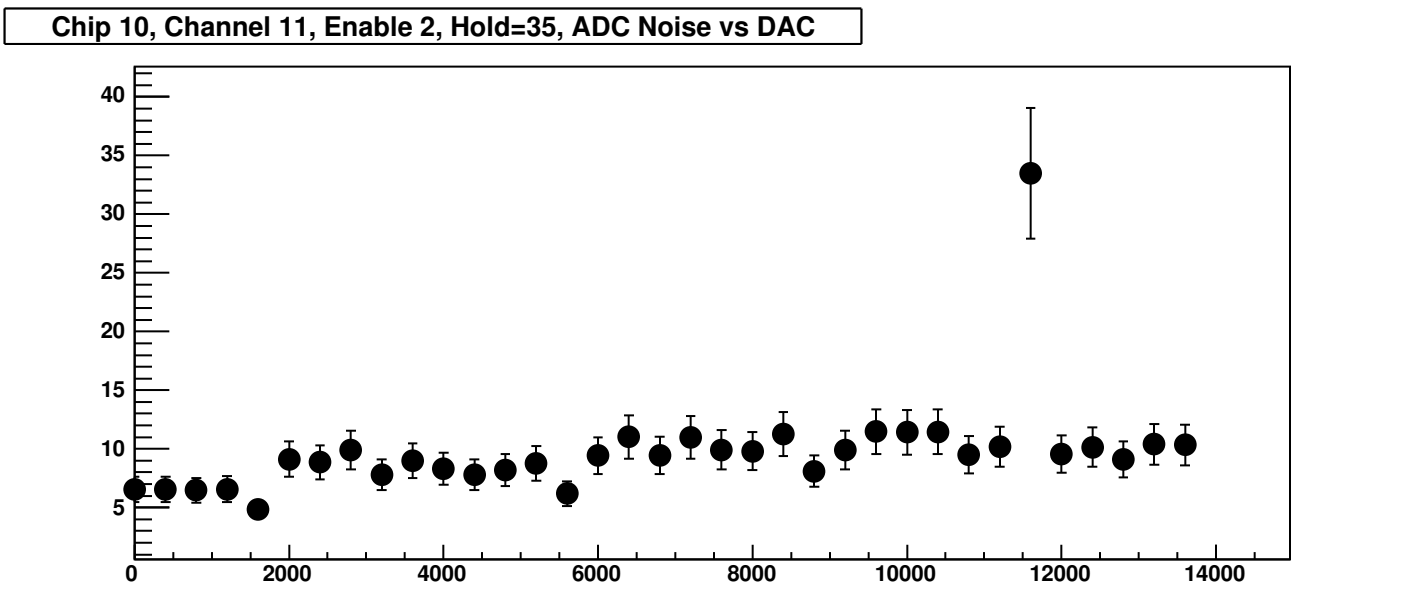

Chip 10, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

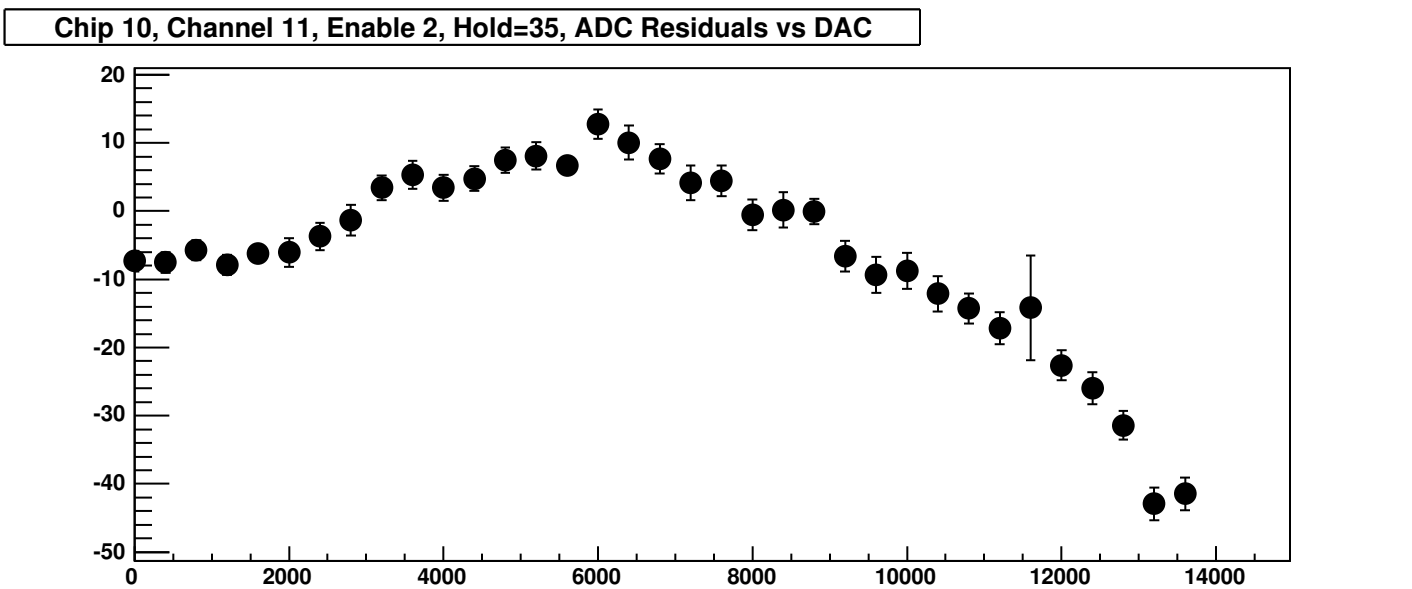

Chip 10, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 29.62 / 23
p0 60.71 ± 0.6908
p1 0.02678 ± 0.0001107
450
400
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

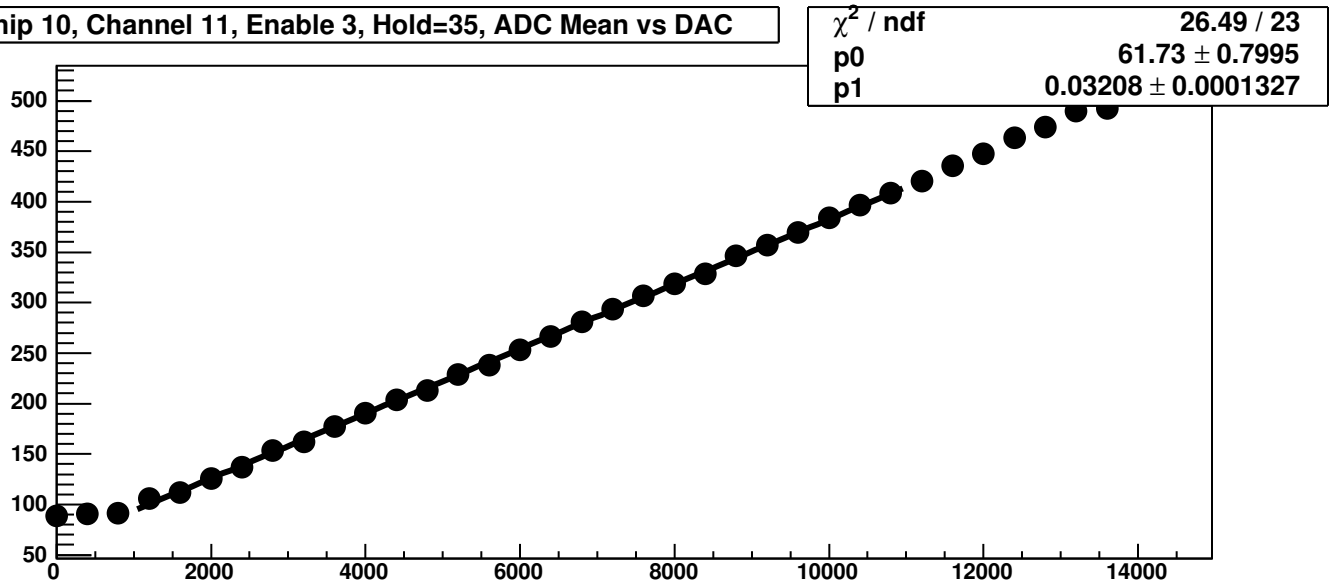

Chip 10, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 311 / 23
p0 78.21 ± 0.7985
p1 -0.008349 ± 0.0001354
80
60
40
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000

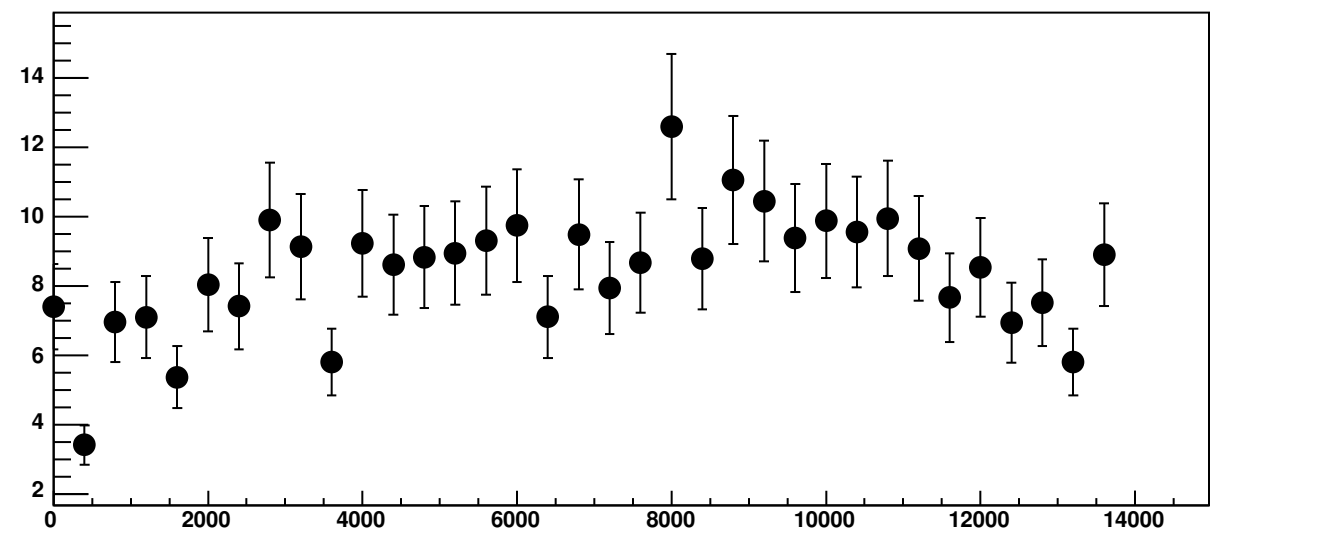

Chip 10, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

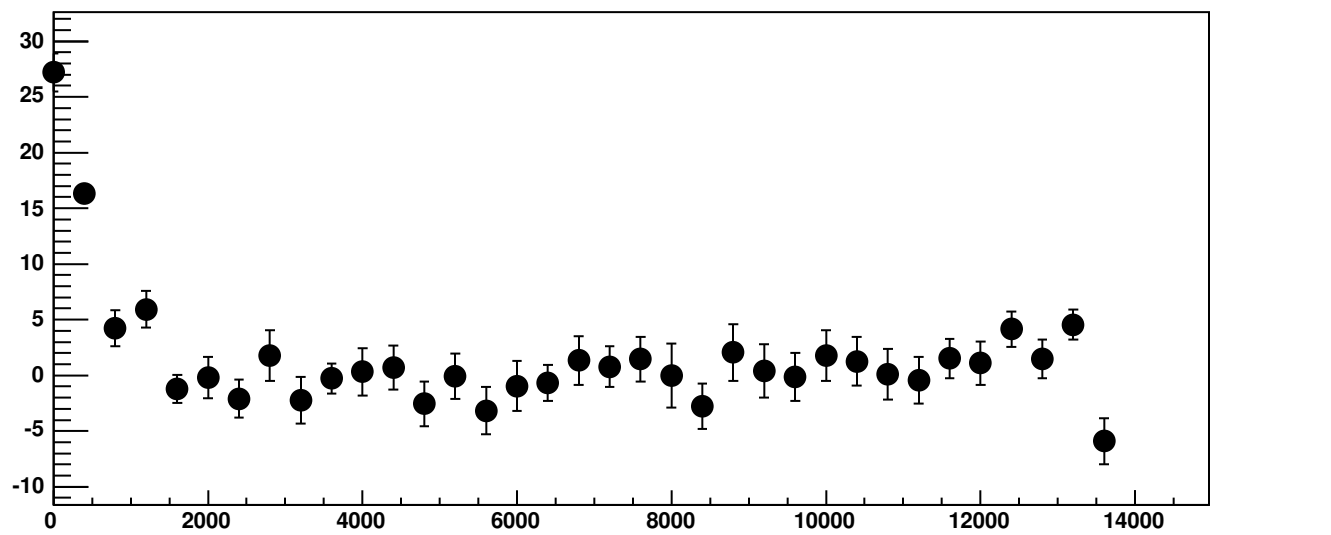
Chip 10, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC



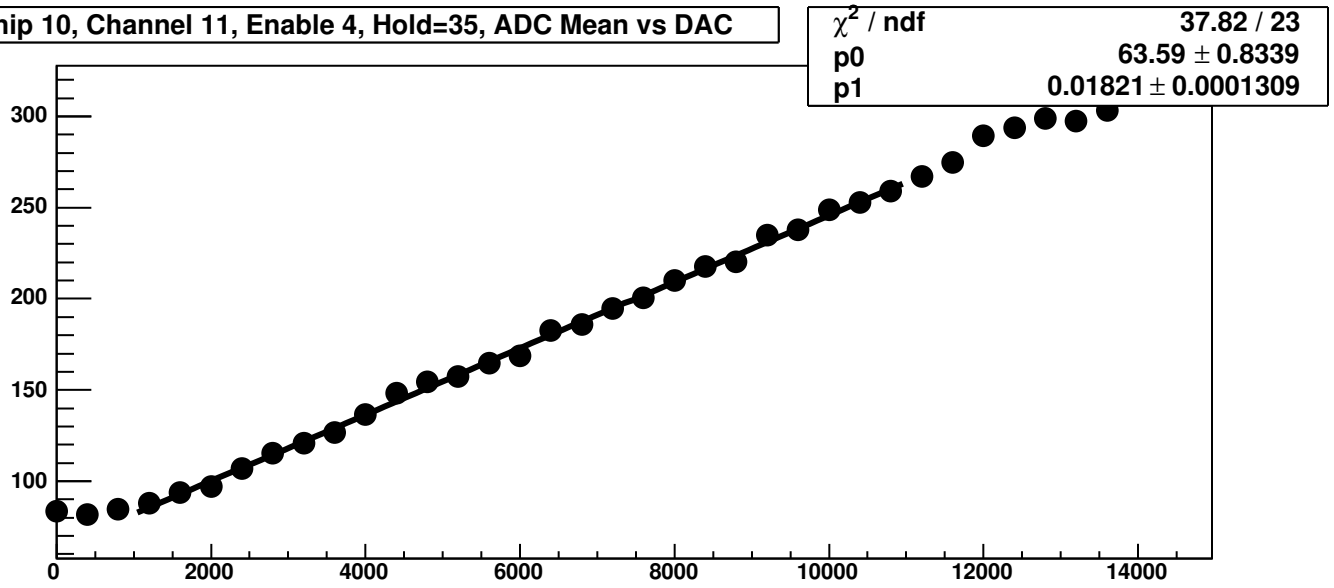
Chip 10, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC



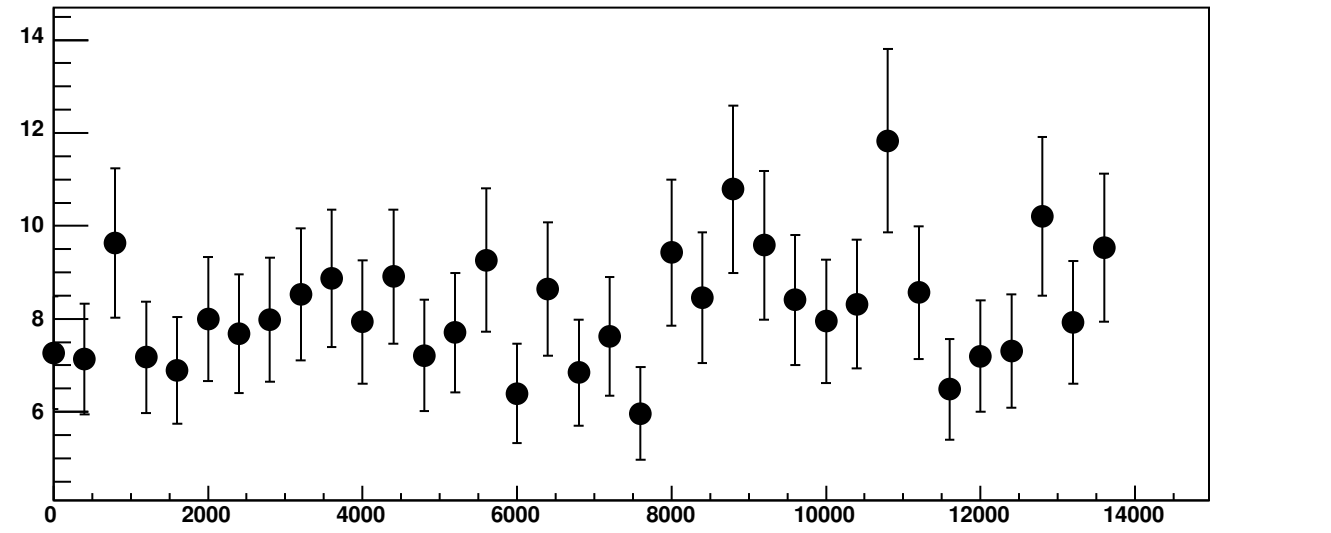
Chip 10, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC



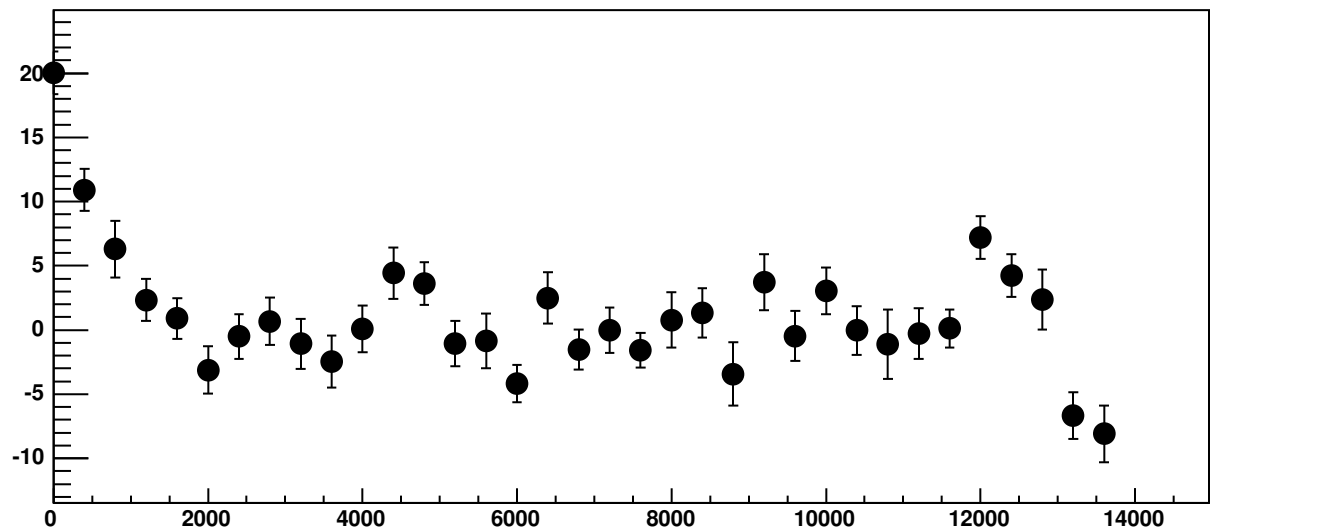
Chip 10, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

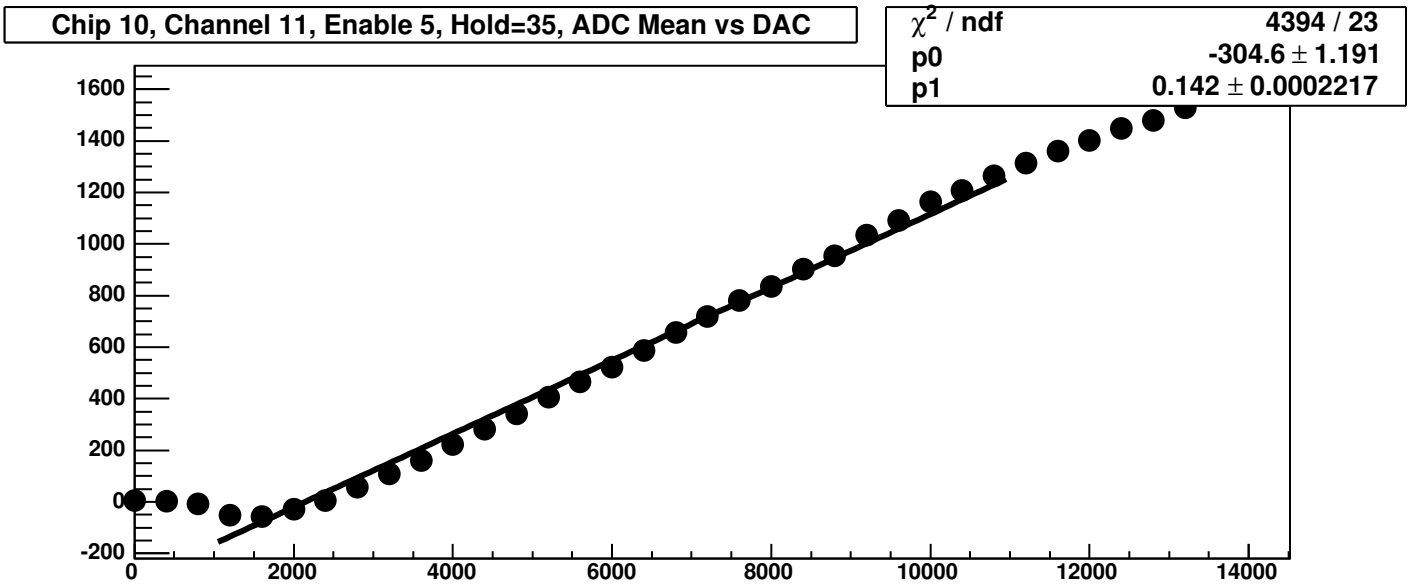


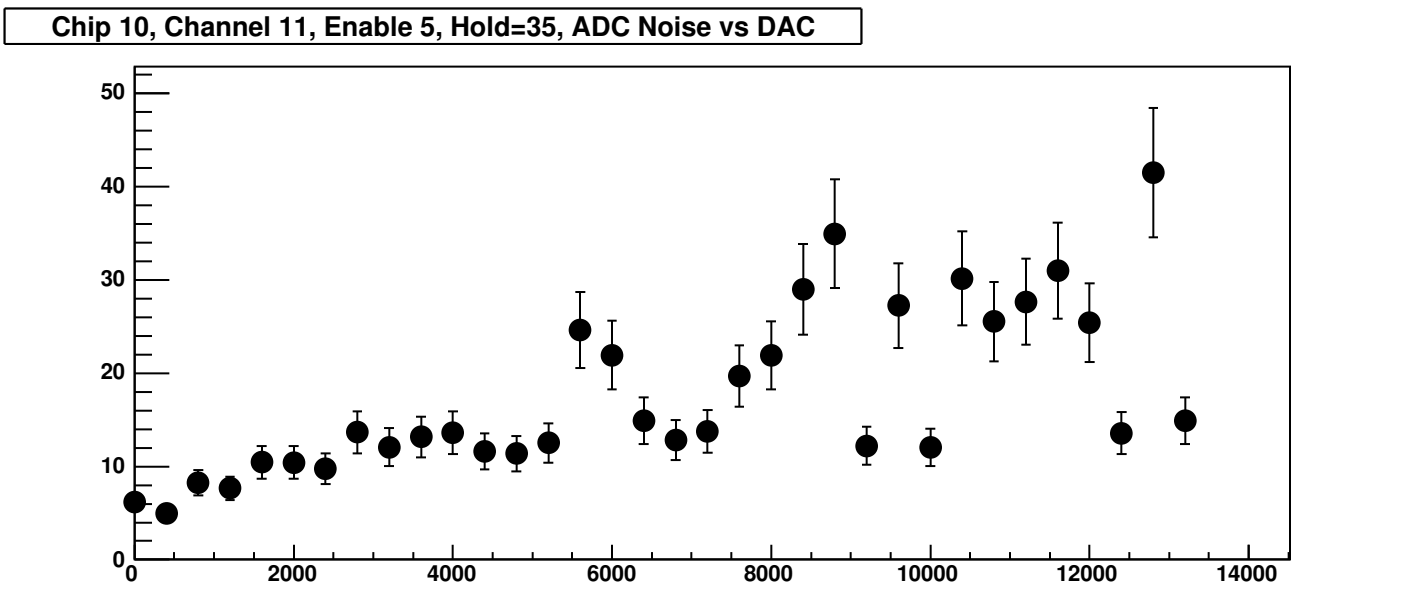
Chip 10, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

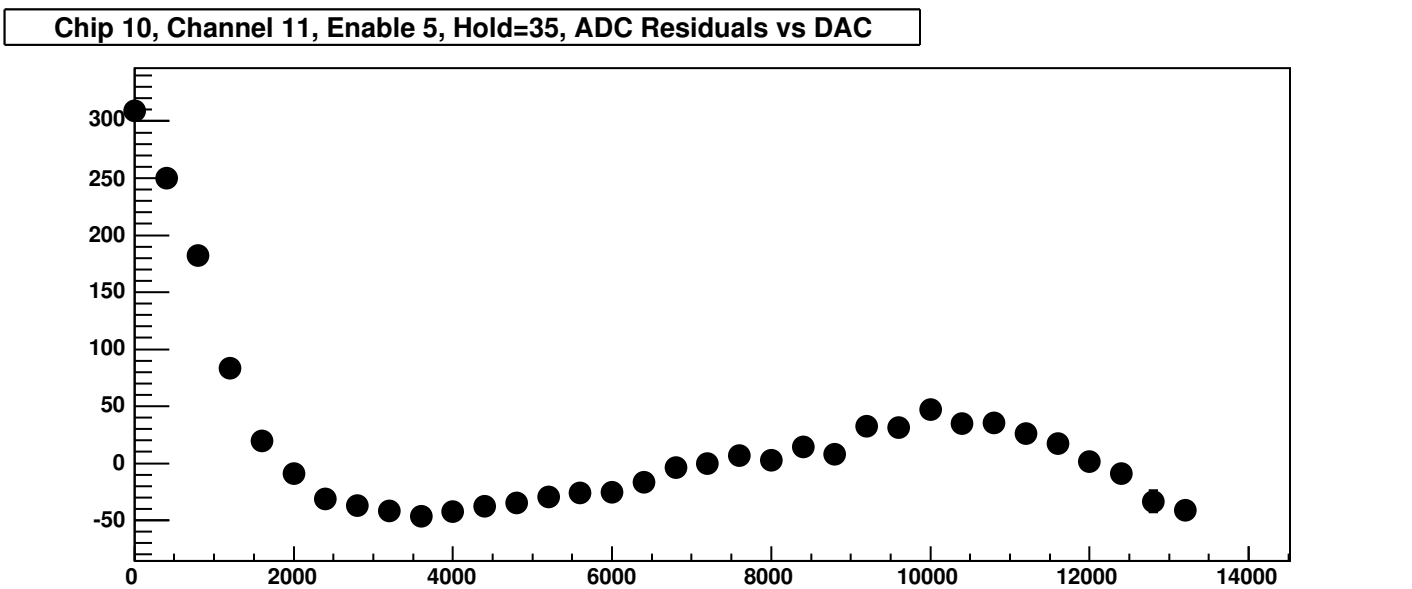


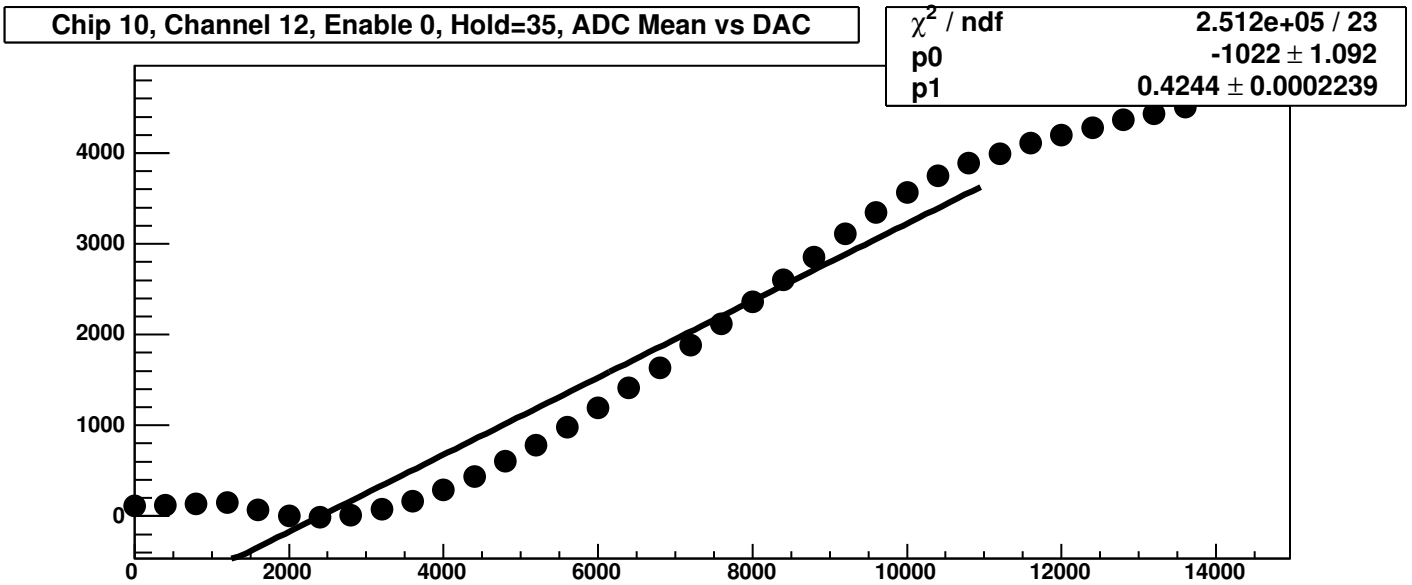
Chip 10, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

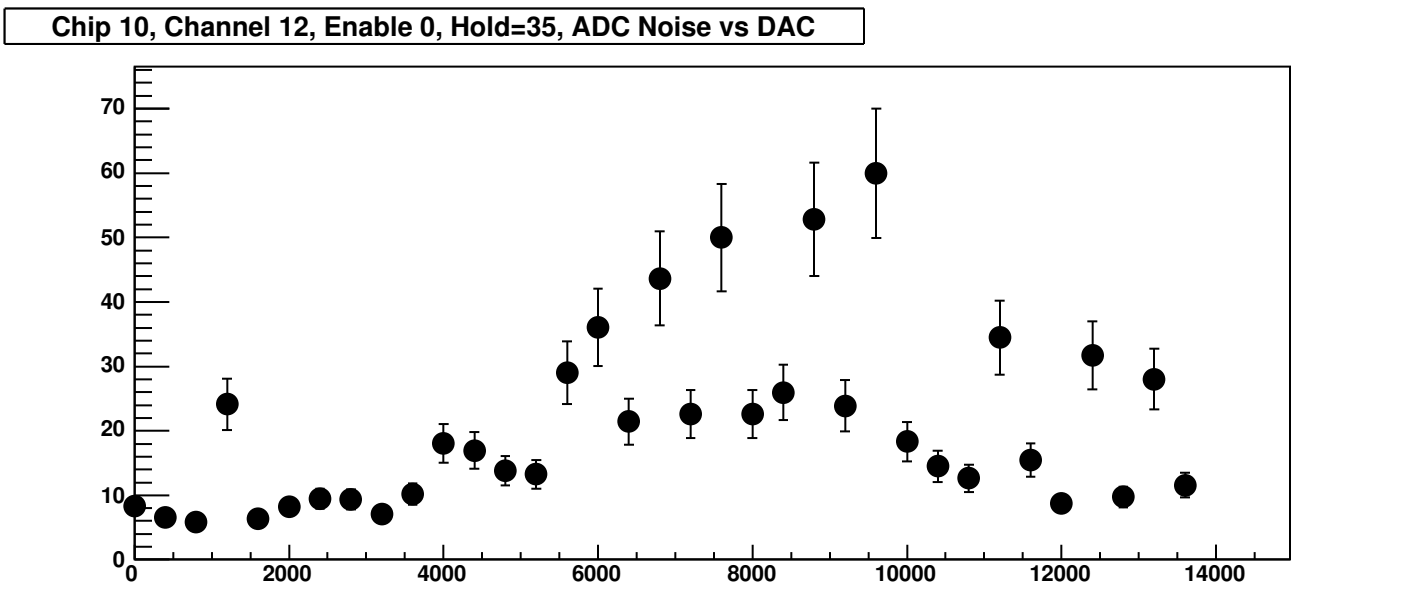


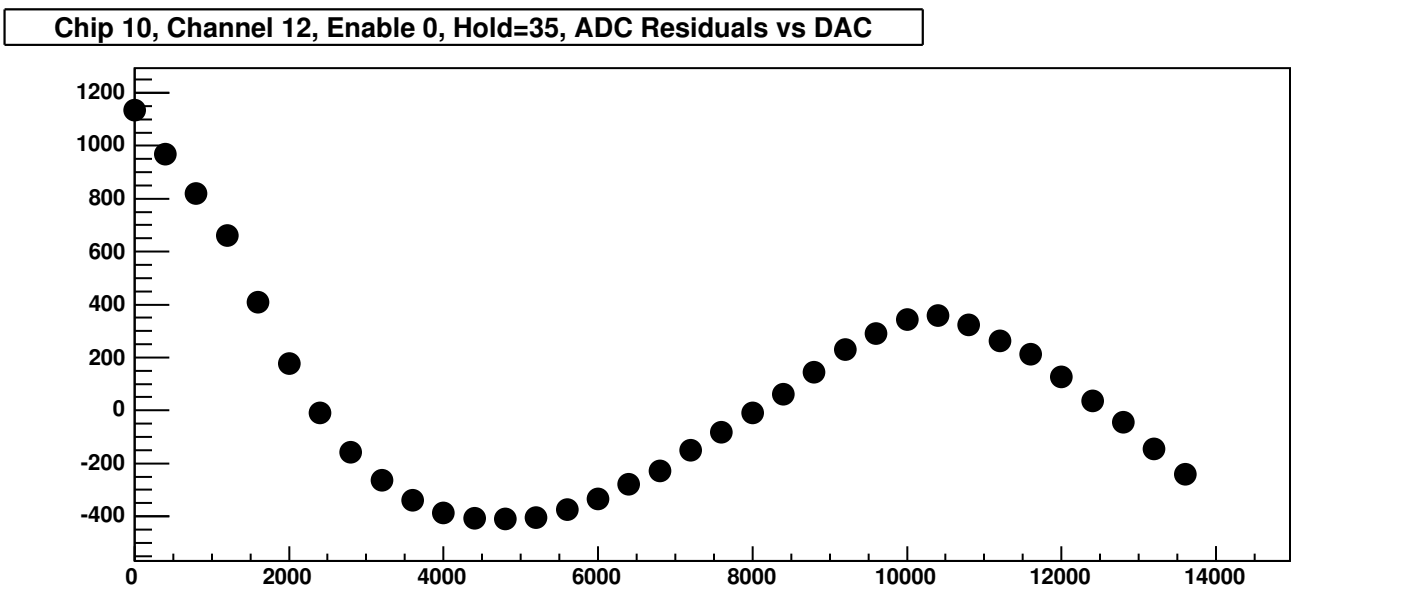

Chip 10, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4394 / 23
p0 -304.6 ± 1.191
p1 0.142 ± 0.0002217
1600
1400
1200
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

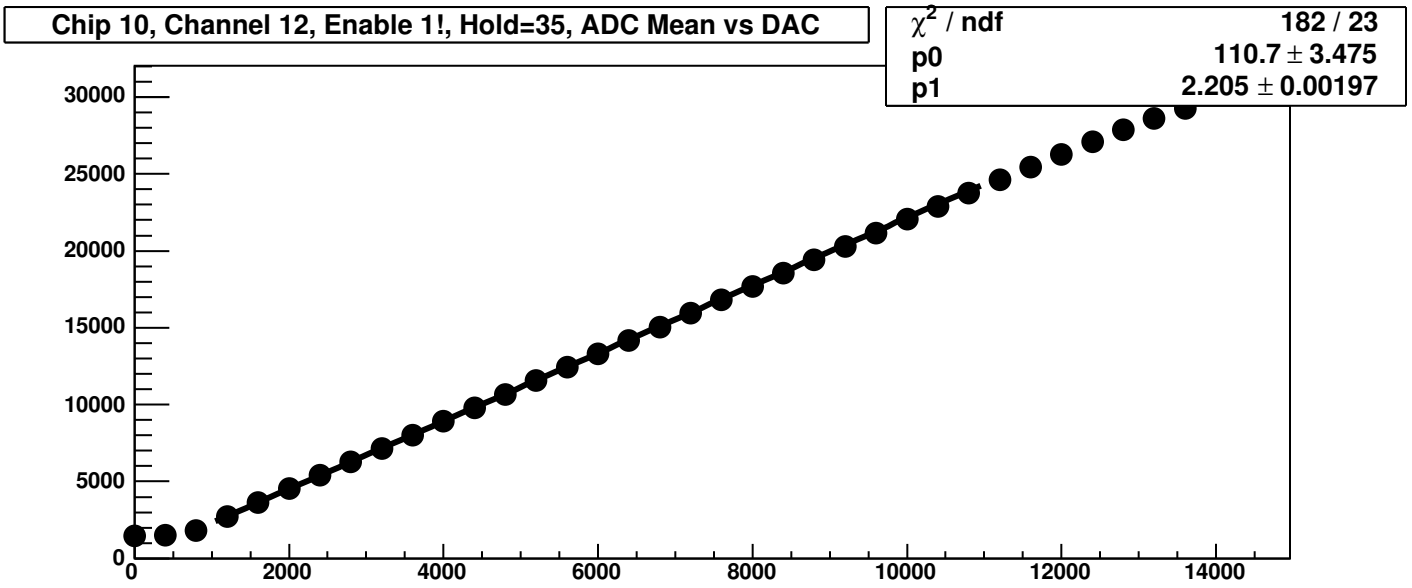

Chip 10, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

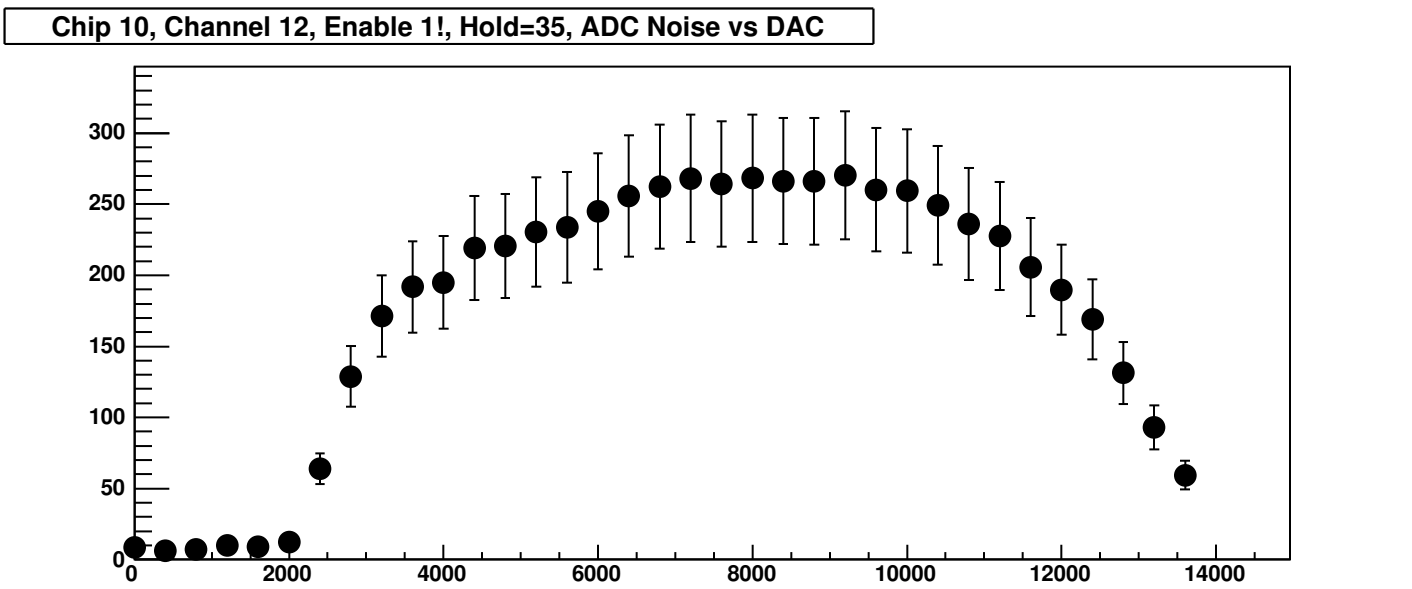

Chip 10, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
300
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

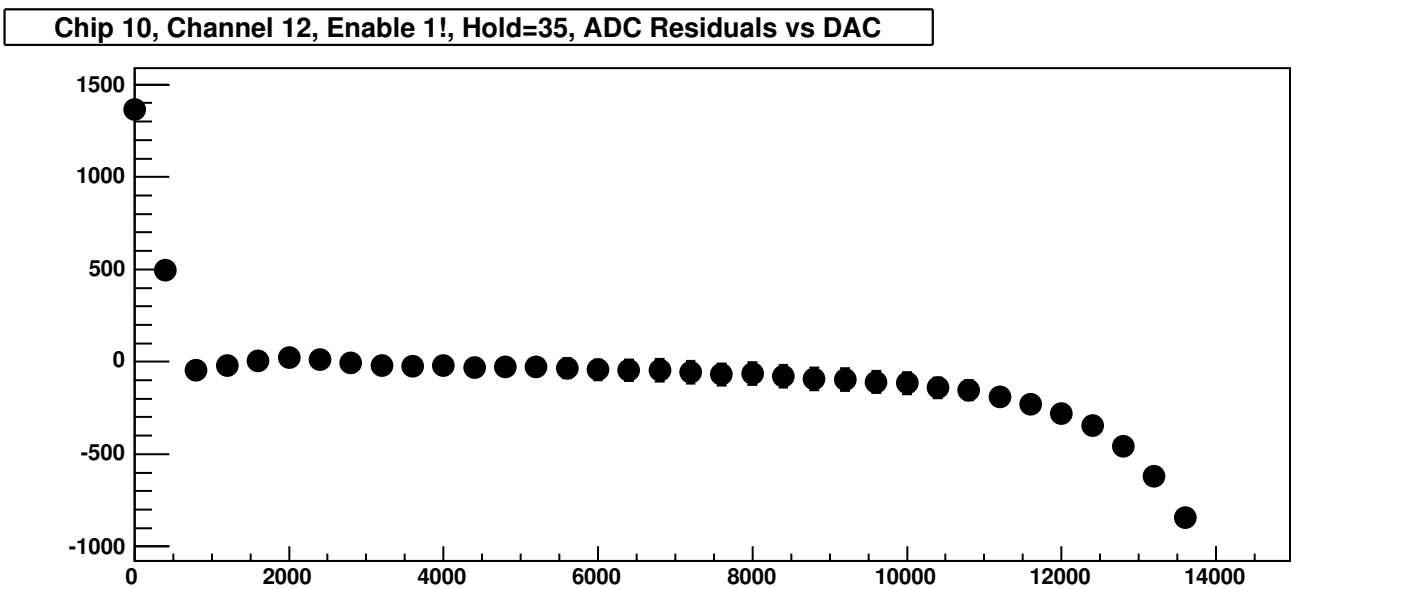

Chip 10, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.512e+05 / 23
p0 -1022 ± 1.092
p1 0.4244 ± 0.0002239
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

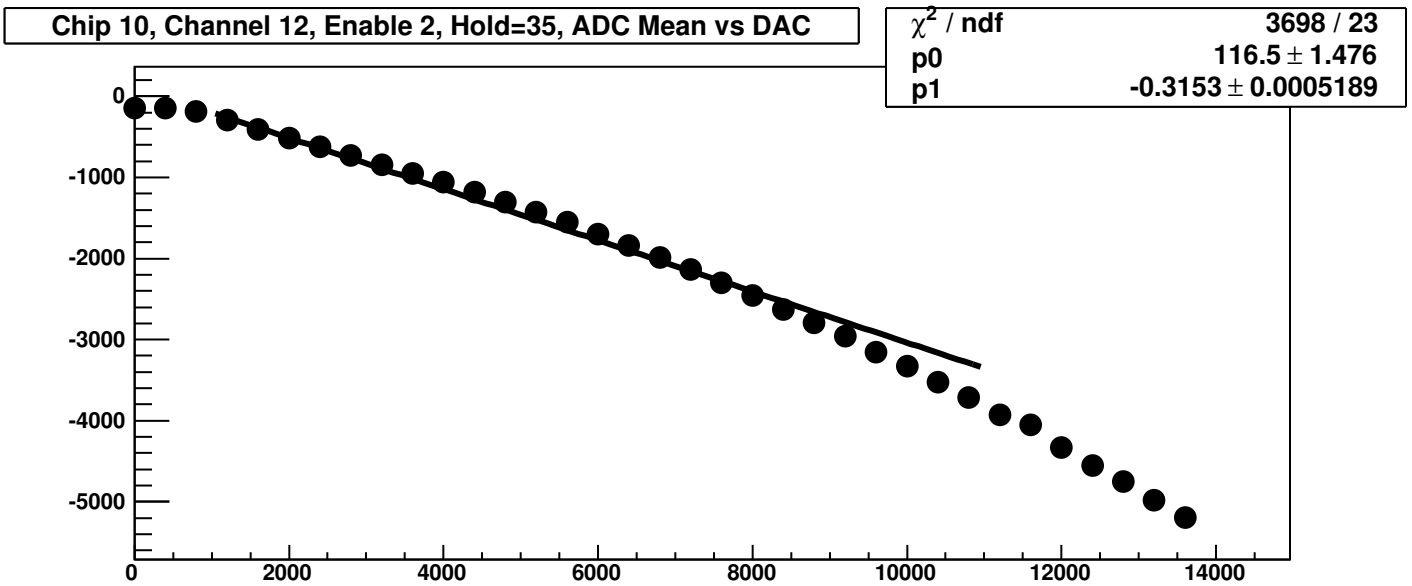

Chip 10, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

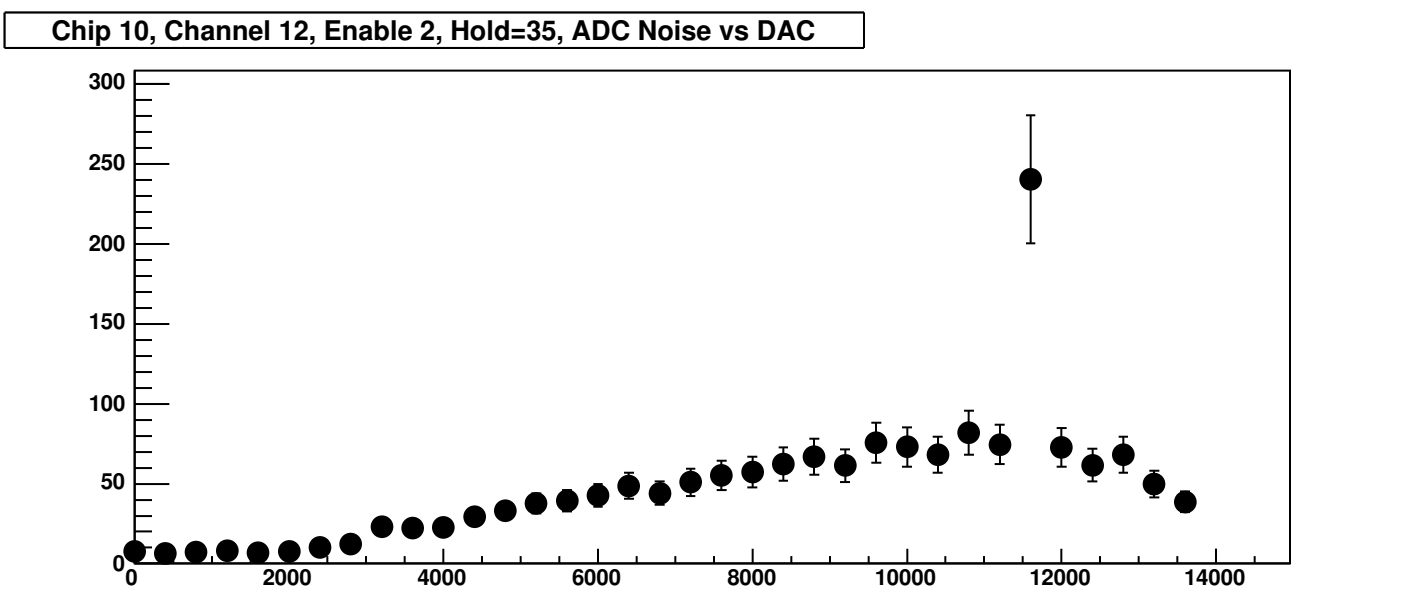

Chip 10, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

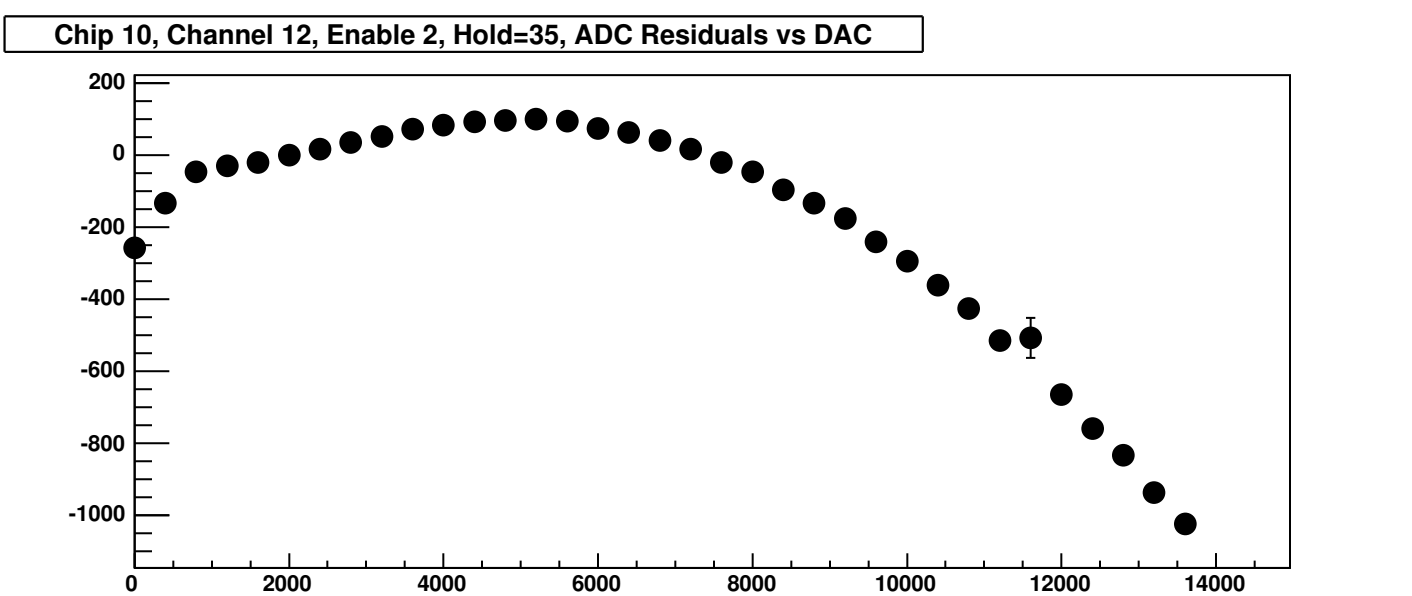

Chip 10, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 182 / 23
p0 110.7 ± 3.475
p1 2.205 ± 0.00197
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

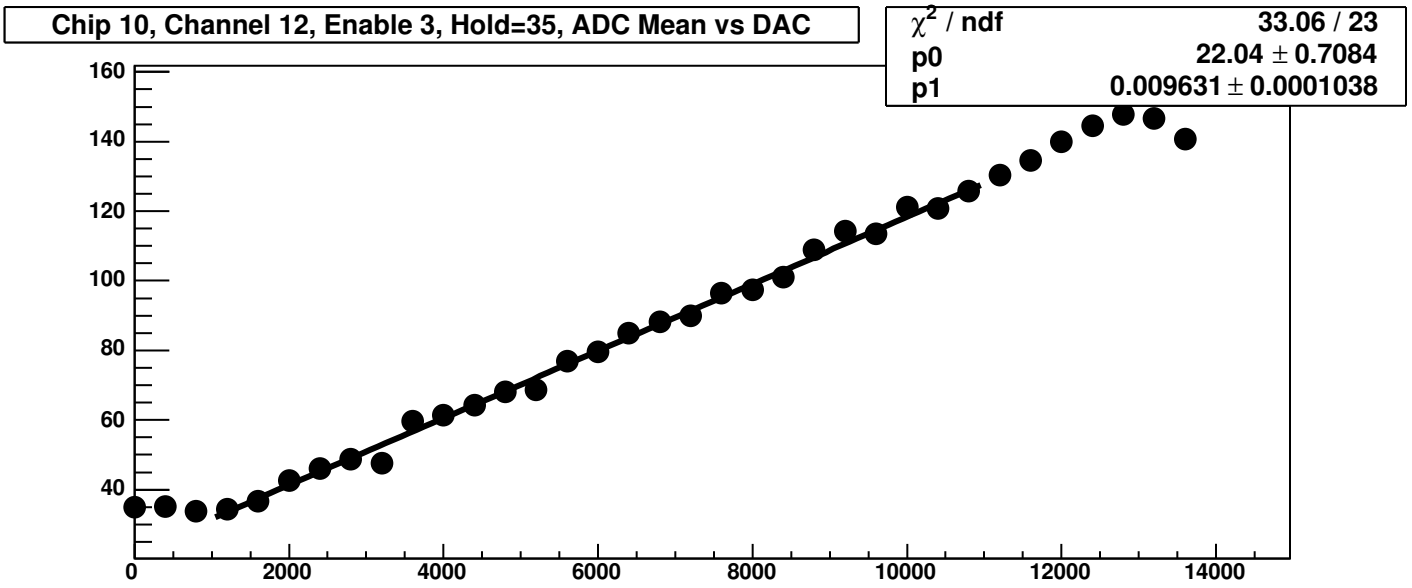

Chip 10, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

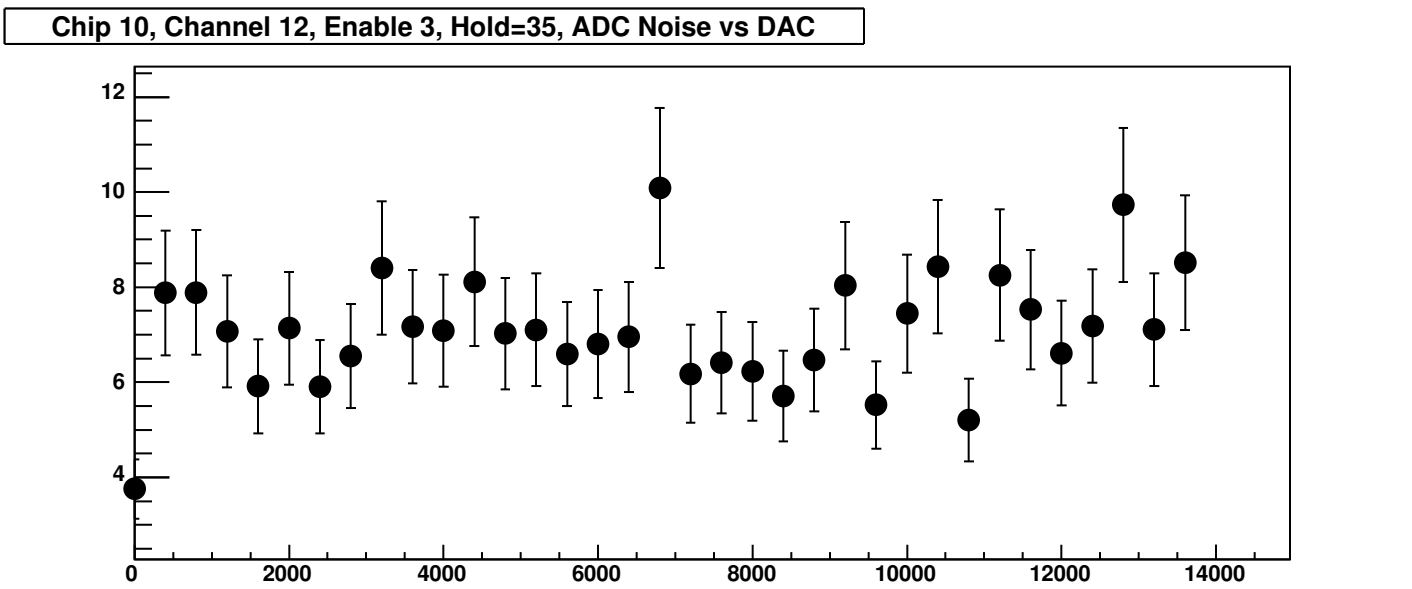

Chip 10, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

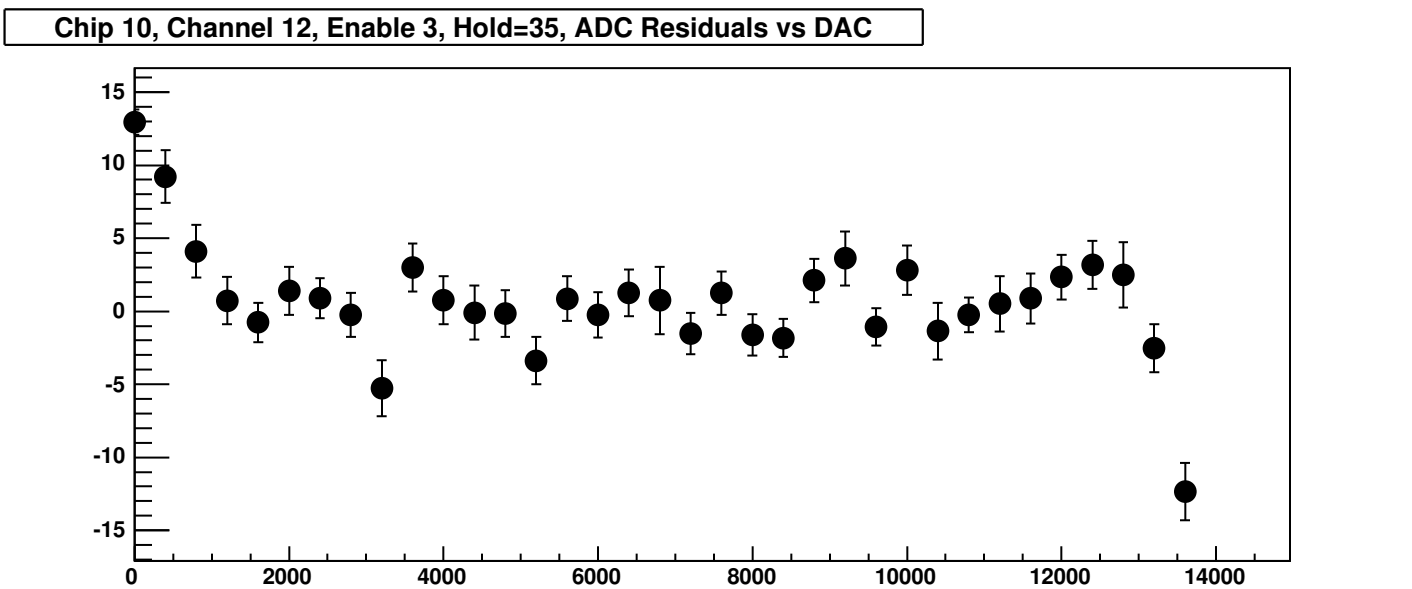

Chip 10, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 3698 / 23
p0 116.5 ± 1.476
p1 -0.3153 ± 0.0005189
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

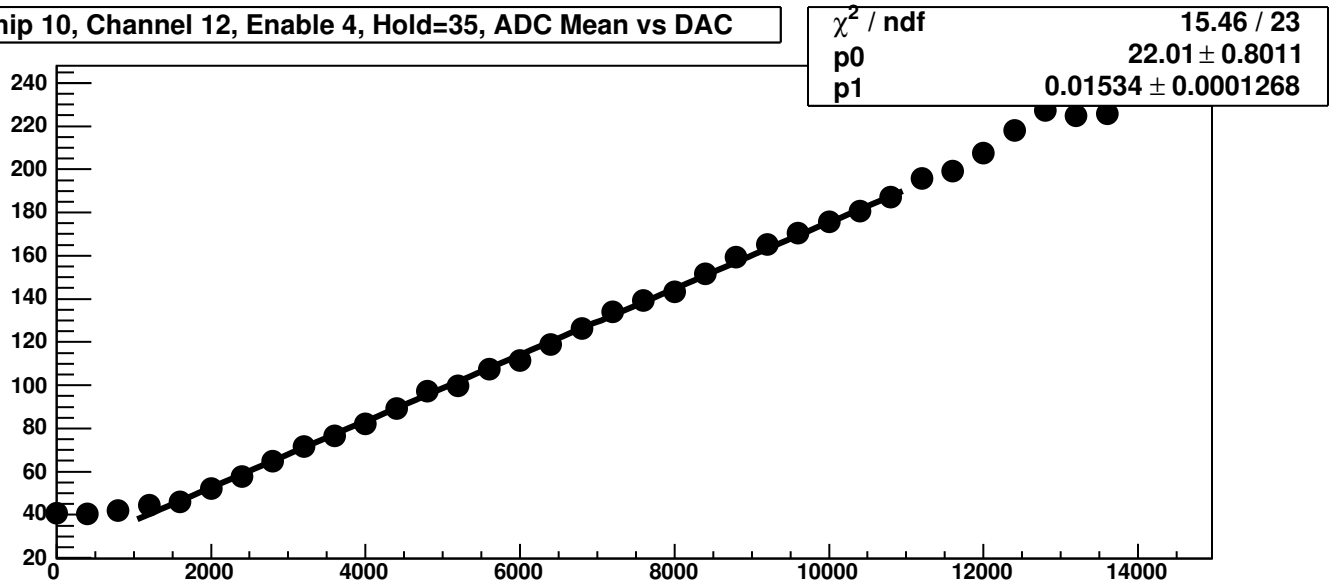

Chip 10, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 33.06 / 23
p0 22.04 ± 0.7084
p1 0.009631 ± 0.0001038
160
140
120
100
80
60
40
0
2000
4000
6000
8000
10000
12000
14000

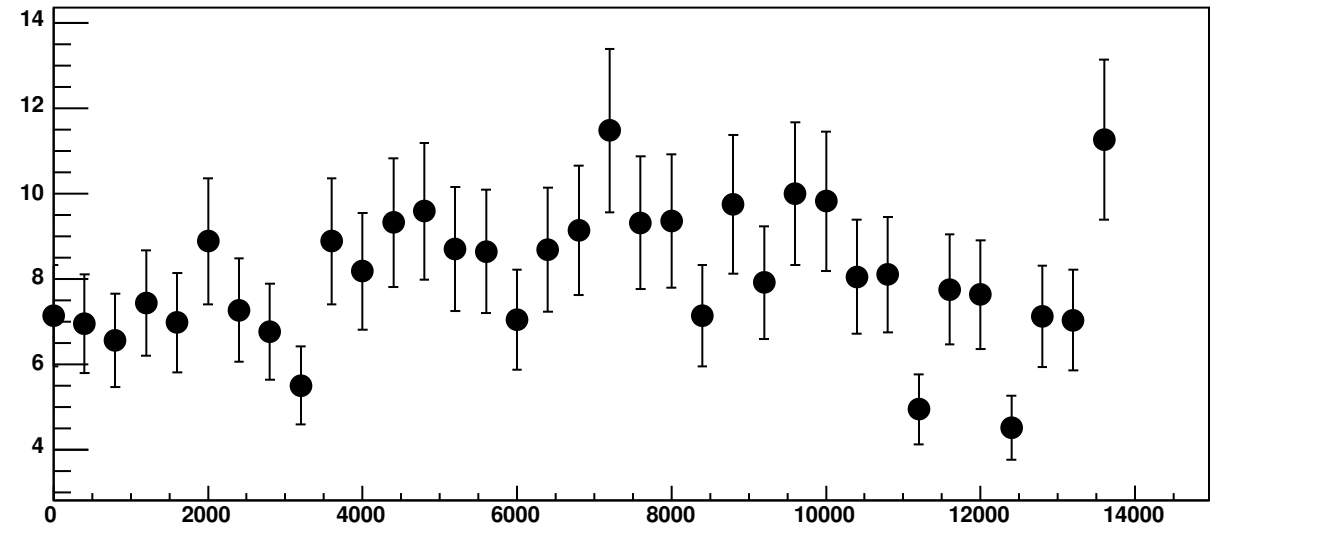

Chip 10, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

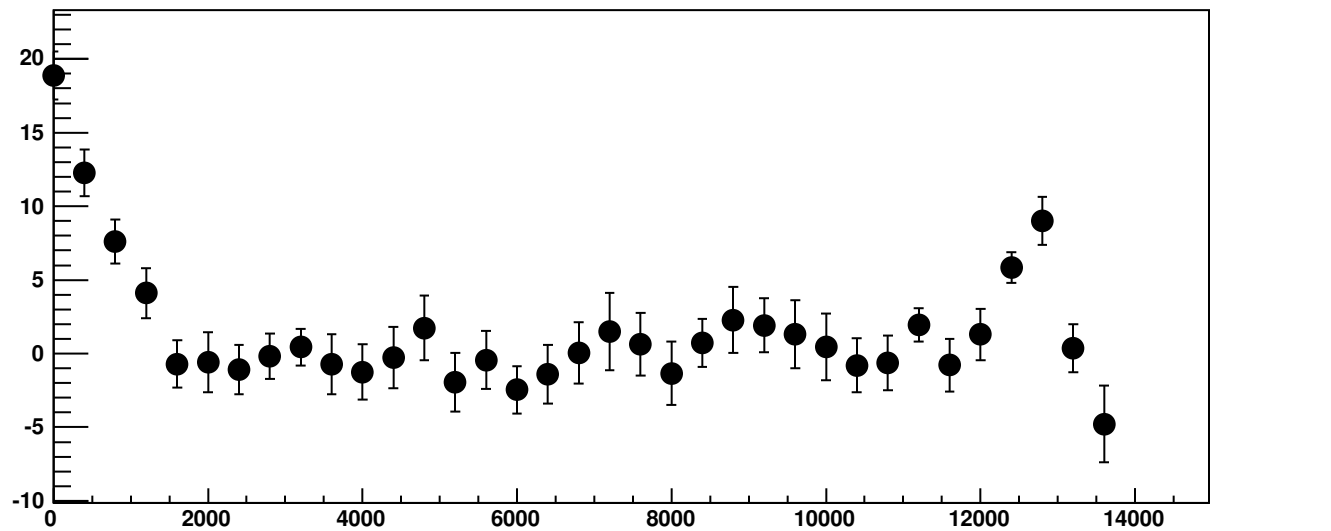
Chip 10, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC



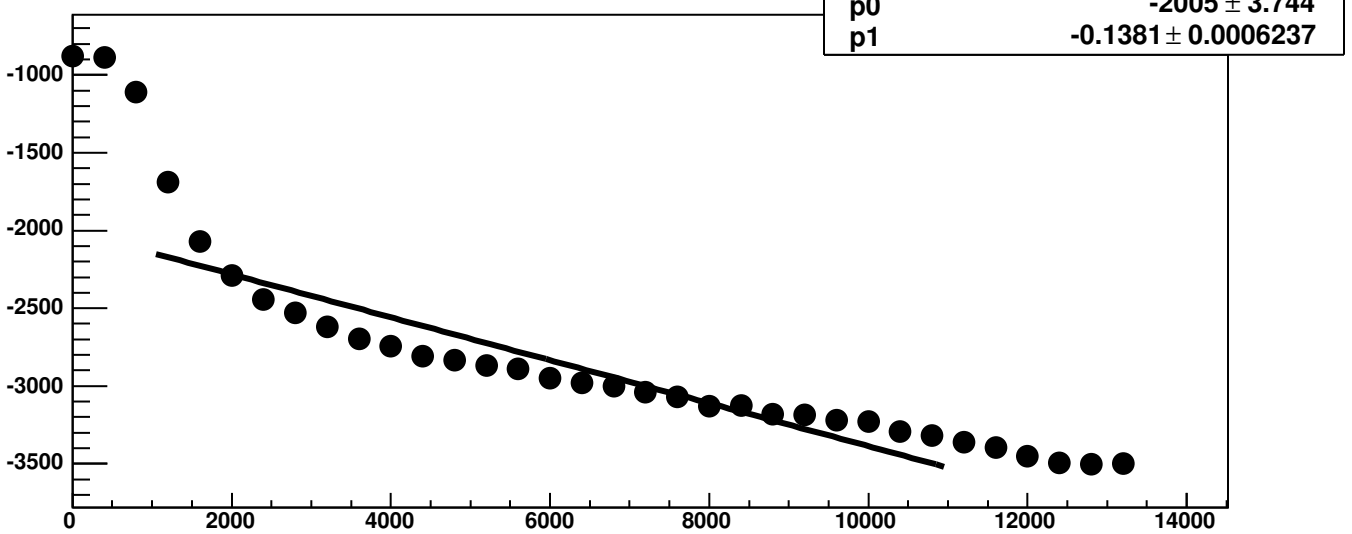
Chip 10, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC



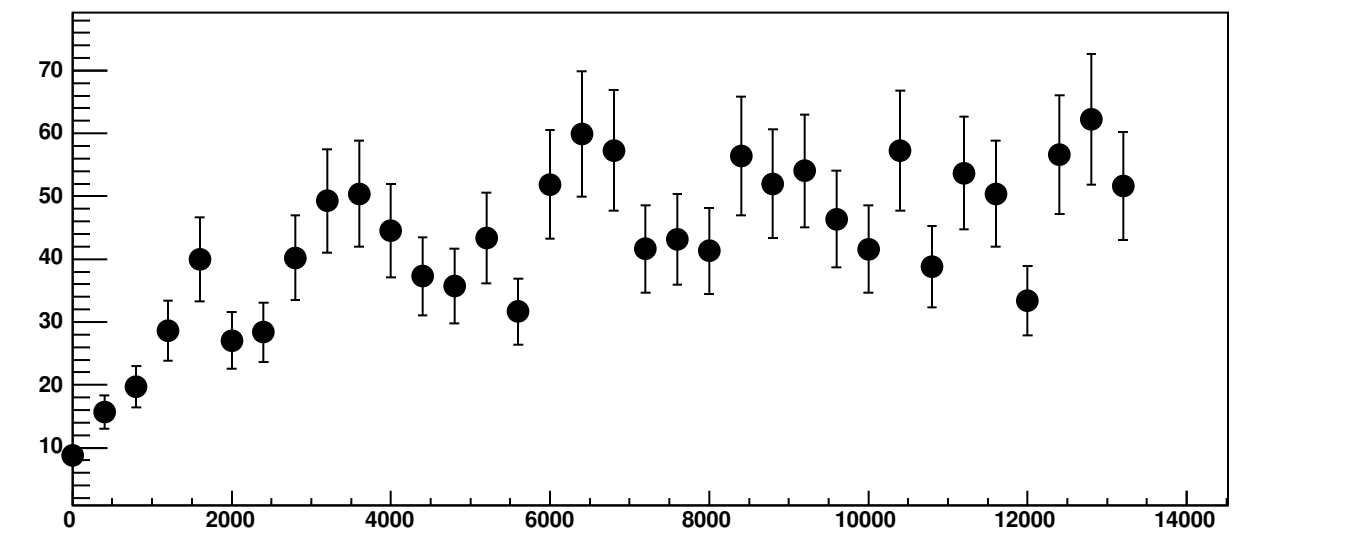
Chip 10, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC



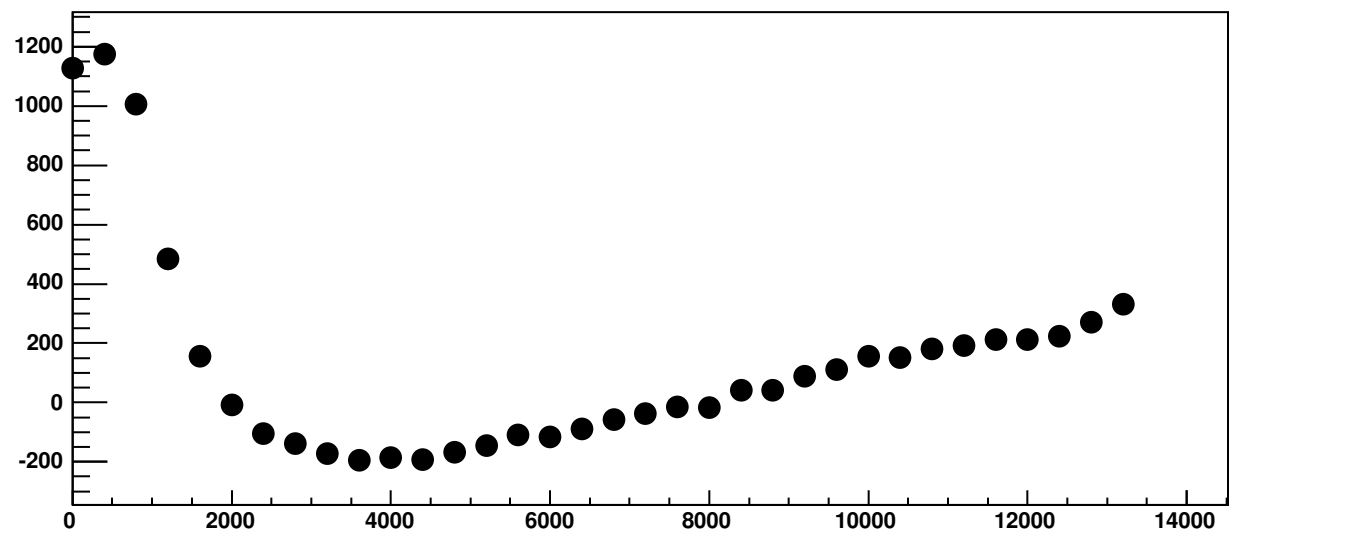
Chip 10, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

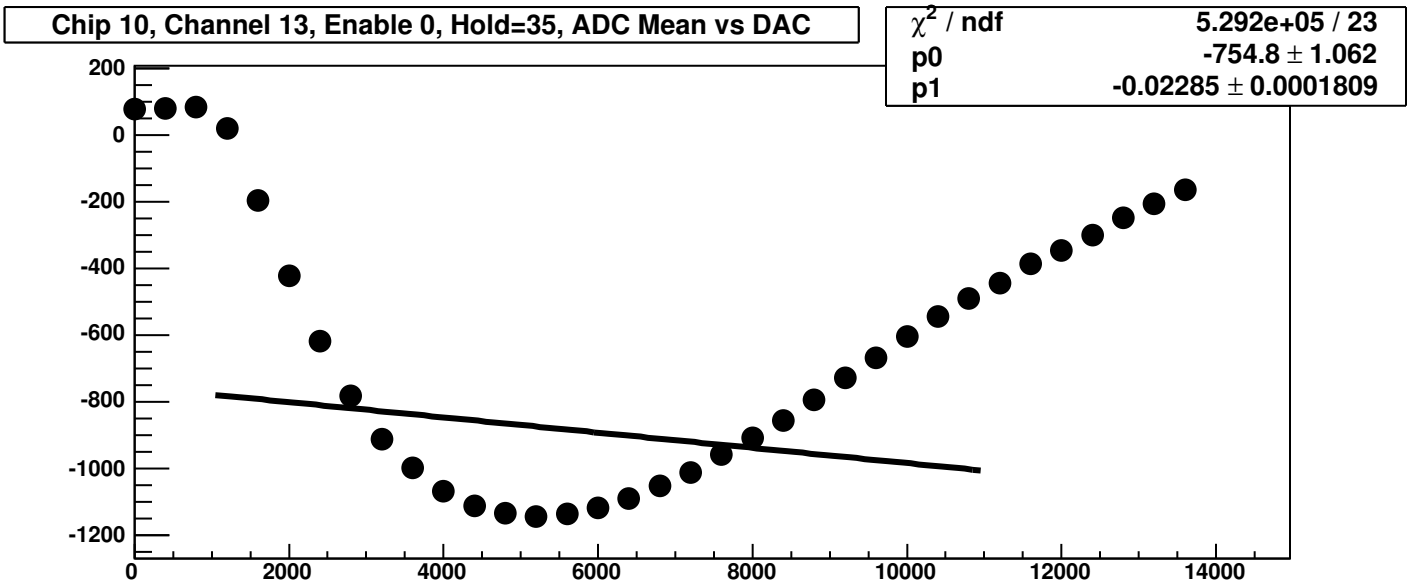


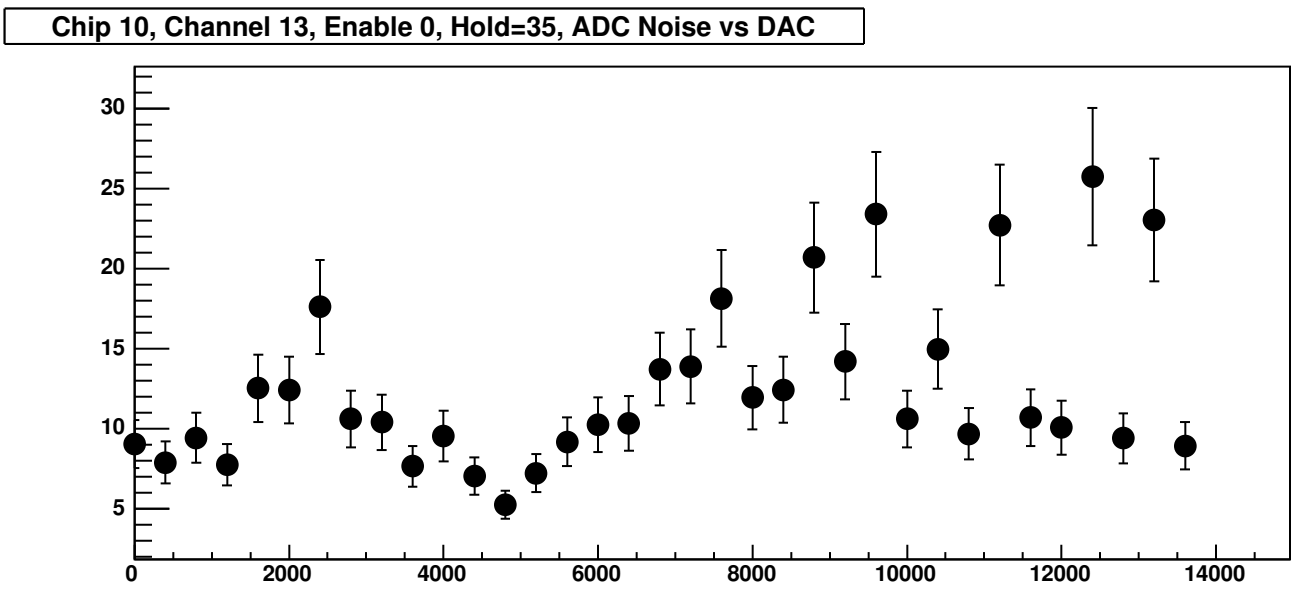
Chip 10, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

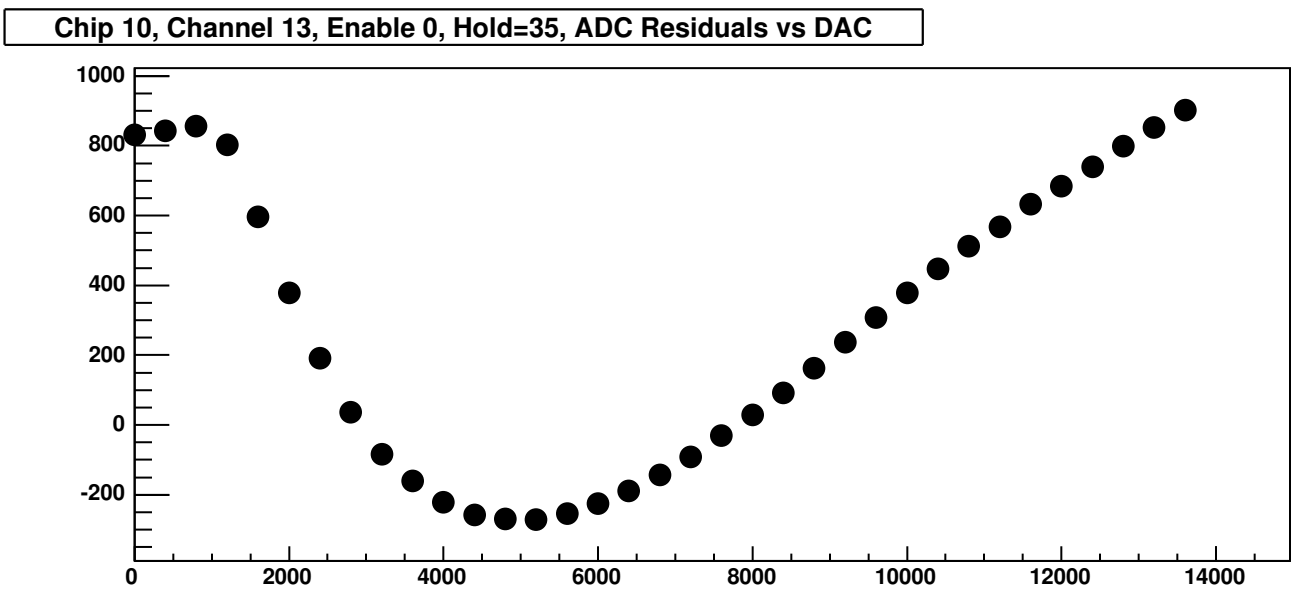


Chip 10, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

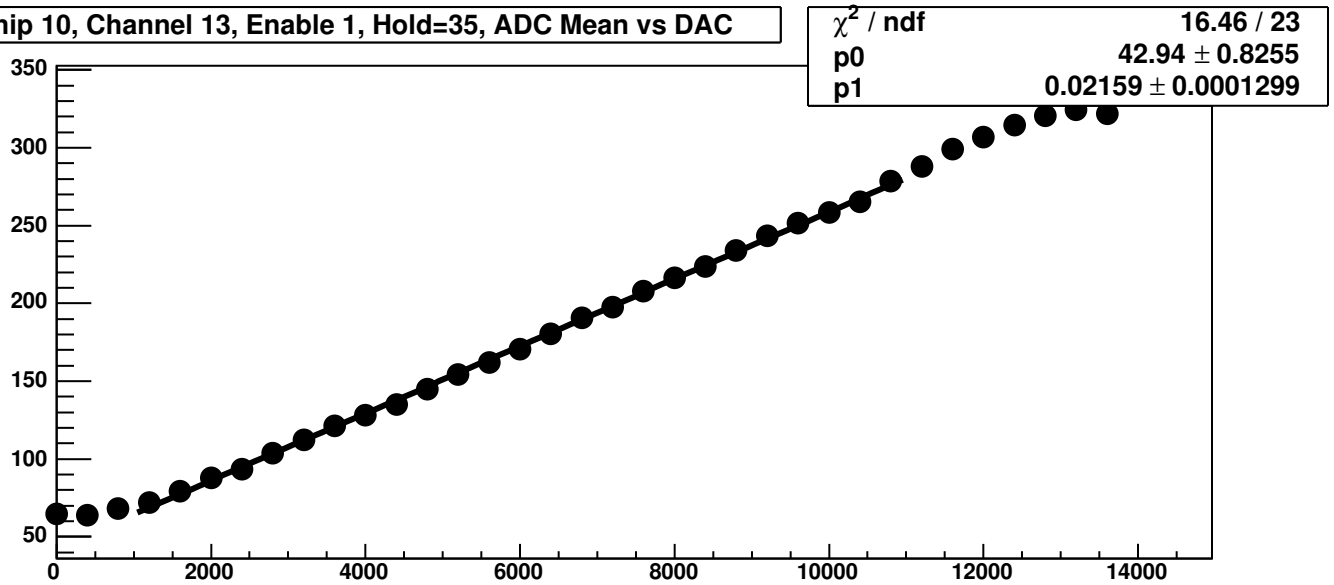



Chip 10, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.292e+05 / 23
p0 -754.8 ± 1.062
p1 -0.02285 ± 0.0001809
200
0
-200
-400
-600
-800
-1000
-1200
0
2000
4000
6000
8000
10000
12000
14000

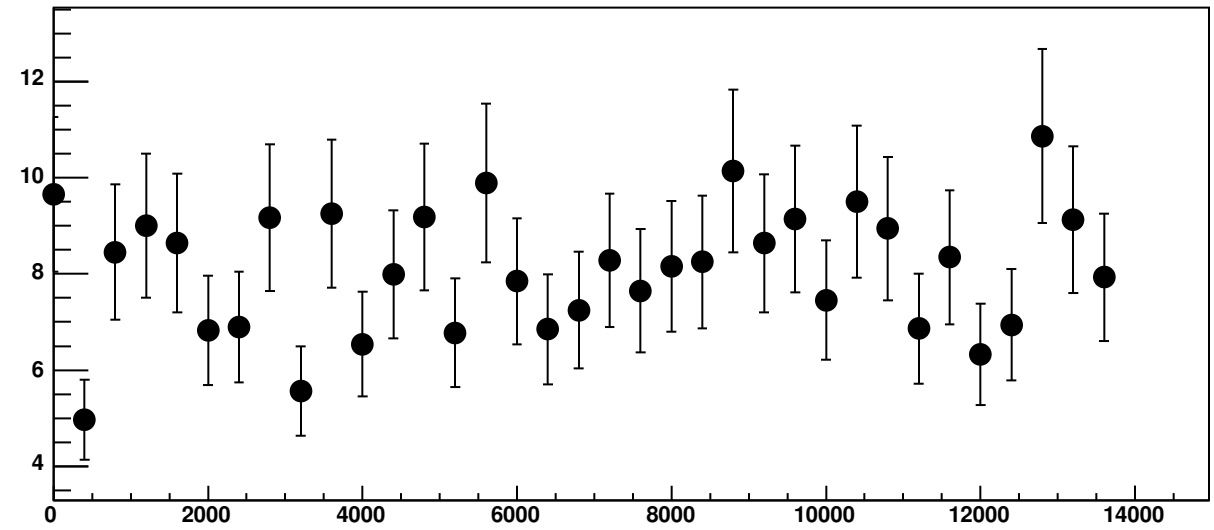

Chip 10, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
1000
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000

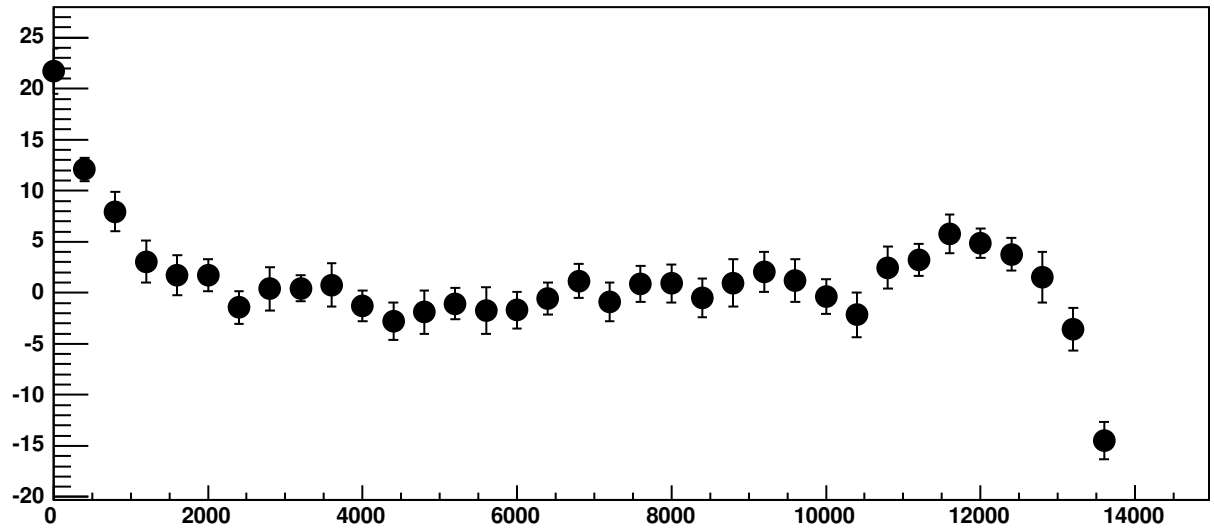
Chip 10, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC

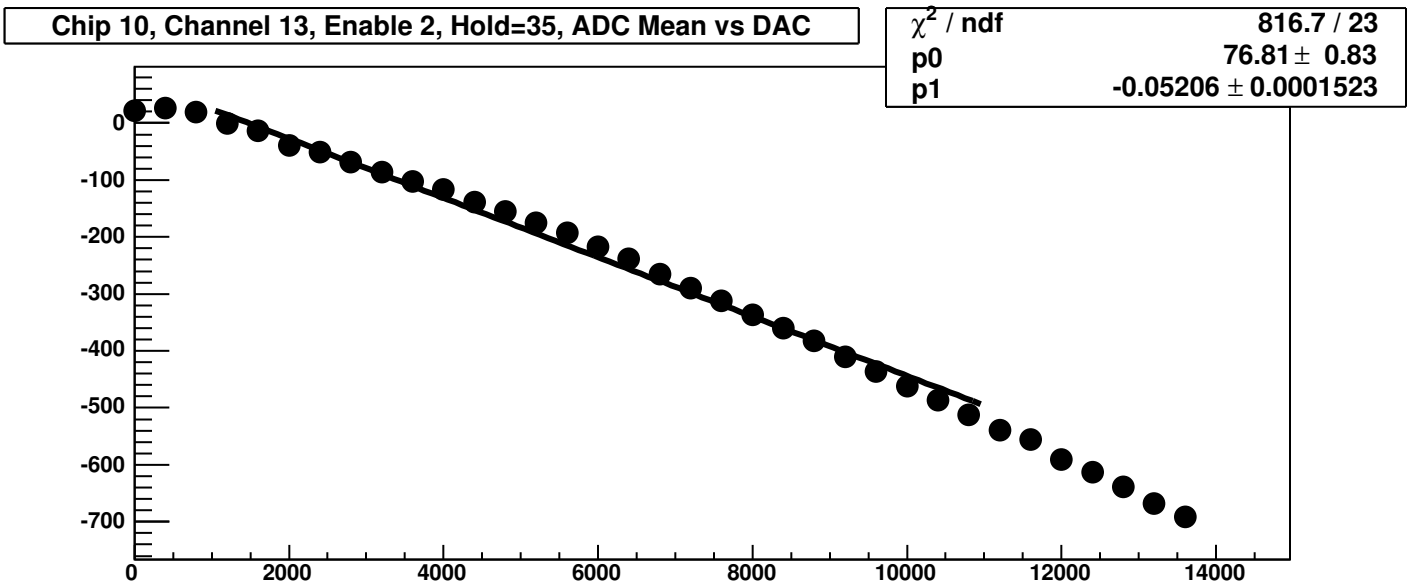


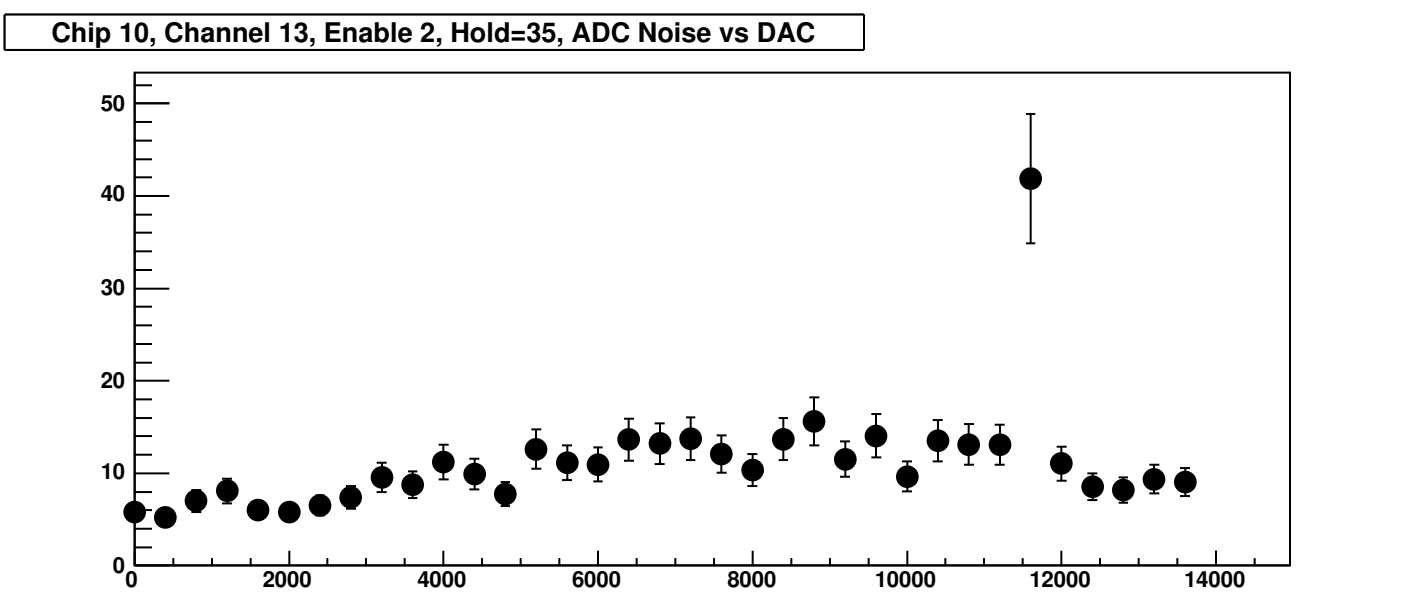
Chip 10, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC

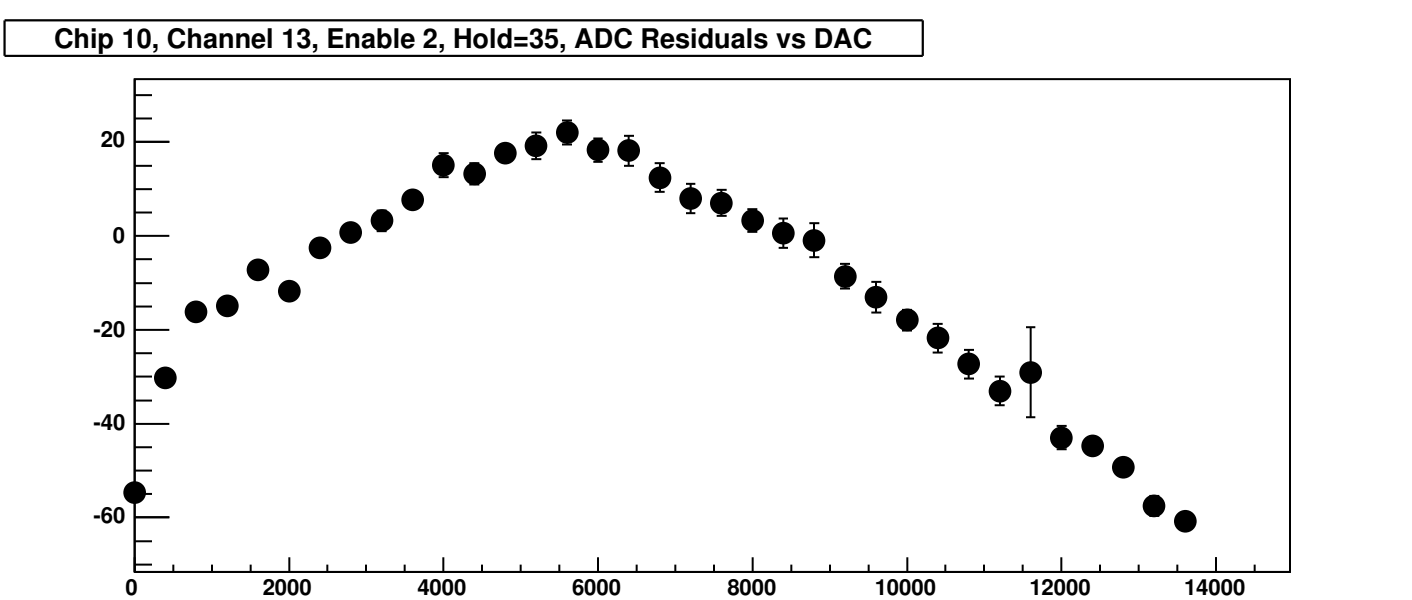


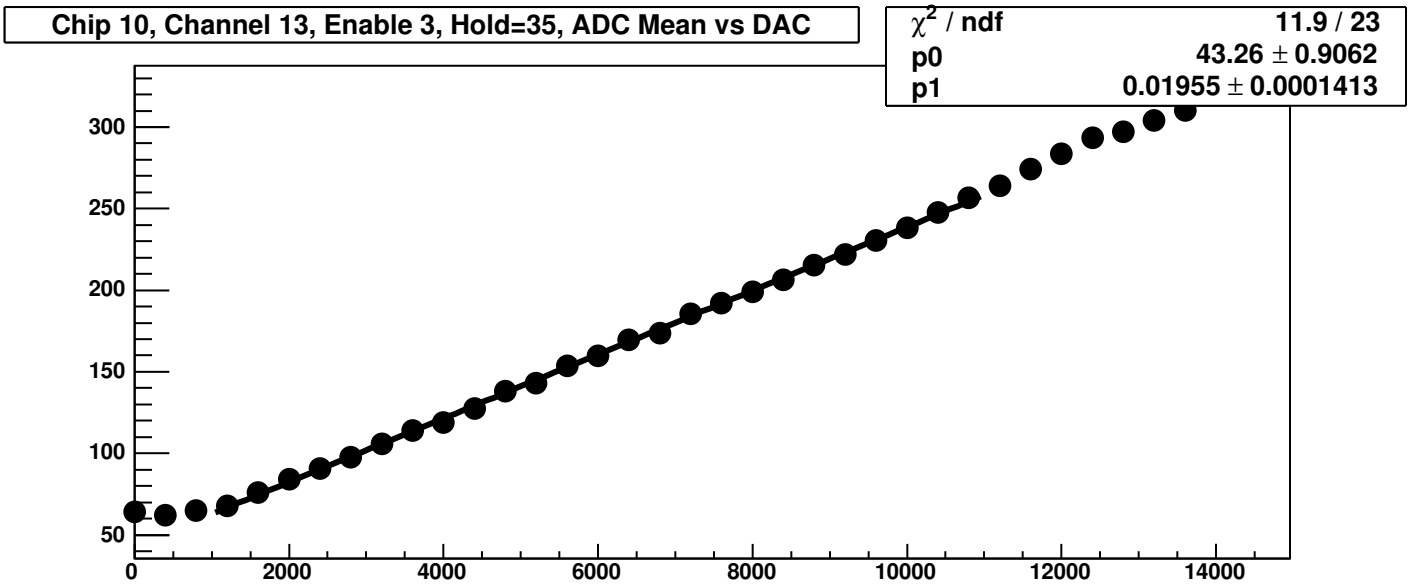
Chip 10, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC

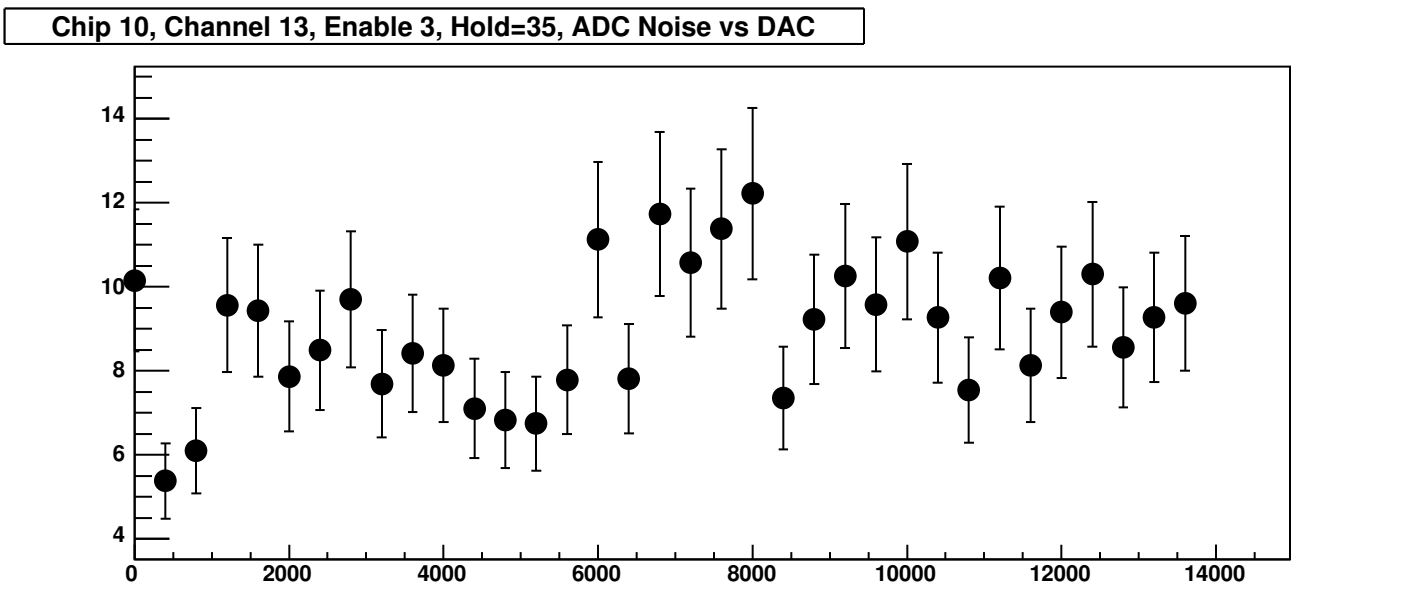


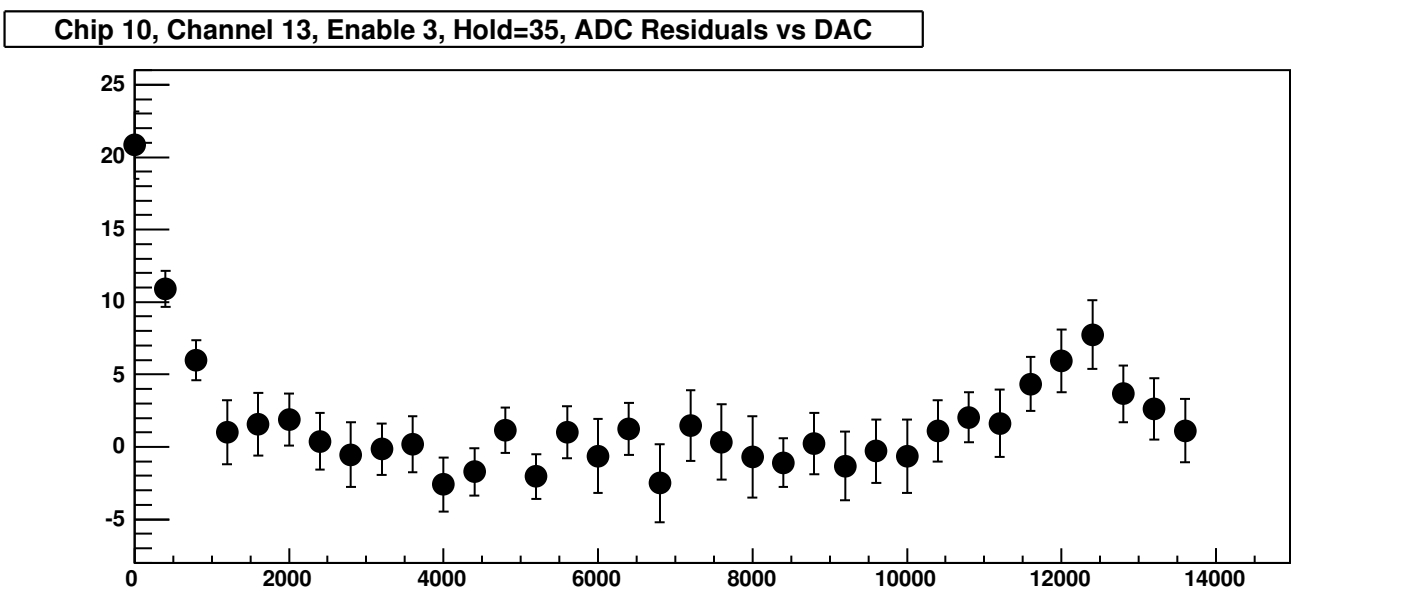

Chip 10, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 816.7 / 23
p0 76.81 ± 0.83
p1 -0.05206 ± 0.0001523
0
-100
-200
-300
-400
-500
-600
-700
0
2000
4000
6000
8000
10000
12000
14000

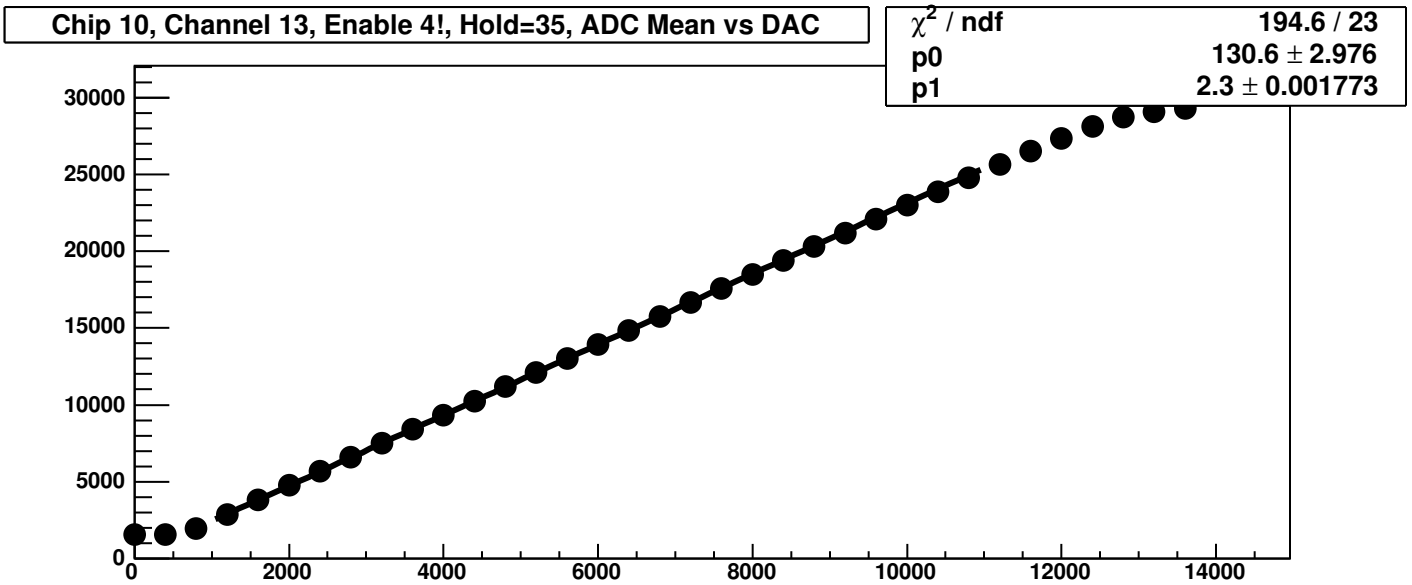

Chip 10, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

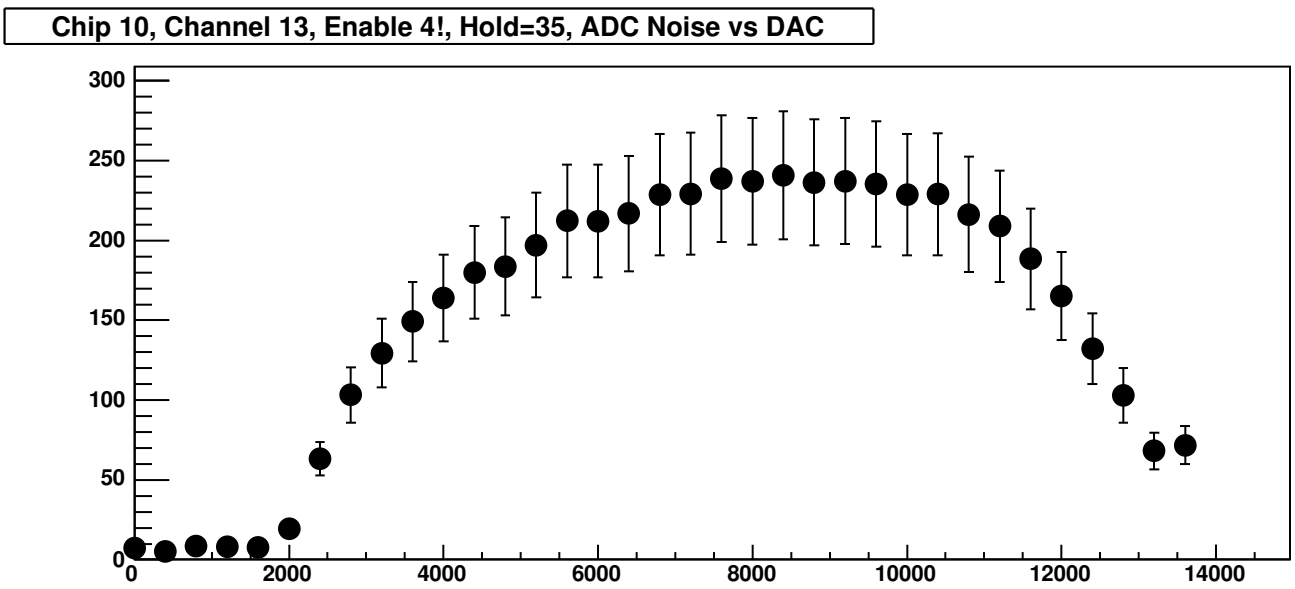

Chip 10, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
0
2000
4000
6000
8000
10000
12000
14000

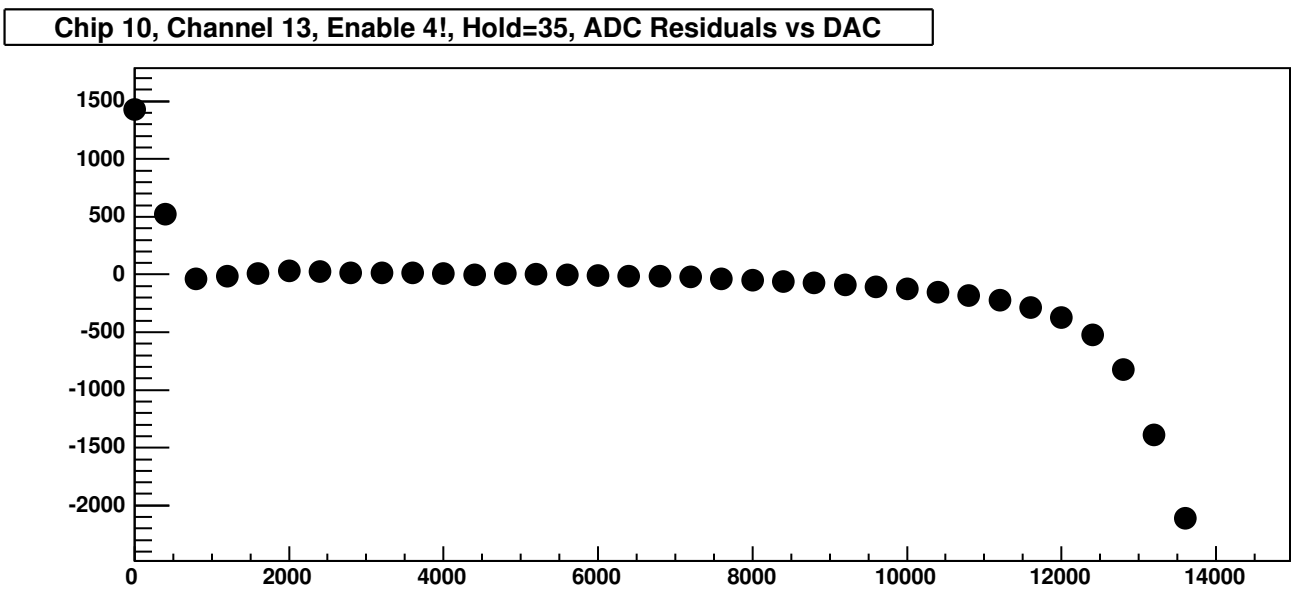

Chip 10, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 11.9 / 23
p0 43.26 ± 0.9062
p1 0.01955 ± 0.0001413
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

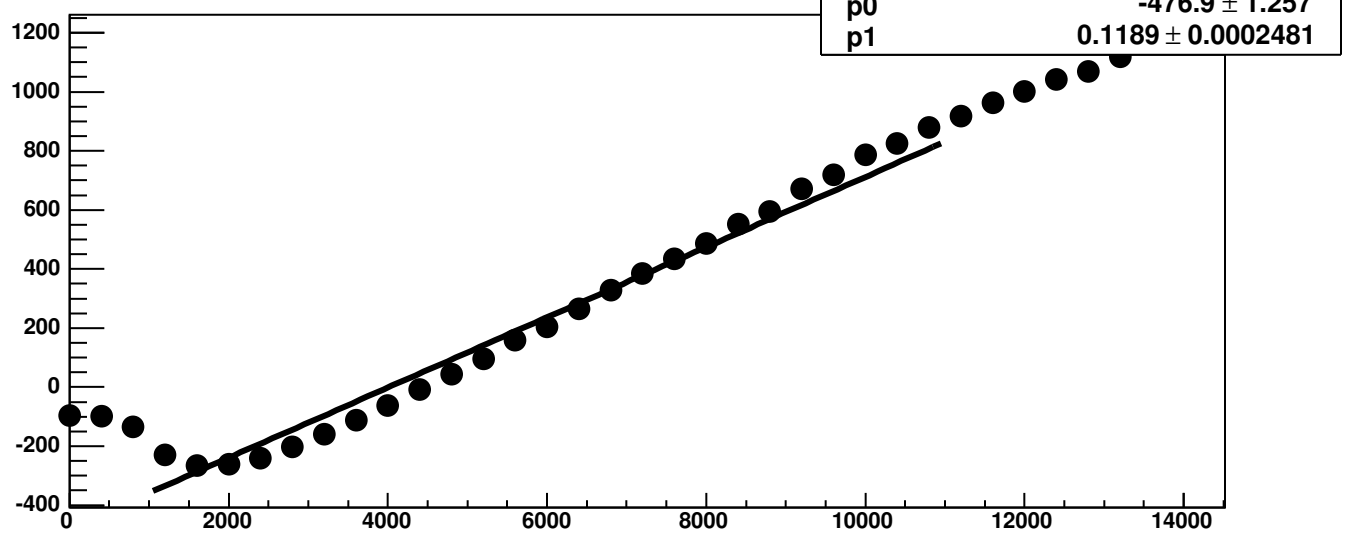

Chip 10, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
25
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 194.6 / 23
p0 130.6 ± 2.976
p1 2.3 ± 0.001773
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

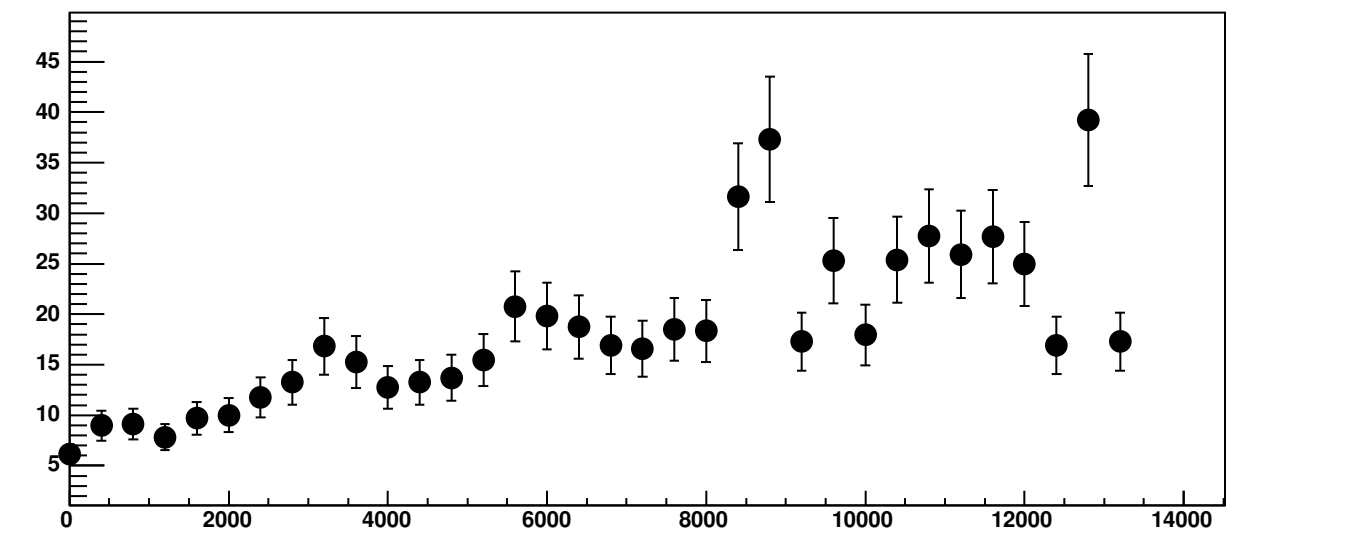

Chip 10, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

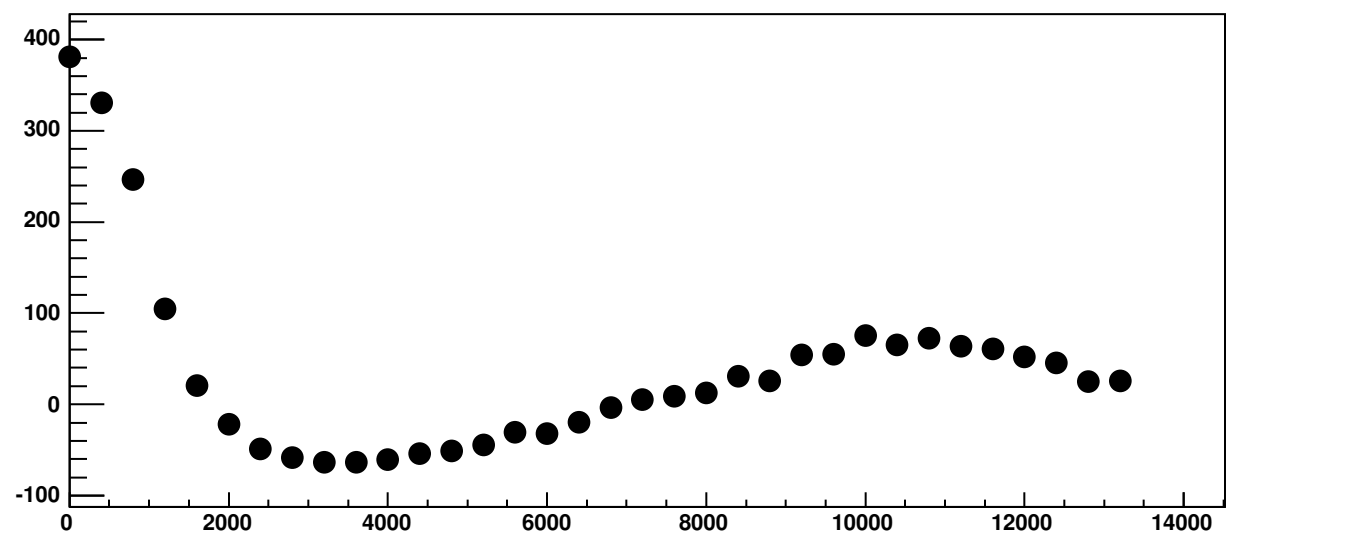
Chip 10, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

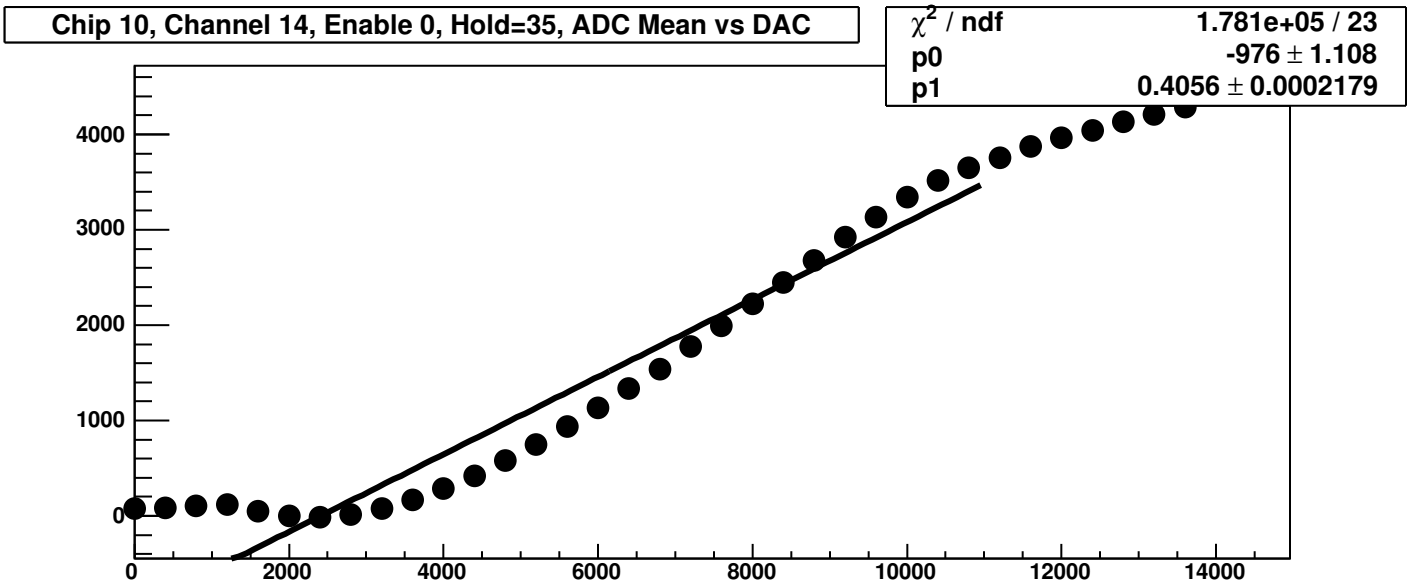


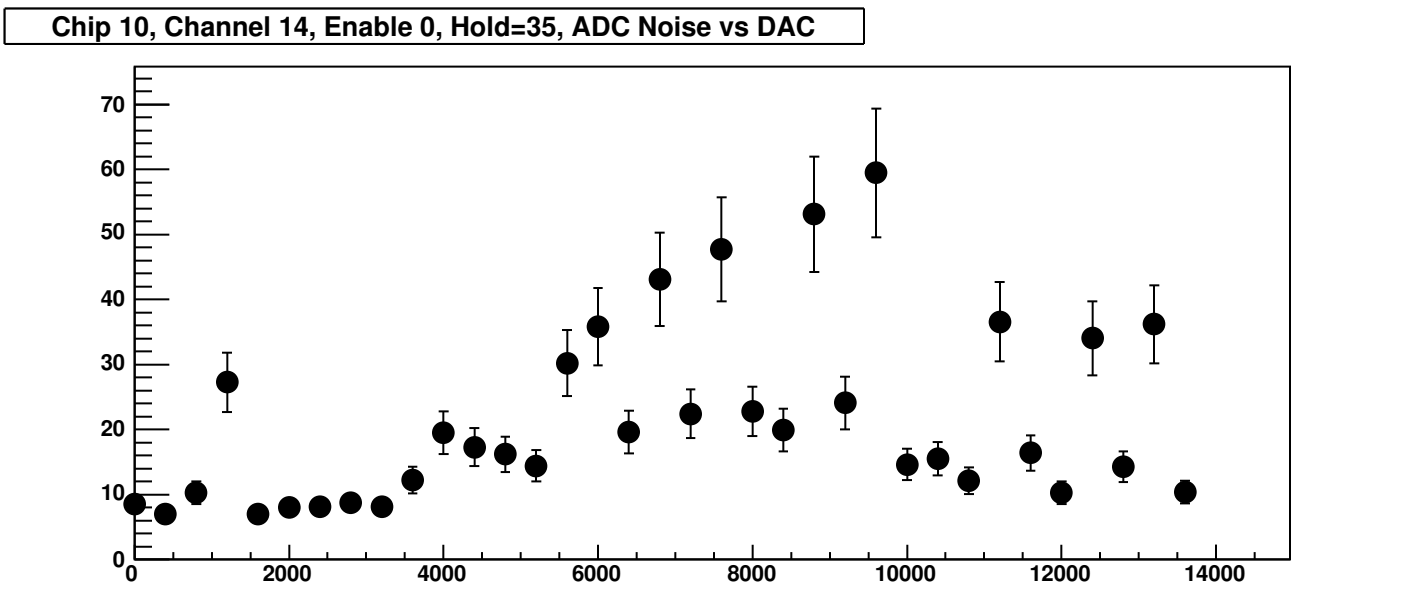
Chip 10, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

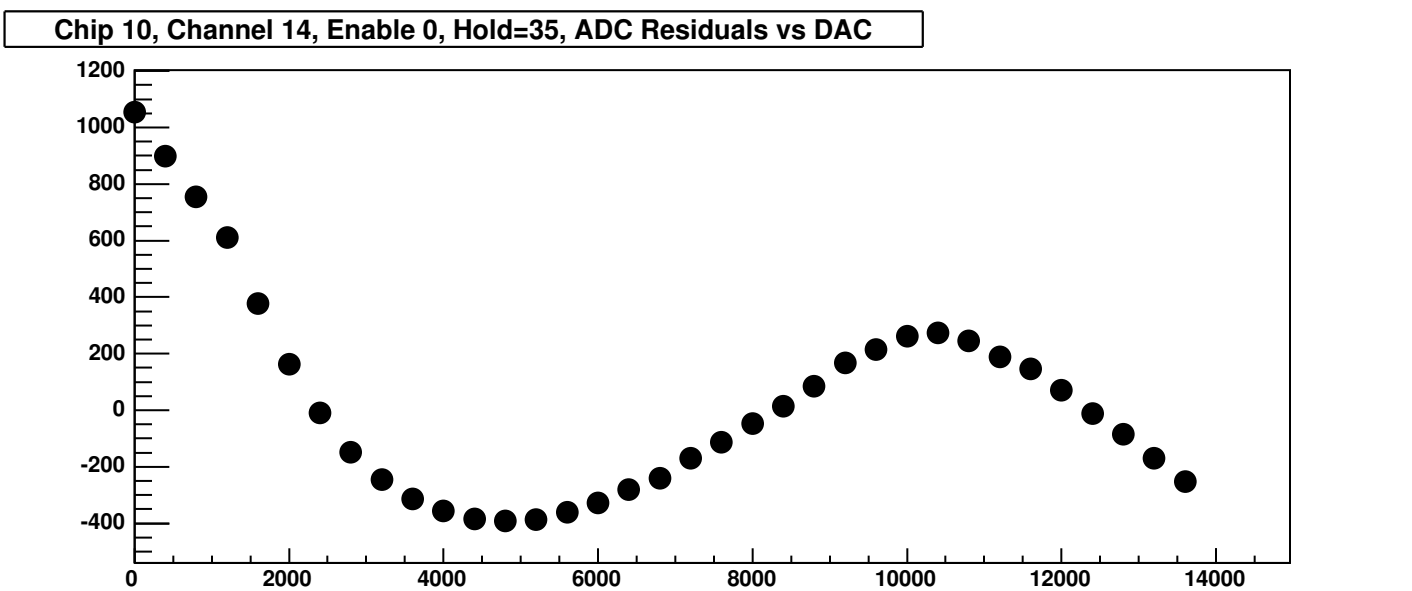


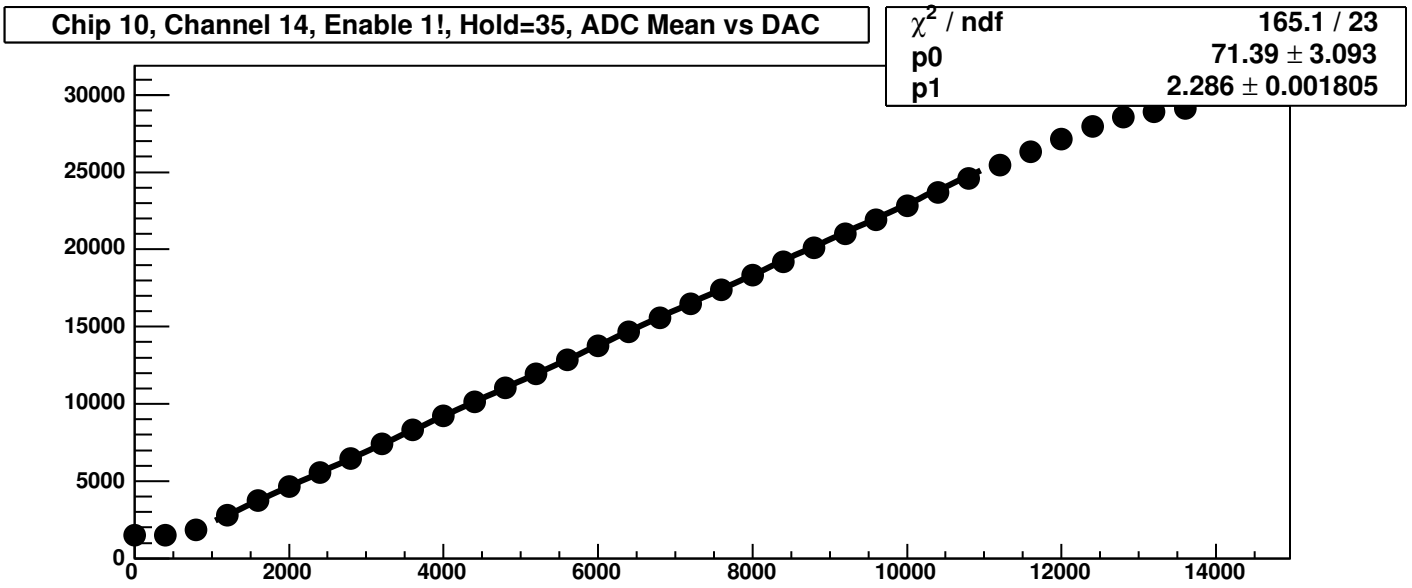
Chip 10, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

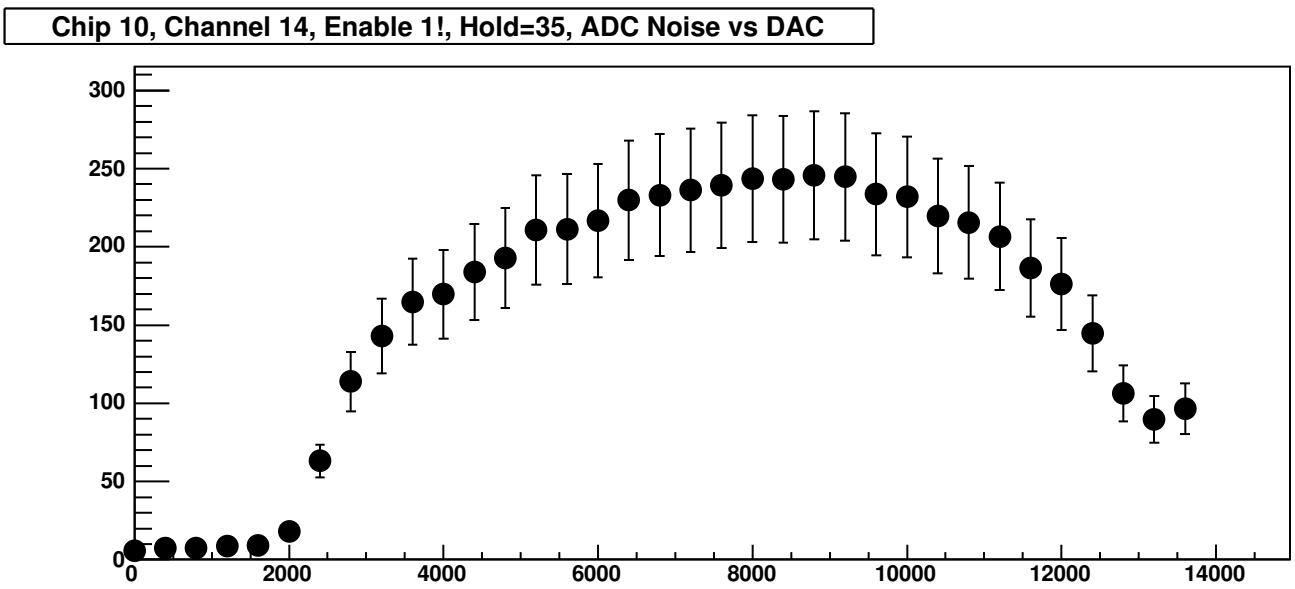


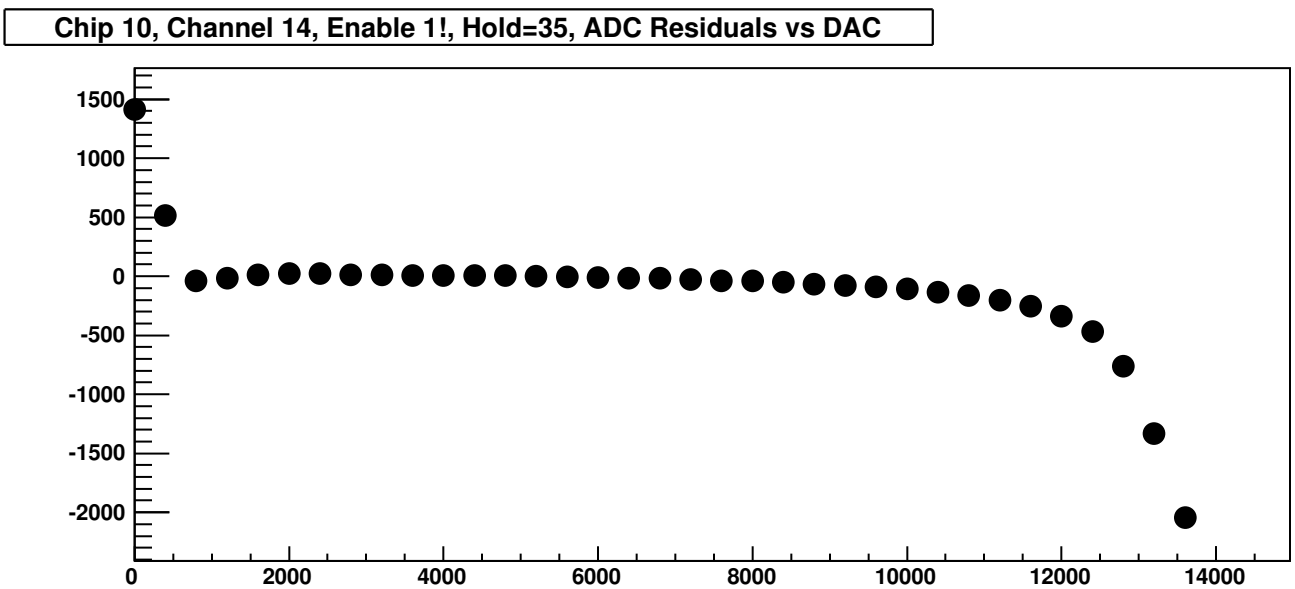

Chip 10, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.781e+05 / 23
p0 -976 ± 1.108
p1 0.4056 ± 0.0002179
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

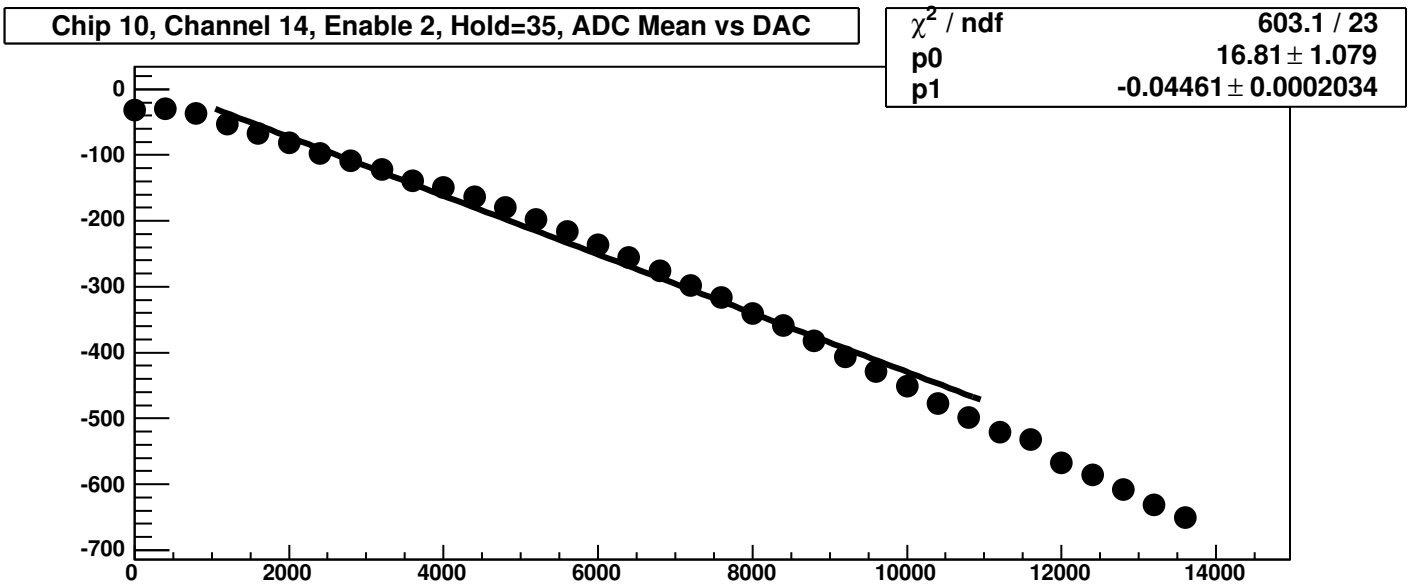

Chip 10, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

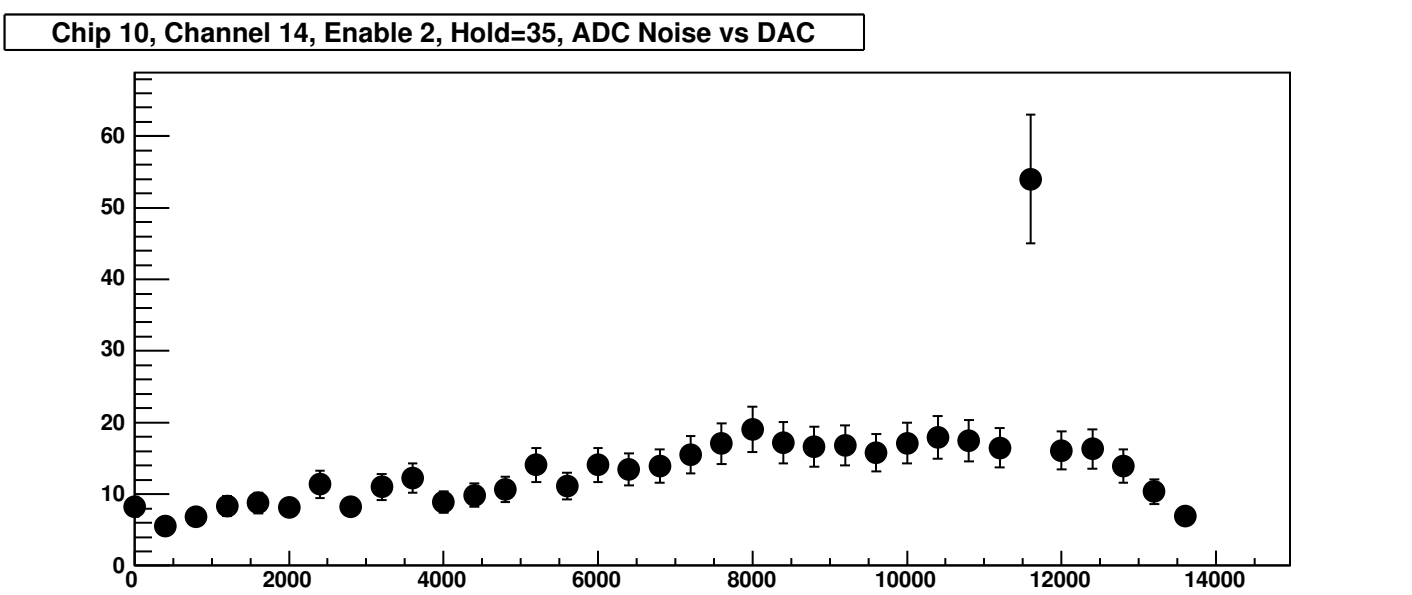

Chip 10, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
1200
1000
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

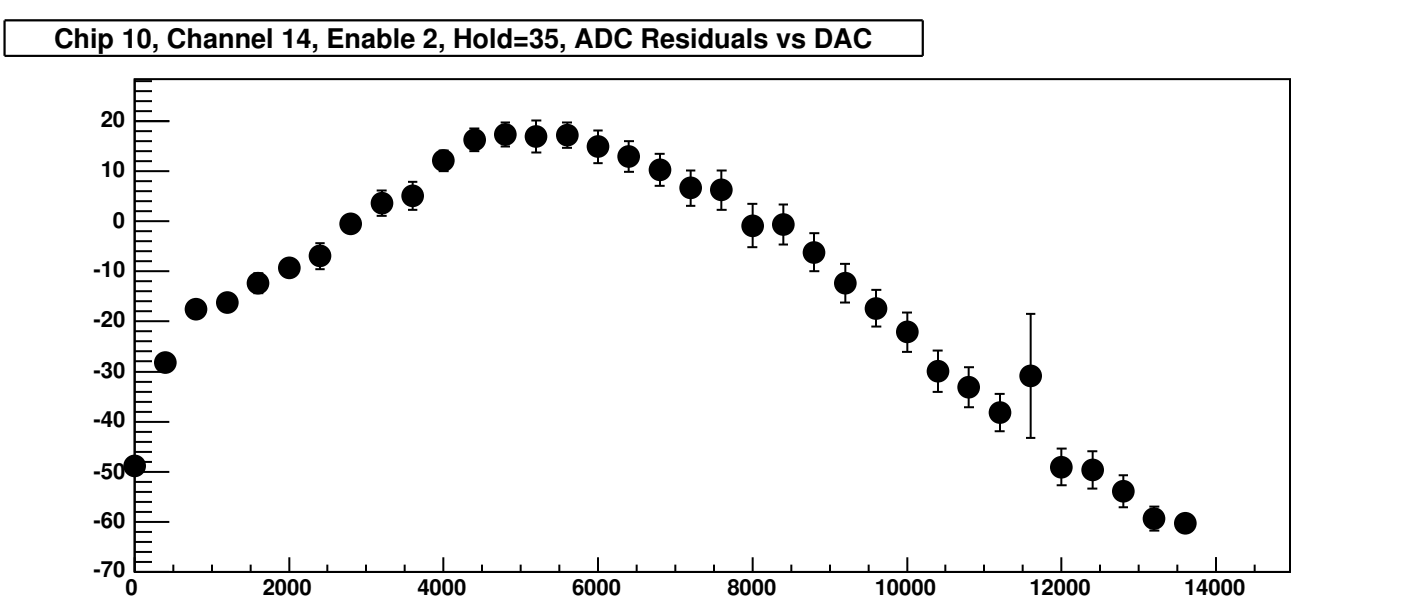

Chip 10, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 165.1 / 23
p0 71.39 ± 3.093
p1 2.286 ± 0.001805
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

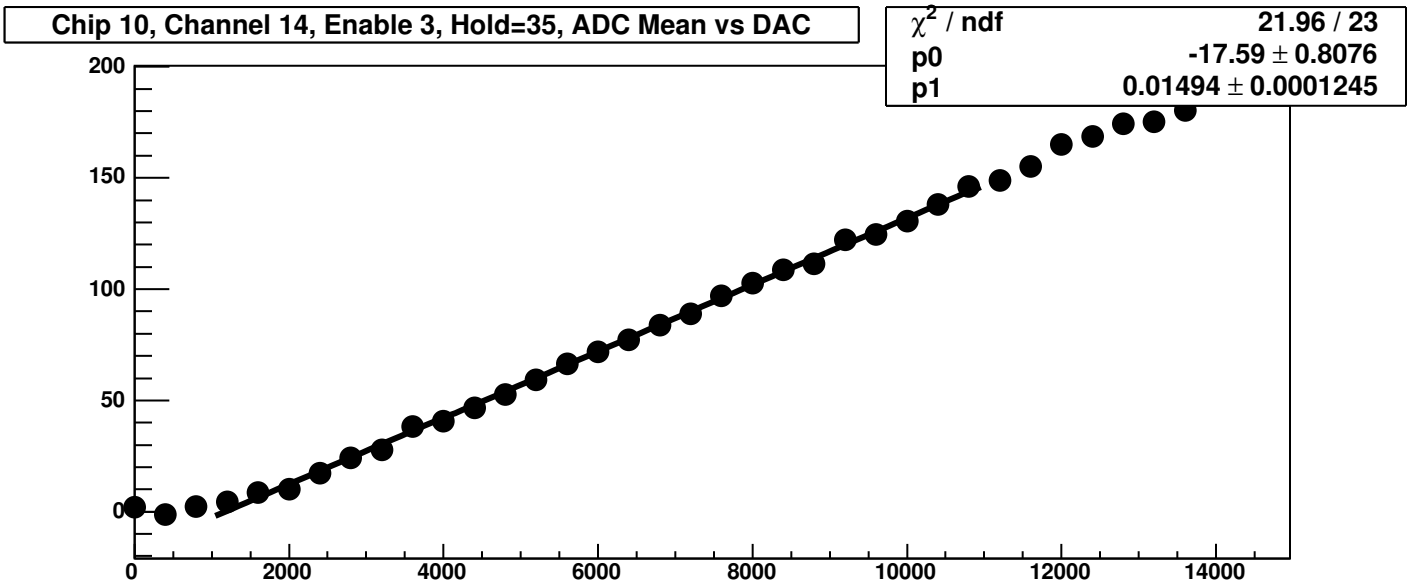

Chip 10, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

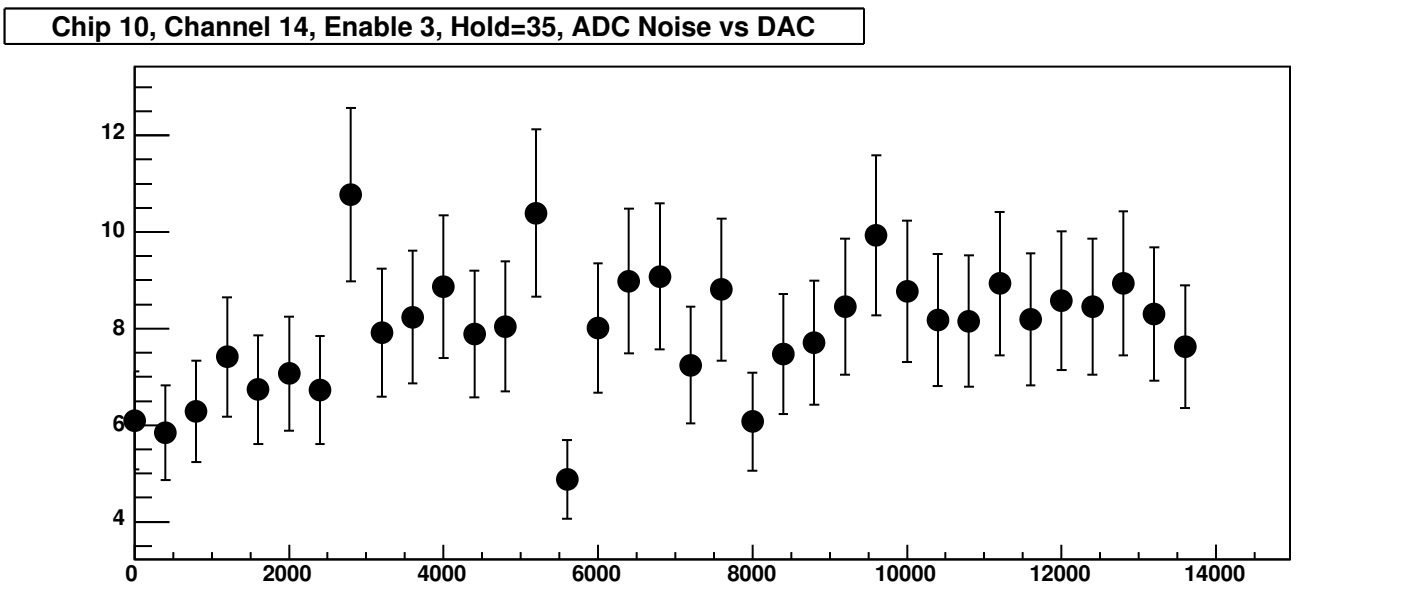

Chip 10, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

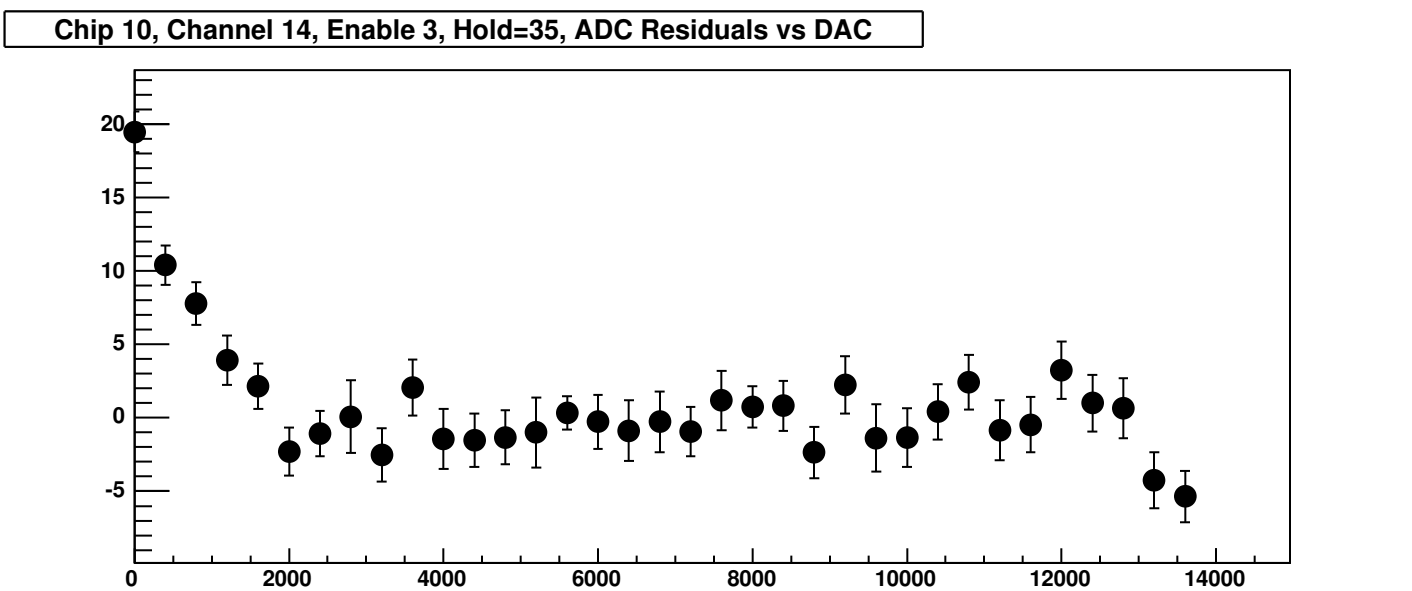

Chip 10, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 603.1 / 23
p0 16.81 ± 1.079
p1 -0.04461 ± 0.0002034
0
-100
-200
-300
-400
-500
-600
-700
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

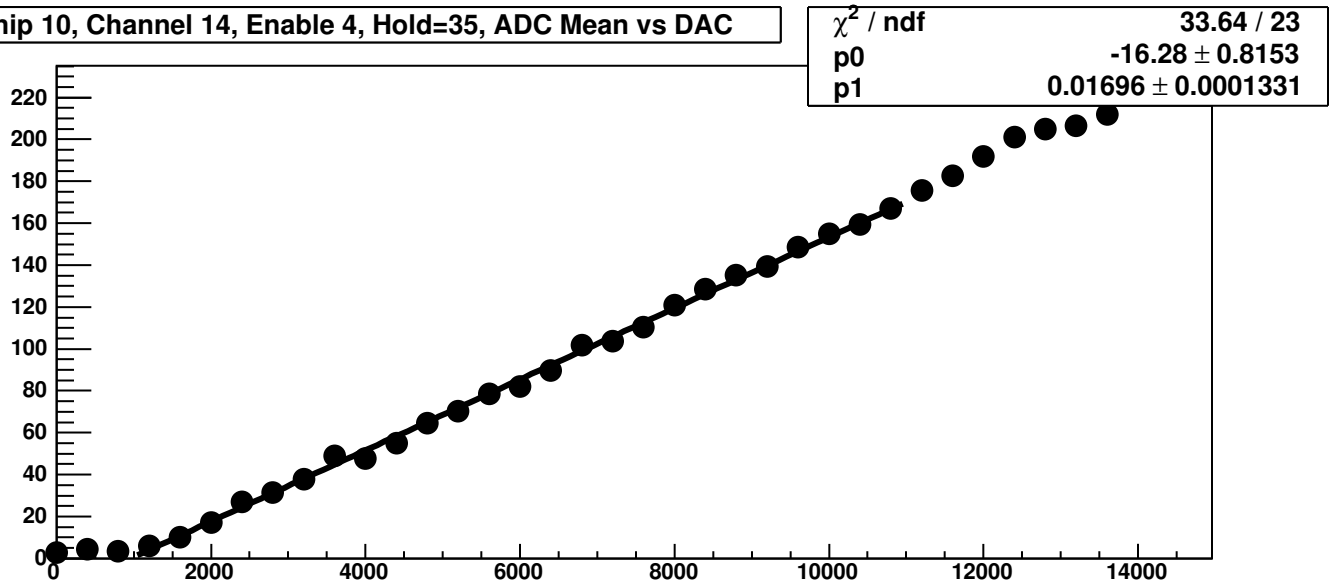

Chip 10, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
-60
-70
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.96 / 23
p0 -17.59 ± 0.8076
p1 0.01494 ± 0.0001245
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

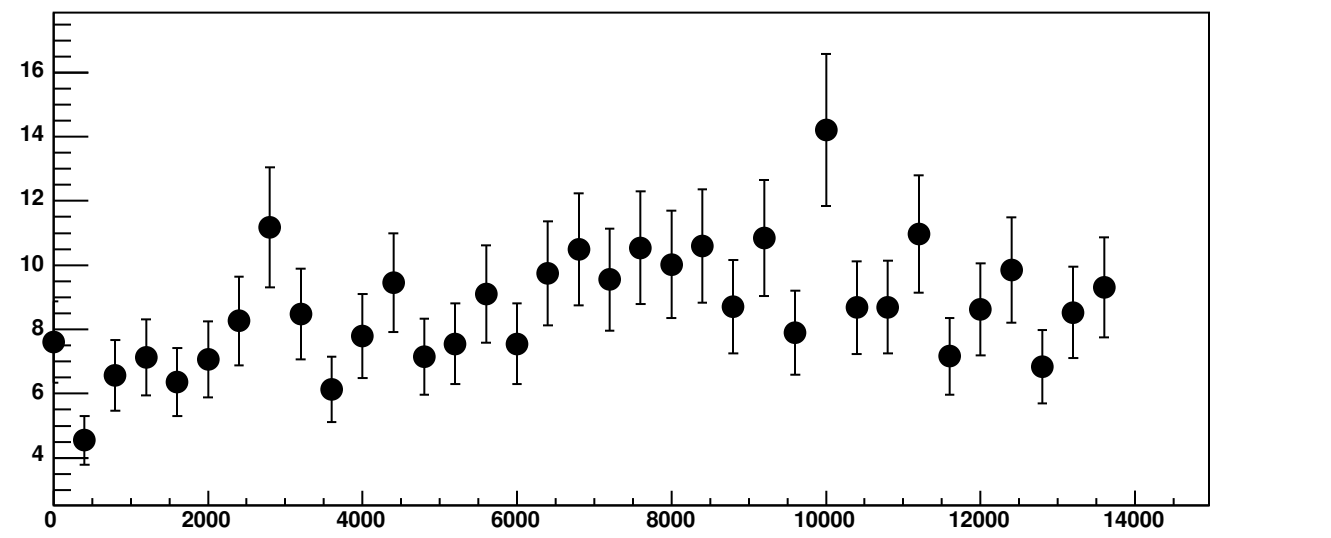

Chip 10, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

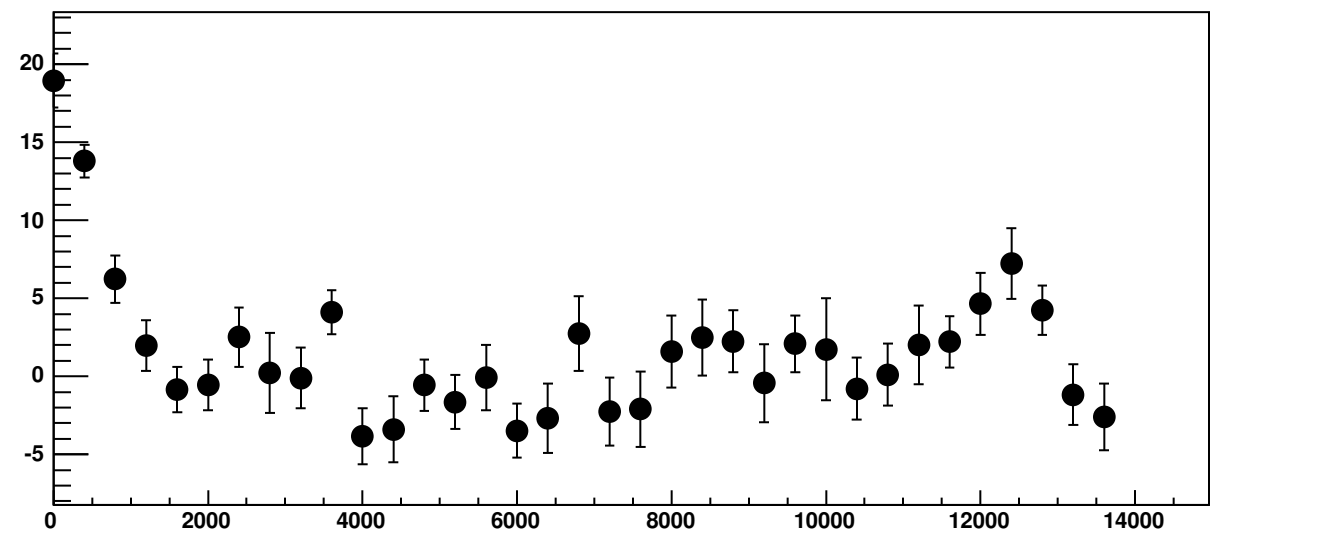
Chip 10, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC



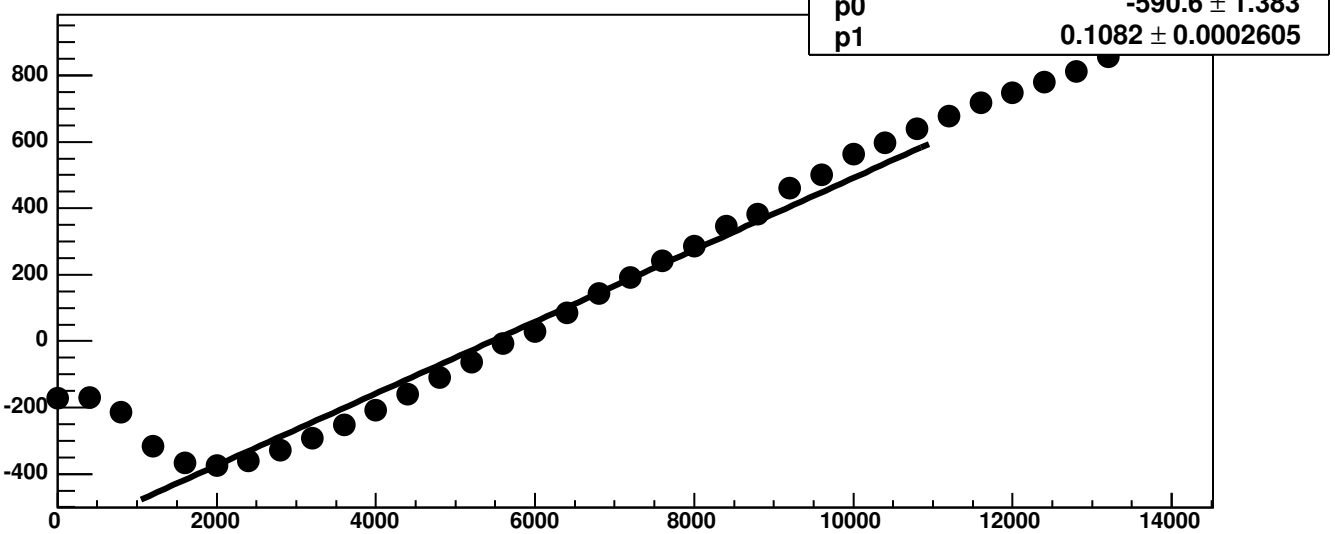
Chip 10, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC



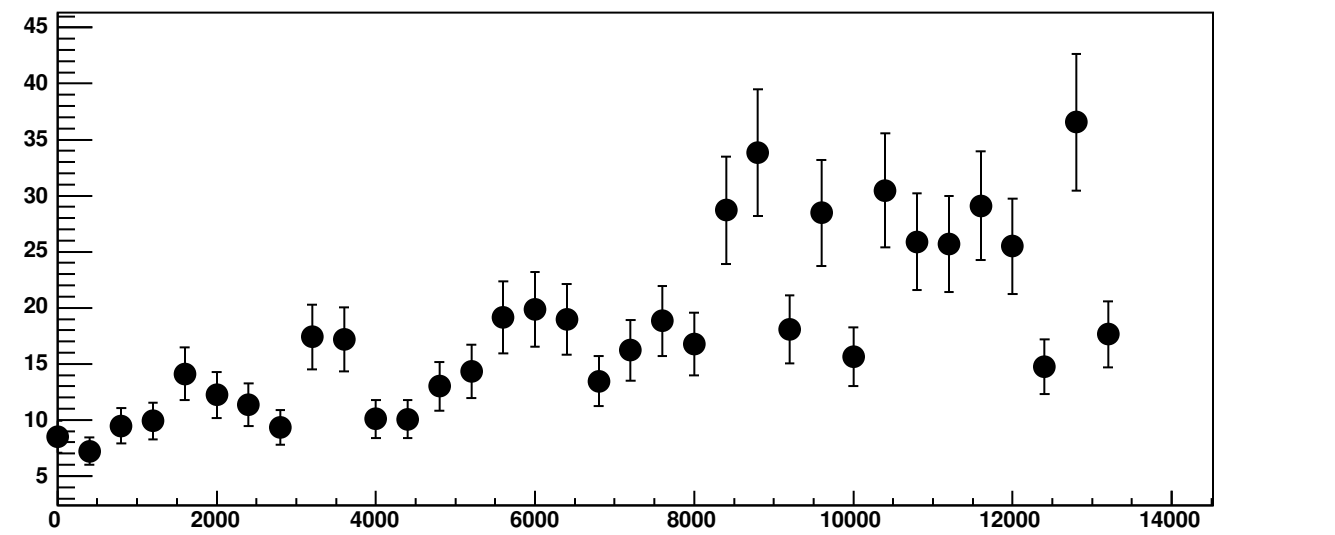
Chip 10, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC



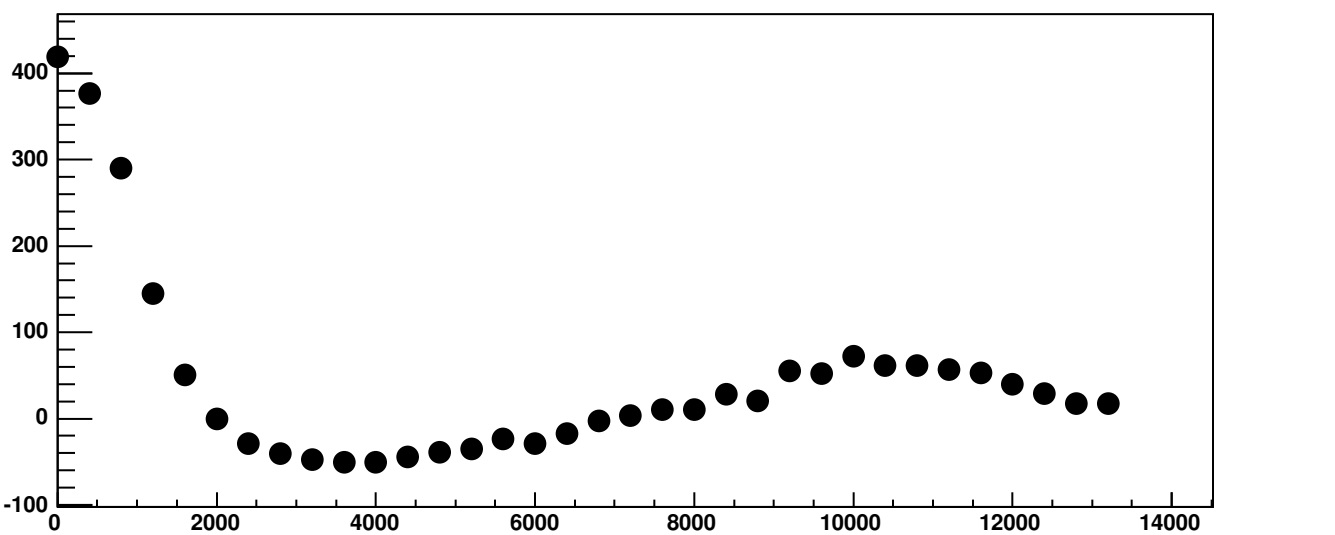
Chip 10, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



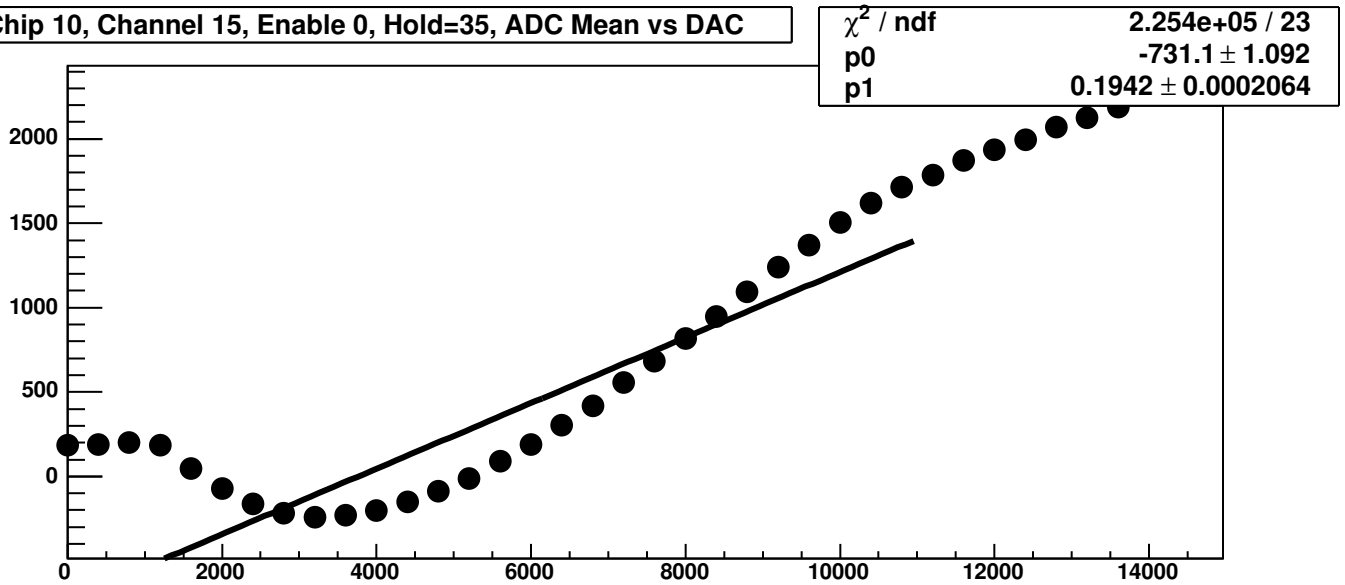
Chip 10, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



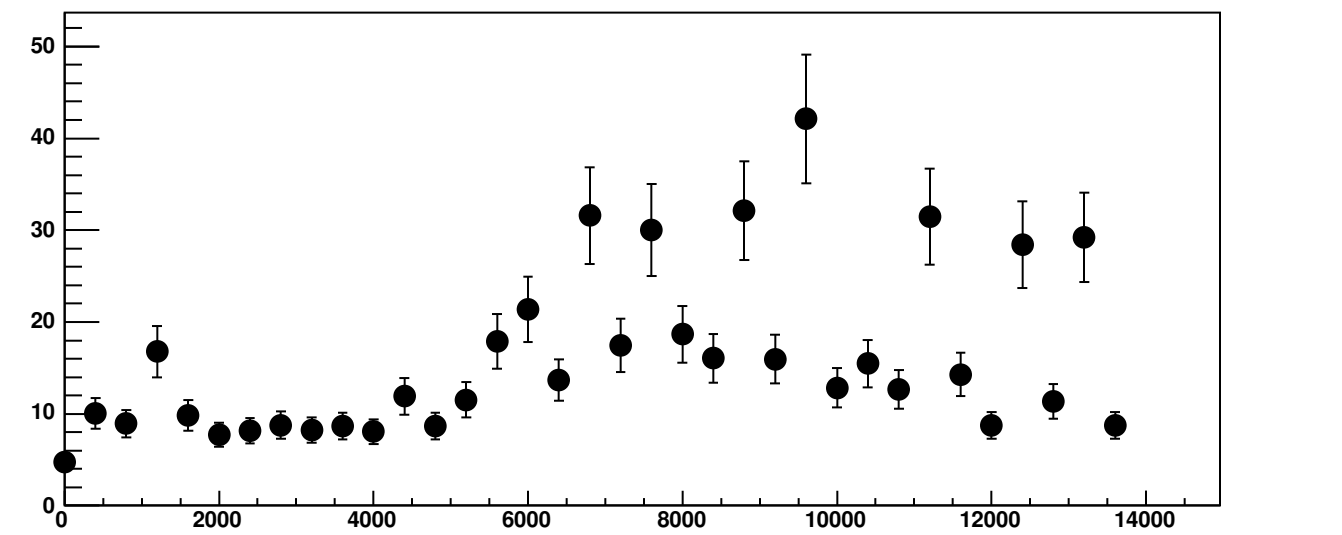
Chip 10, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



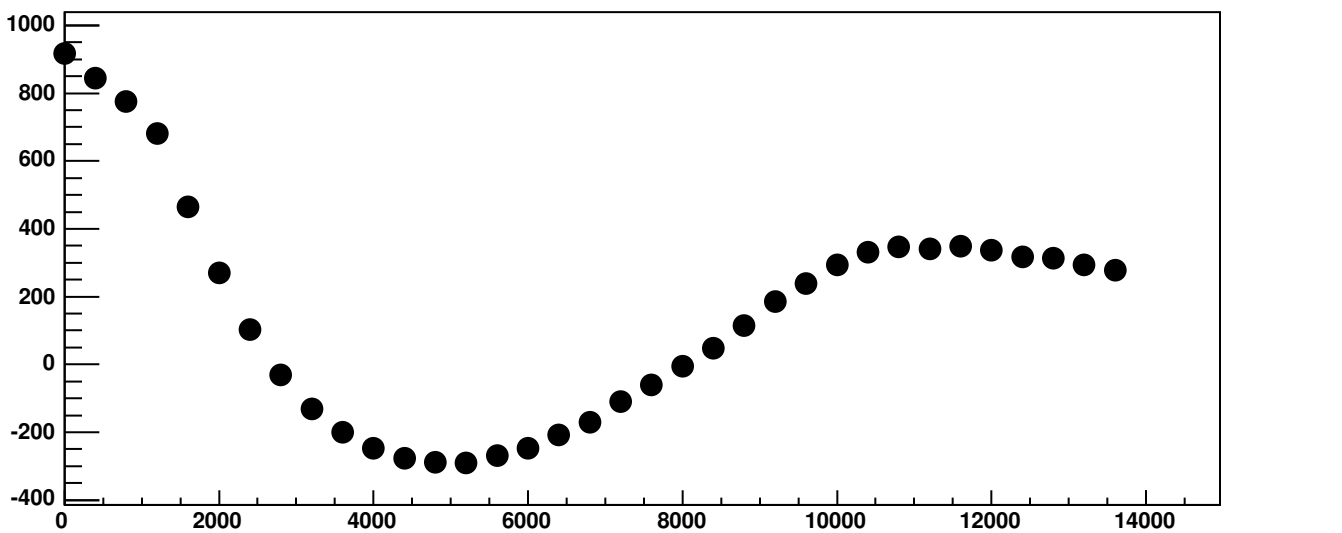
Chip 10, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

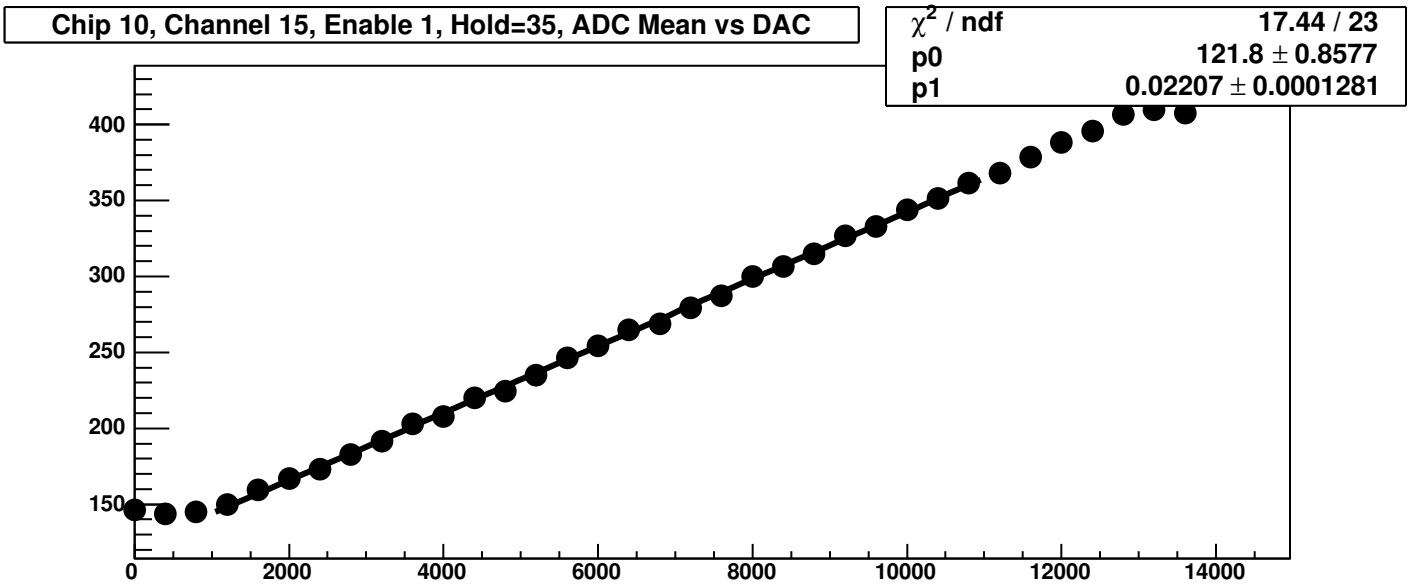


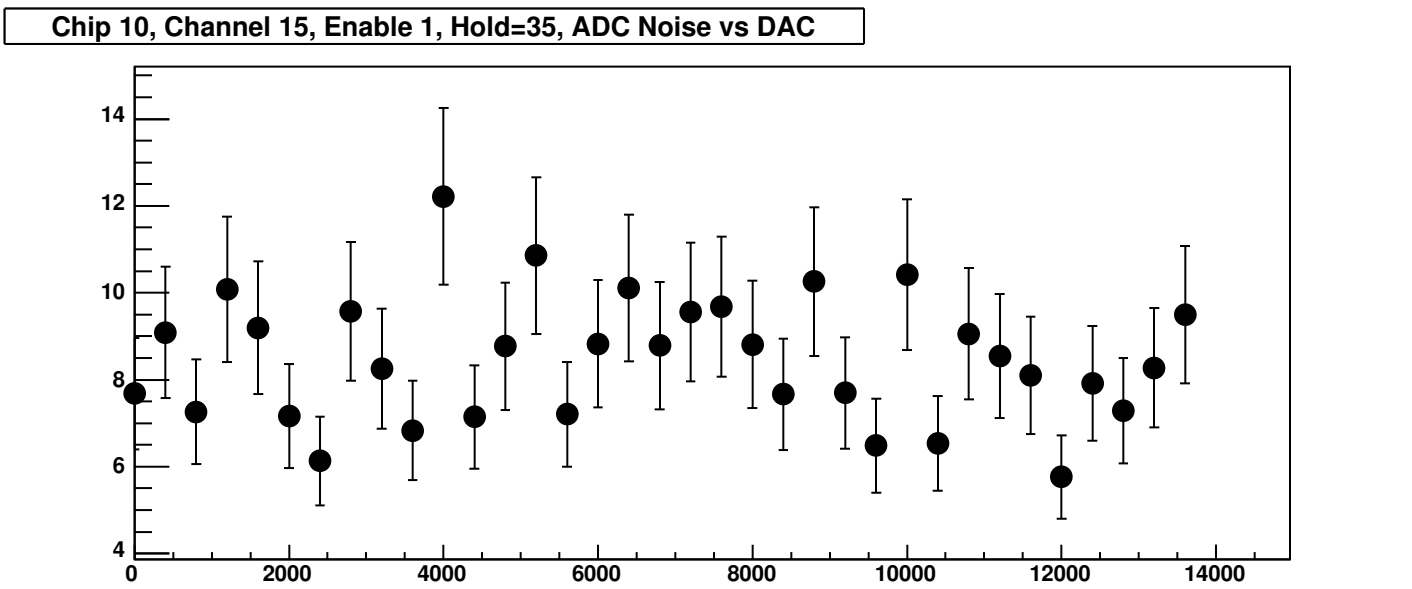
Chip 10, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC

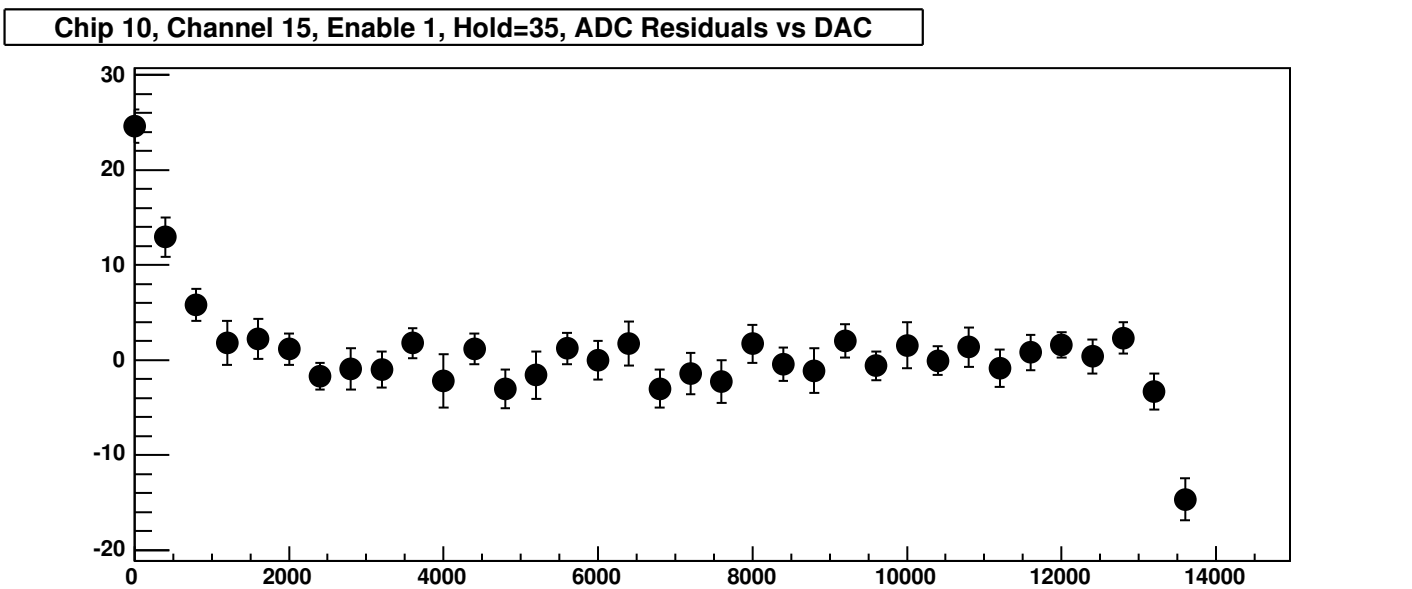


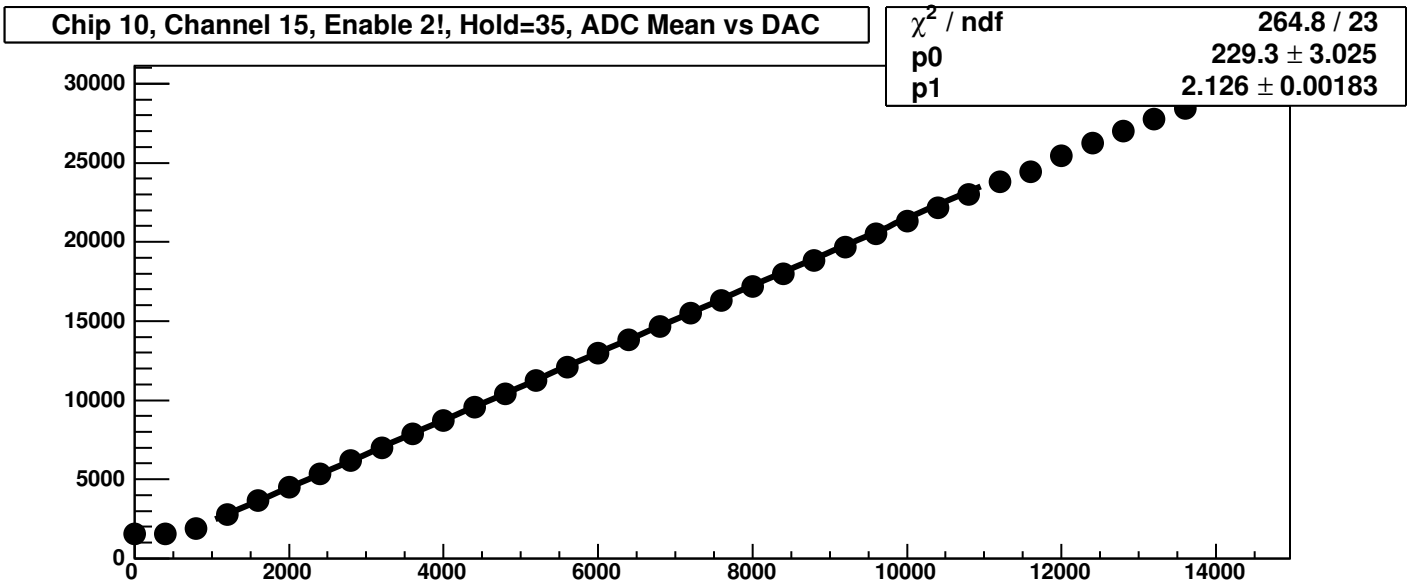
Chip 10, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

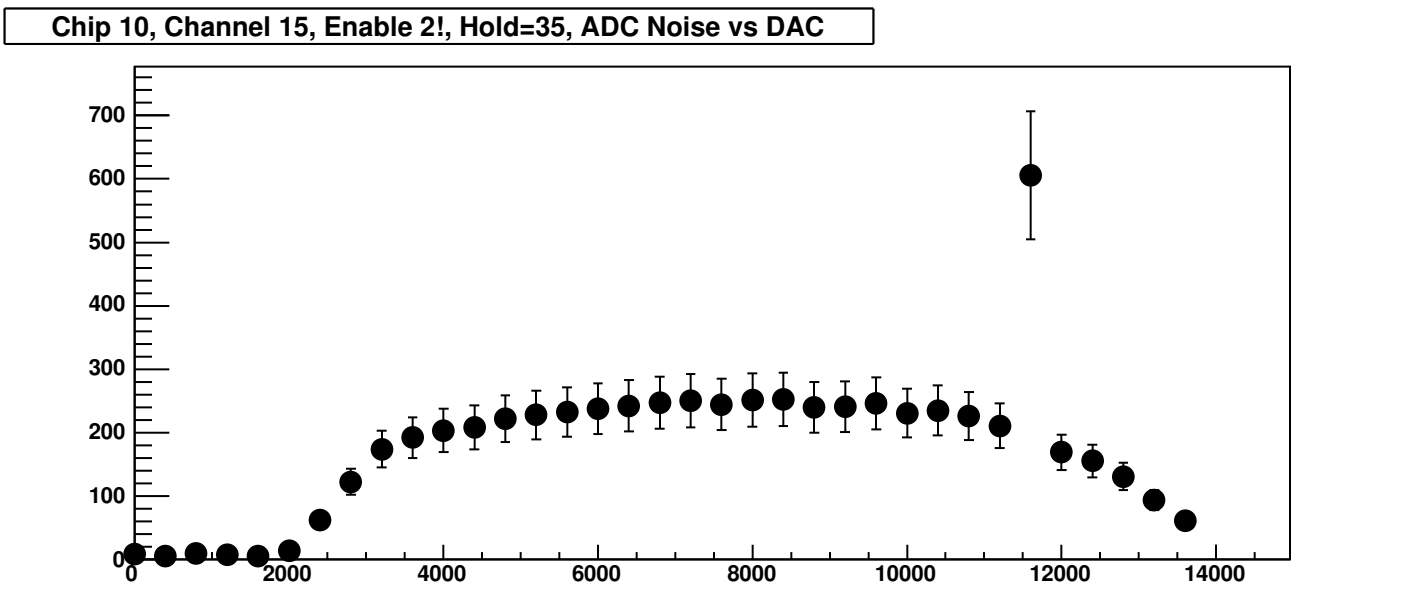


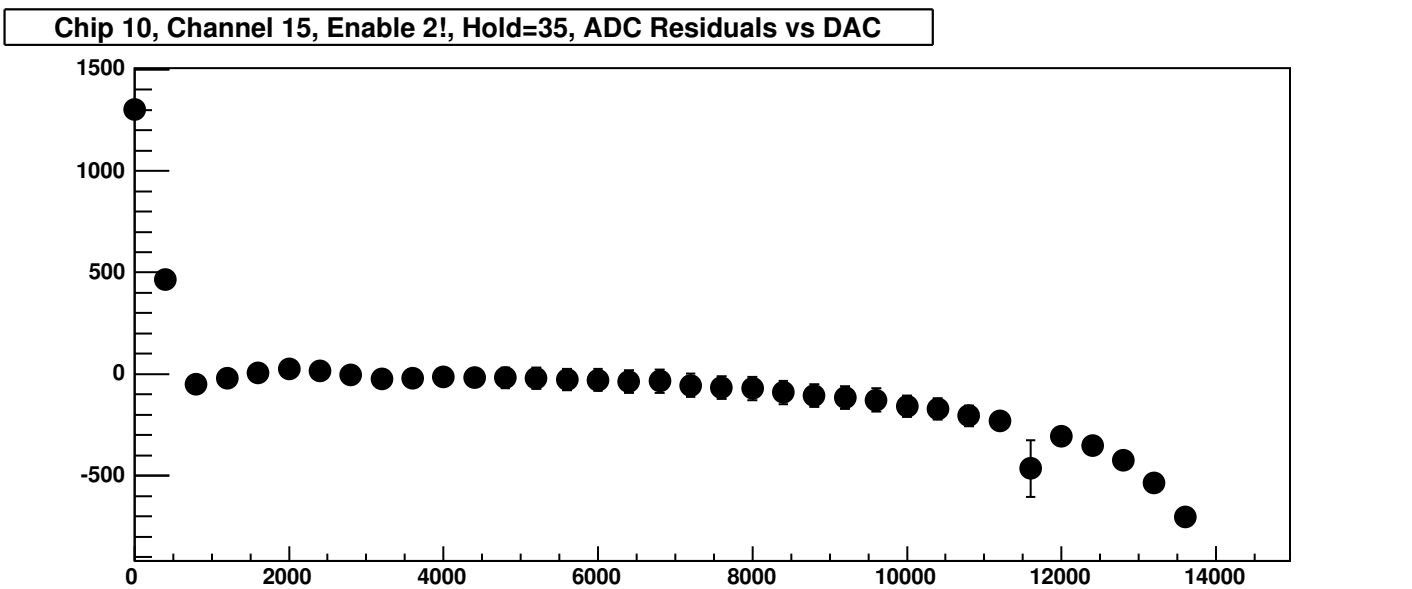

Chip 10, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 17.44 / 23
p0 121.8 ± 0.8577
p1 0.02207 ± 0.0001281
400
350
300
250
200
150
0
2000
4000
6000
8000
10000
12000
14000

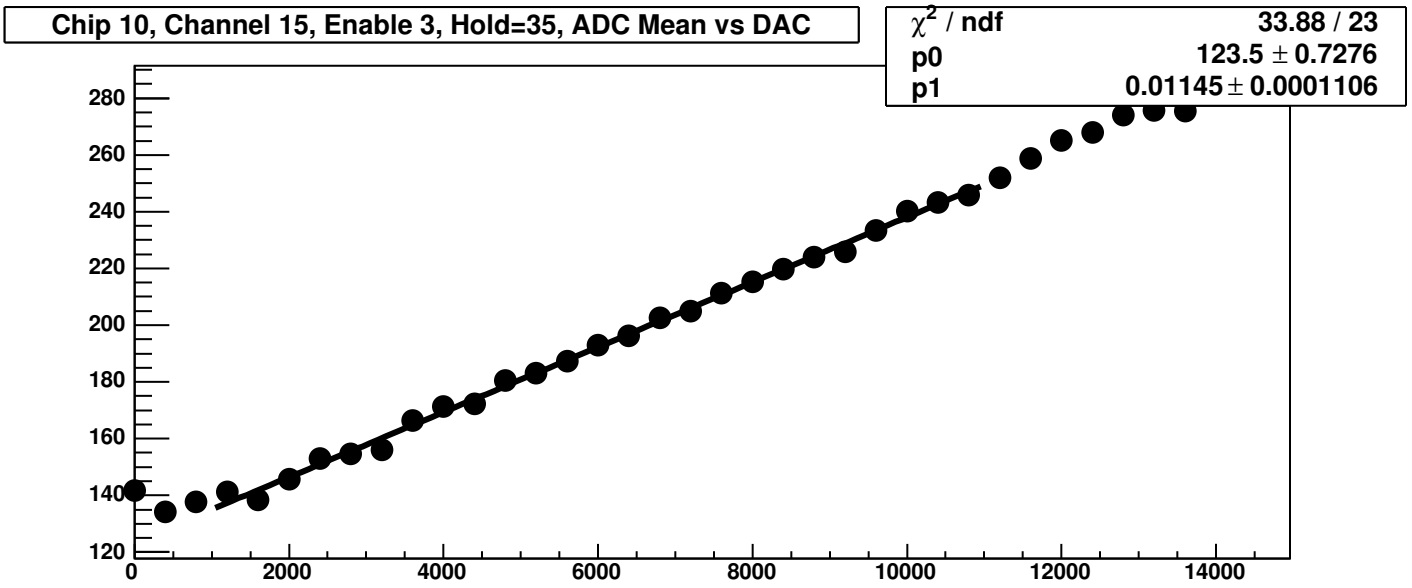

Chip 10, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

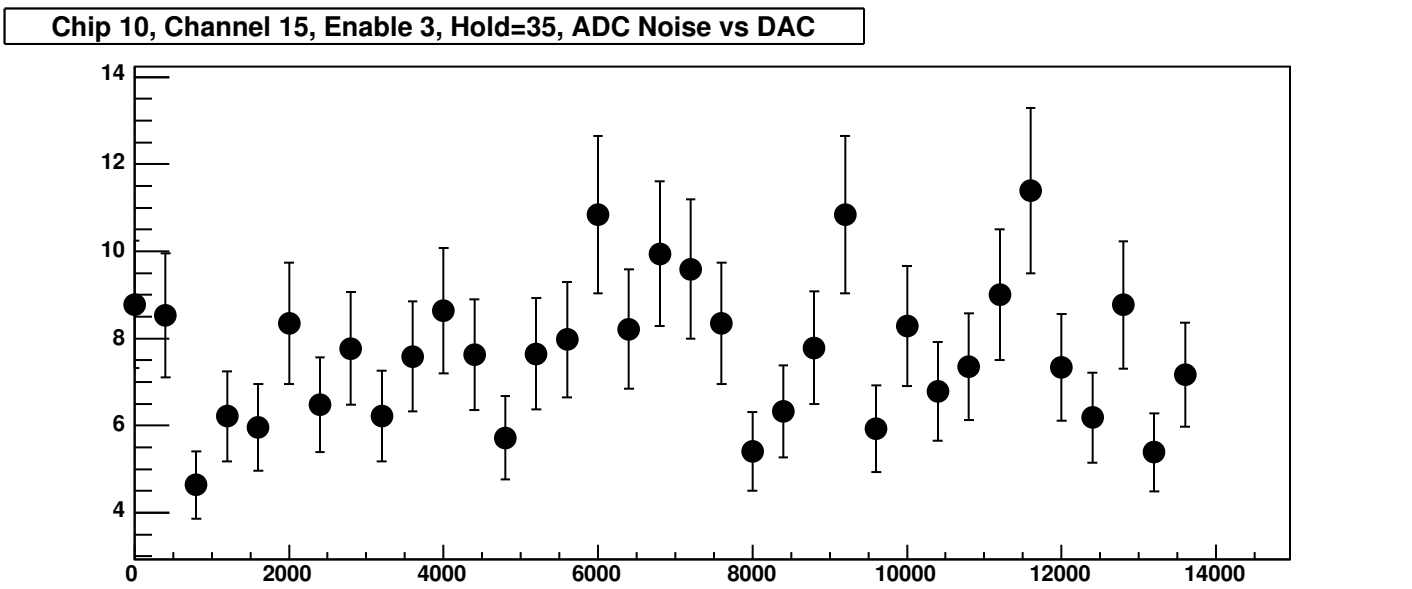

Chip 10, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
30
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

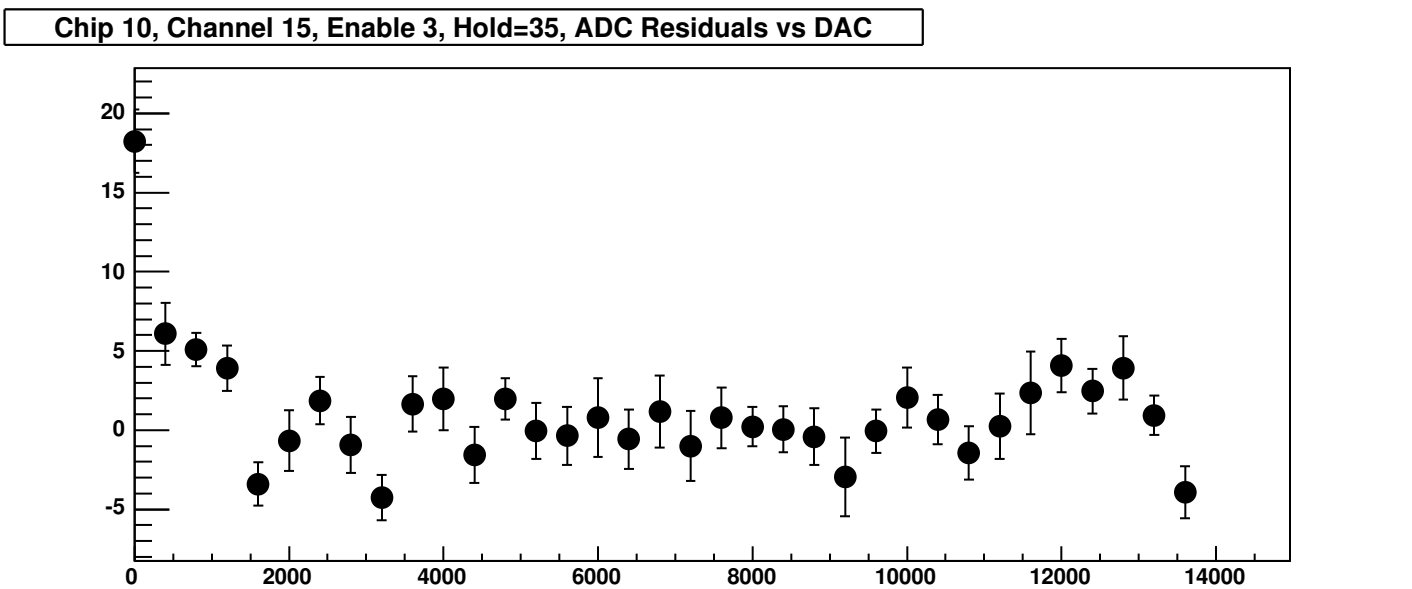

Chip 10, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 264.8 / 23
p0 229.3 ± 3.025
p1 2.126 ± 0.00183
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

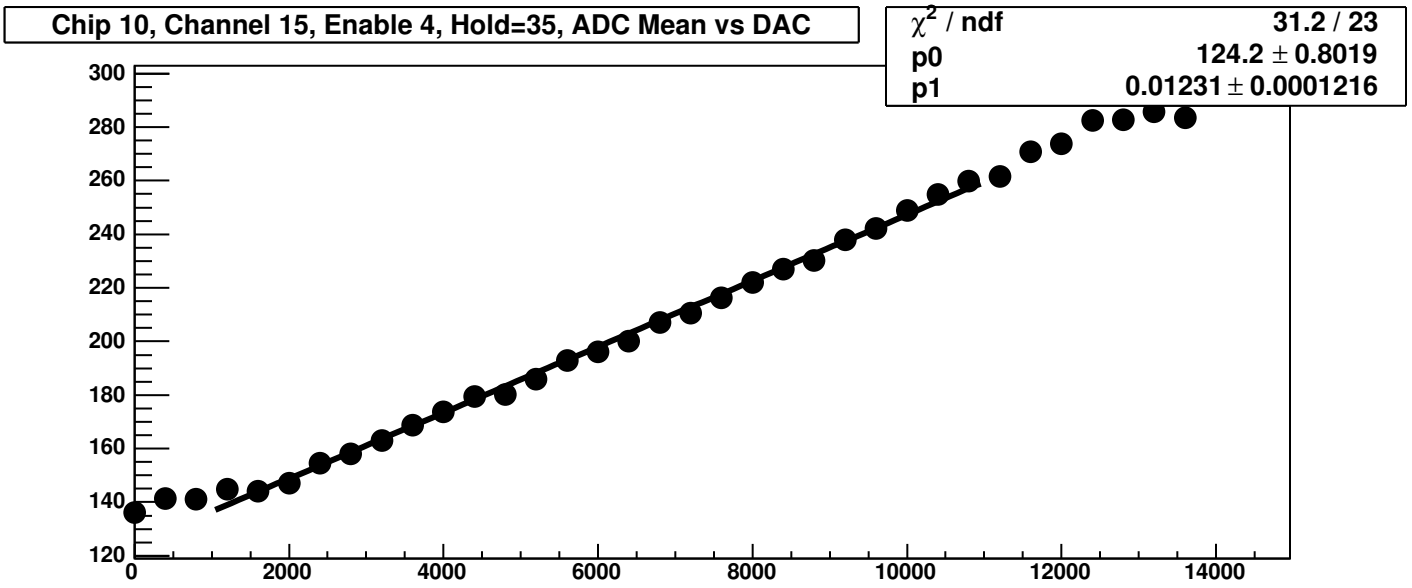

Chip 10, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

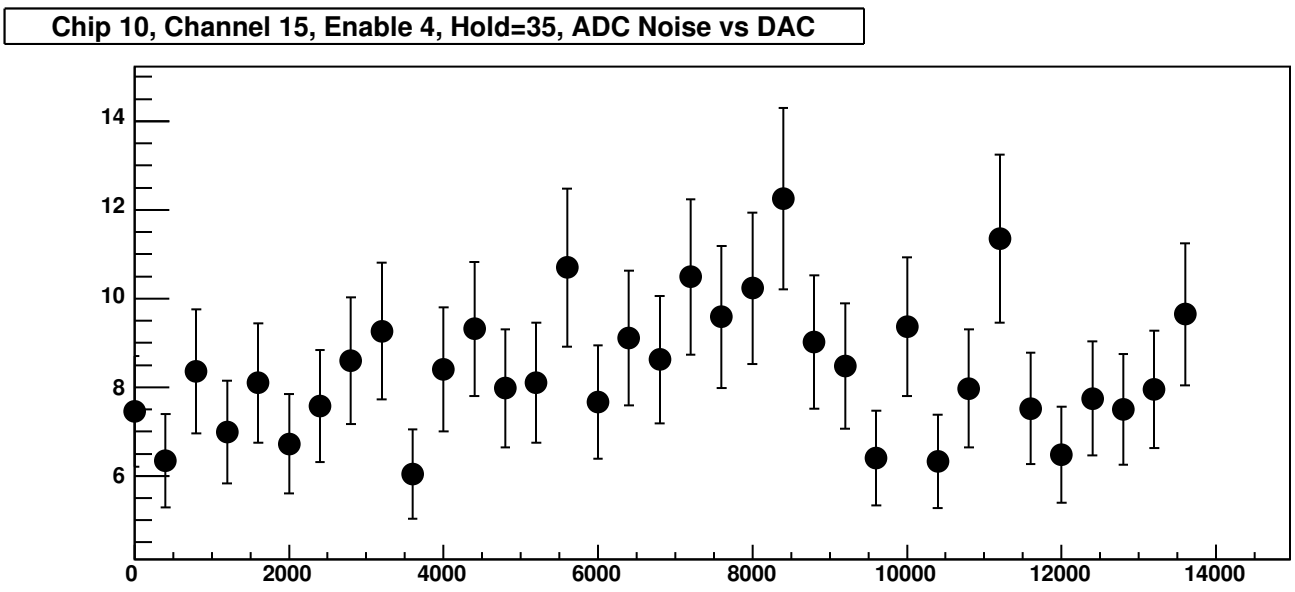

Chip 10, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

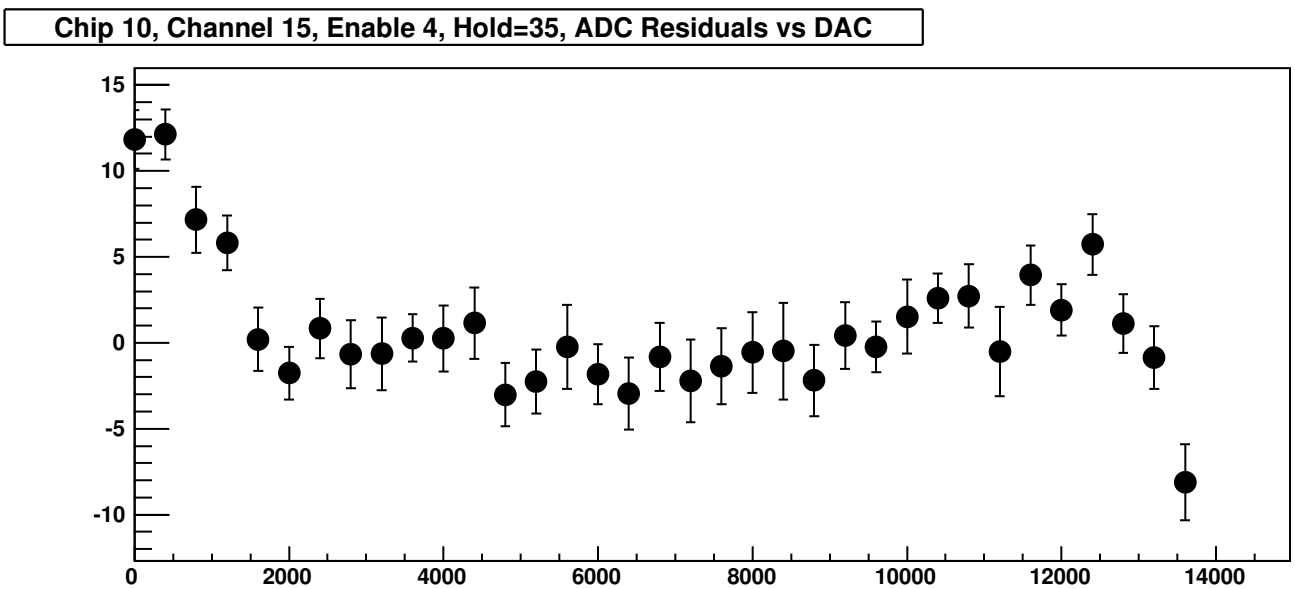

Chip 10, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 33.88 / 23
p0 123.5 ± 0.7276
p1 0.01145 ± 0.0001106
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

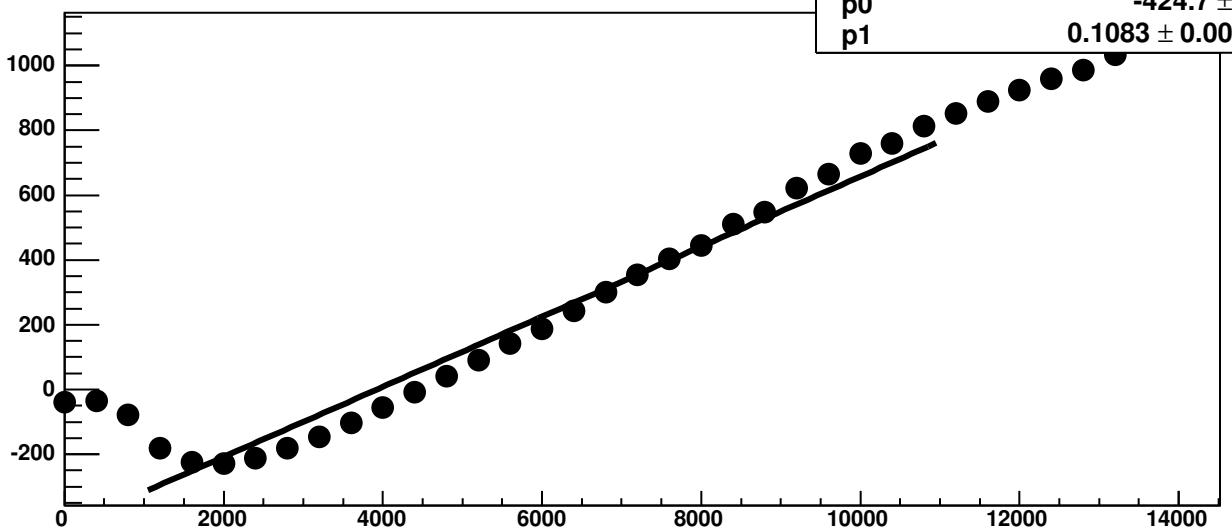

Chip 10, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 31.2 / 23
p0 124.2 ± 0.8019
p1 0.01231 ± 0.0001216
300
280
260
240
220
200
180
160
140
120
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
2000
4000
6000
8000
10000
12000
14000

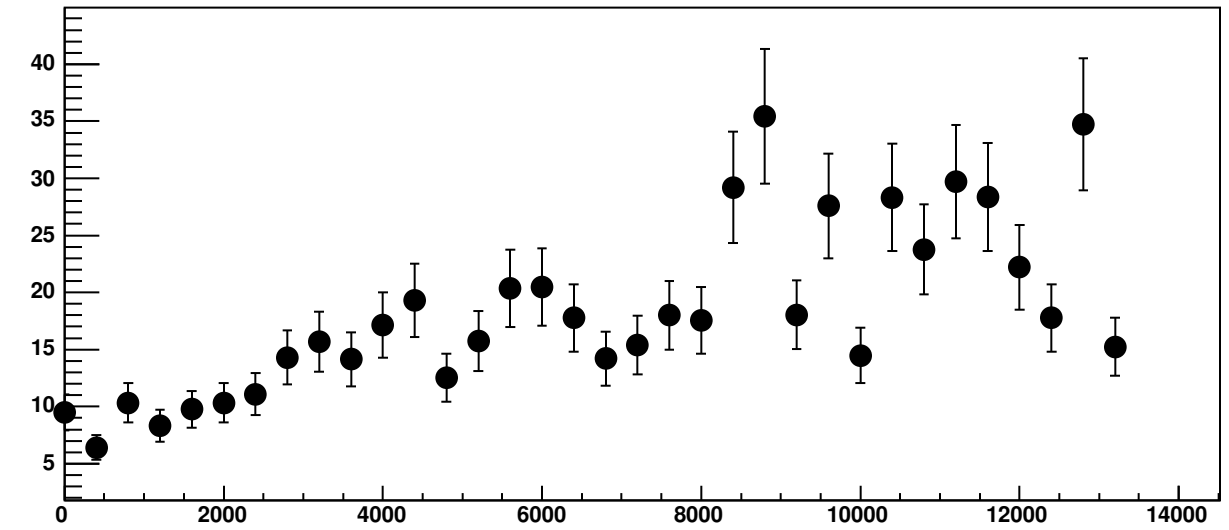

Chip 10, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

Chip 10, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC

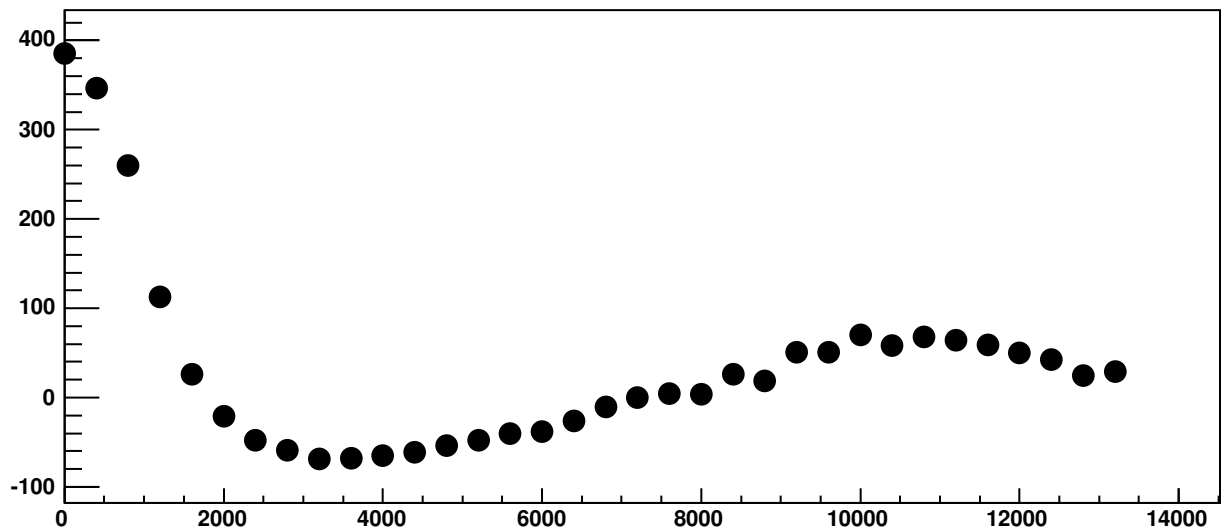
χ^2 / ndf	7310 / 23
p0	-424.7 ± 1.277
p1	0.1083 ± 0.0002409

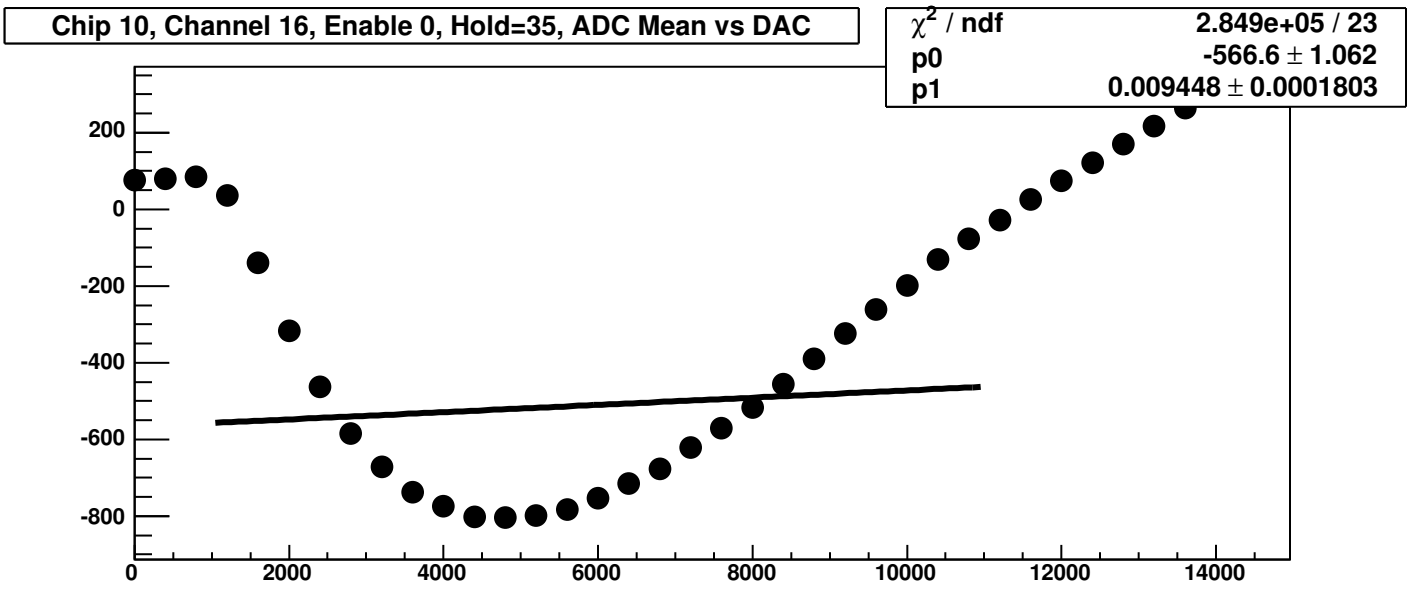


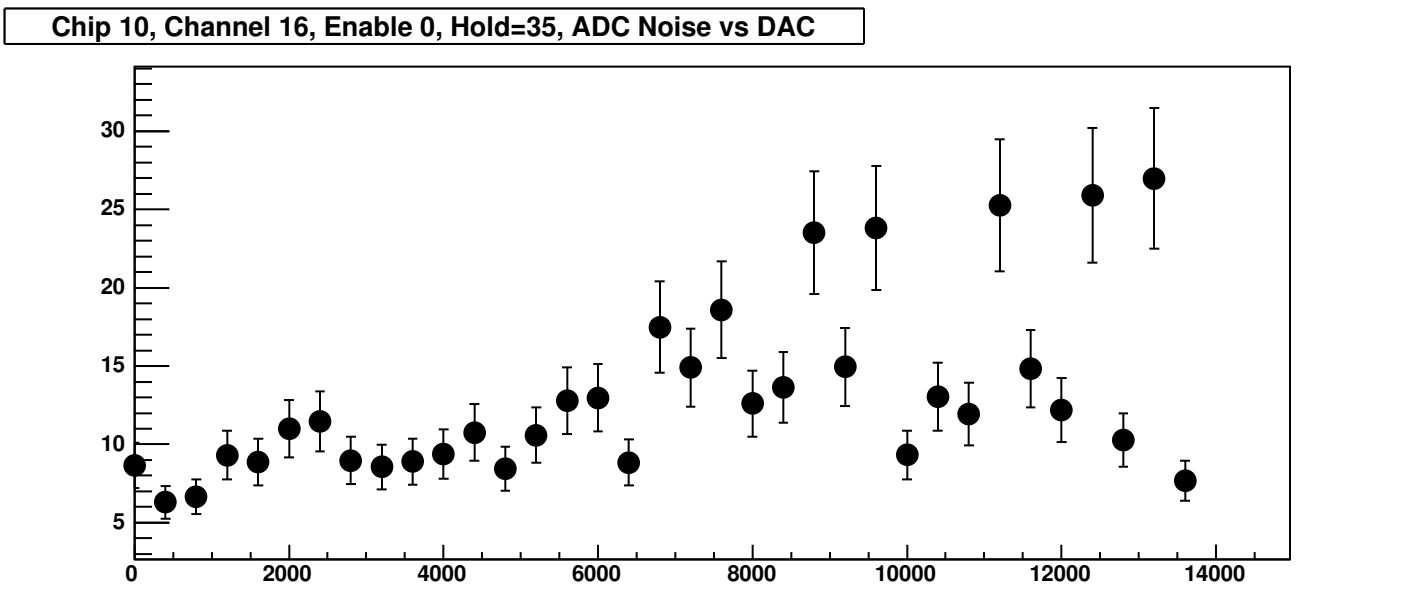
Chip 10, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC

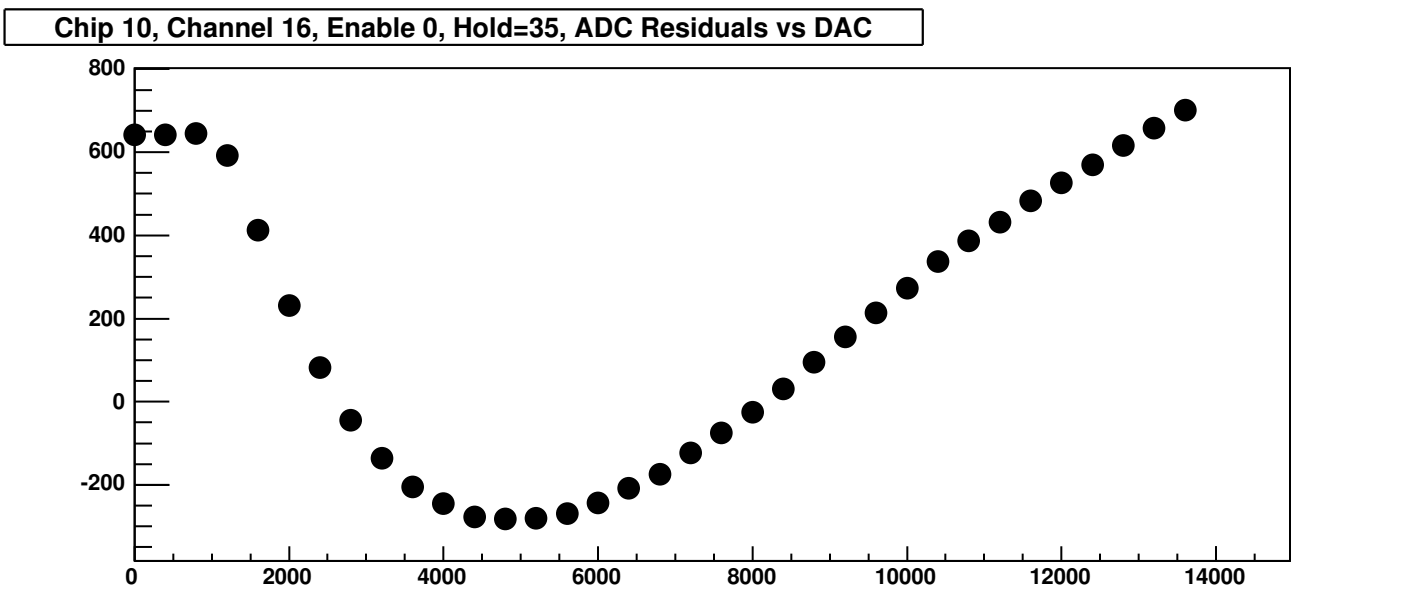


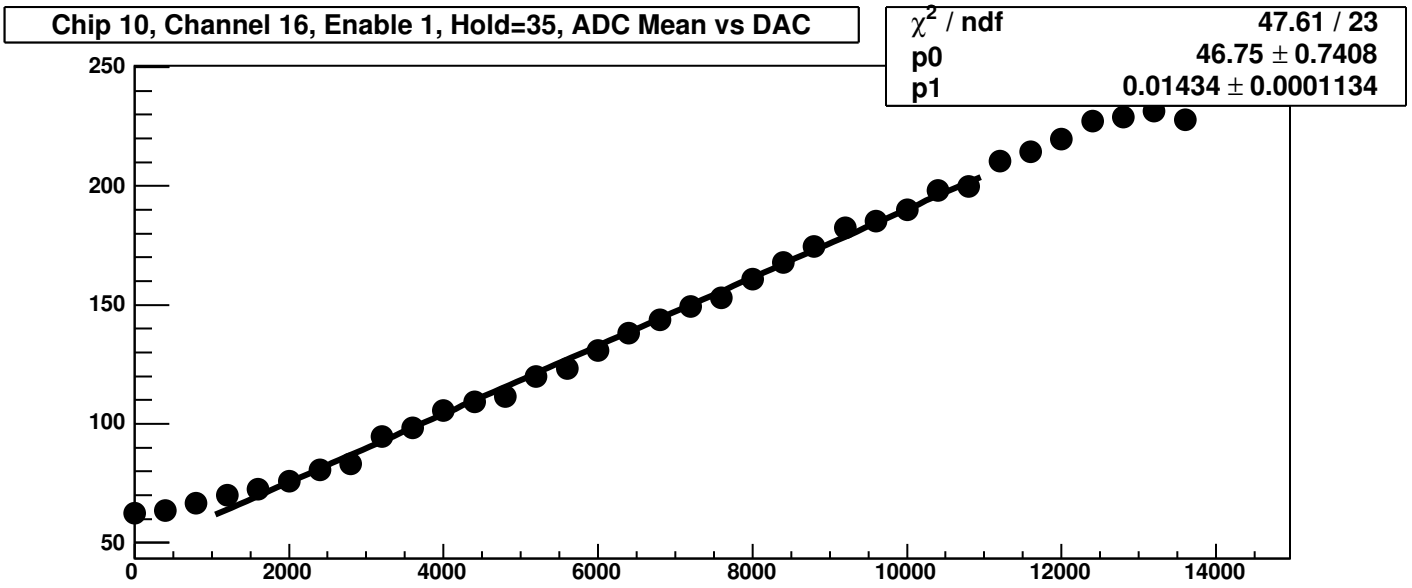
Chip 10, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC

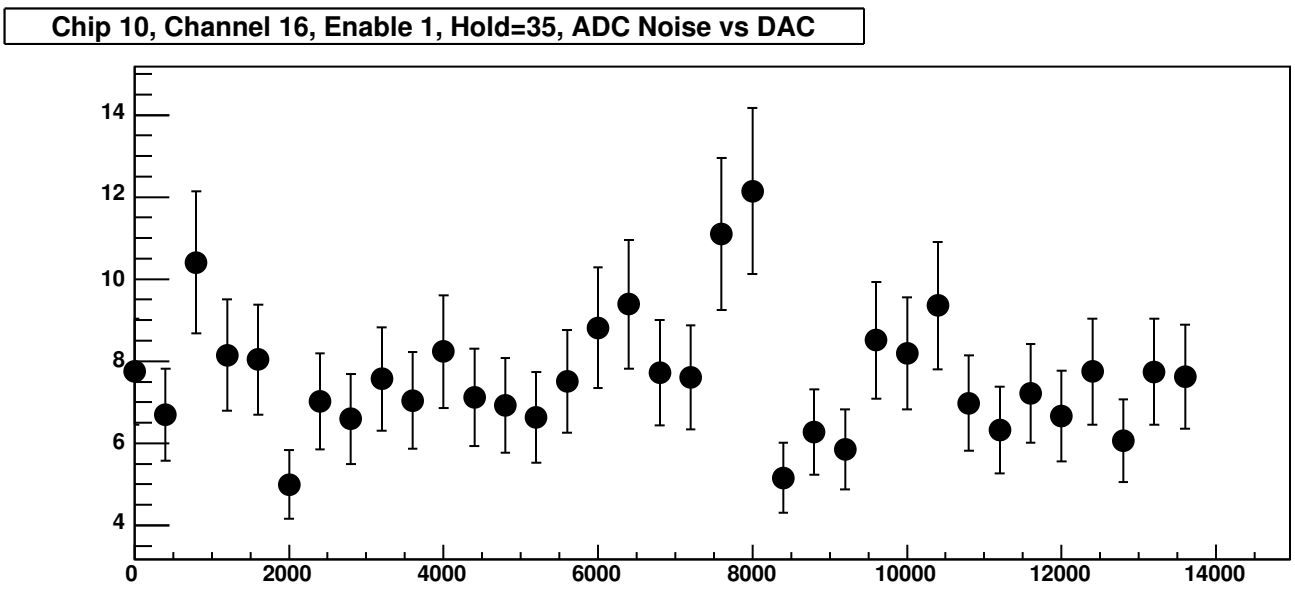


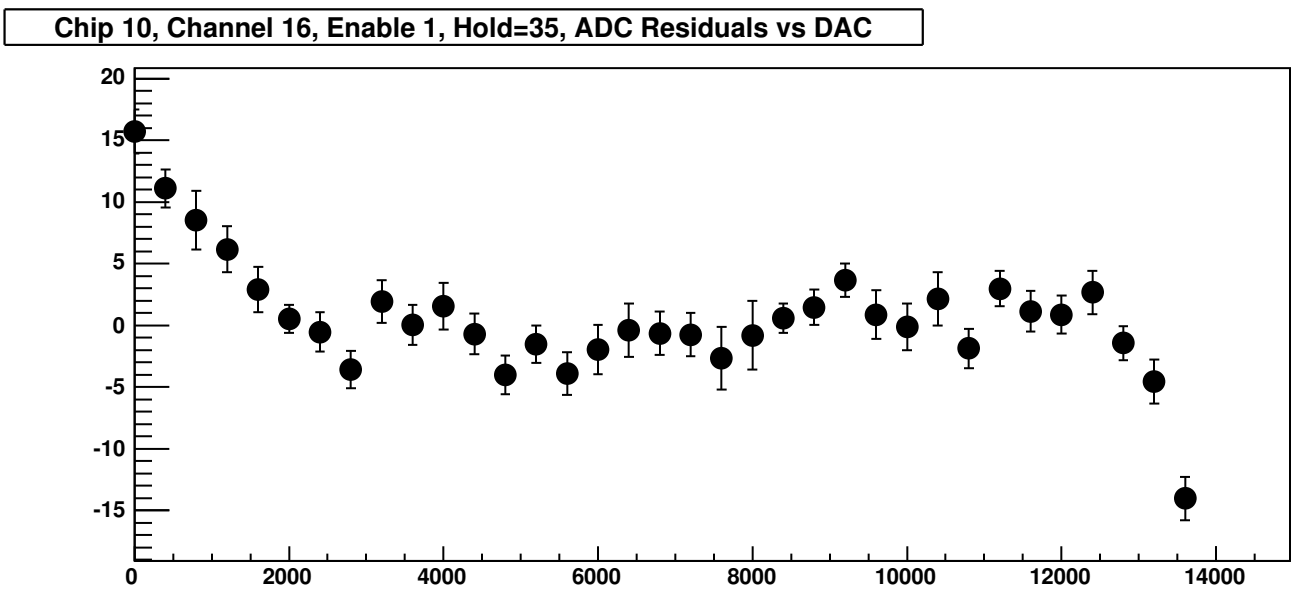

Chip 10, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 2.849e+05 / 23
p0 -566.6 ± 1.062
p1 0.009448 ± 0.0001803
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

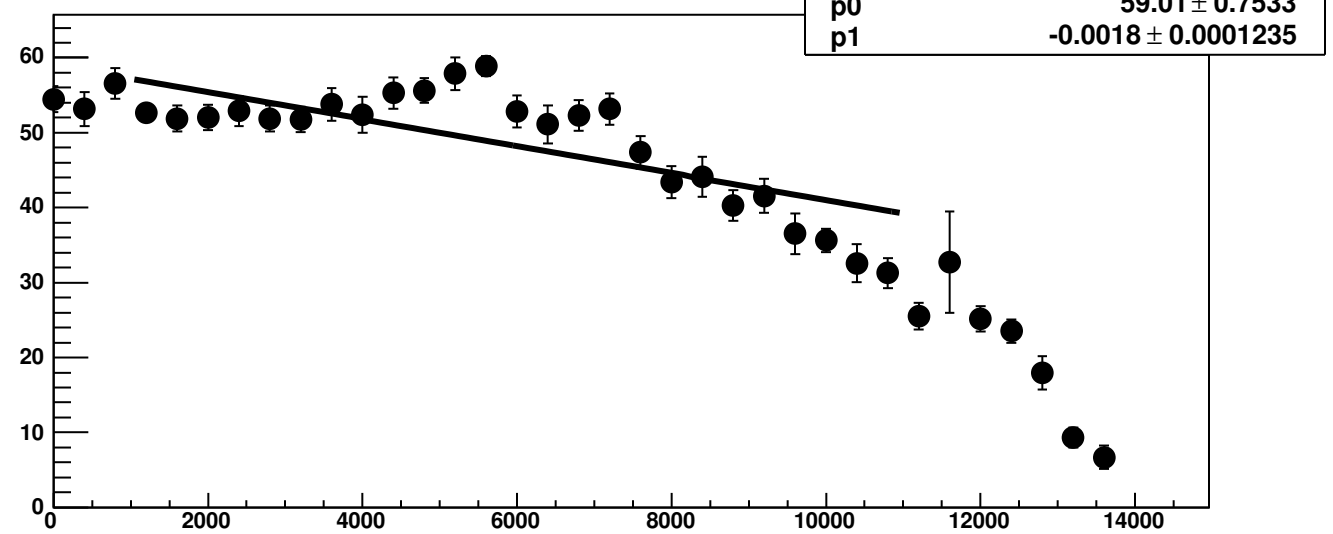

Chip 10, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 47.61 / 23
p0 46.75 ± 0.7408
p1 0.01434 ± 0.0001134
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

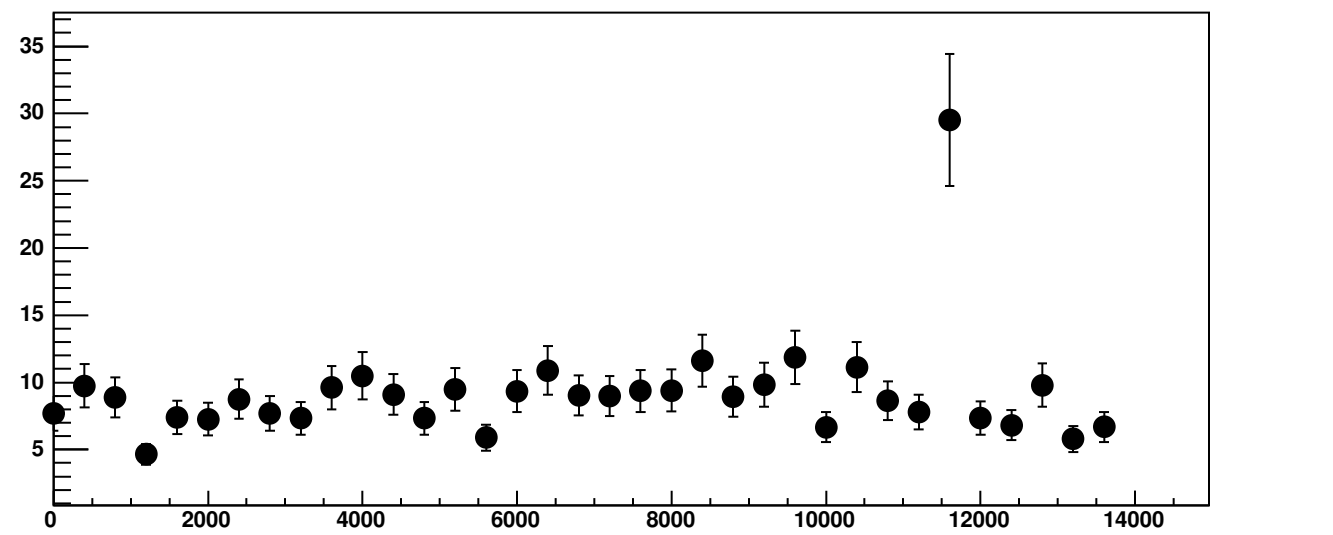

Chip 10, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

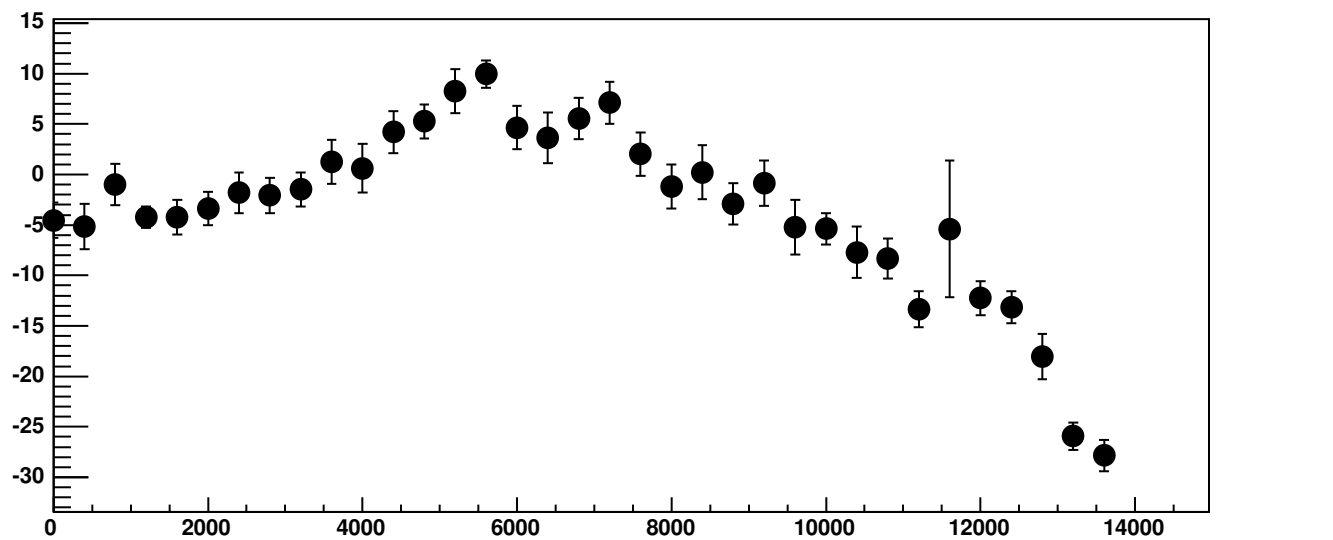
Chip 10, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC



Chip 10, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC

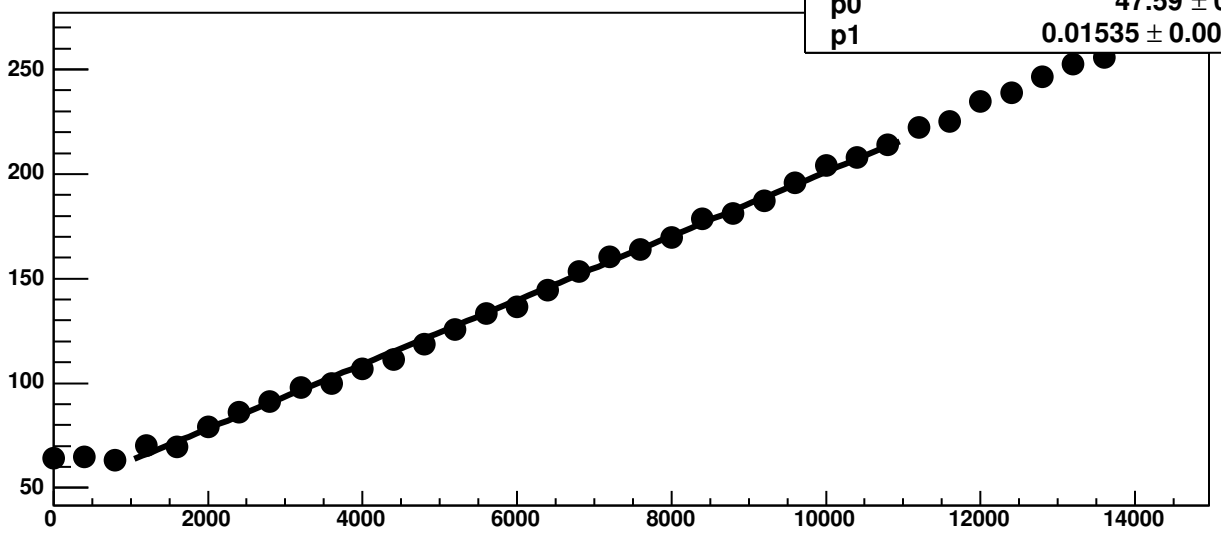


Chip 10, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

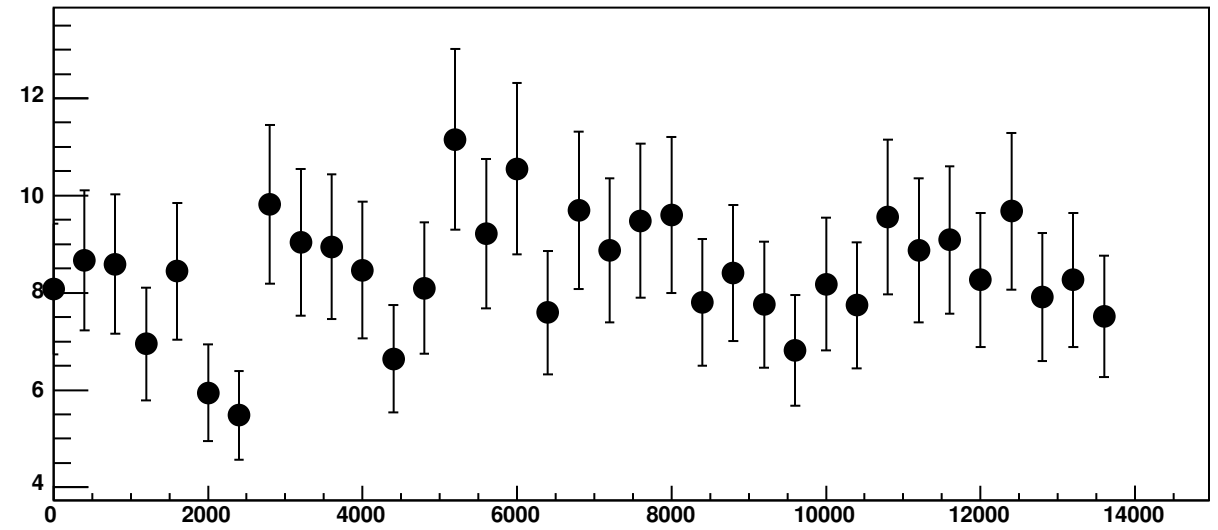


Chip 10, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC

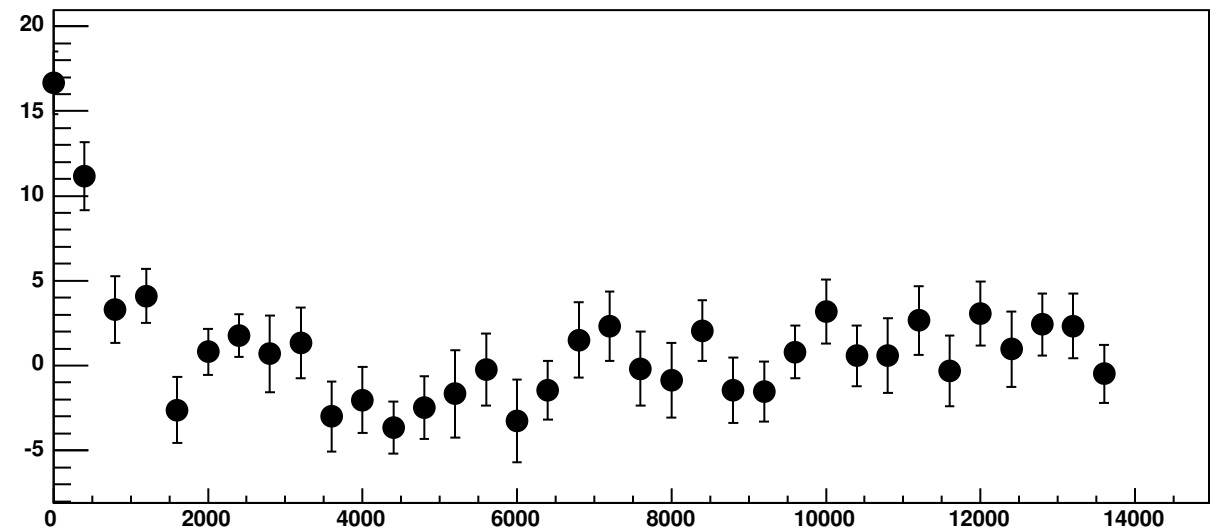
χ^2 / ndf	32.89 / 23
p0	47.59 ± 0.7825
p1	0.01535 ± 0.0001212



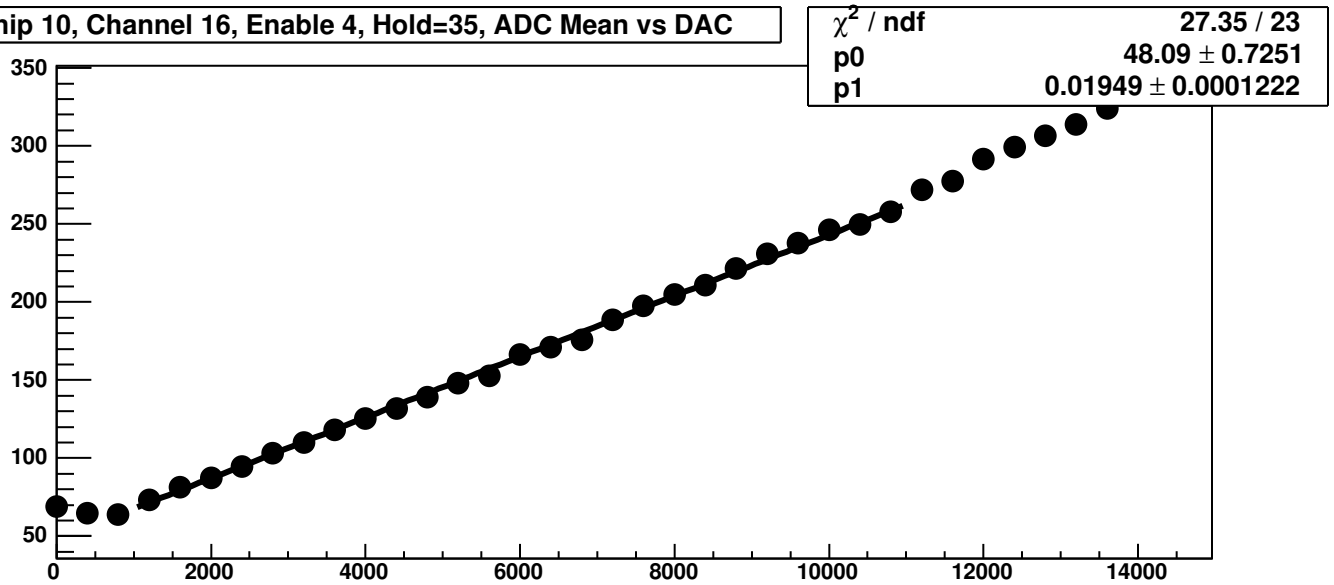
Chip 10, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC



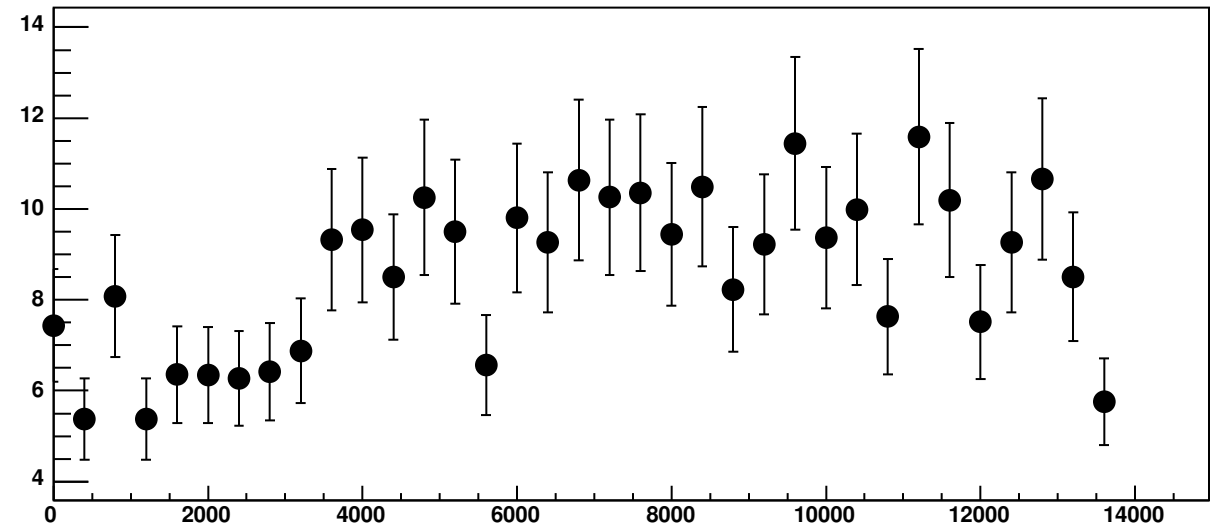
Chip 10, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC



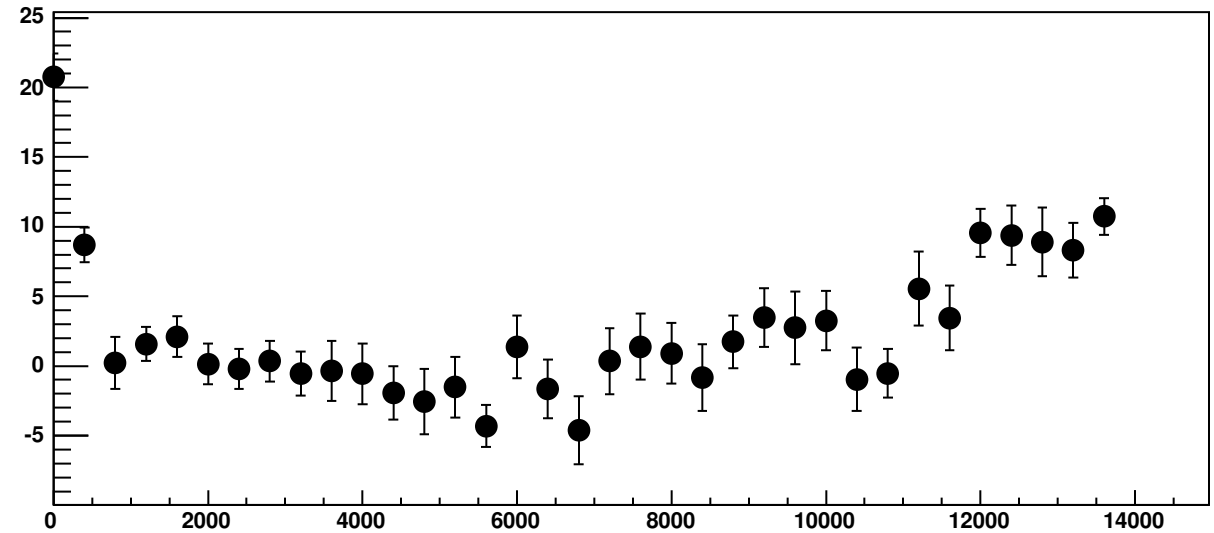
Chip 10, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

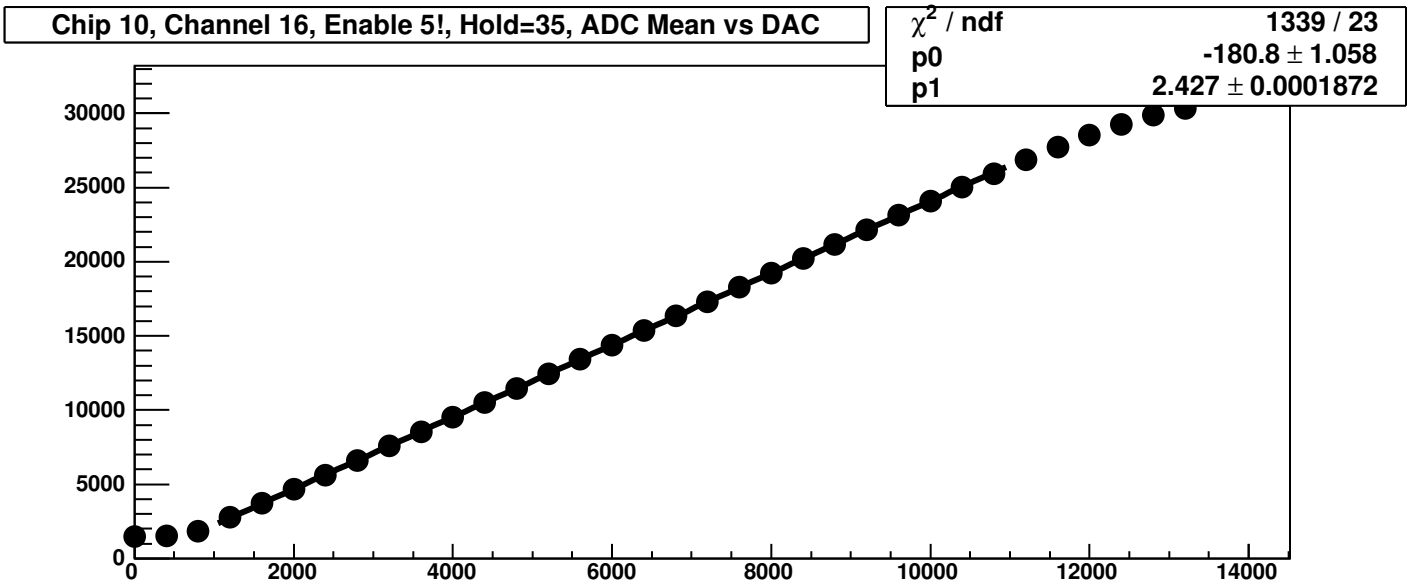


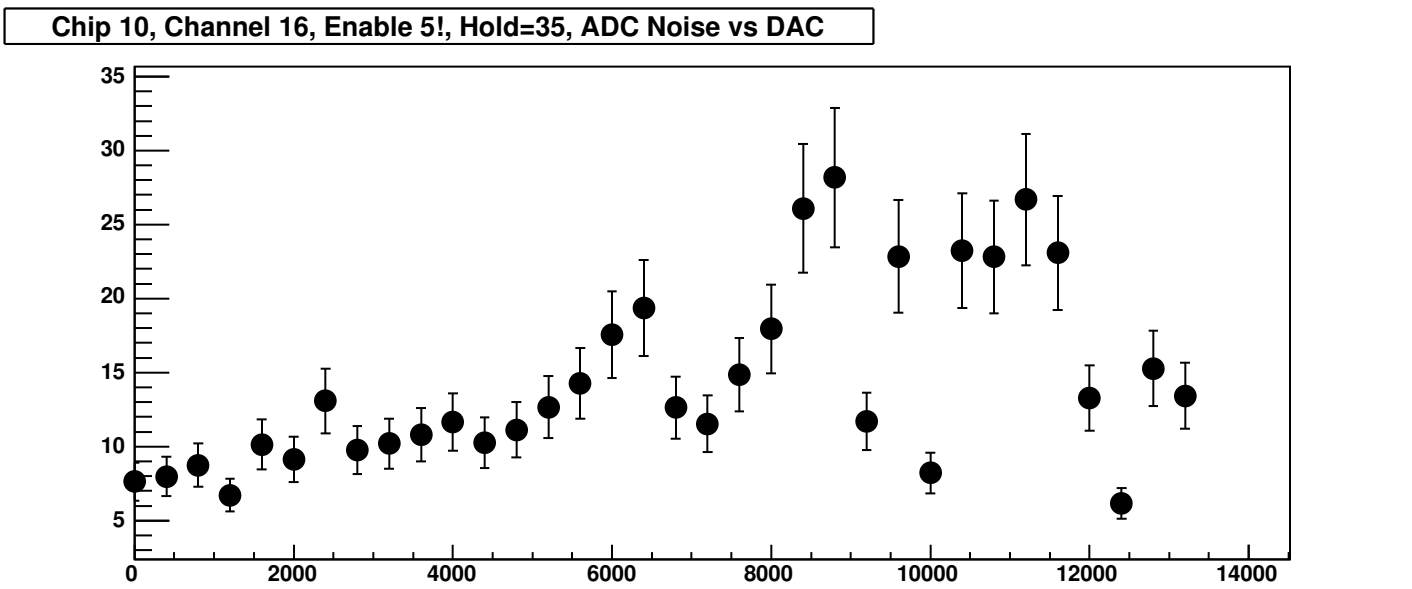
Chip 10, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC

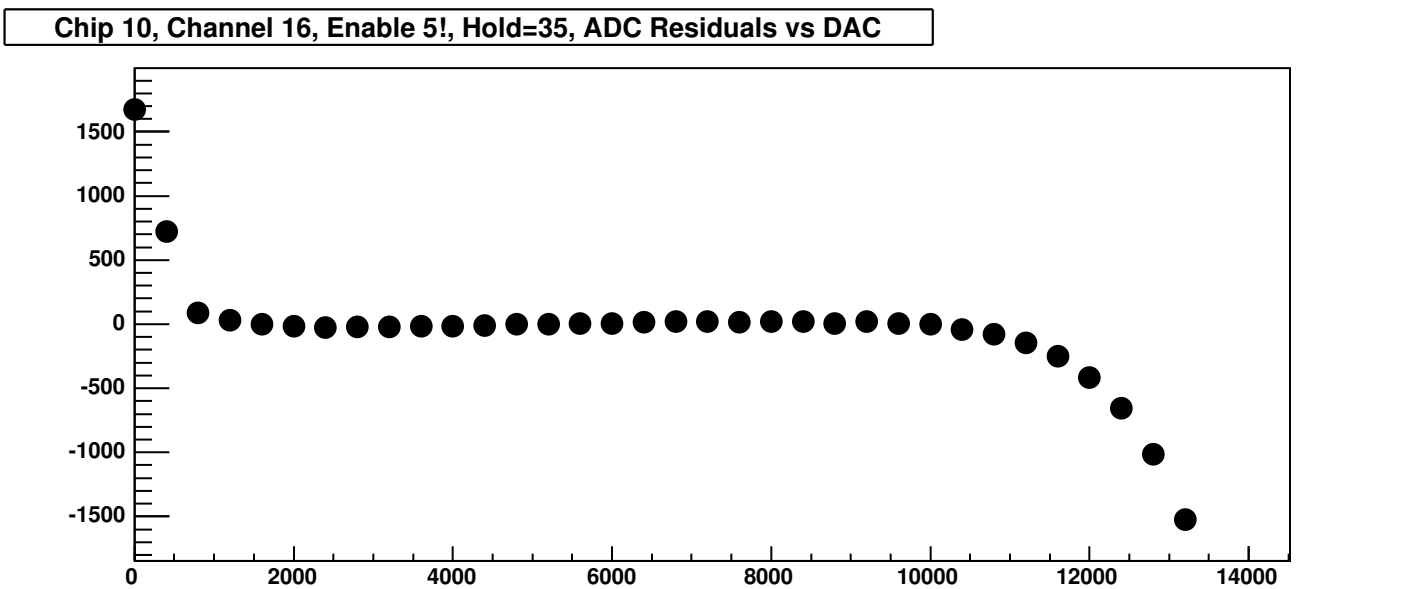


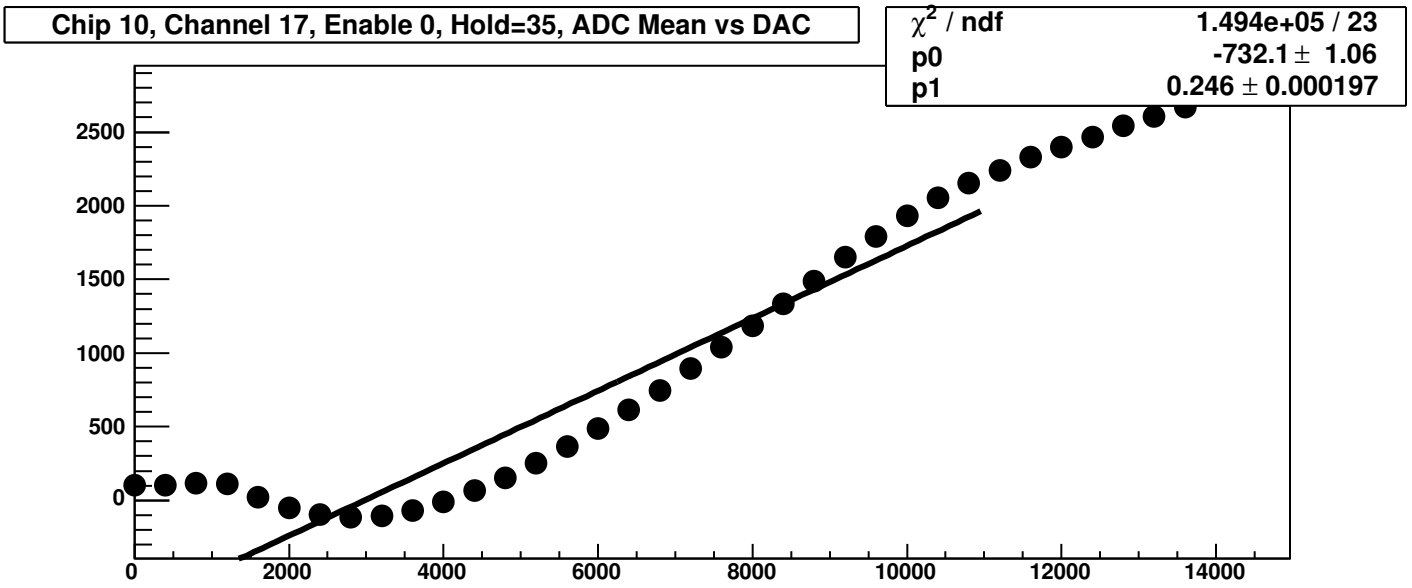
Chip 10, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

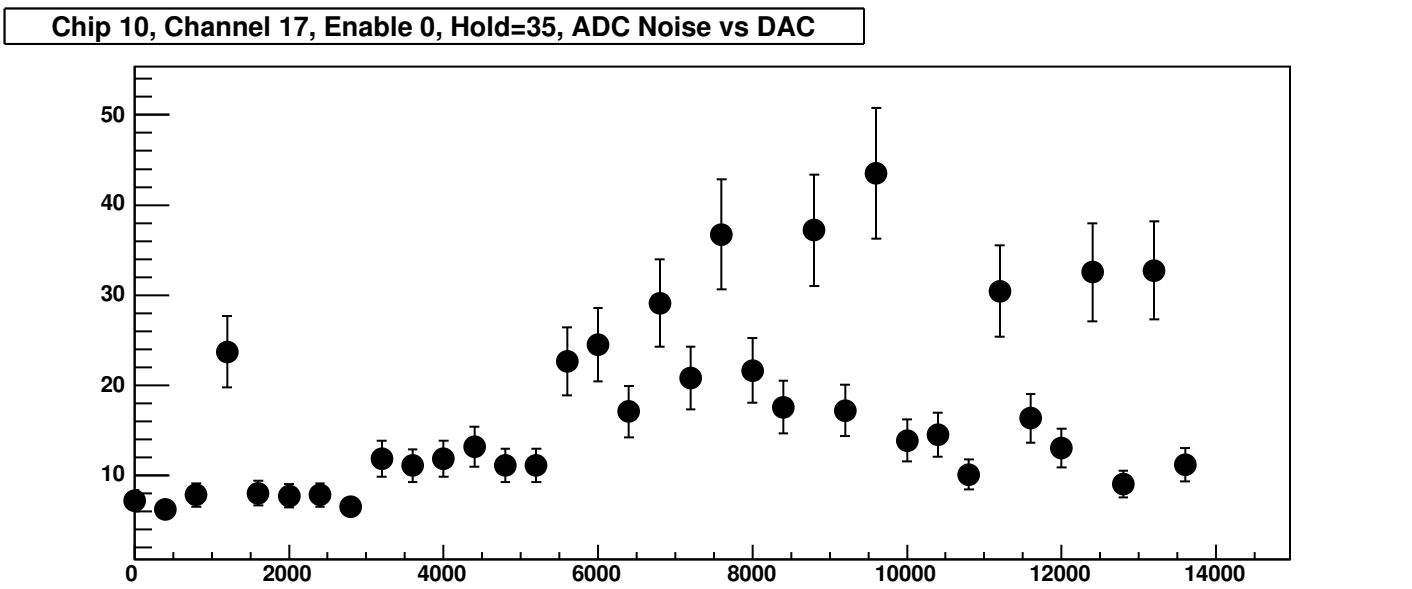


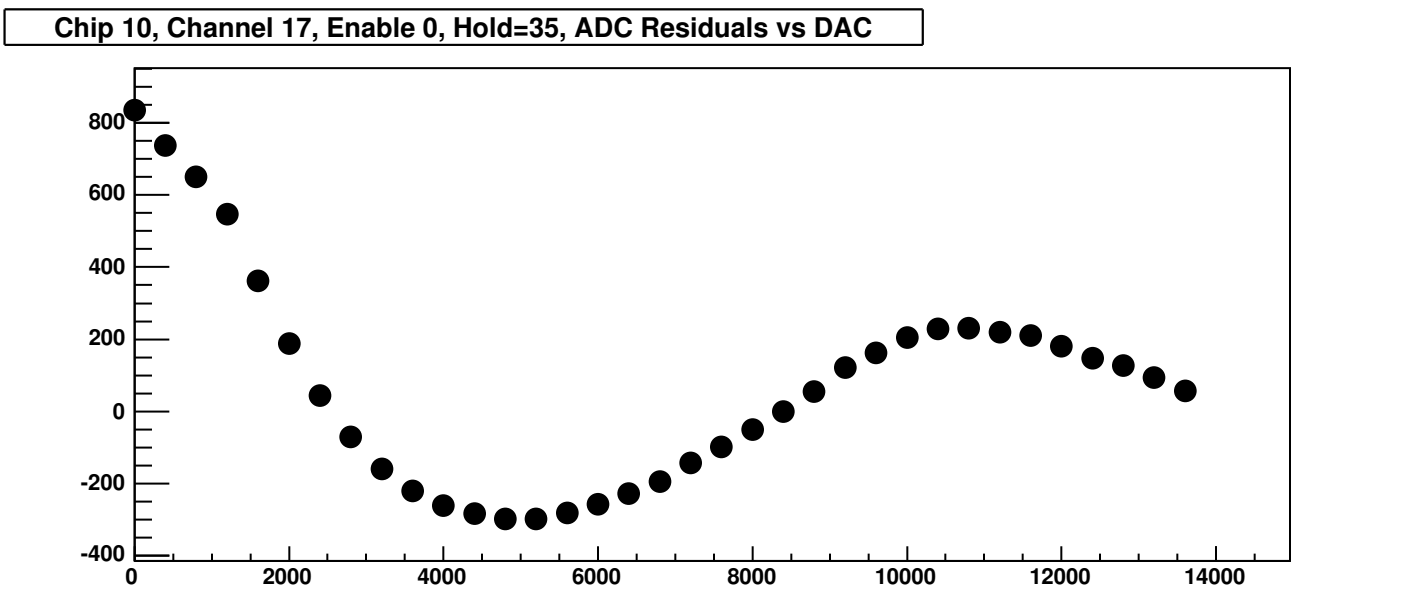

Chip 10, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1339 / 23
p0 -180.8 ± 1.058
p1 2.427 ± 0.0001872
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

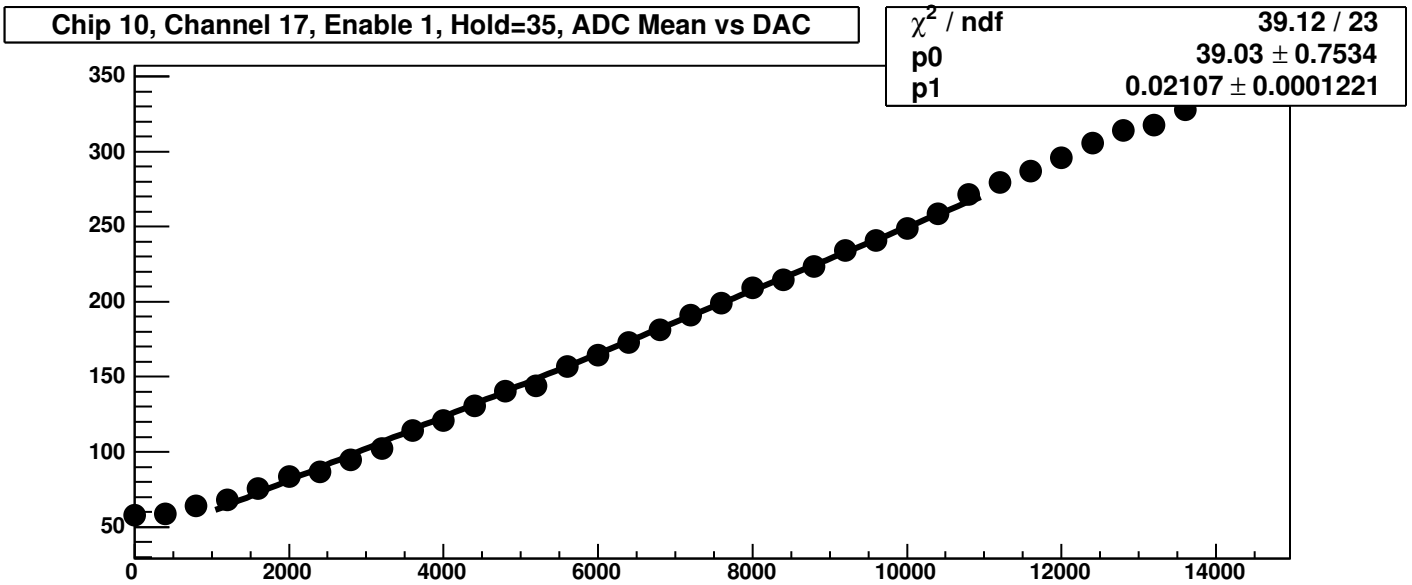

Chip 10, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

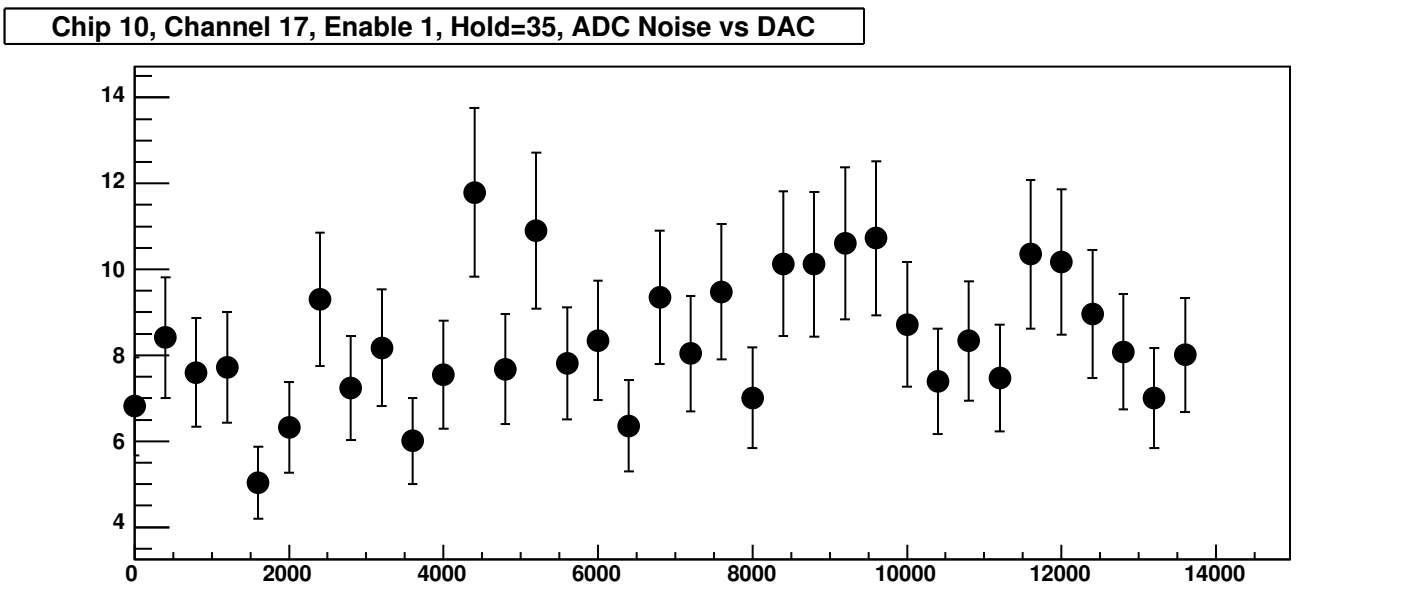

Chip 10, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

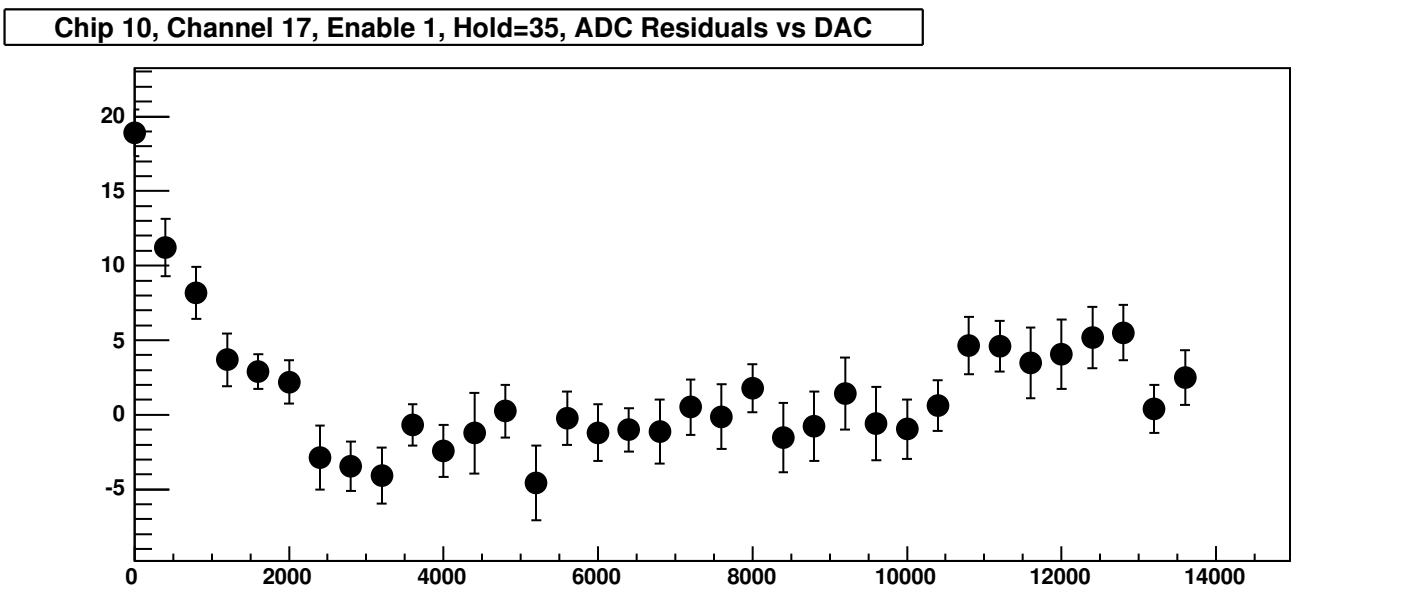

Chip 10, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.494e+05 / 23
p0 -732.1 ± 1.06
p1 0.246 ± 0.000197
2500
2000
1500
1000
500
0
0
2000
4000
6000
8000
10000
12000
14000

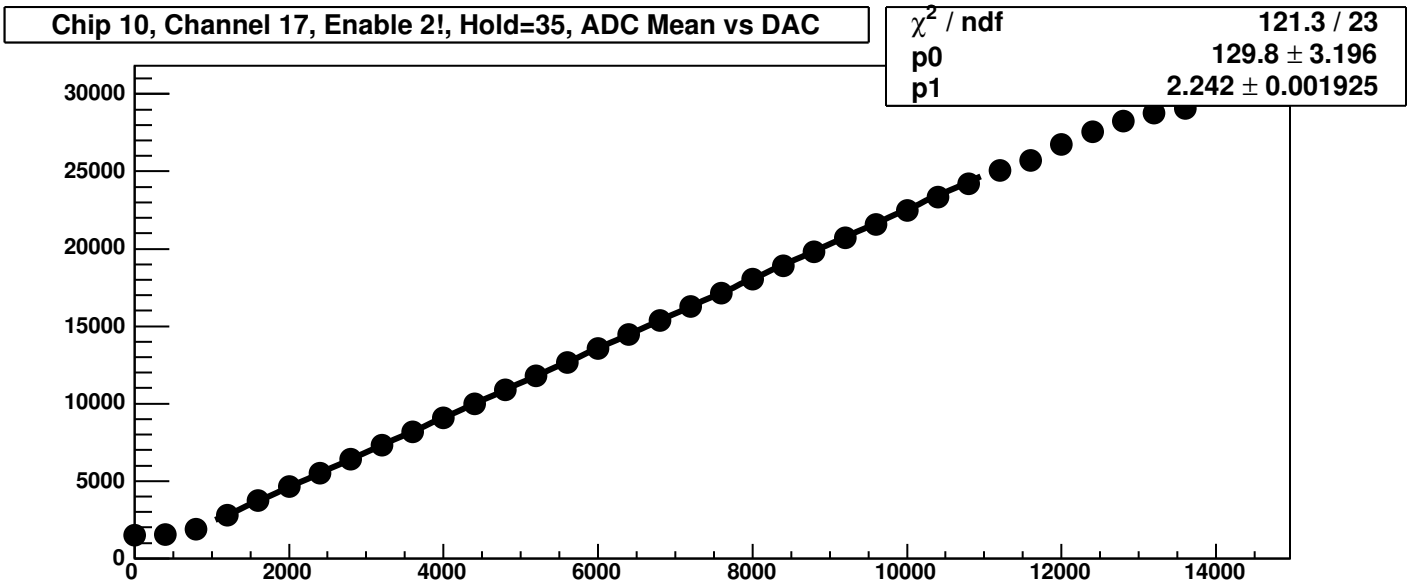

Chip 10, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

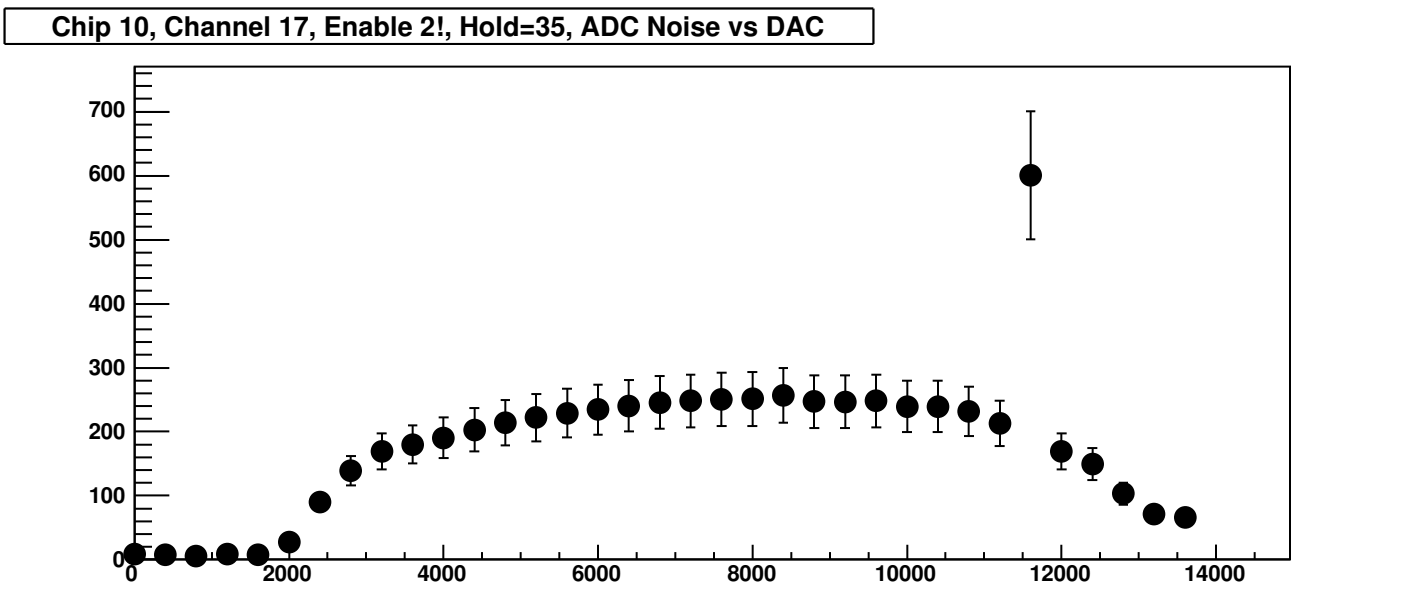

Chip 10, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

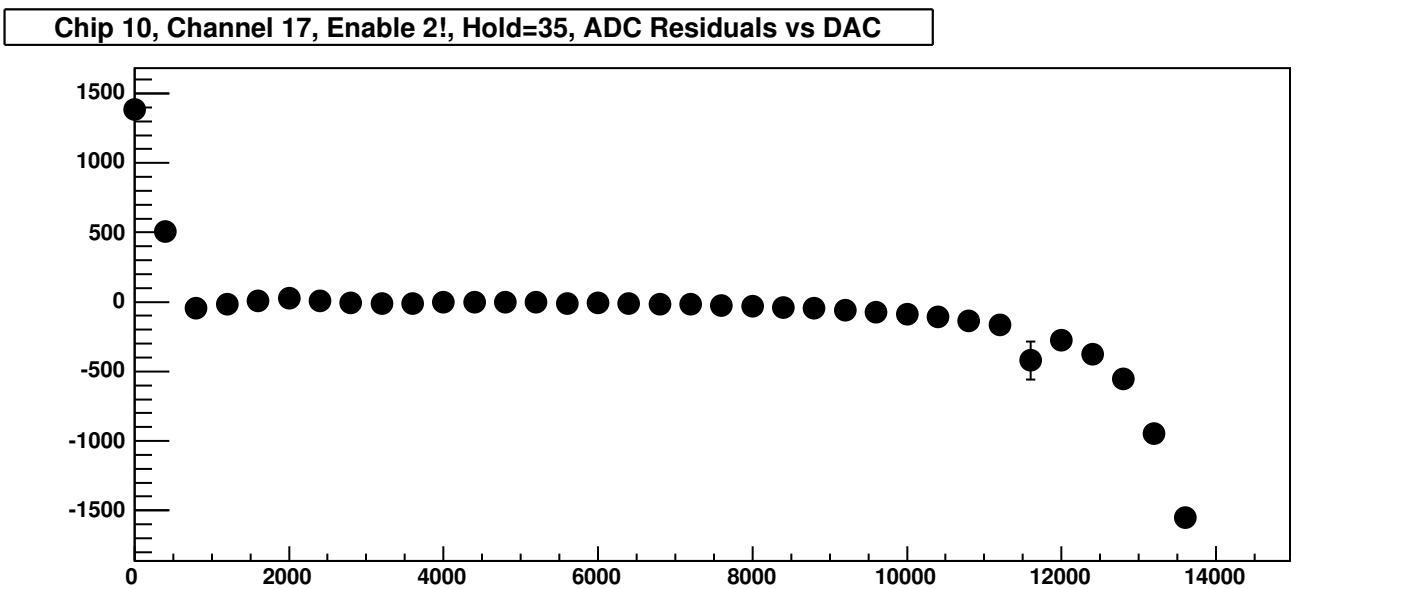

Chip 10, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 39.12 / 23
p0 39.03 ± 0.7534
p1 0.02107 ± 0.0001221
350
300
250
200
150
100
50
0
2000
4000
6000
8000
10000
12000
14000

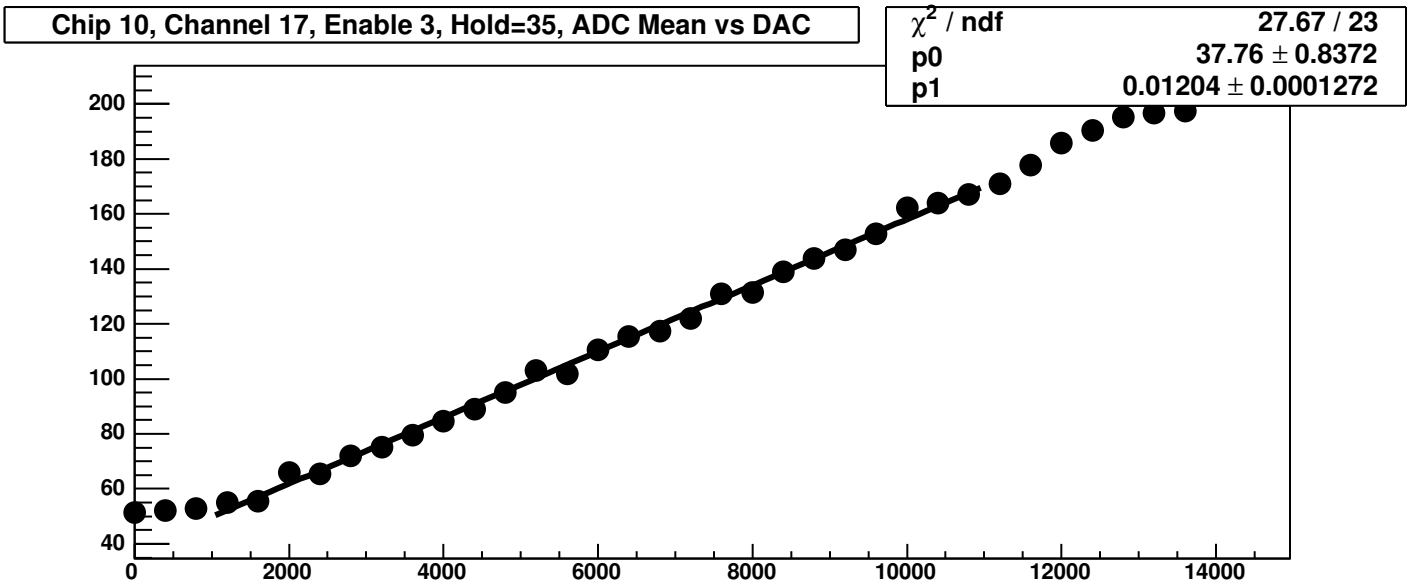

Chip 10, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

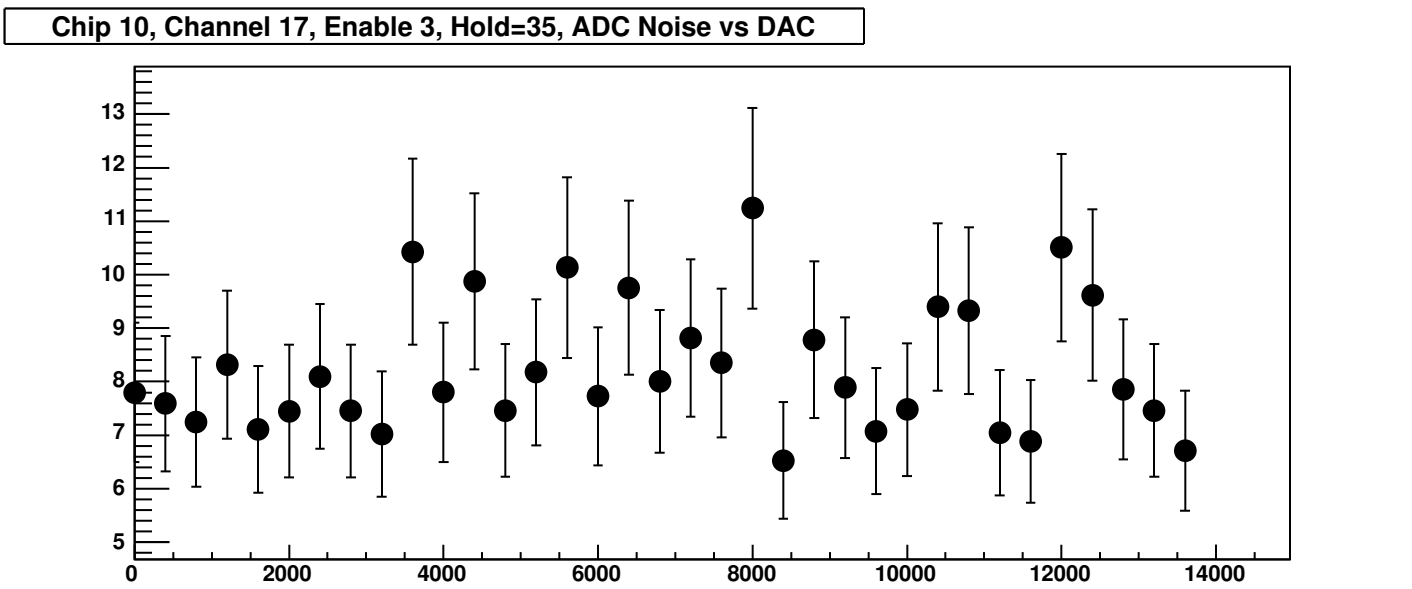

Chip 10, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

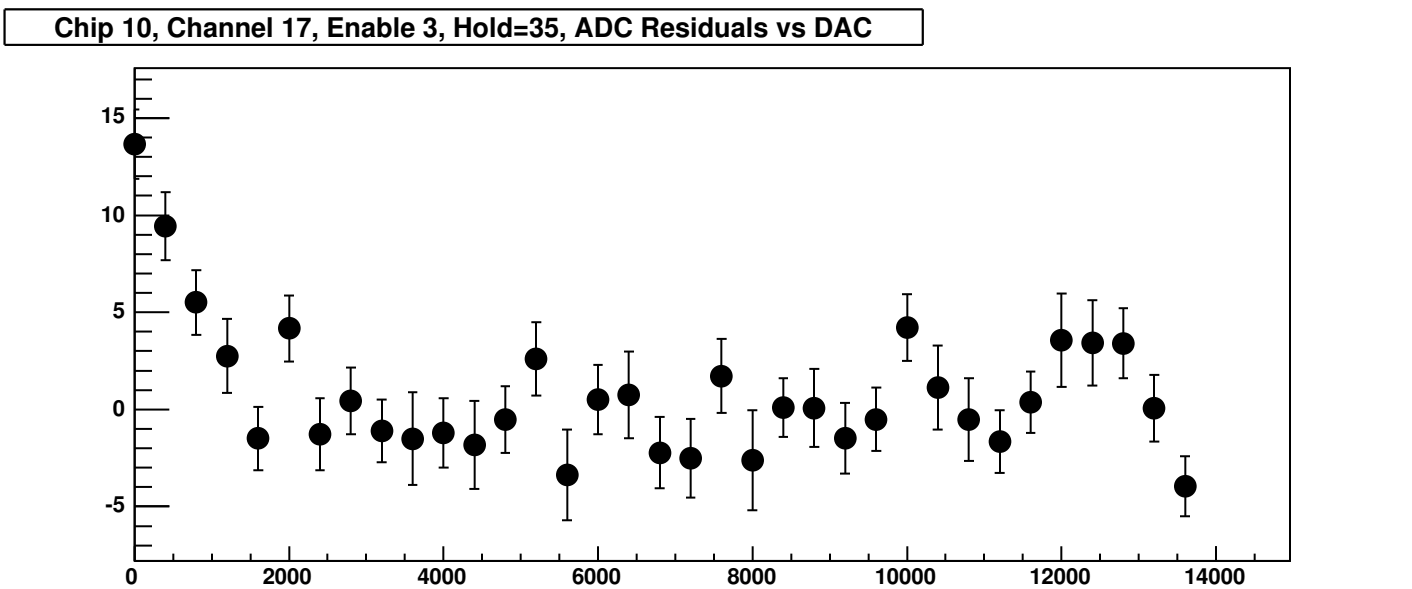

Chip 10, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 121.3 / 23
p0 129.8 ± 3.196
p1 2.242 ± 0.001925
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

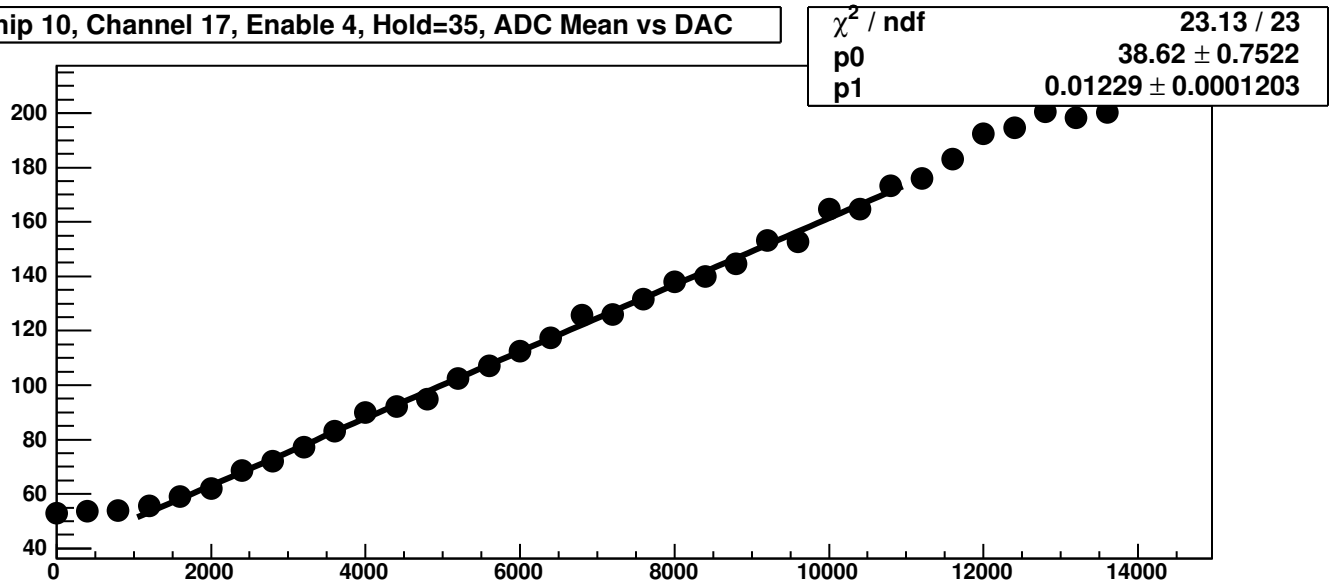

Chip 10, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 27.67 / 23
p0 37.76 ± 0.8372
p1 0.01204 ± 0.0001272
200
180
160
140
120
100
80
60
40
0
2000
4000
6000
8000
10000
12000
14000

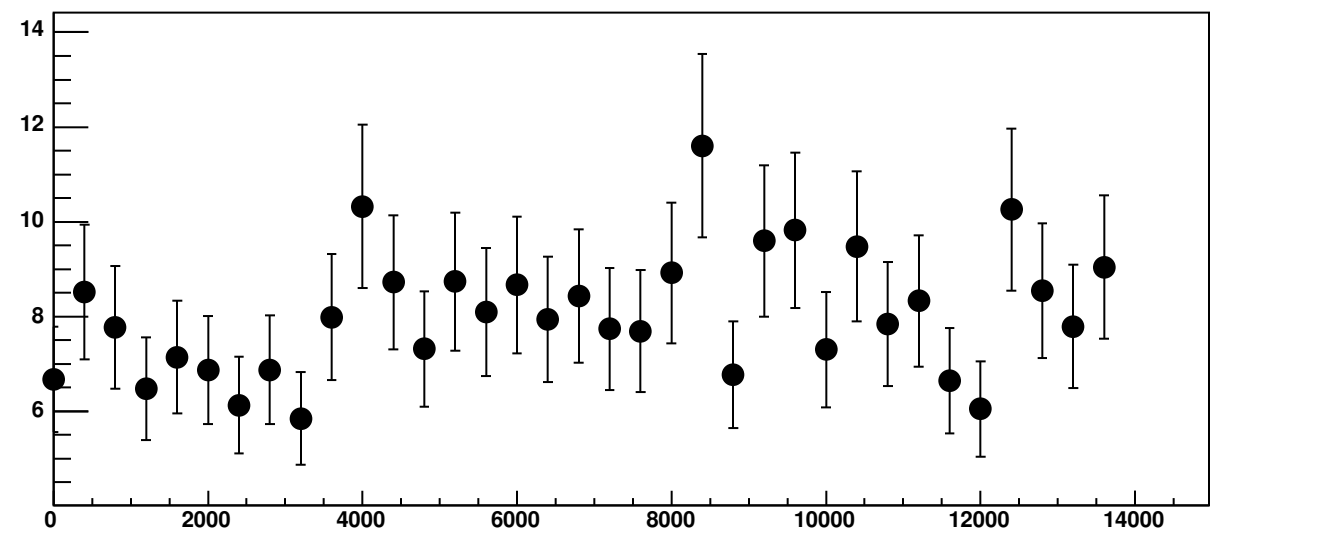

Chip 10, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

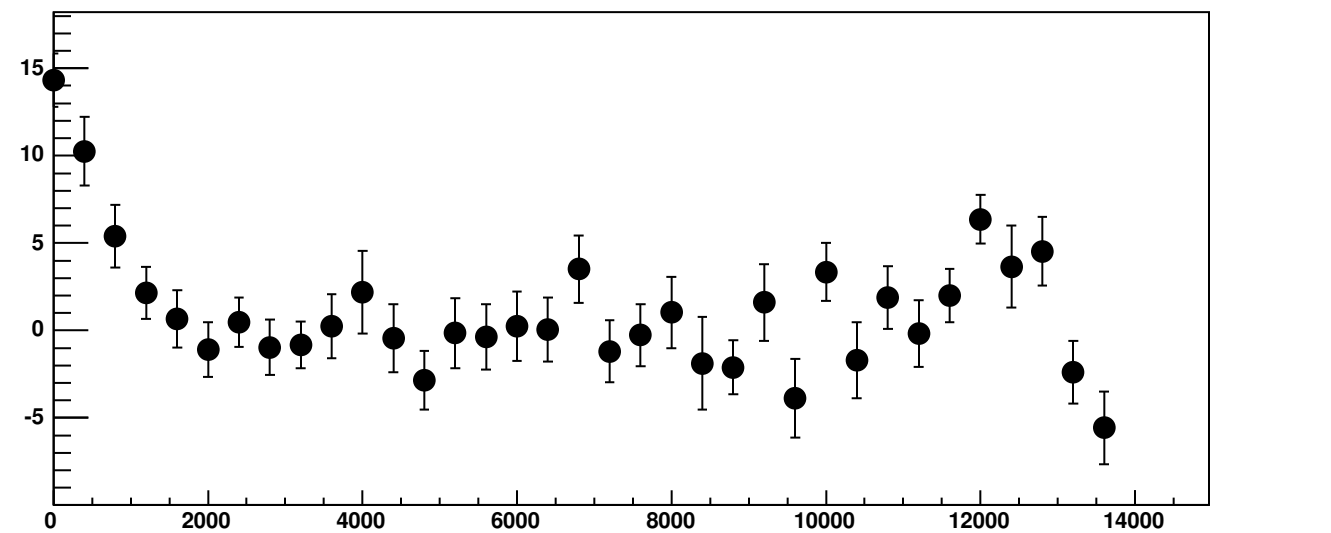
Chip 10, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC

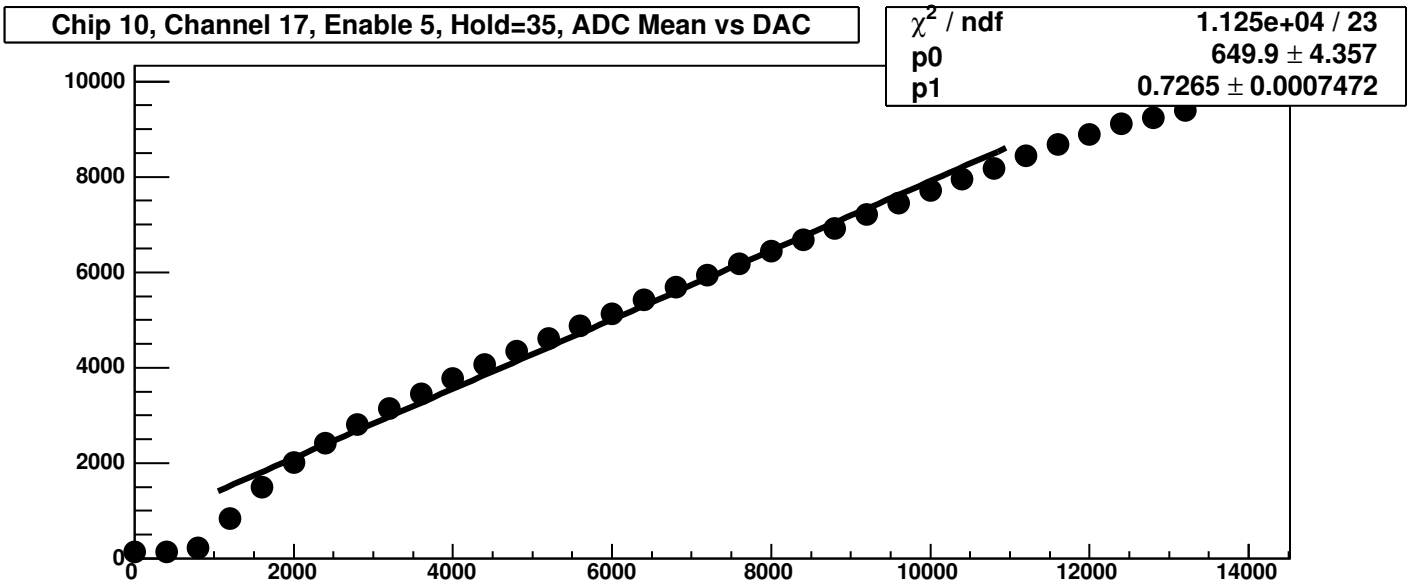


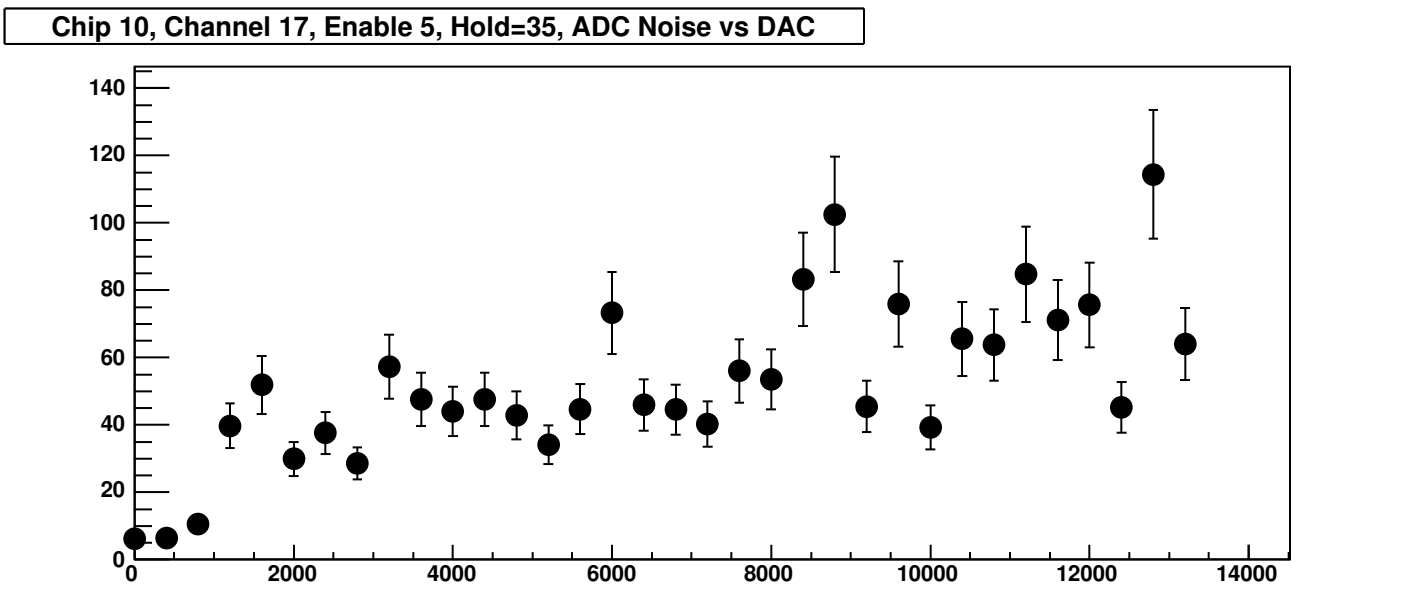
Chip 10, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC

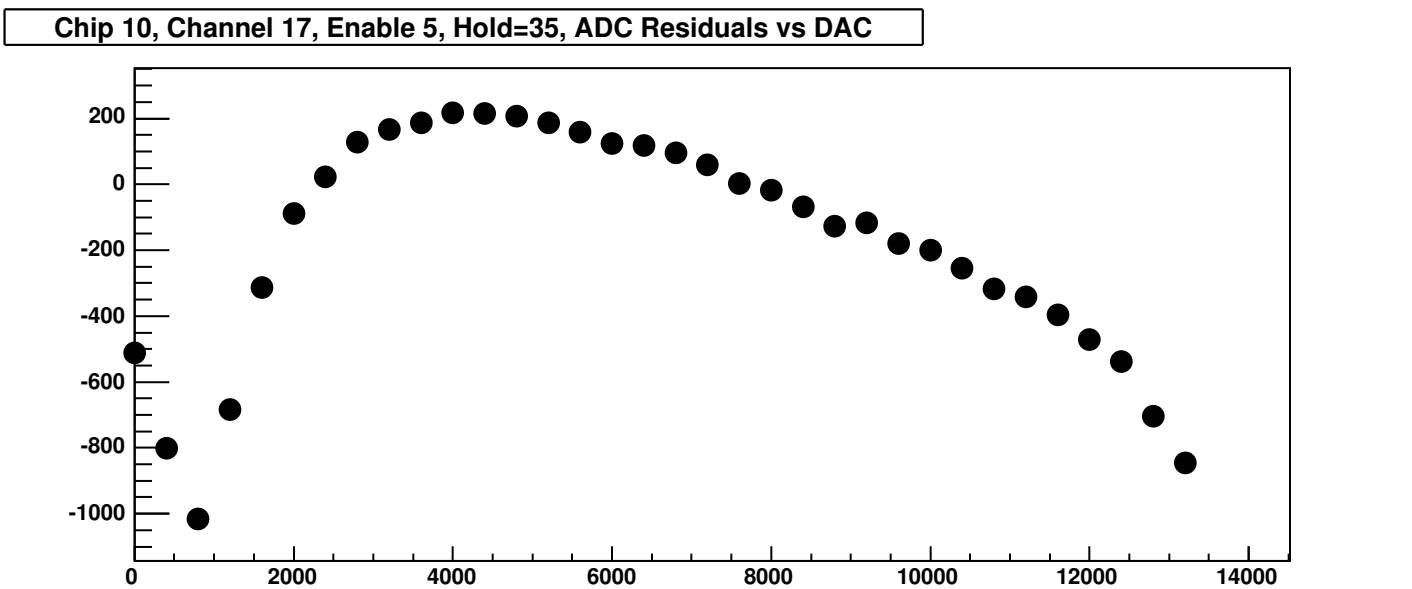


Chip 10, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC

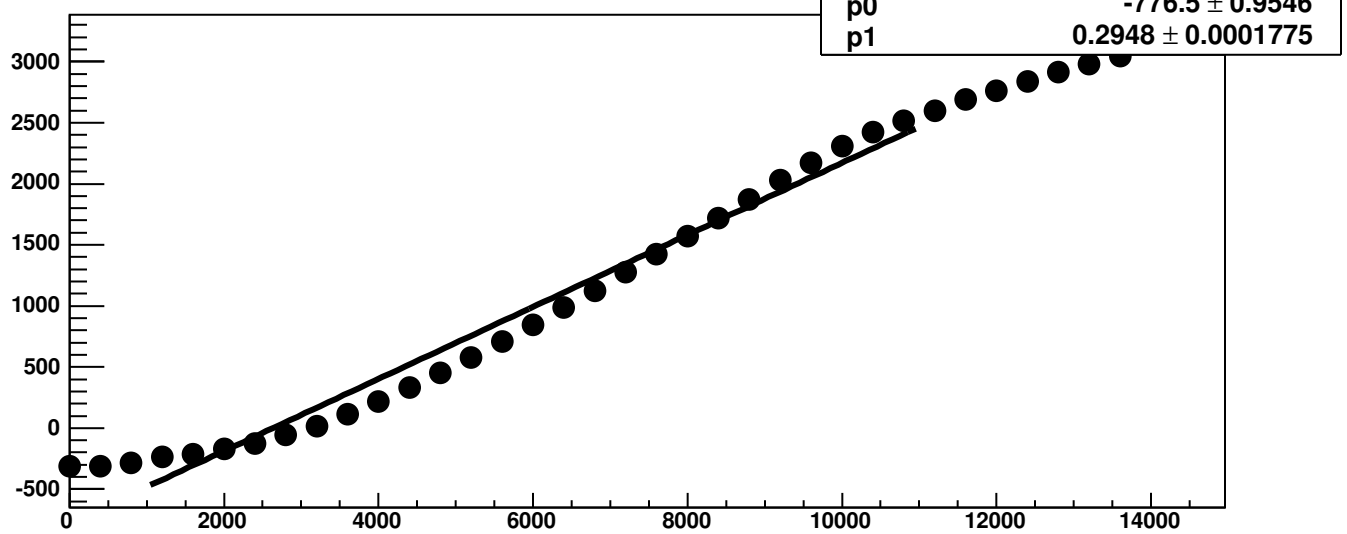



Chip 10, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.125e+04 / 23
p0 649.9 ± 4.357
p1 0.7265 ± 0.0007472
10000
8000
6000
4000
2000
0
0
2000
4000
6000
8000
10000
12000
14000

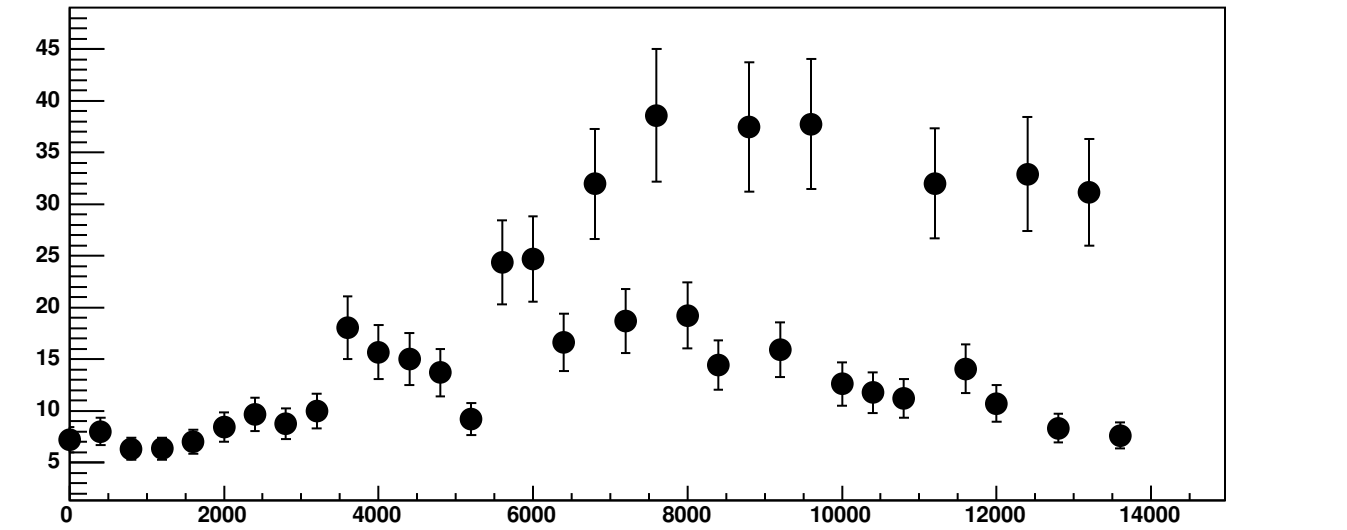

Chip 10, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 10, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

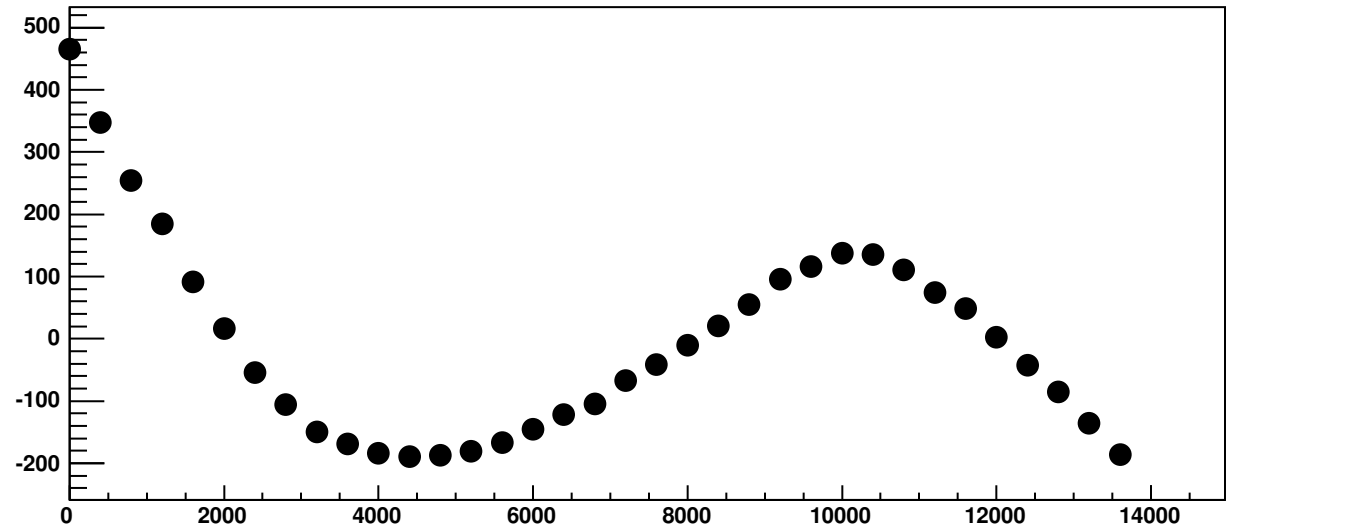
Chip 11, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC



Chip 11, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC

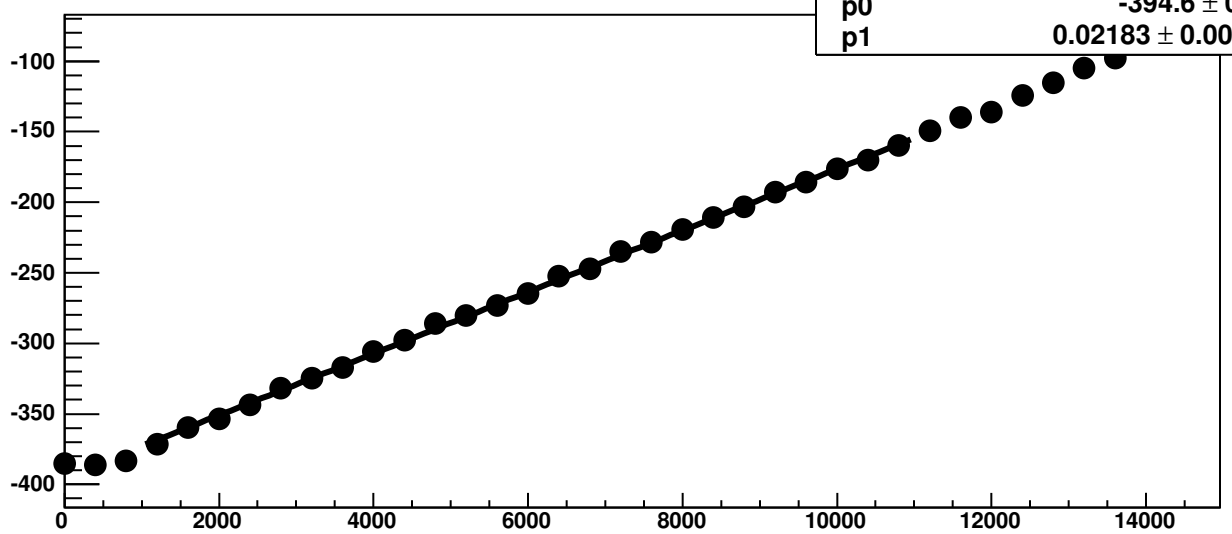


Chip 11, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

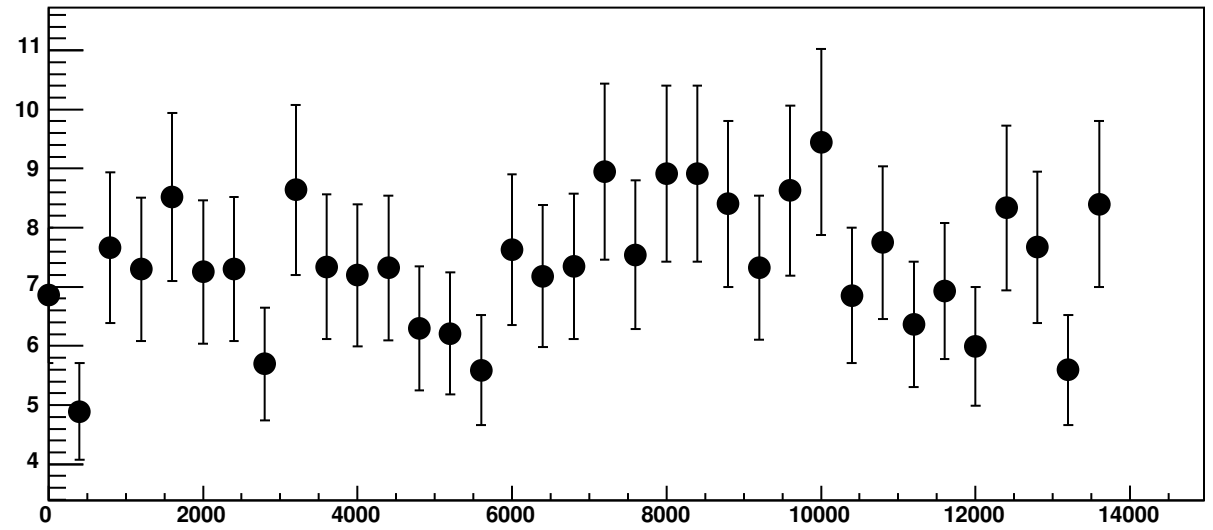


Chip 11, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC

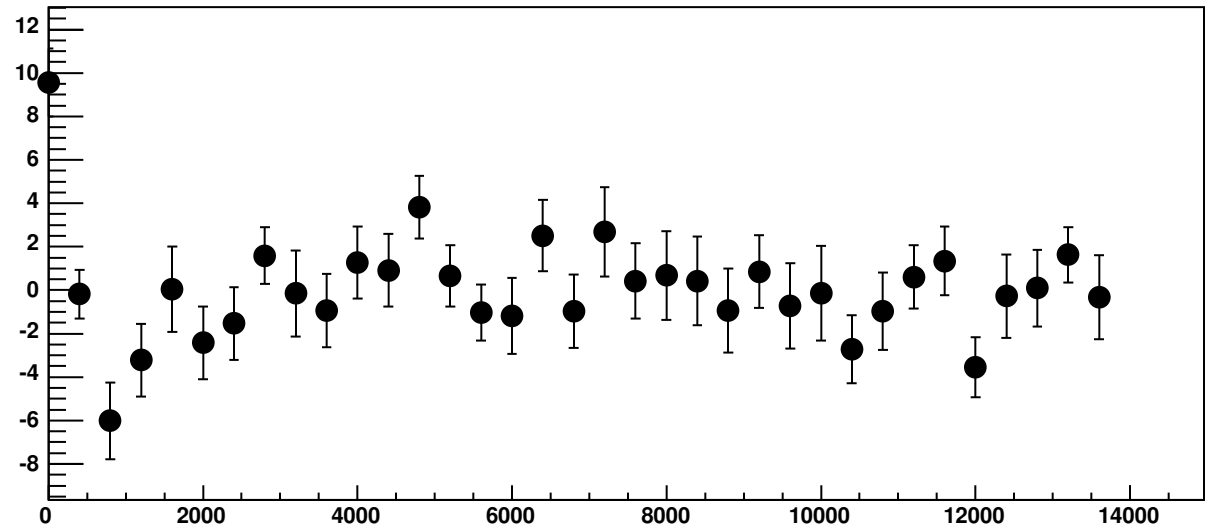
χ^2 / ndf	26.07 / 23
p0	-394.6 ± 0.7738
p1	0.02183 ± 0.0001213

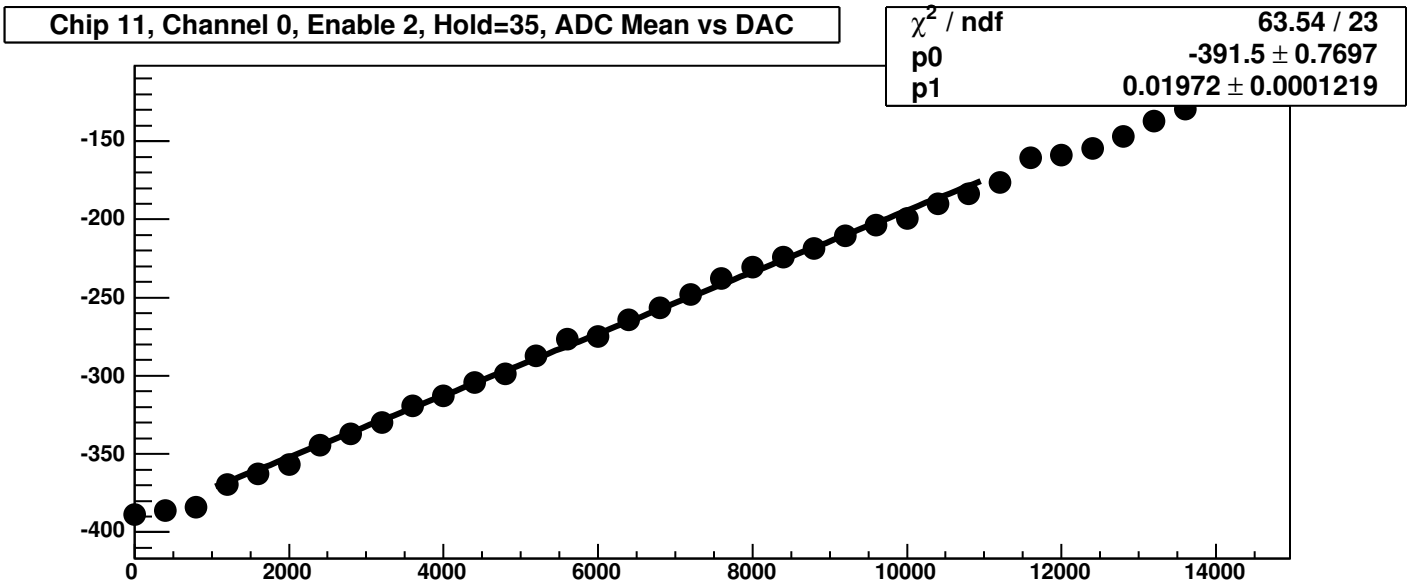


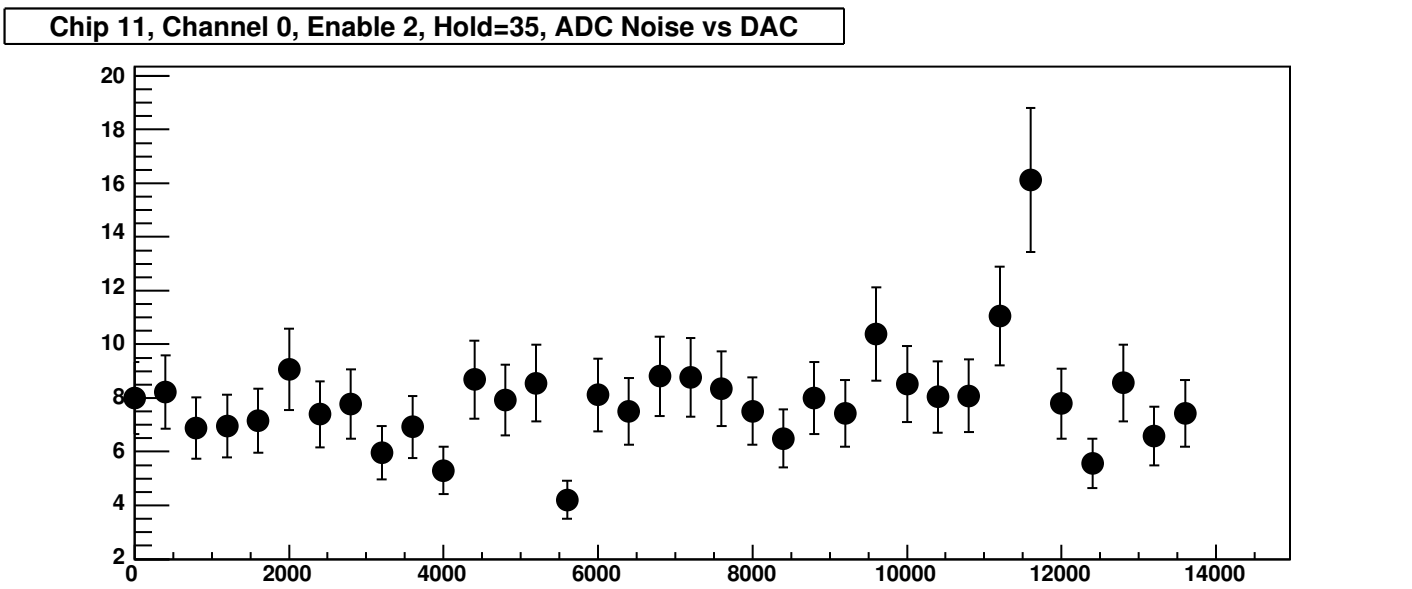
Chip 11, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC

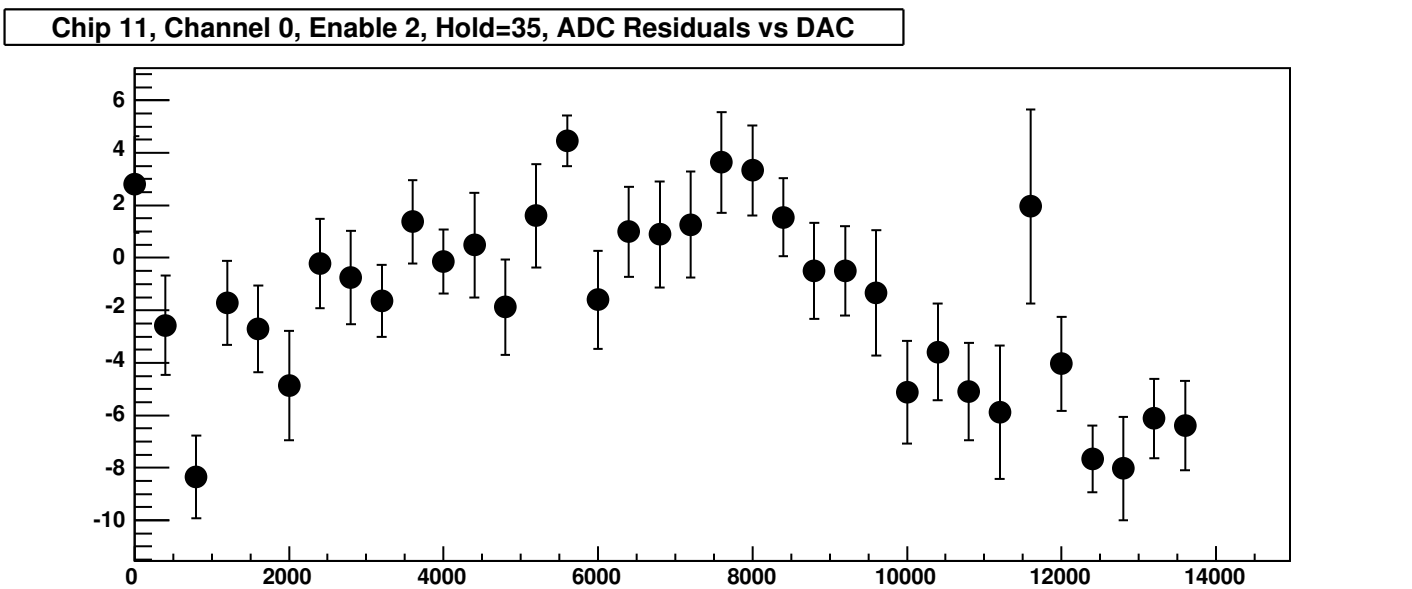


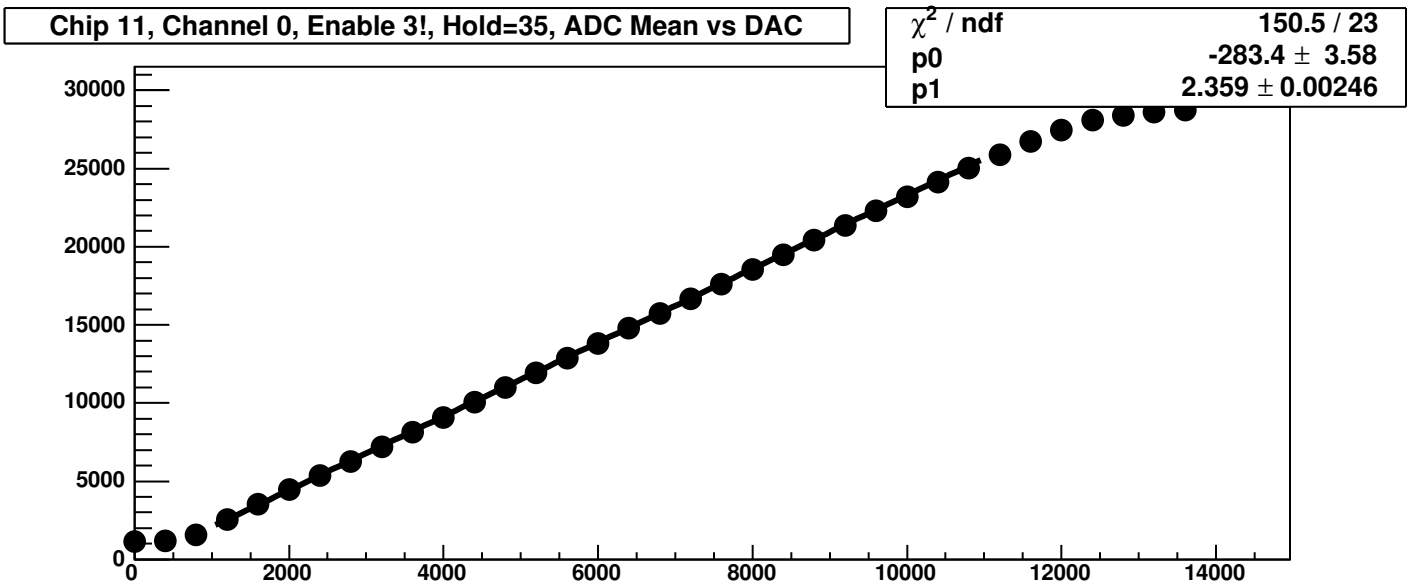
Chip 11, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC

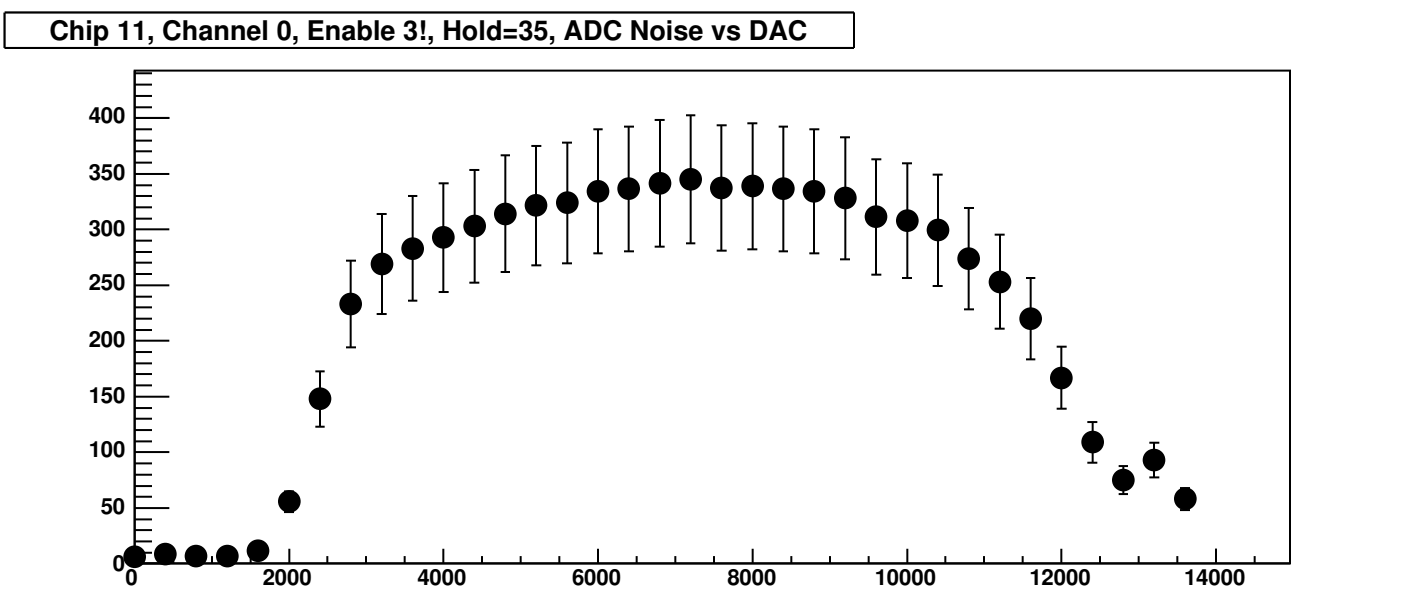


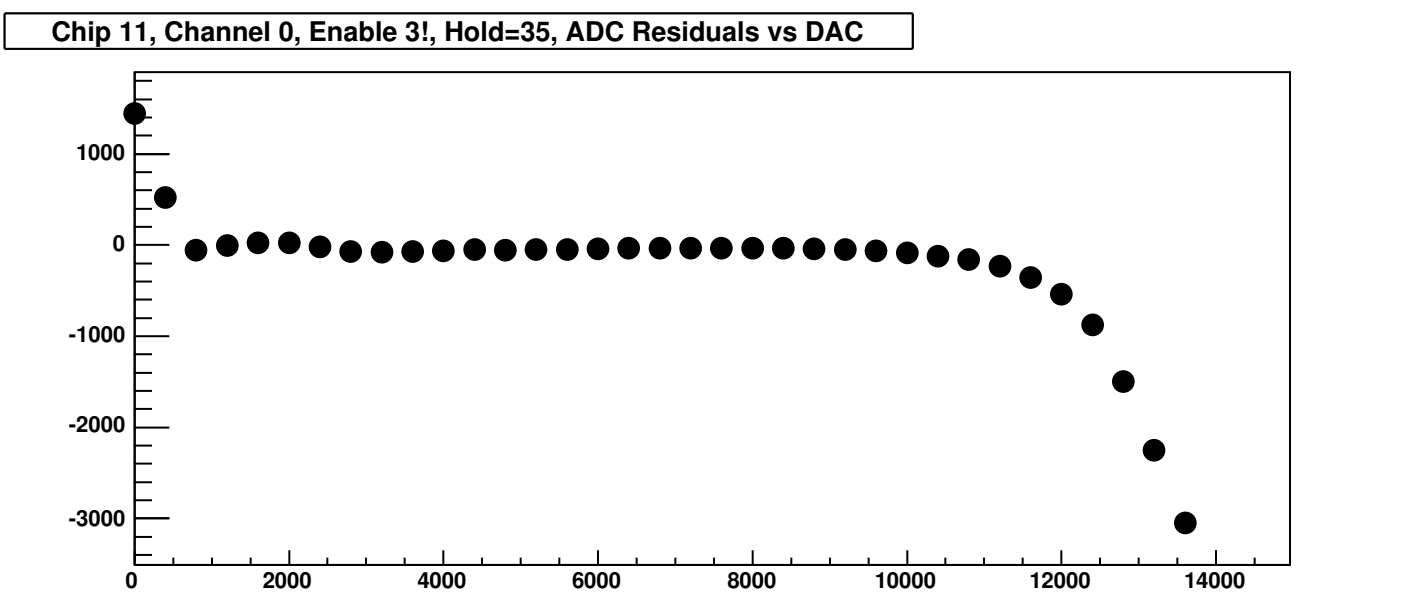

Chip 11, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 63.54 / 23
p0 -391.5 ± 0.7697
p1 0.01972 ± 0.0001219
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

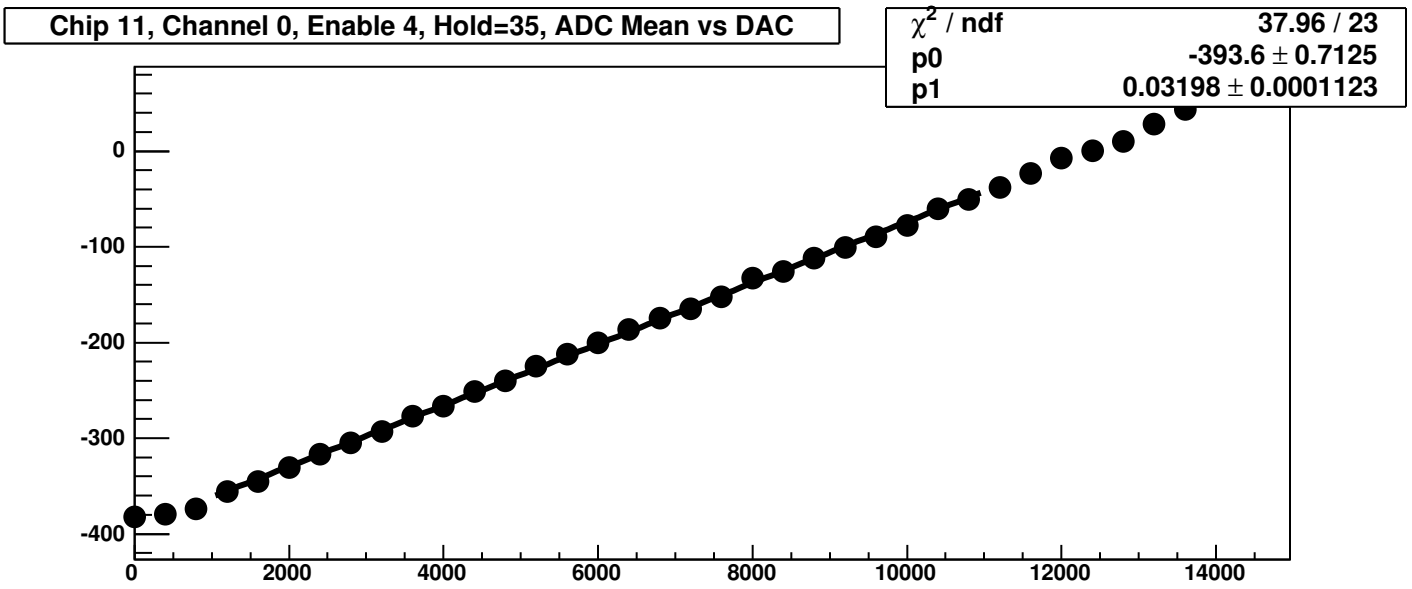

Chip 11, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
20
18
16
14
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

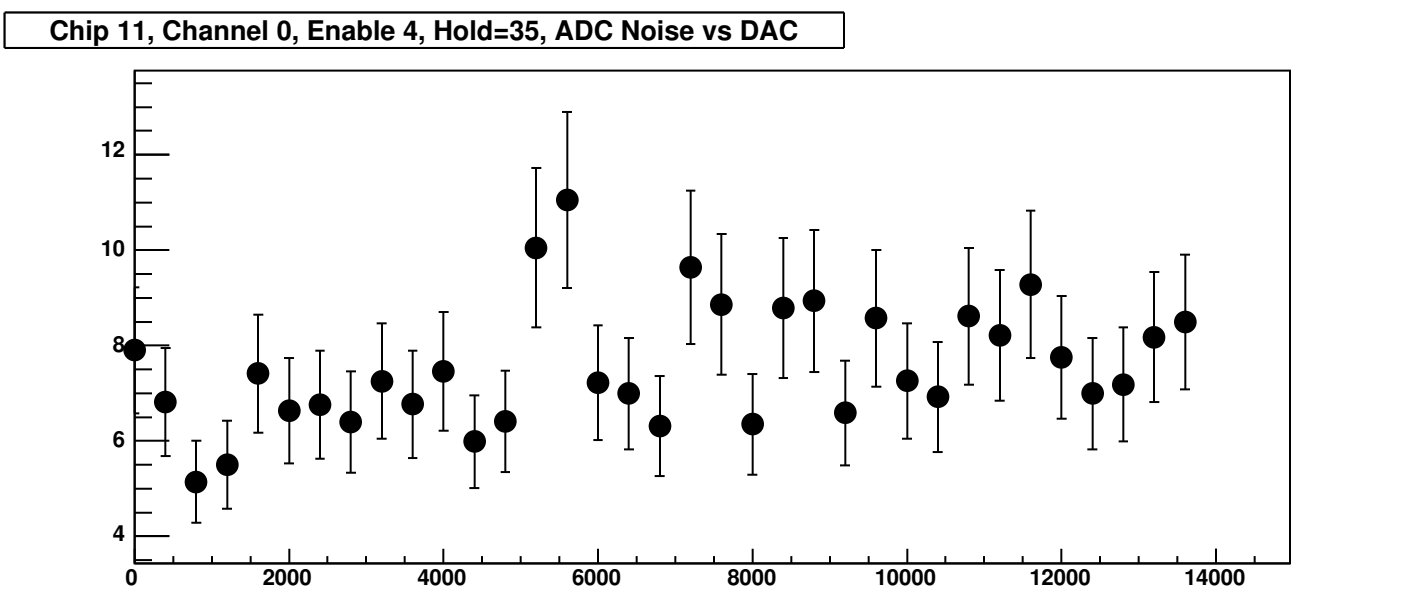

Chip 11, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
0
2000
4000
6000
8000
10000
12000
14000

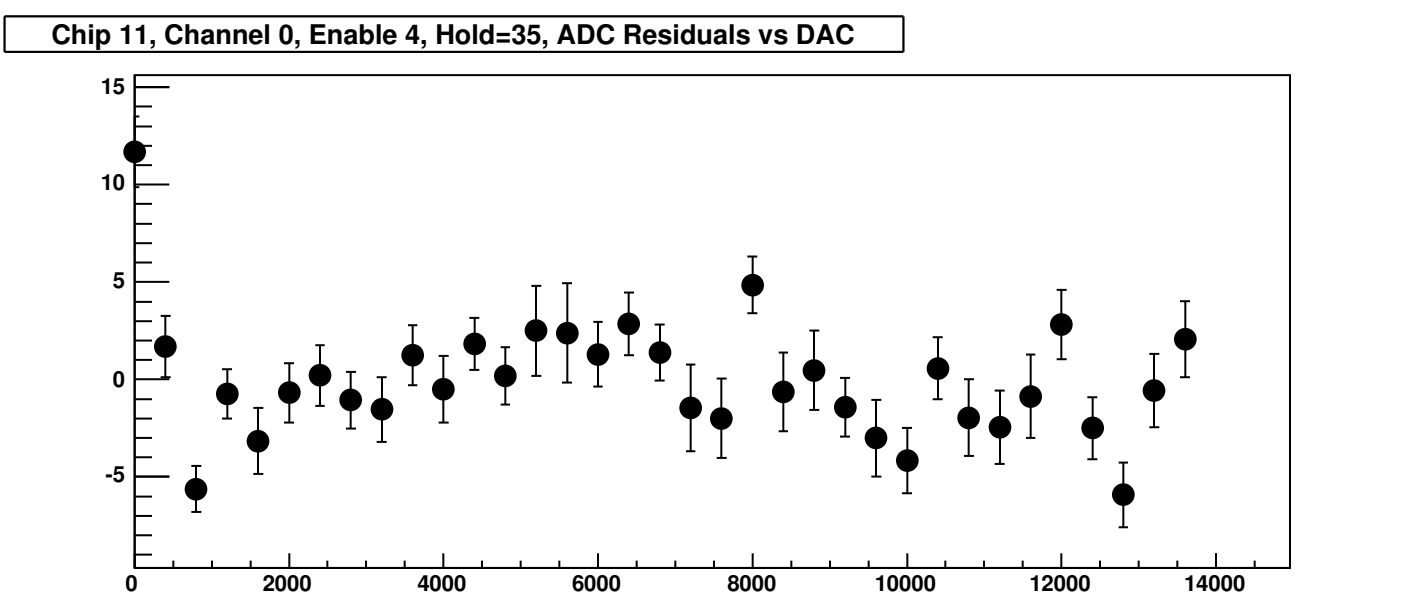

Chip 11, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 150.5 / 23
p0 -283.4 ± 3.58
p1 2.359 ± 0.00246
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

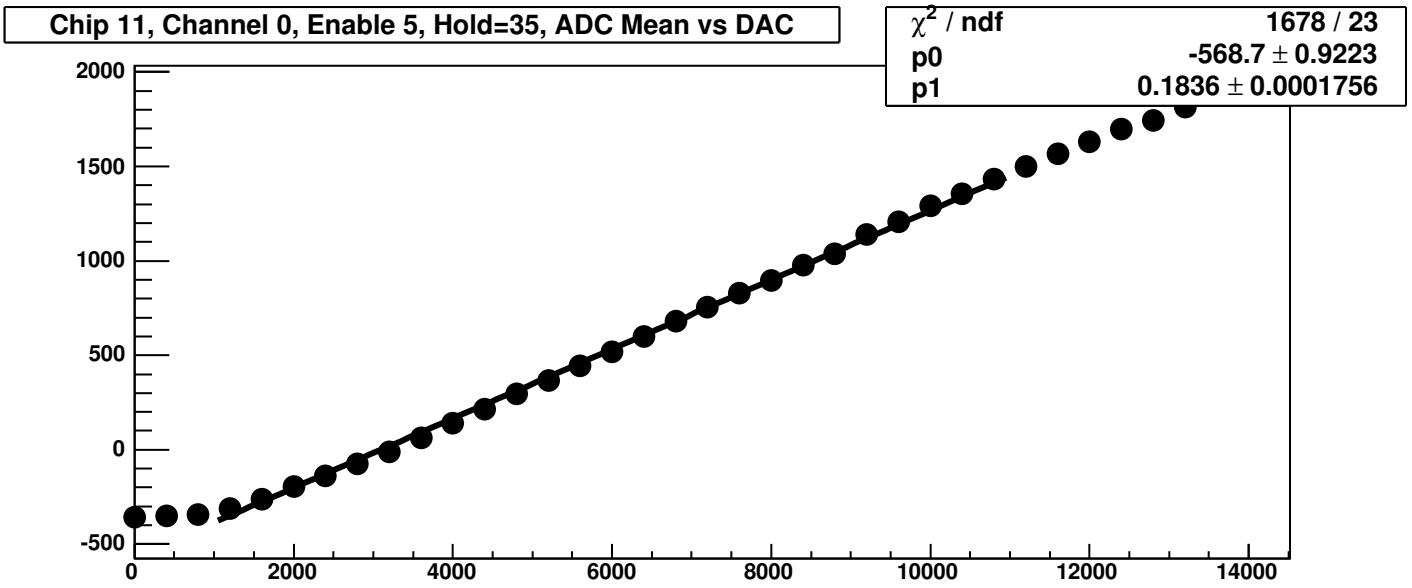

Chip 11, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

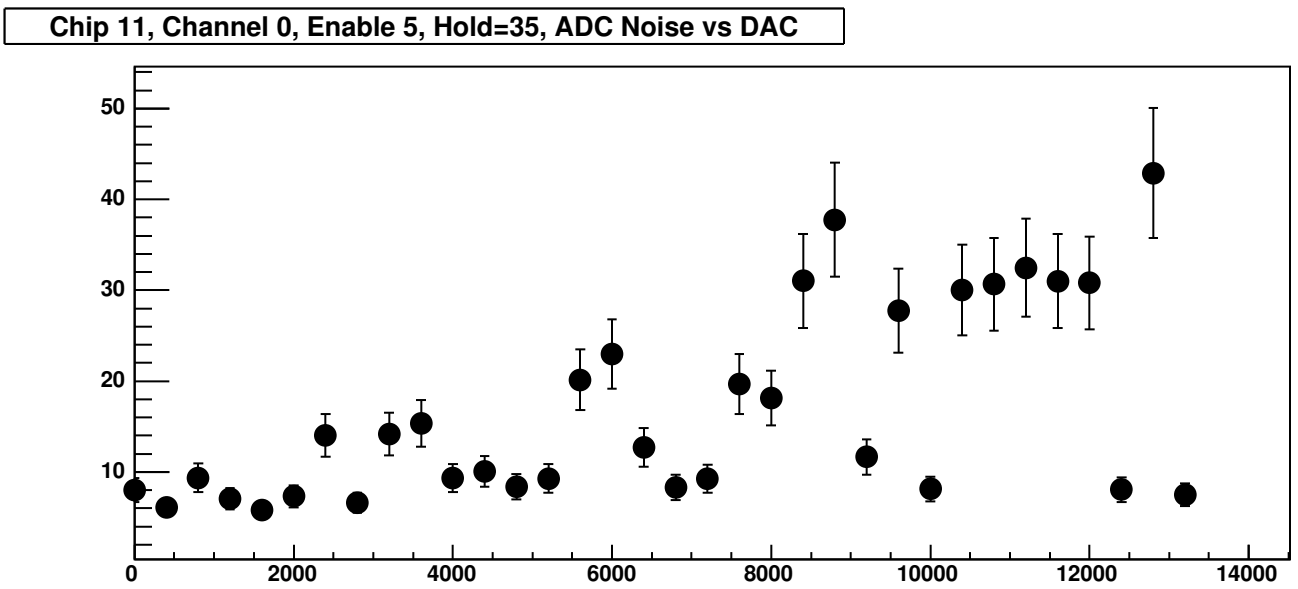

Chip 11, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
1000
0
-1000
-2000
-3000
0
2000
4000
6000
8000
10000
12000
14000

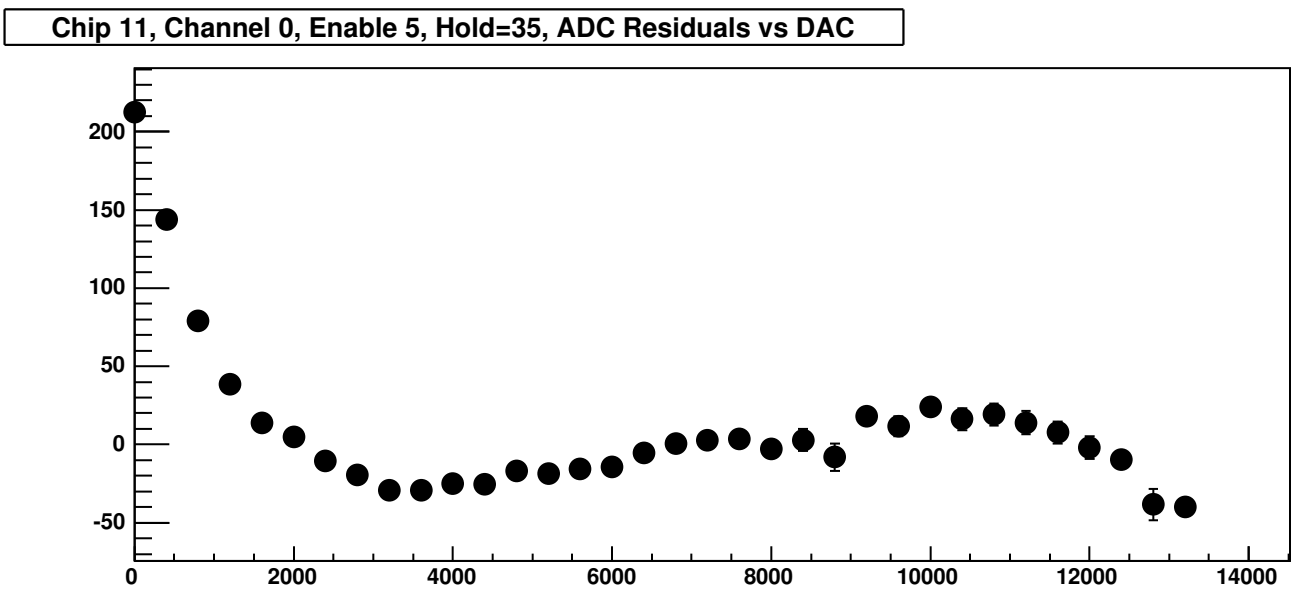

Chip 11, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 37.96 / 23
p0 -393.6 ± 0.7125
p1 0.03198 ± 0.0001123
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

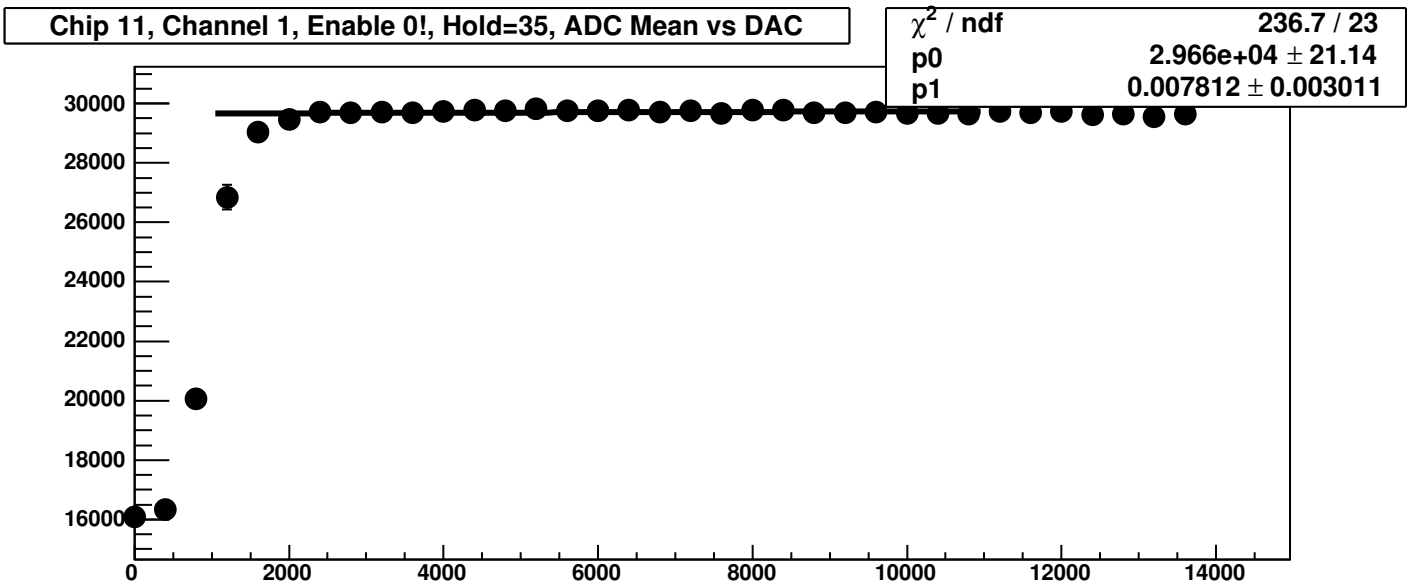

Chip 11, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

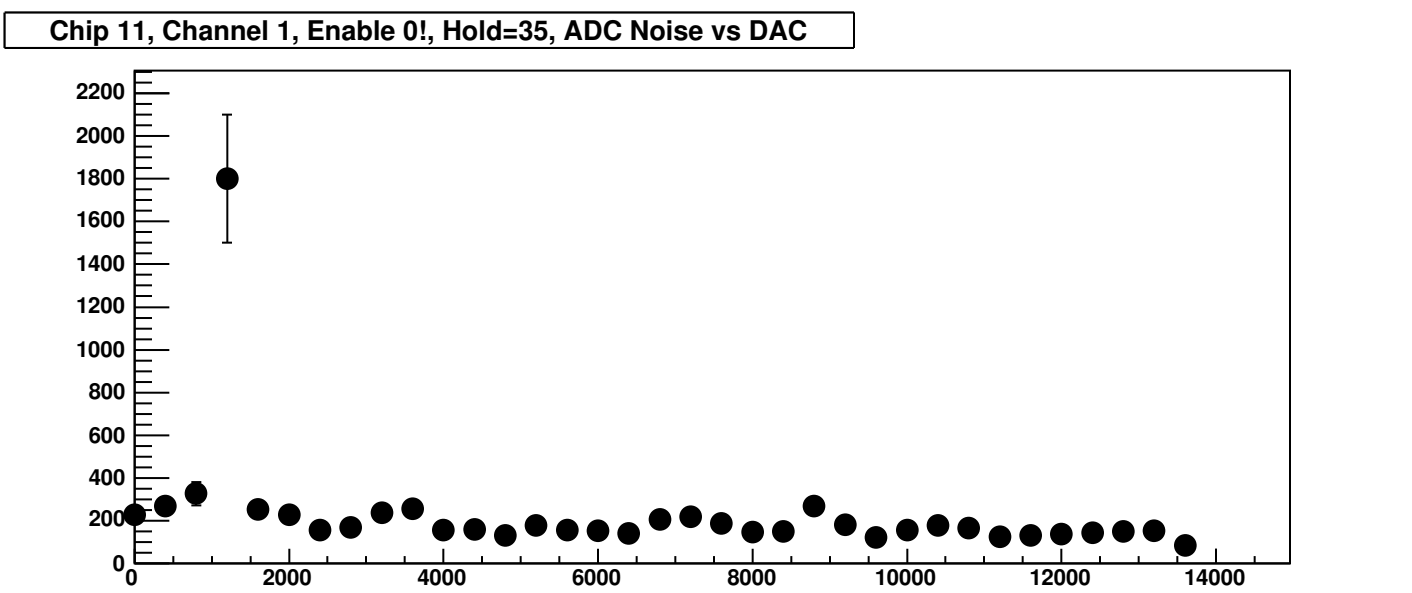

Chip 11, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

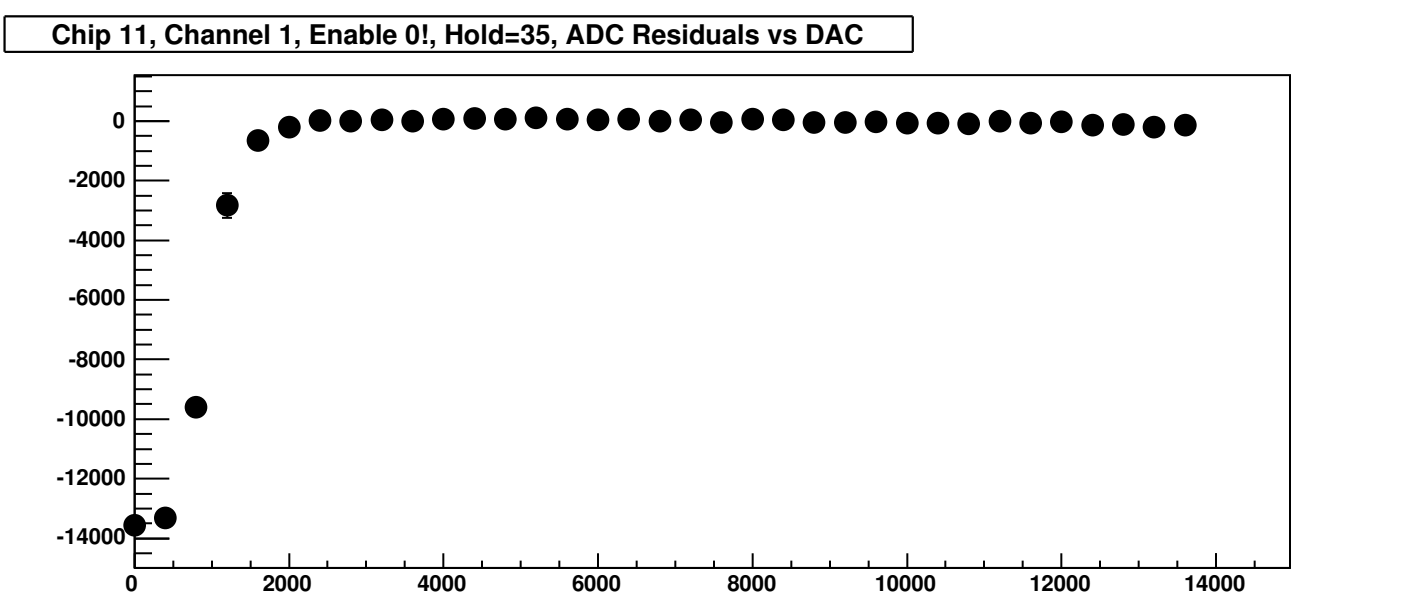

Chip 11, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1678 / 23
p0 -568.7 ± 0.9223
p1 0.1836 ± 0.0001756
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

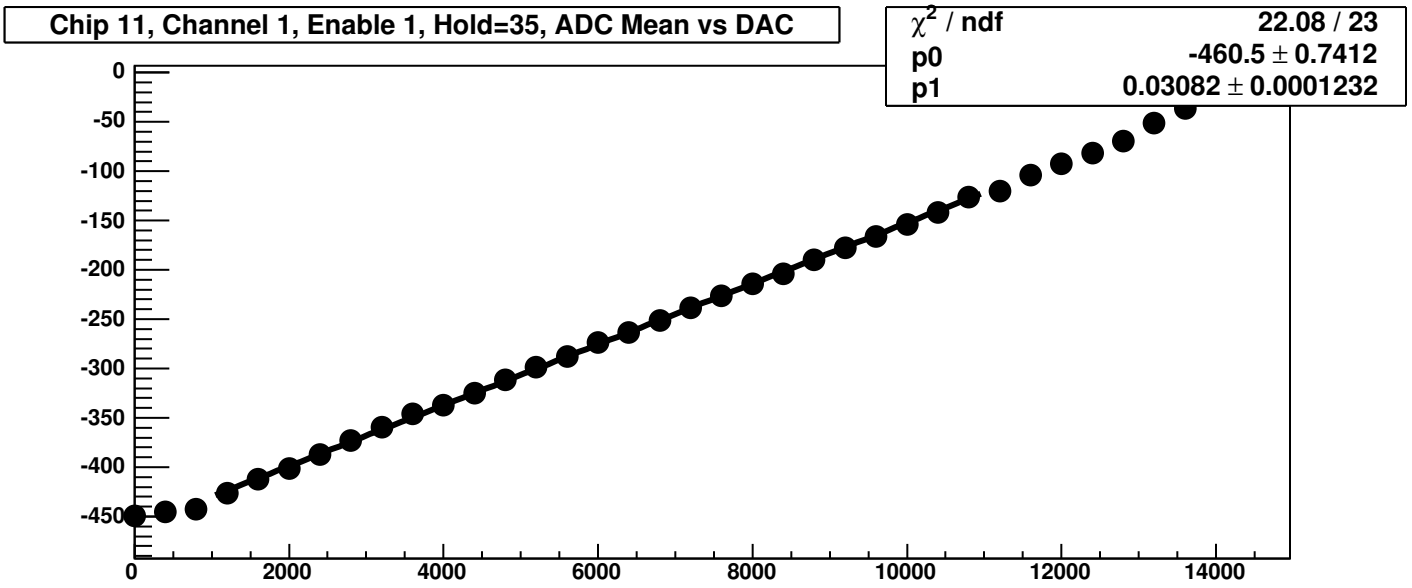

Chip 11, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

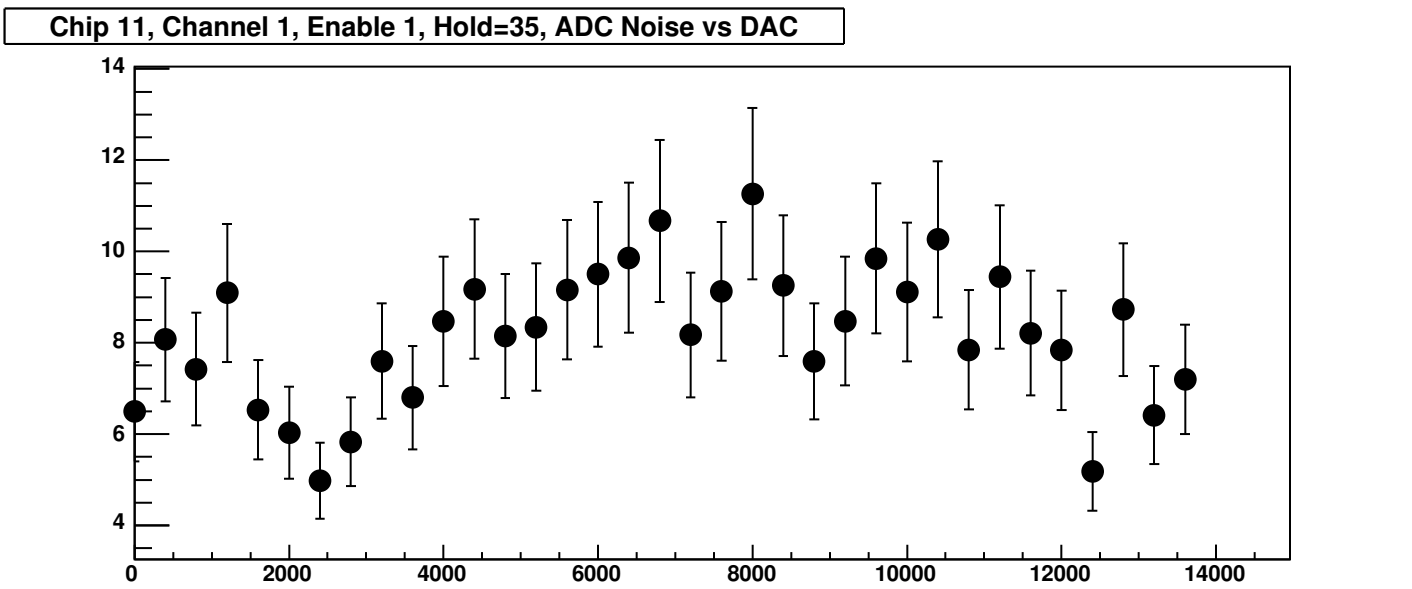

Chip 11, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

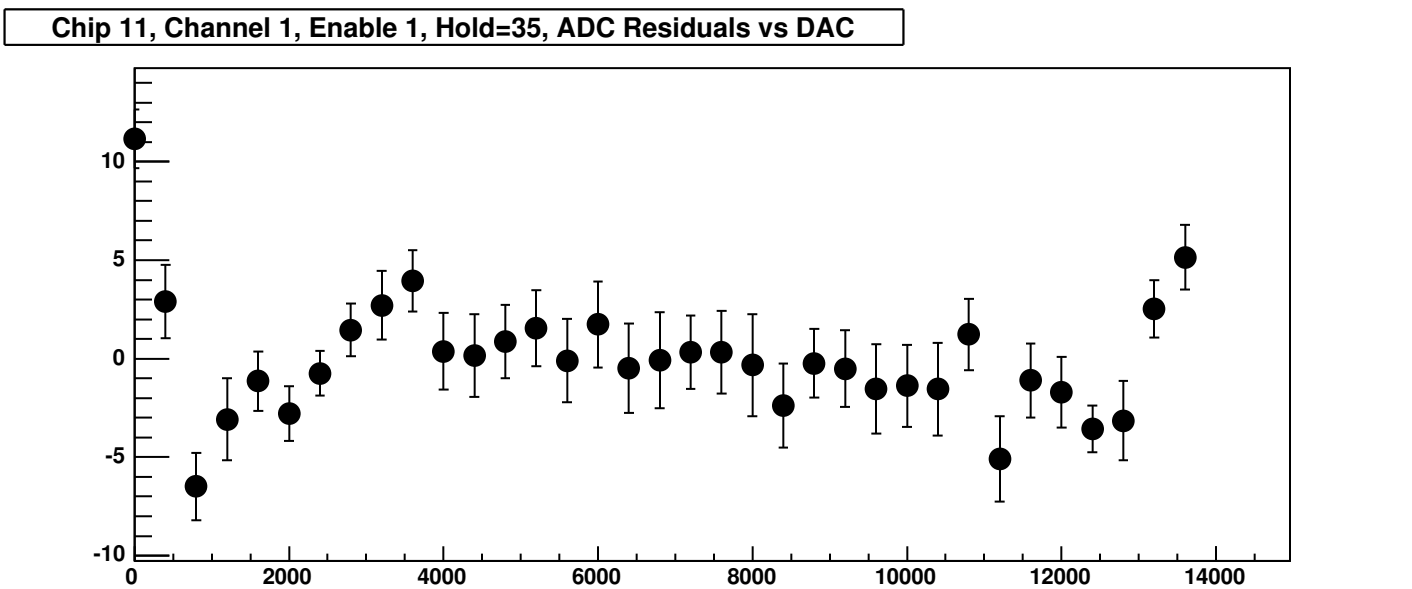

Chip 11, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 236.7 / 23
p0 2.966e+04 ± 21.14
p1 0.007812 ± 0.003011
30000
28000
26000
24000
22000
20000
18000
16000
0
2000
4000
6000
8000
10000
12000
14000

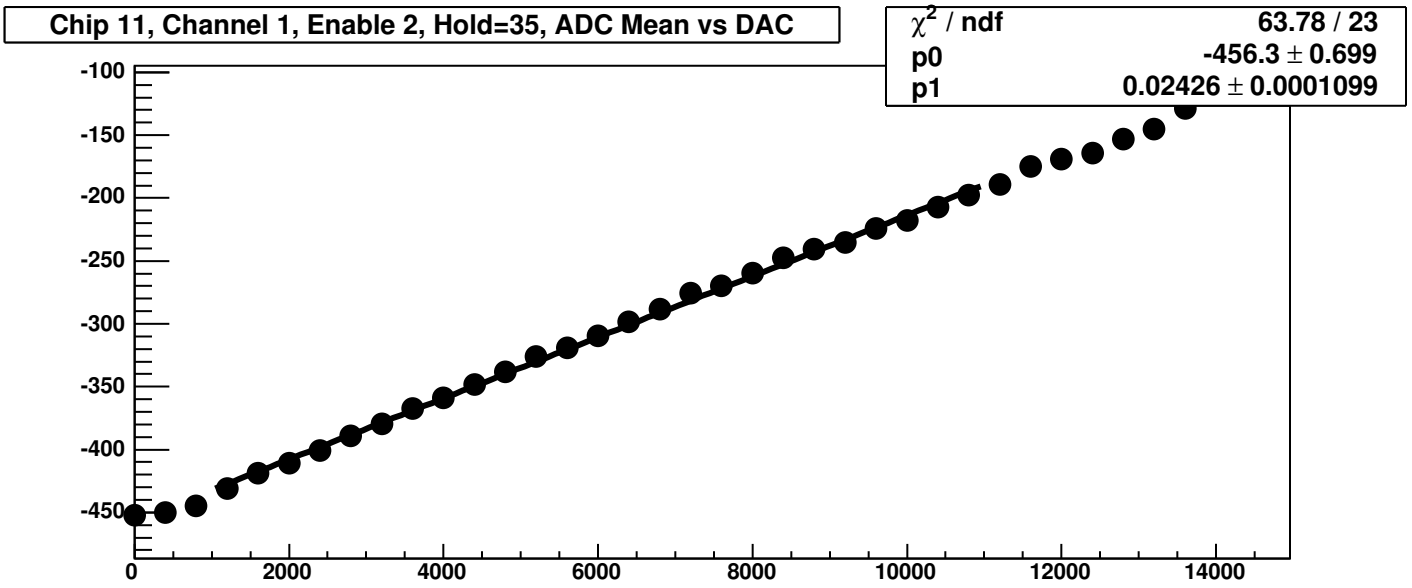

Chip 11, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

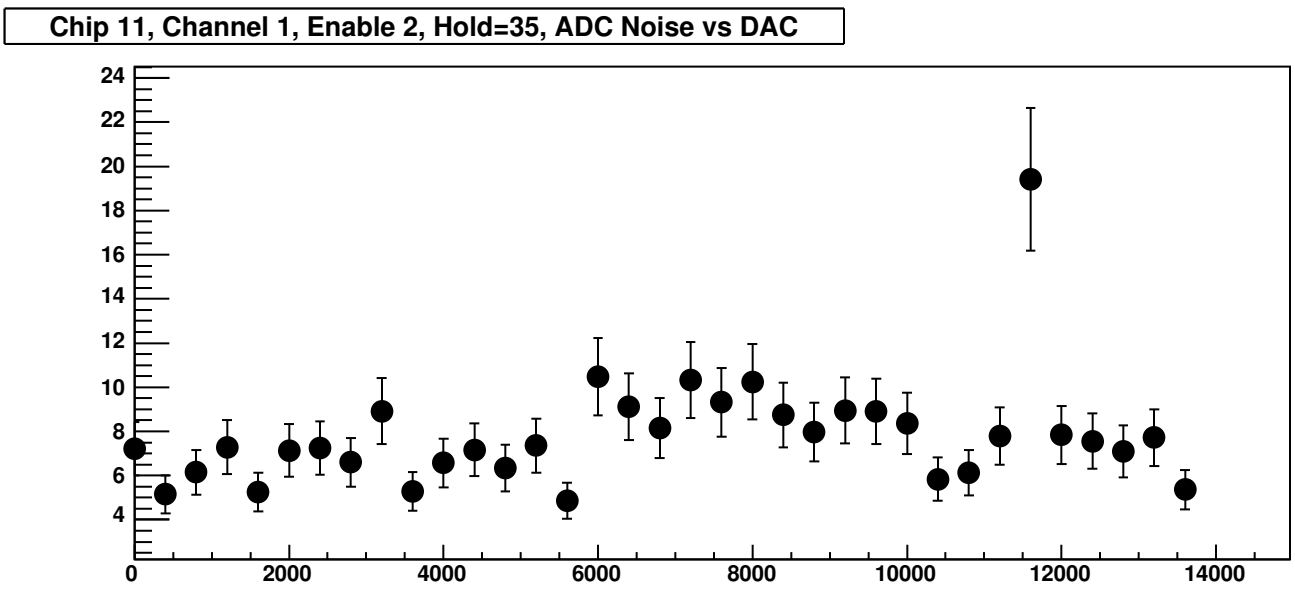

Chip 11, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

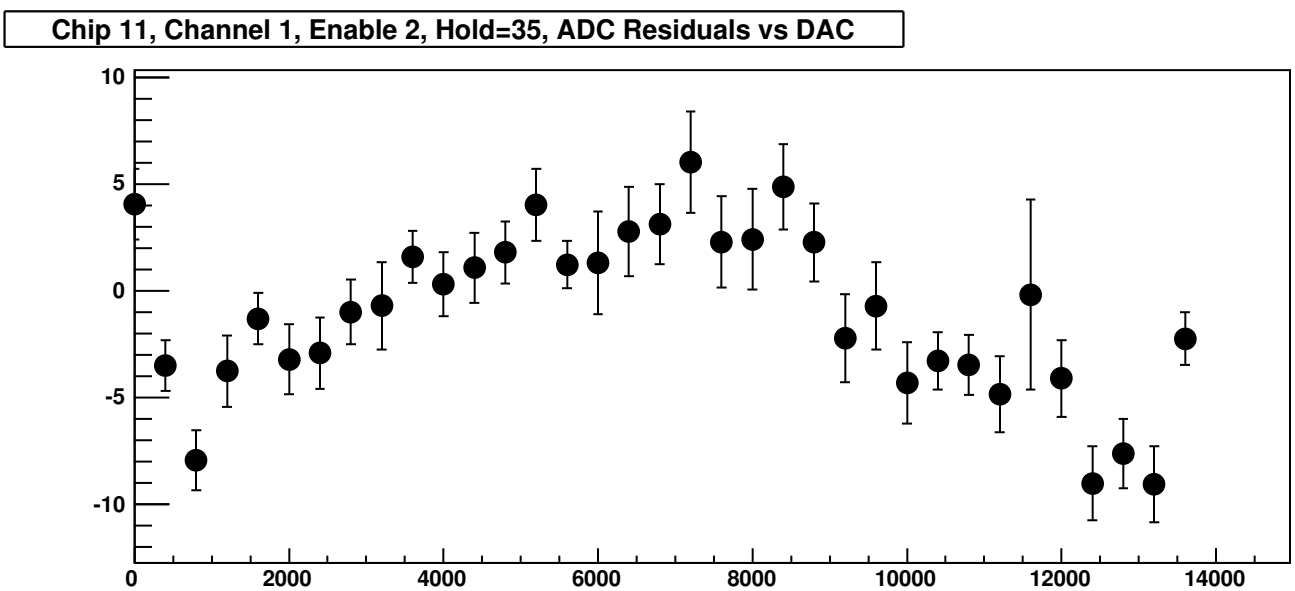

Chip 11, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 22.08 / 23
p0 -460.5 ± 0.7412
p1 0.03082 ± 0.0001232
0
-50
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

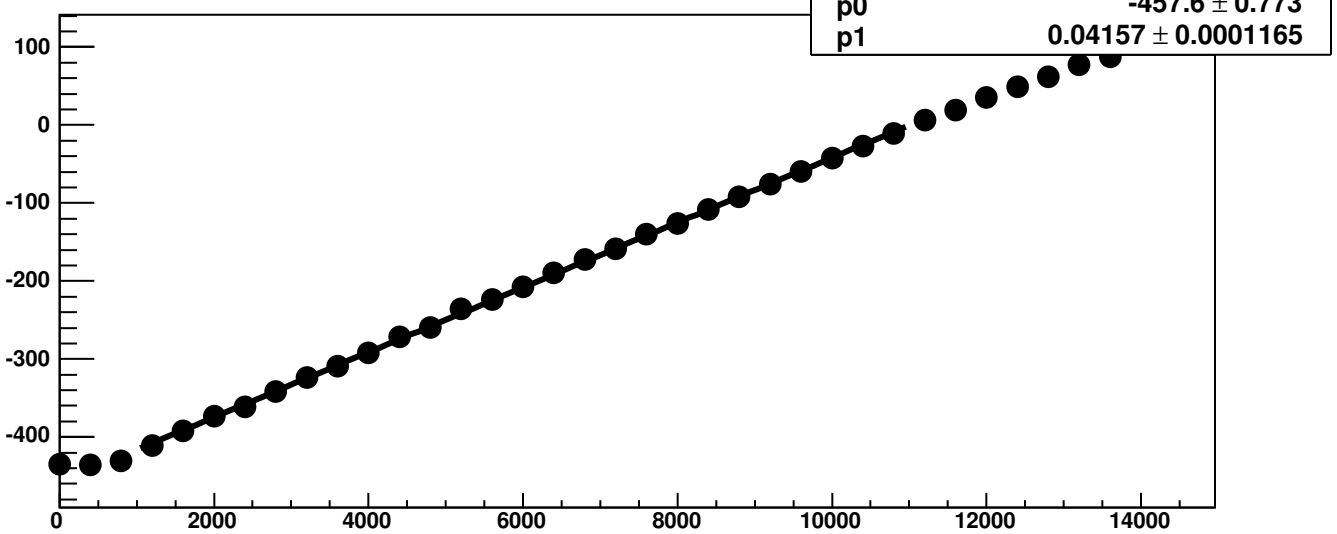

Chip 11, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 63.78 / 23
p0 -456.3 ± 0.699
p1 0.02426 ± 0.0001099
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

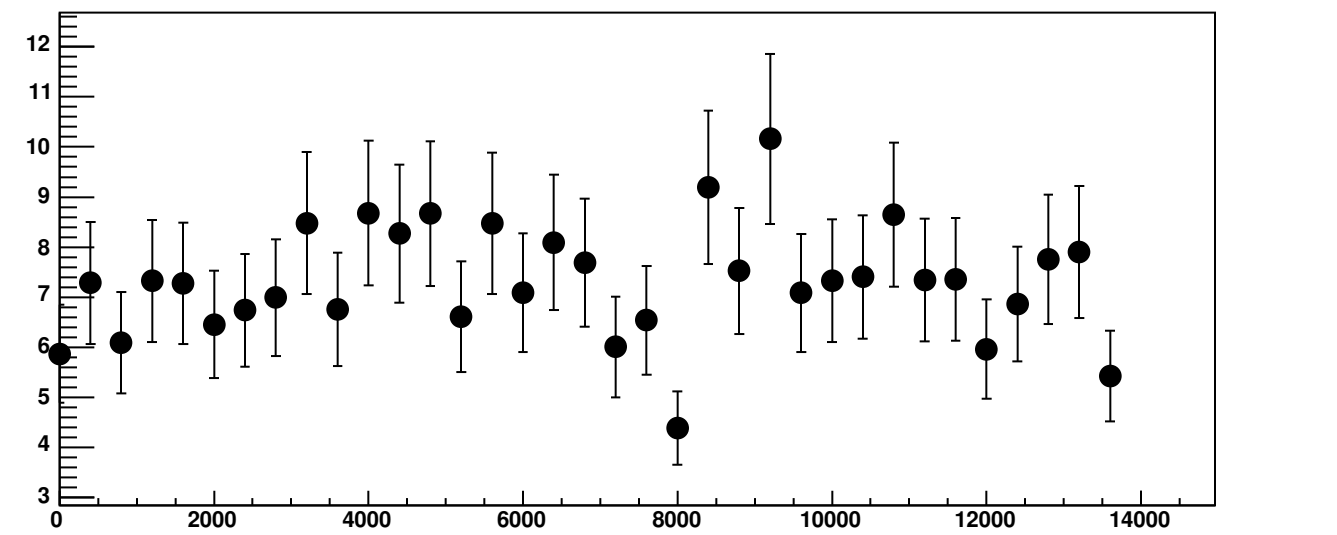

Chip 11, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
24
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

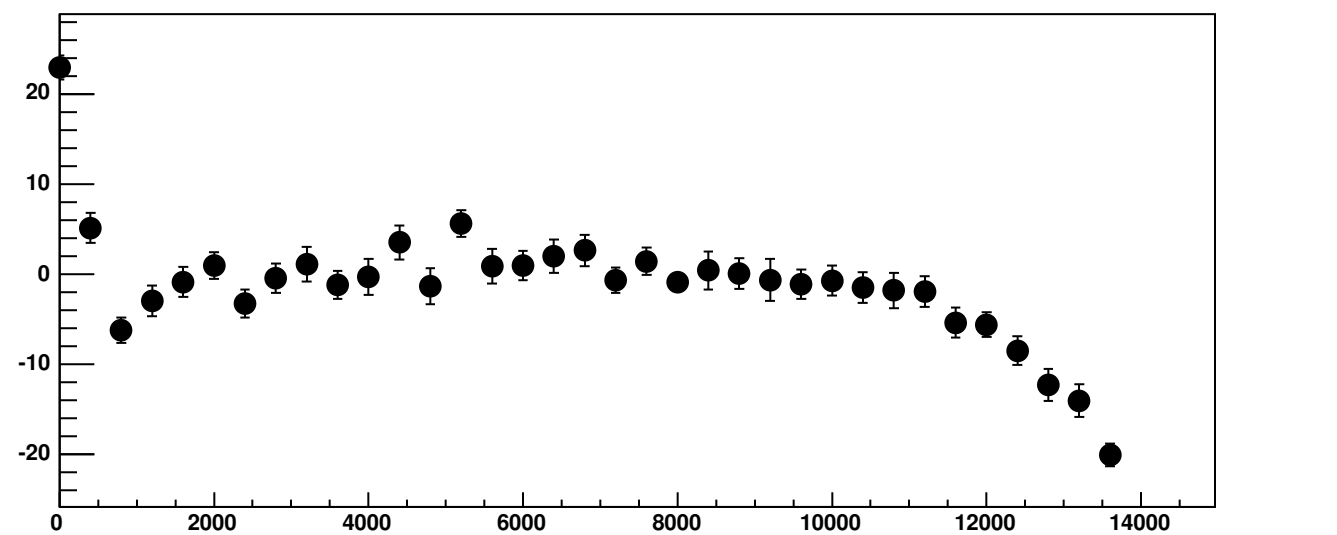
Chip 11, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC

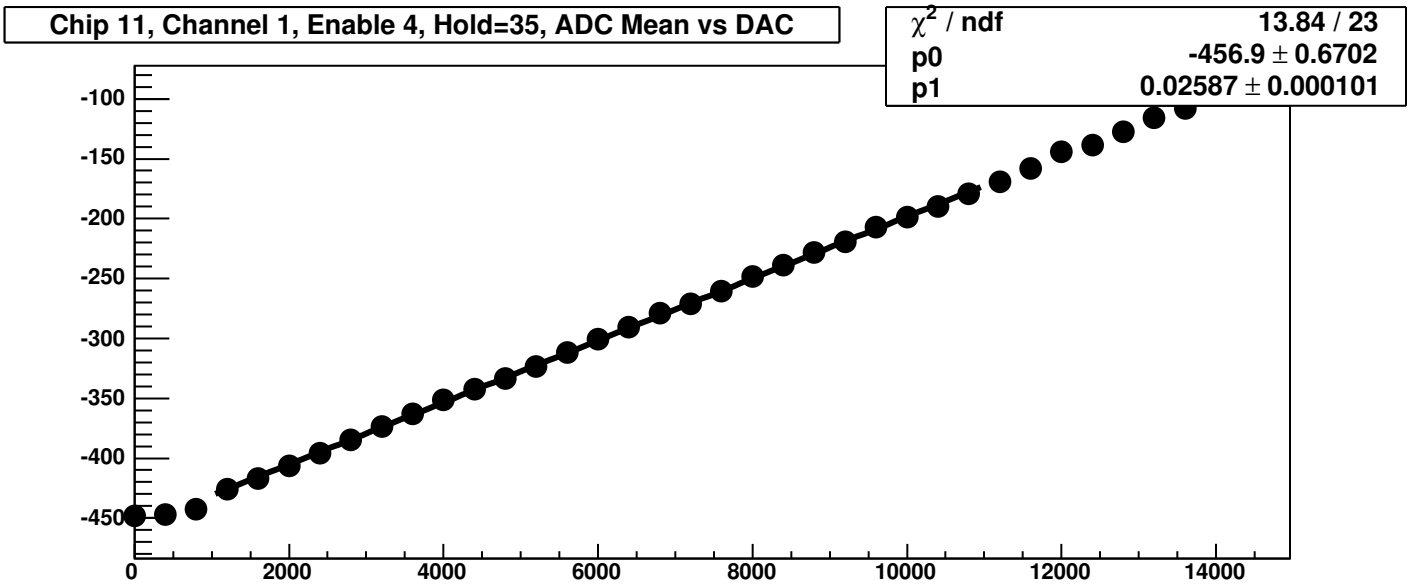


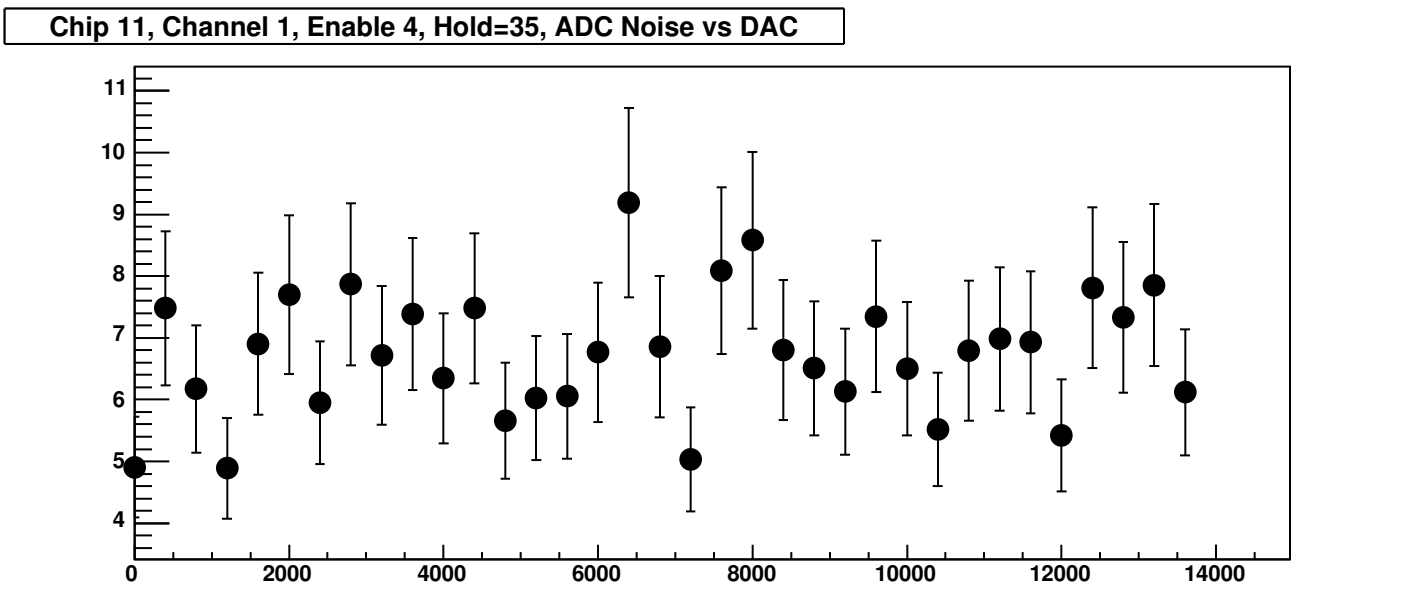
Chip 11, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC

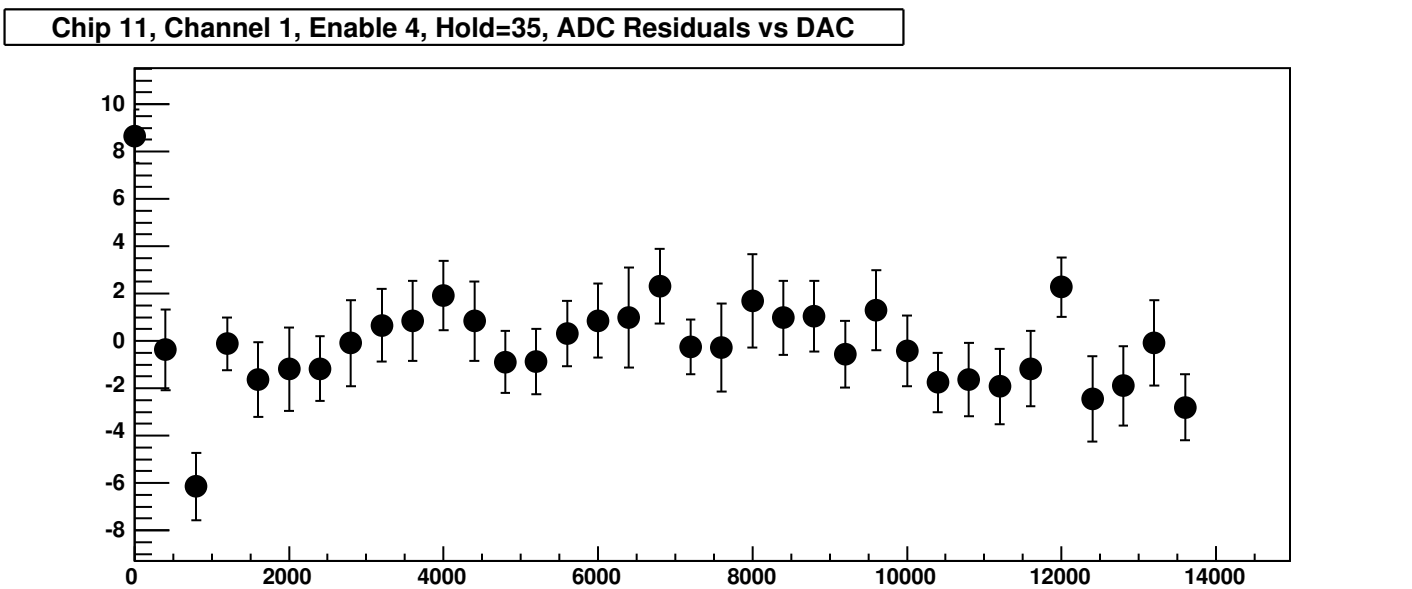


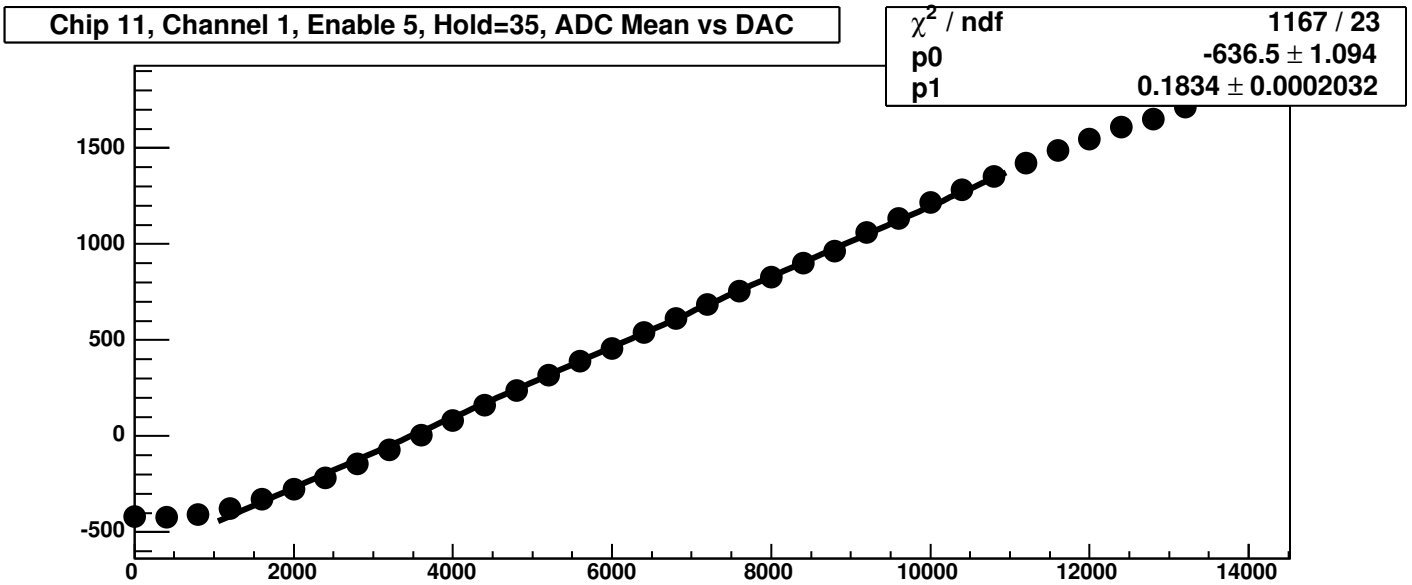
Chip 11, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC

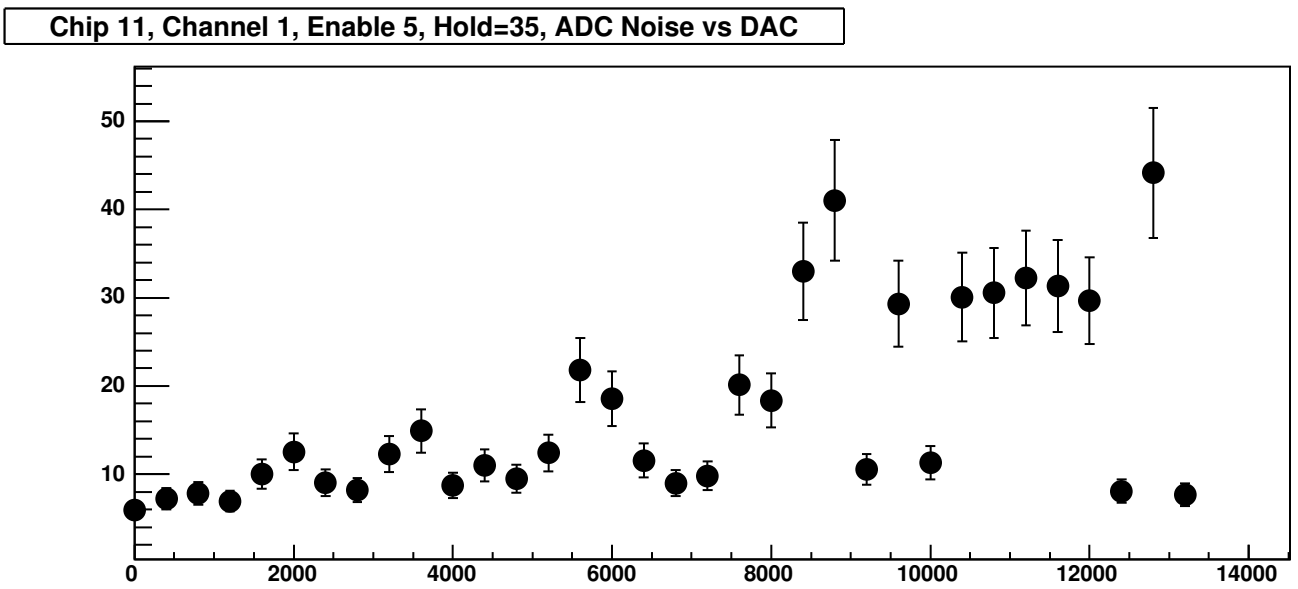


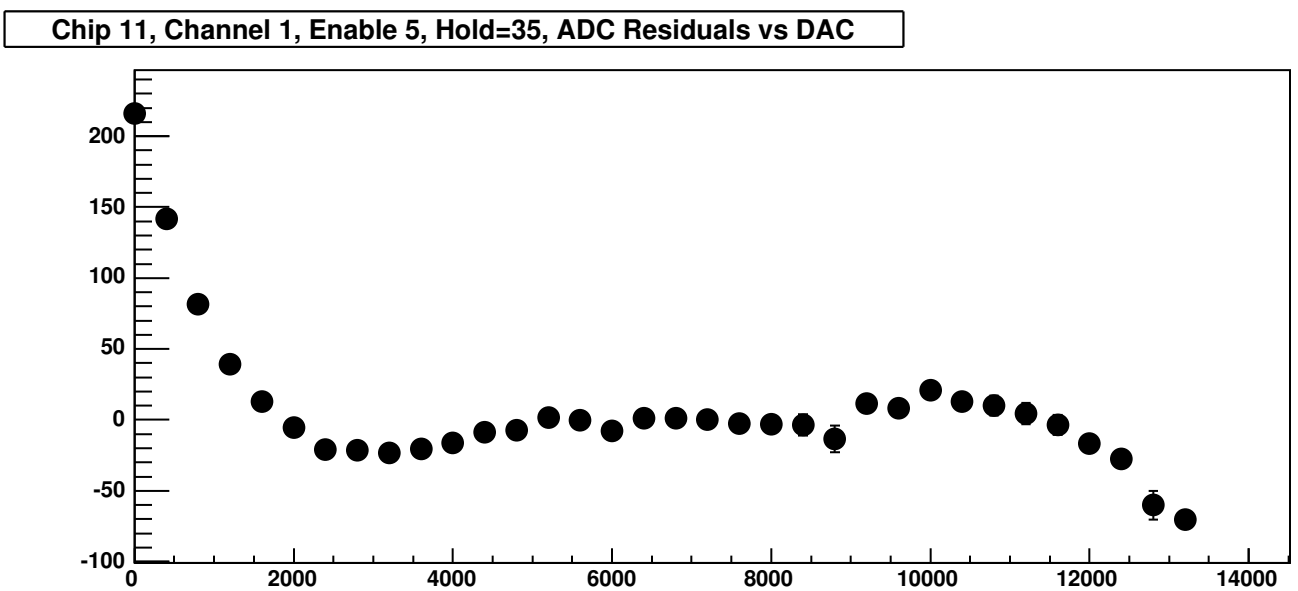

Chip 11, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 13.84 / 23
p0 -456.9 ± 0.6702
p1 0.02587 ± 0.000101
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

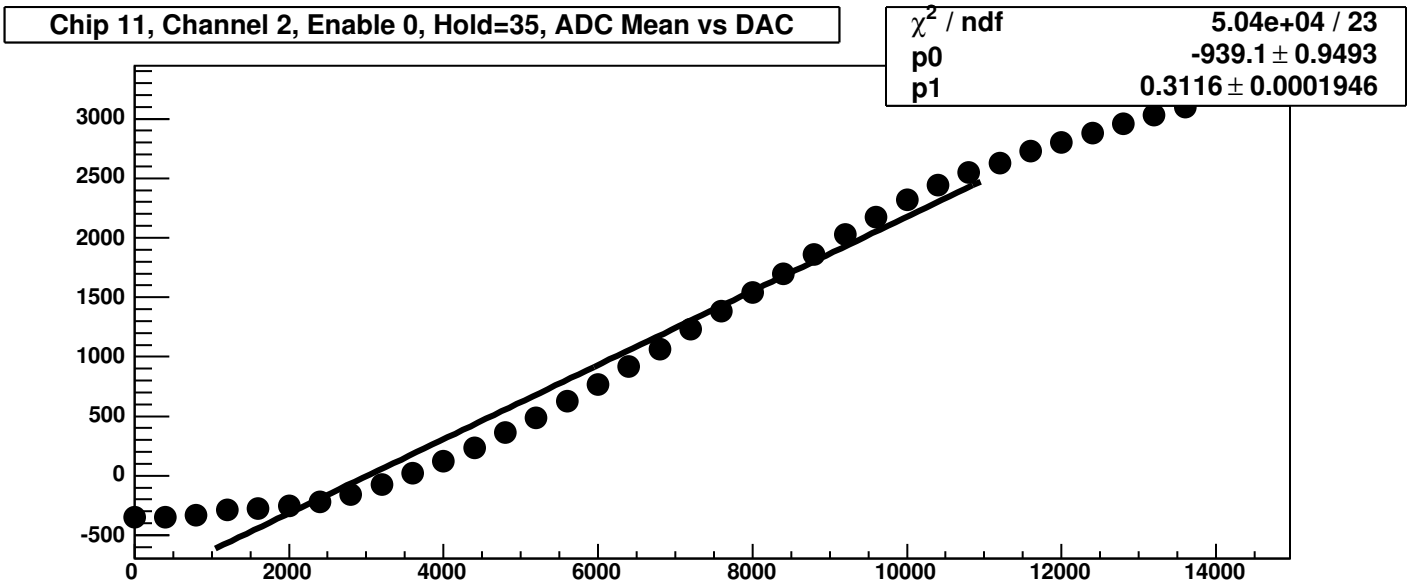

Chip 11, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

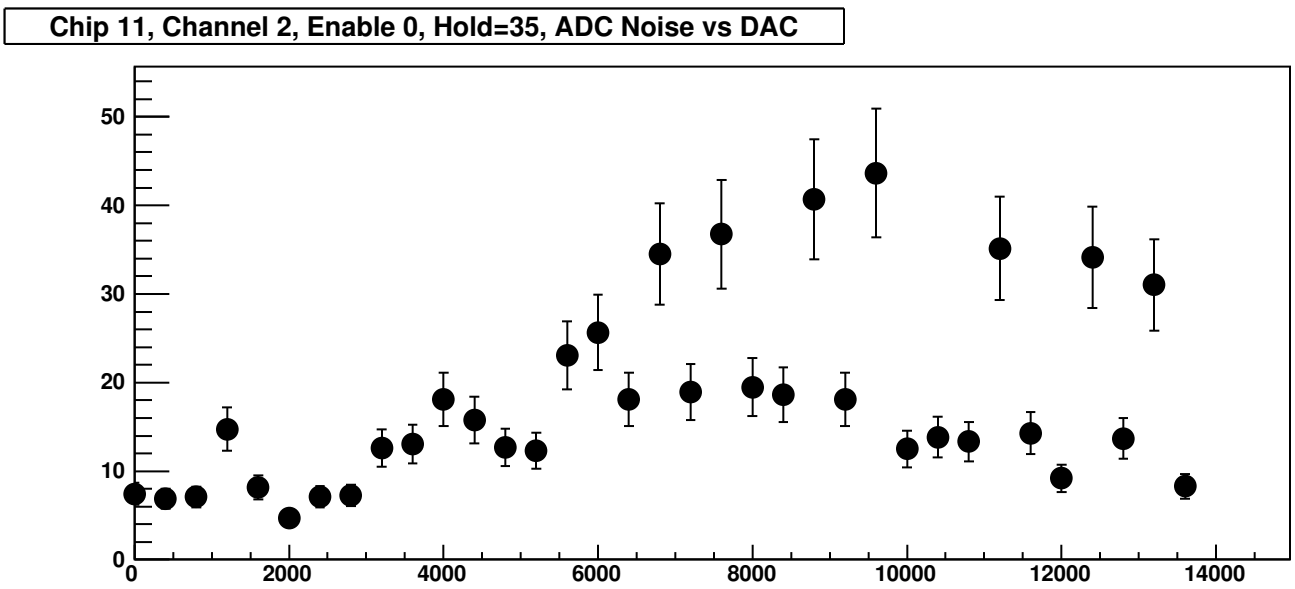

Chip 11, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

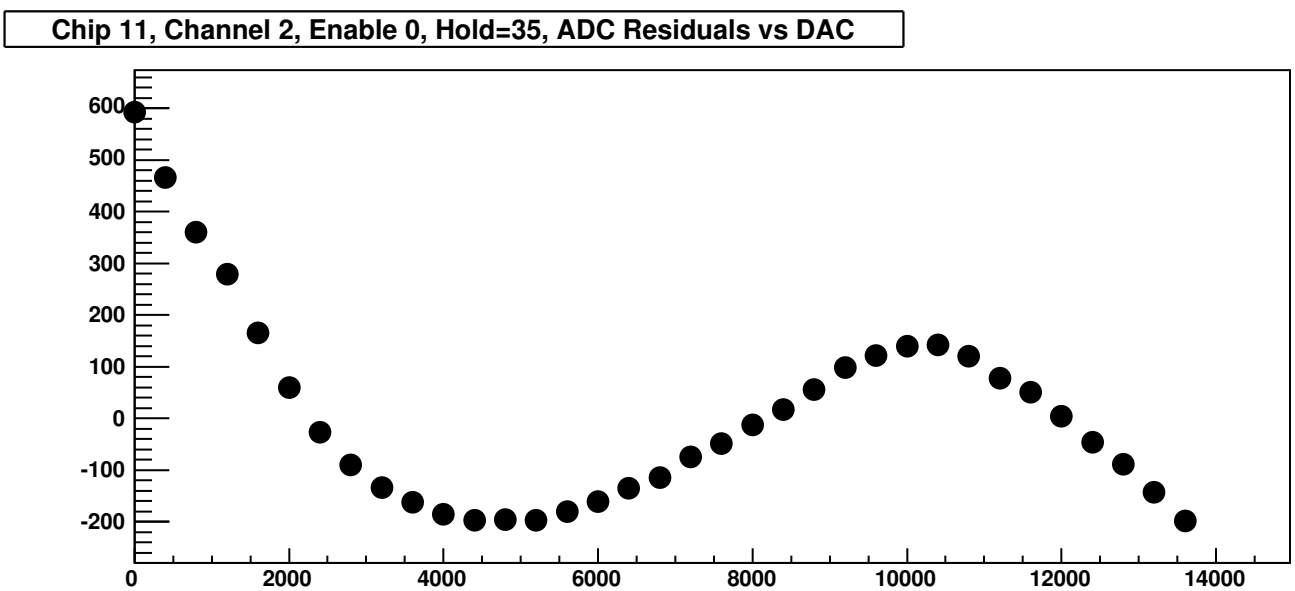

Chip 11, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1167 / 23
p0 -636.5 ± 1.094
p1 0.1834 ± 0.0002032
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

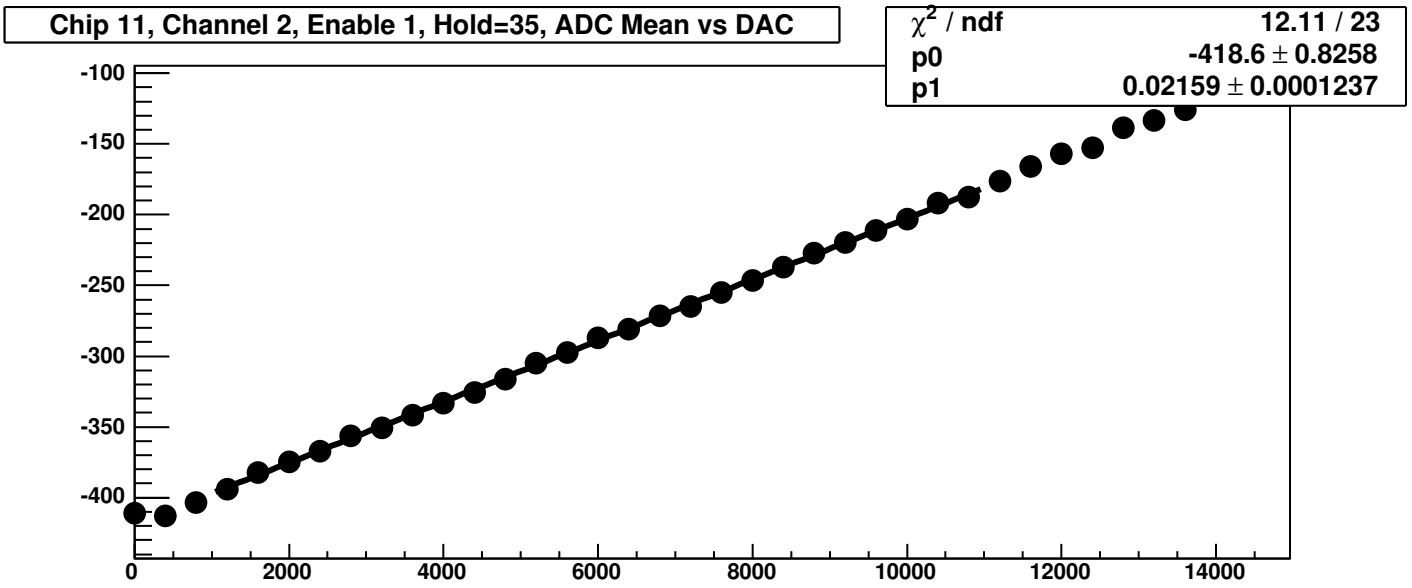

Chip 11, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

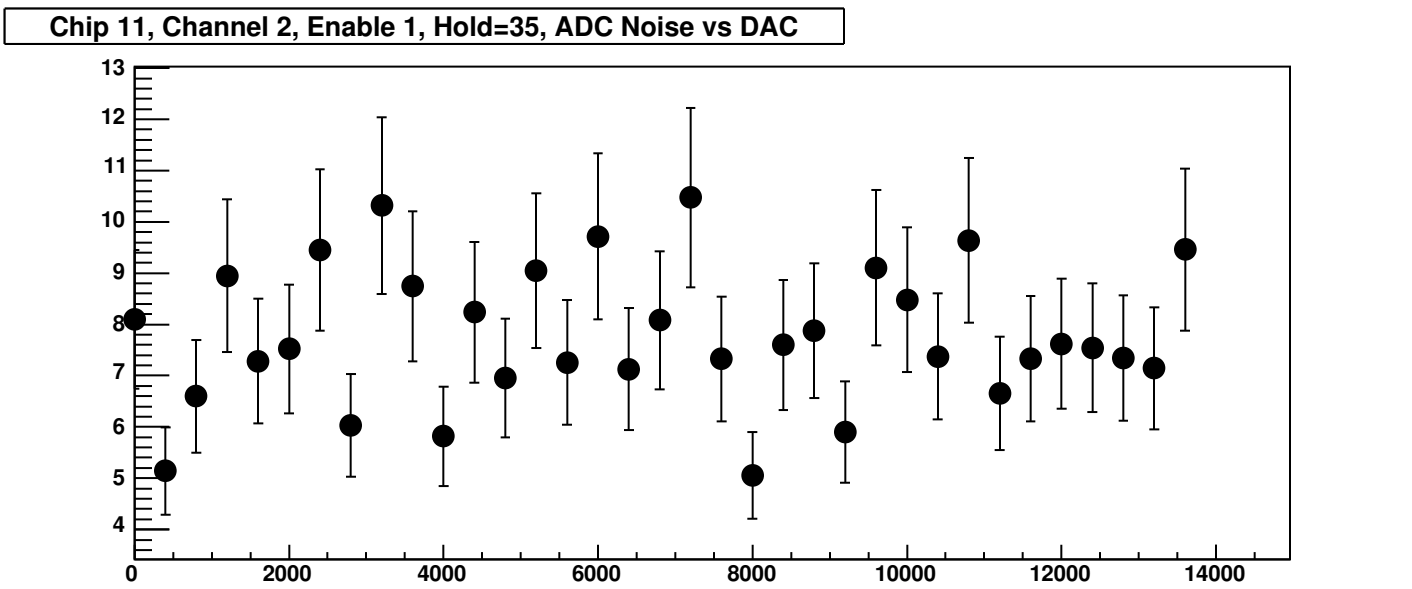

Chip 11, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

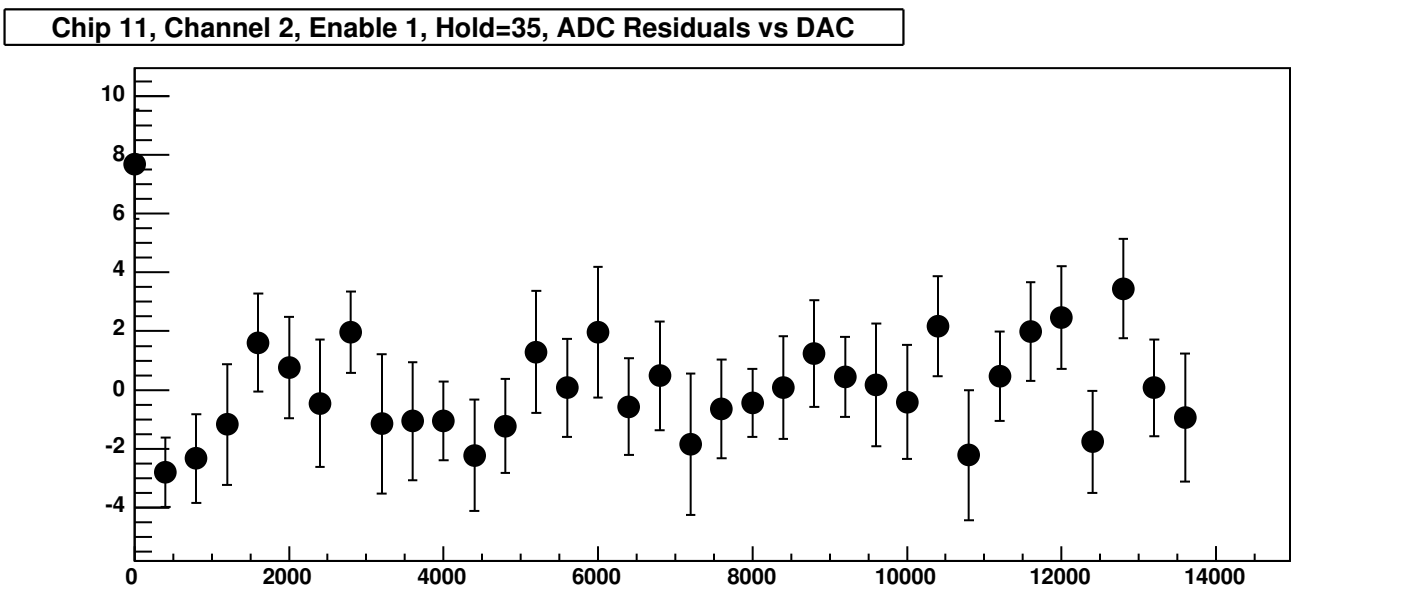

Chip 11, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 5.04e+04 / 23
p0 -939.1 ± 0.9493
p1 0.3116 ± 0.0001946
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

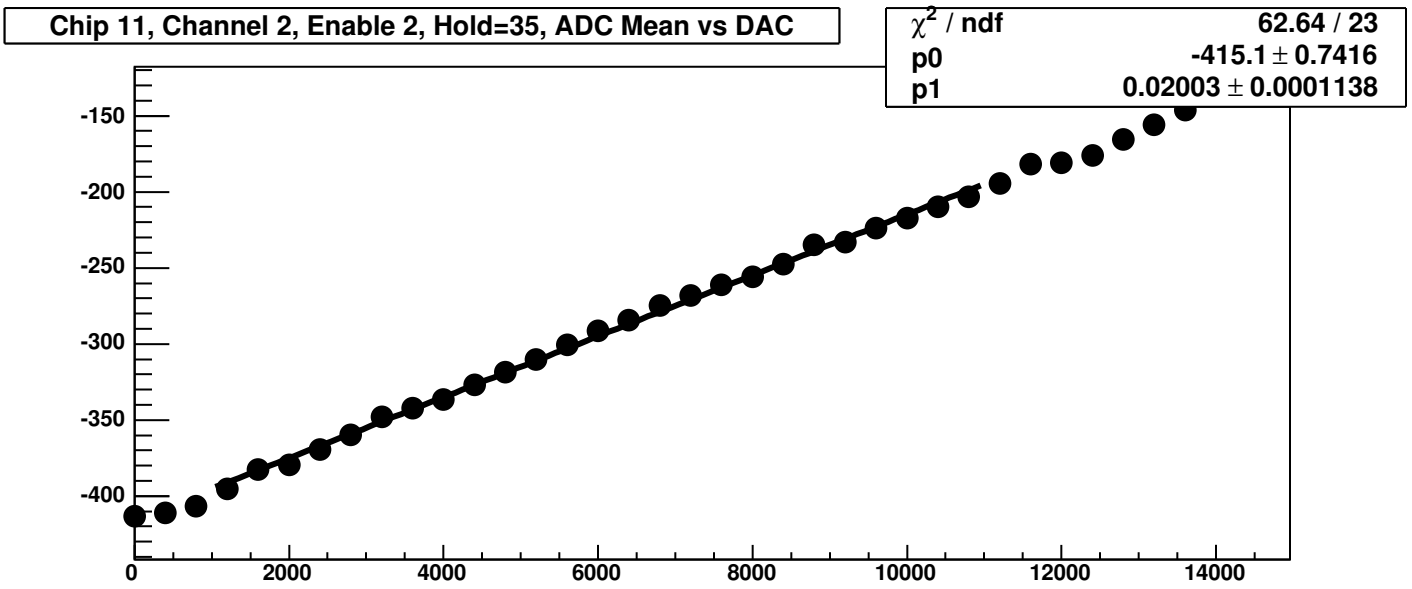

Chip 11, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

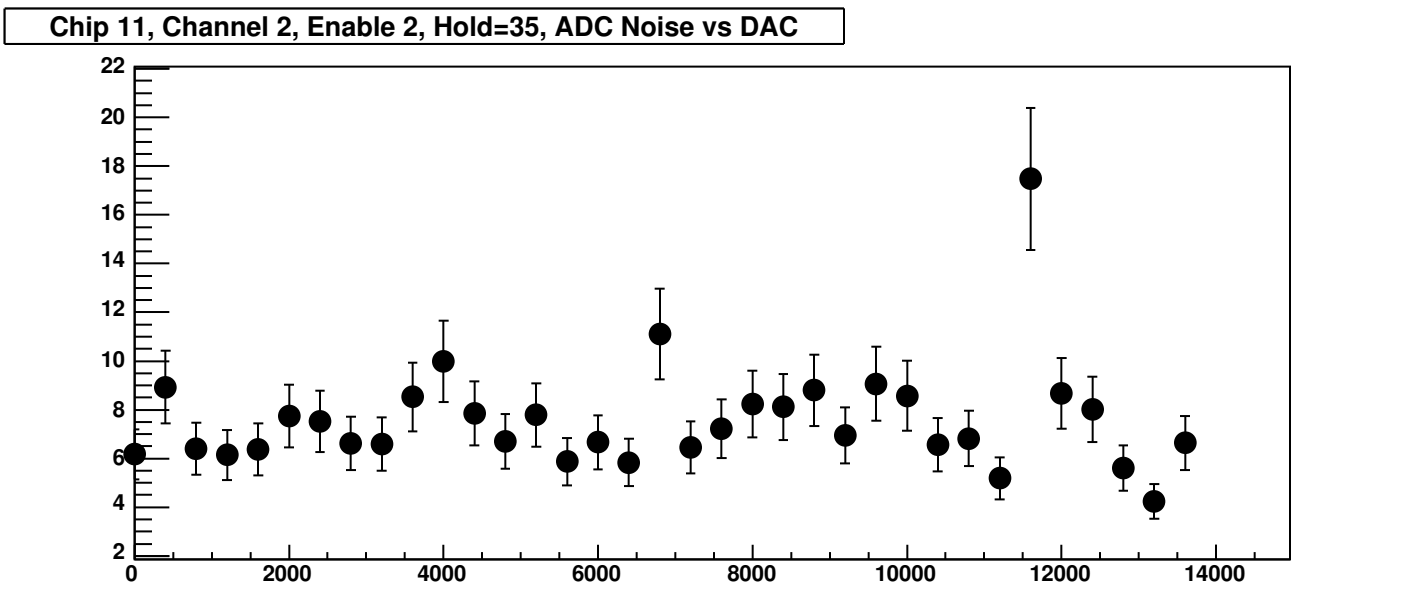

Chip 11, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

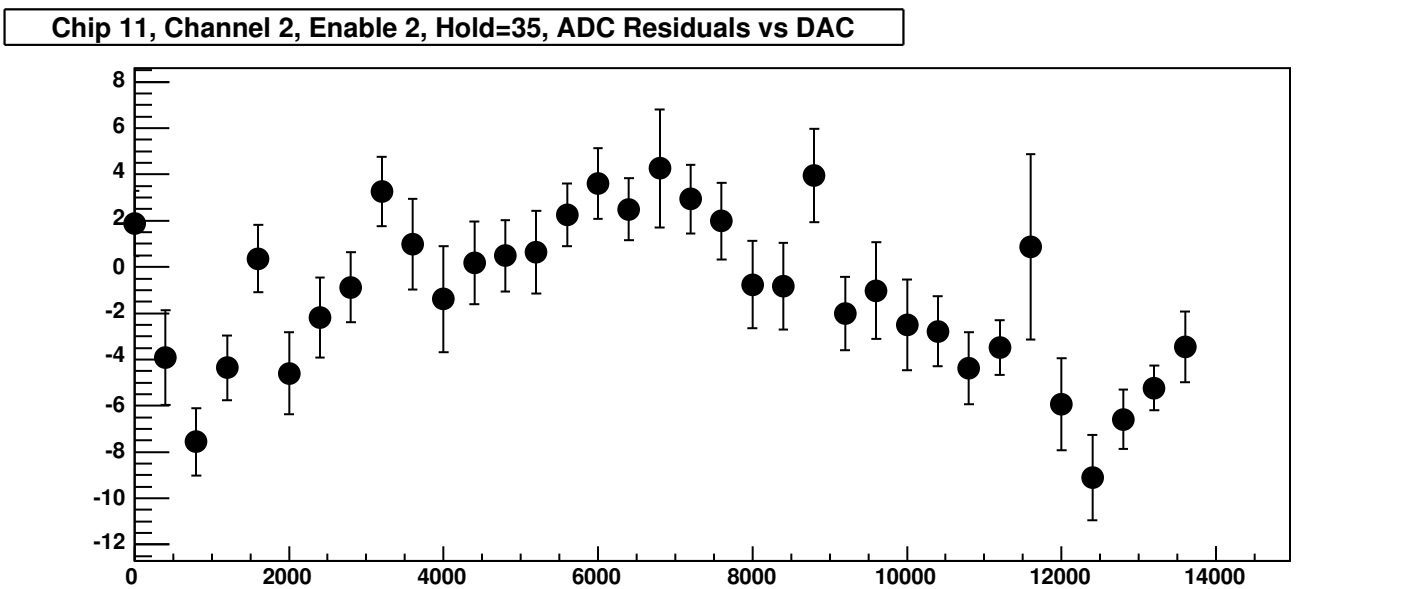

Chip 11, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 12.11 / 23
p0 -418.6 ± 0.8258
p1 0.02159 ± 0.0001237
-100
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

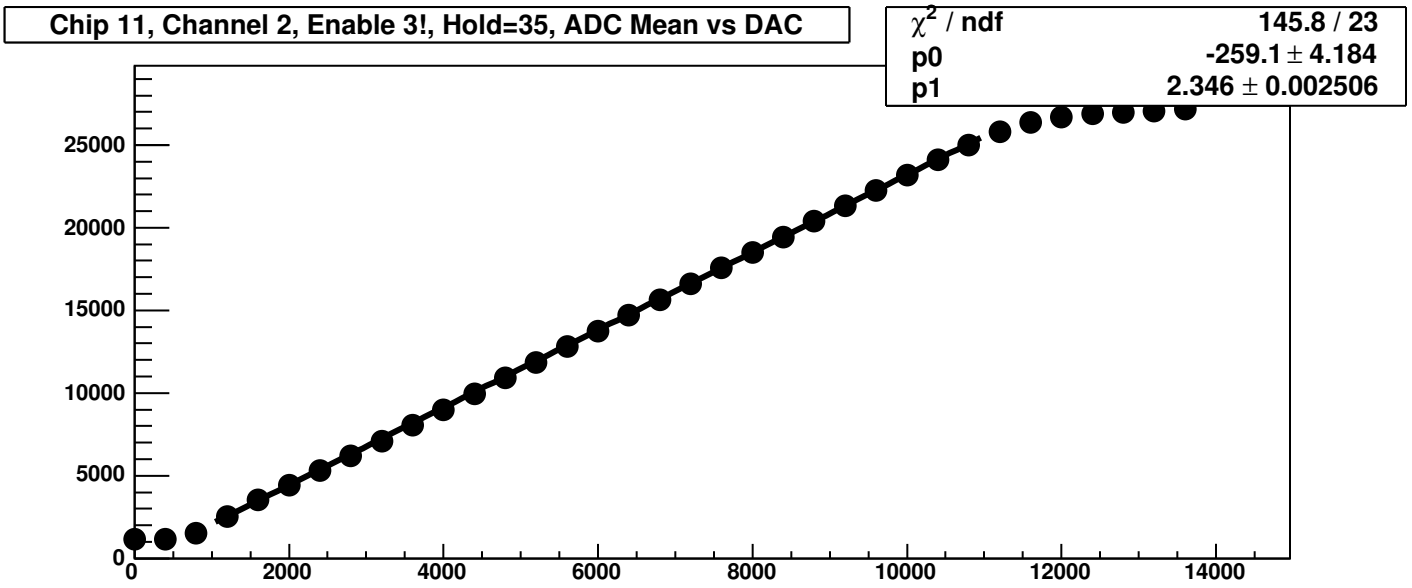

Chip 11, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

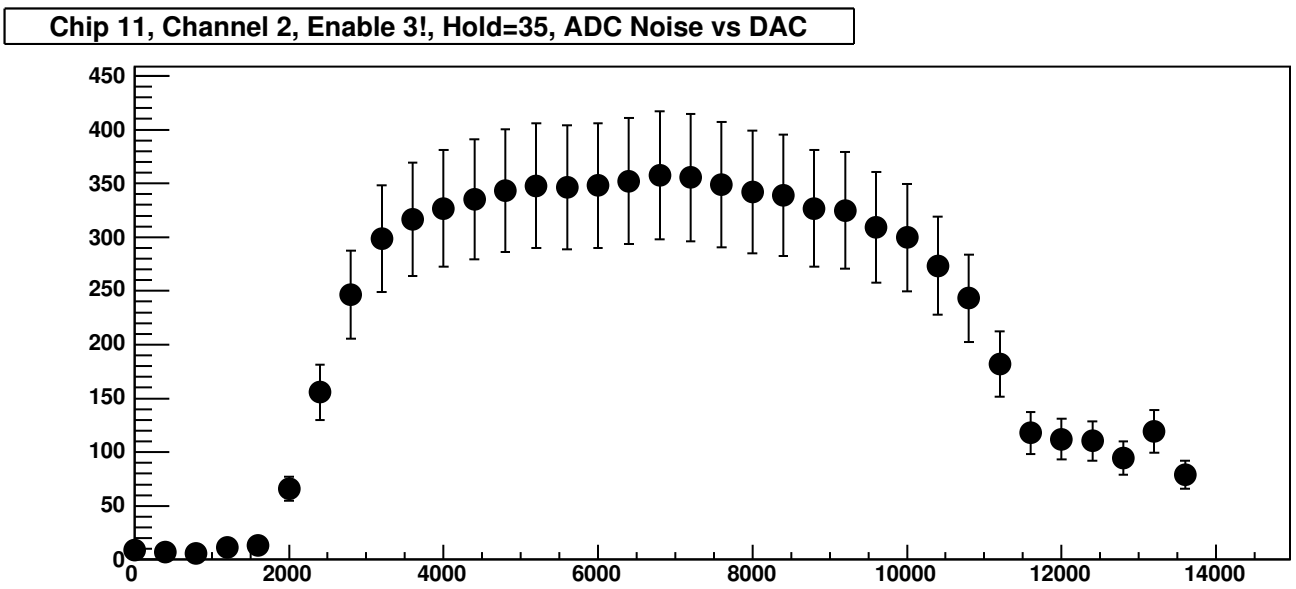

Chip 11, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
0
2000
4000
6000
8000
10000
12000
14000

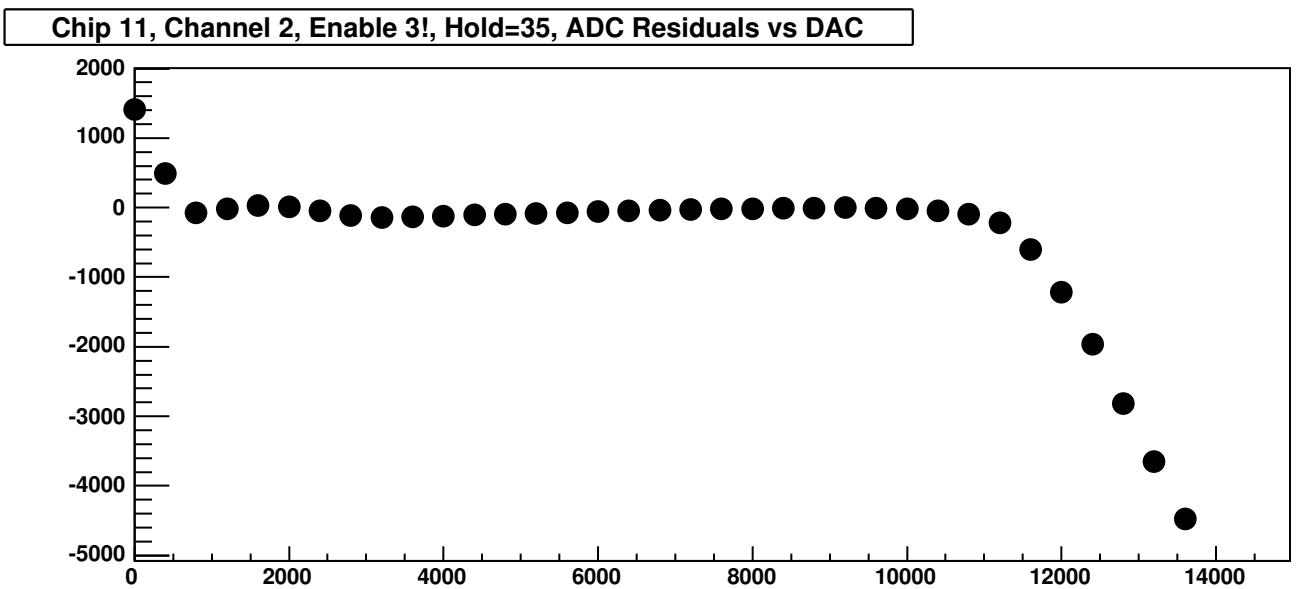

Chip 11, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 62.64 / 23
p0 -415.1 ± 0.7416
p1 0.02003 ± 0.0001138
-150
-200
-250
-300
-350
-400
0
2000
4000
6000
8000
10000
12000
14000

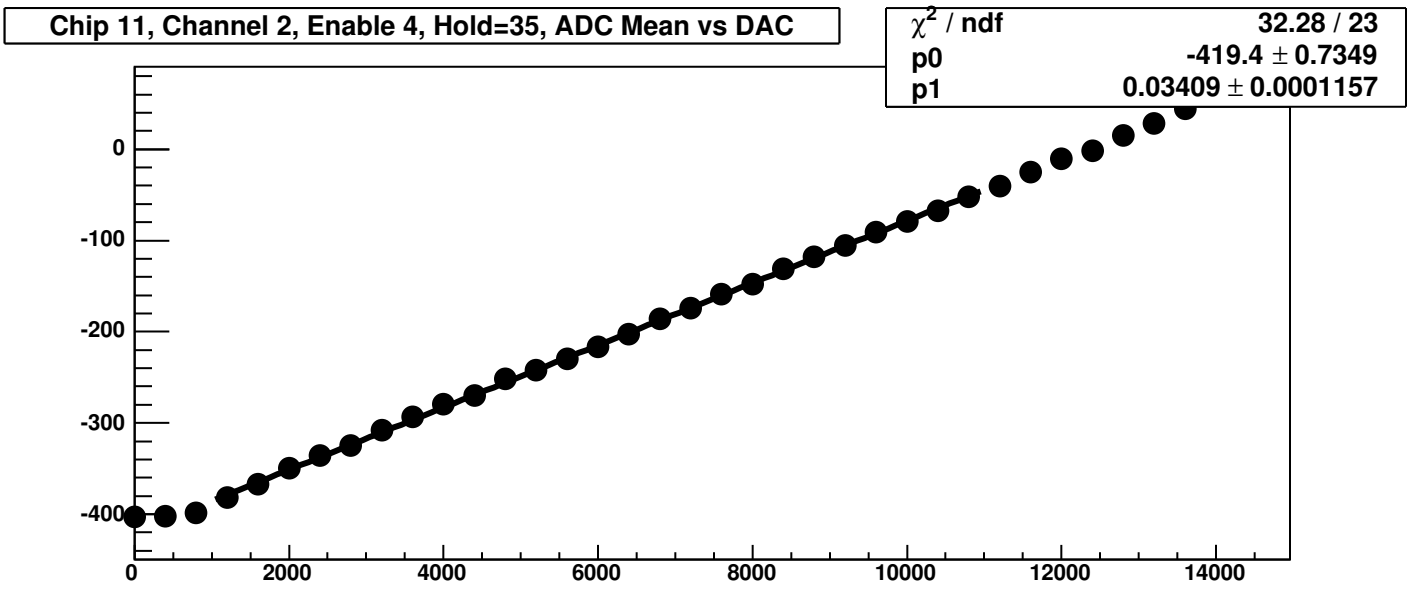

Chip 11, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
2
0
2000
4000
6000
8000
10000
12000
14000

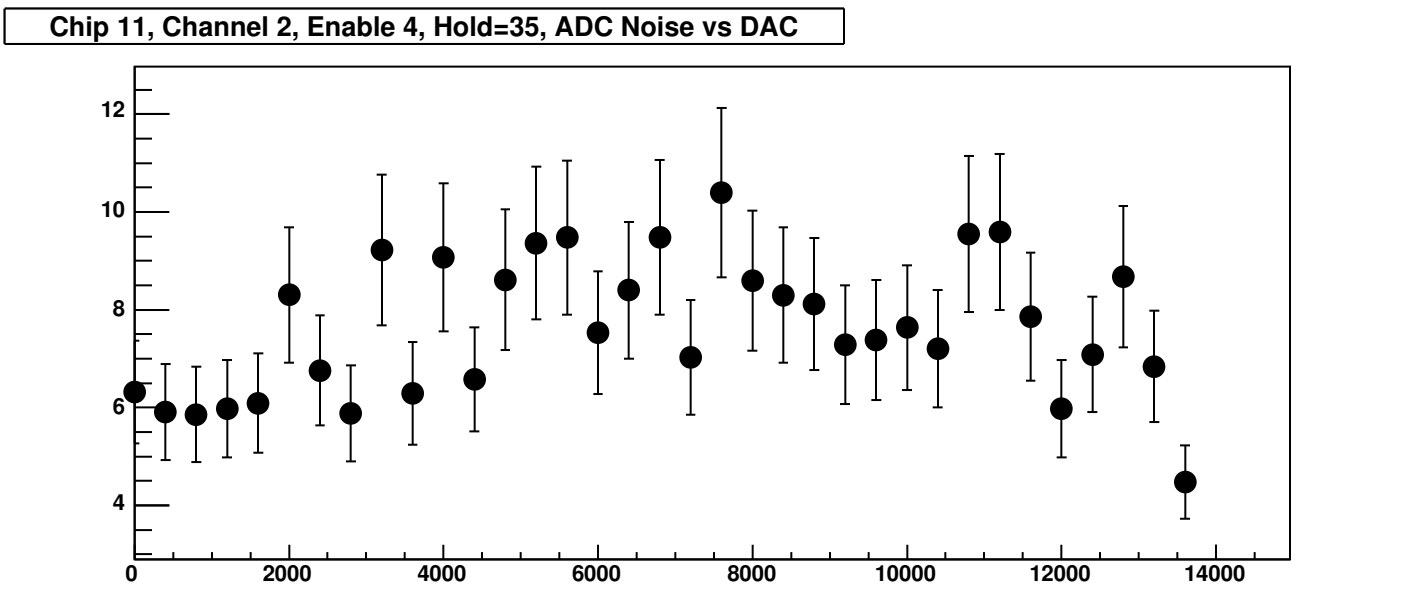

Chip 11, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
0
2000
4000
6000
8000
10000
12000
14000

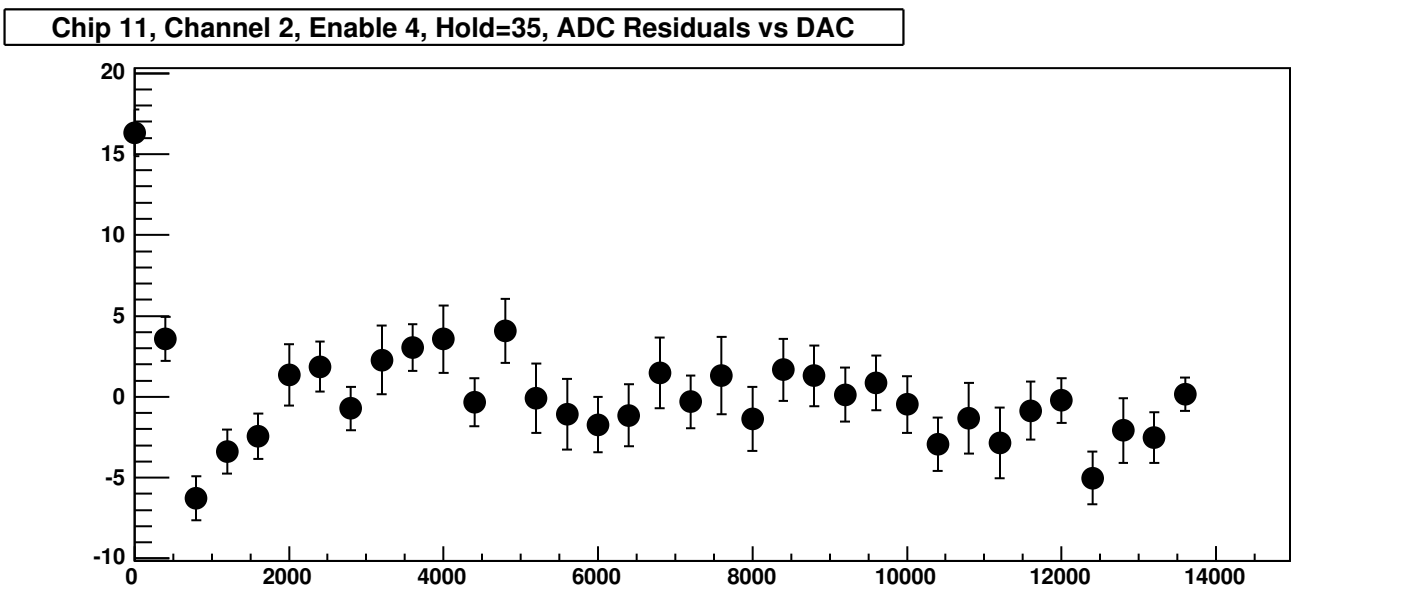

Chip 11, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 145.8 / 23
p0 -259.1 ± 4.184
p1 2.346 ± 0.002506
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

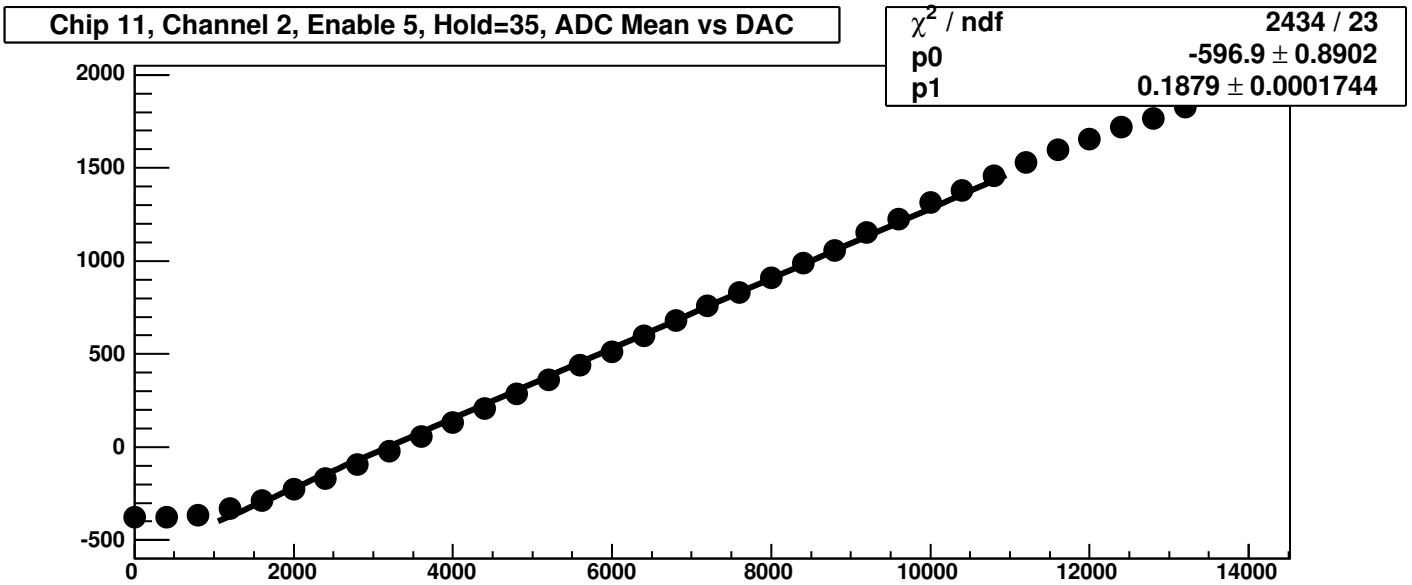

Chip 11, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

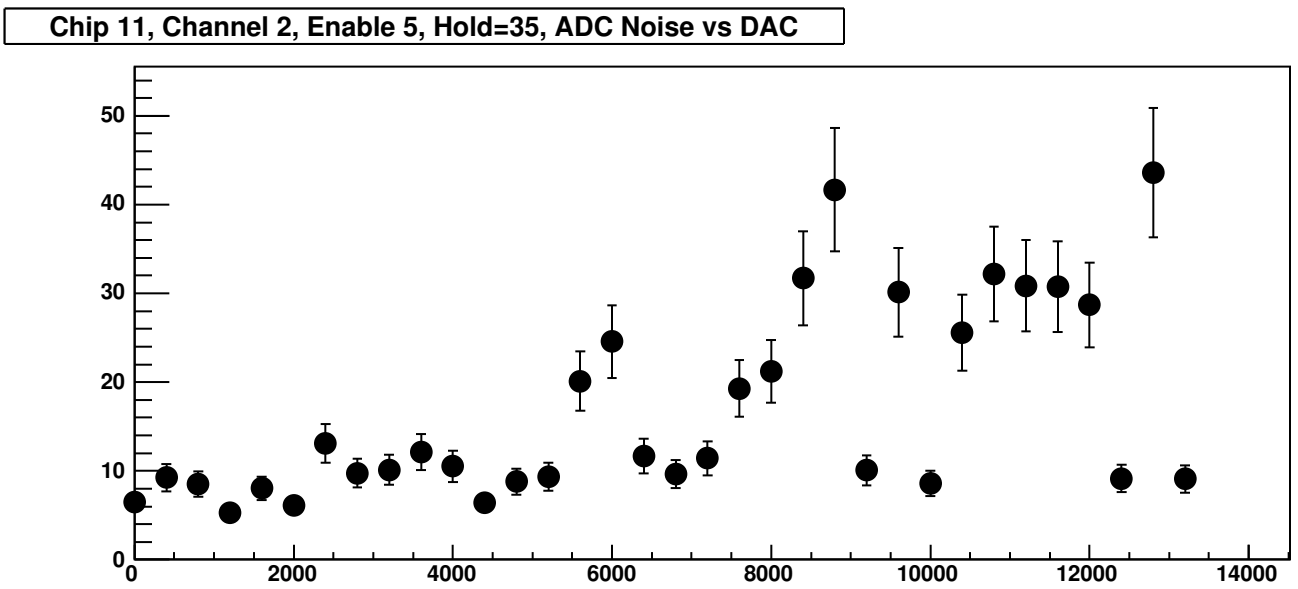

Chip 11, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
2000
4000
6000
8000
10000
12000
14000

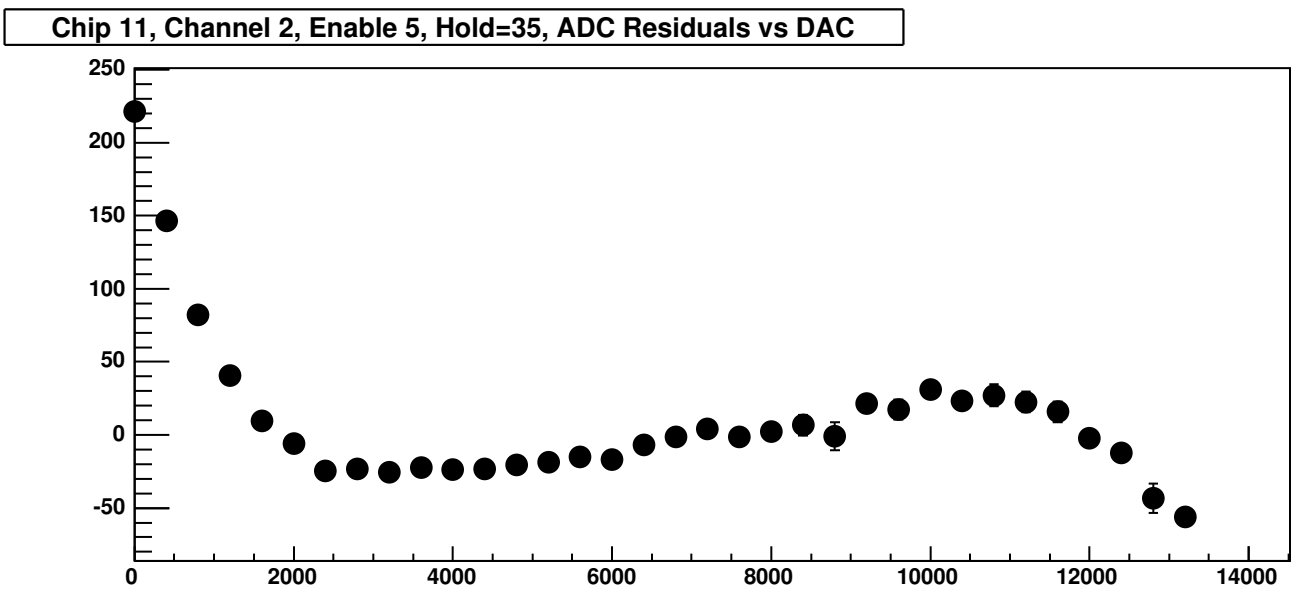

Chip 11, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 32.28 / 23
p0 -419.4 ± 0.7349
p1 0.03409 ± 0.0001157
0
-100
-200
-300
-400
0
2000
4000
6000
8000
10000
12000
14000

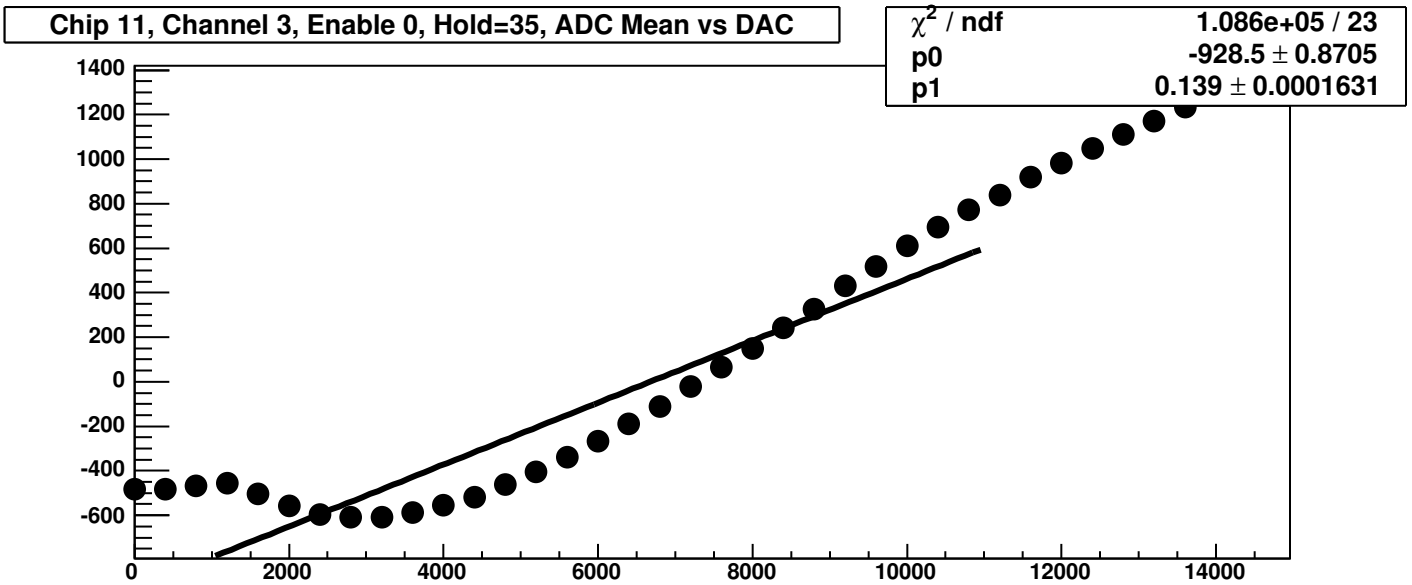

Chip 11, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

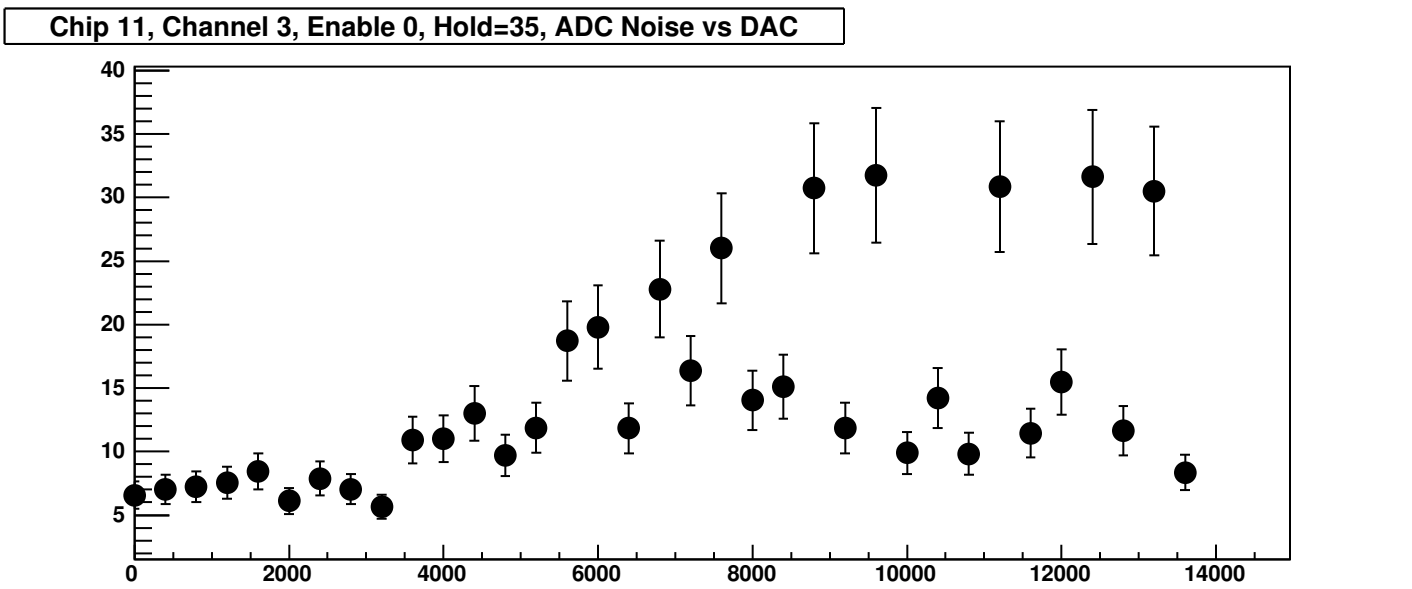

Chip 11, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

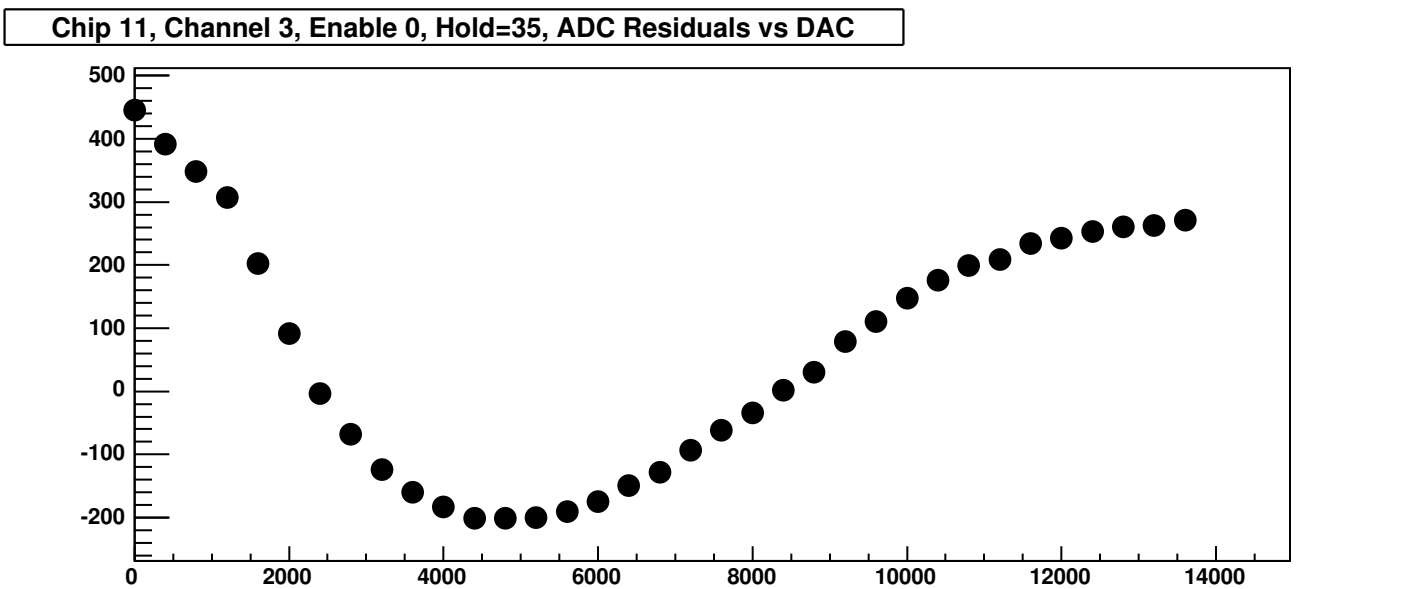

Chip 11, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2434 / 23
p0 -596.9 ± 0.8902
p1 0.1879 ± 0.0001744
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

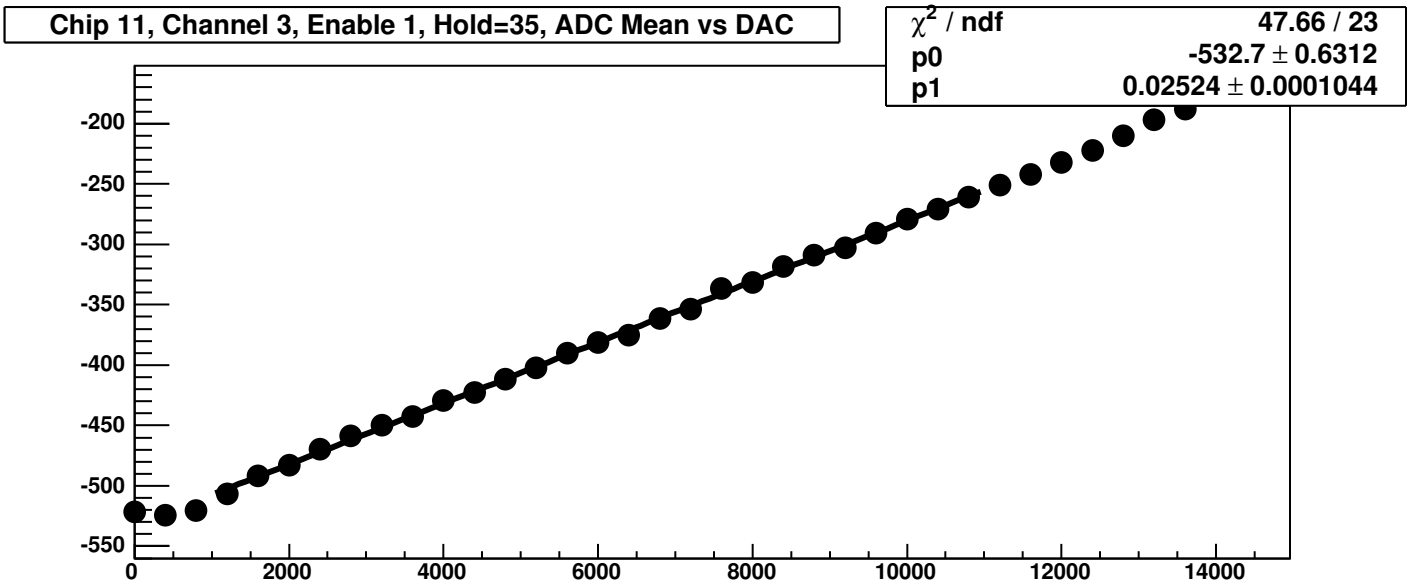

Chip 11, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

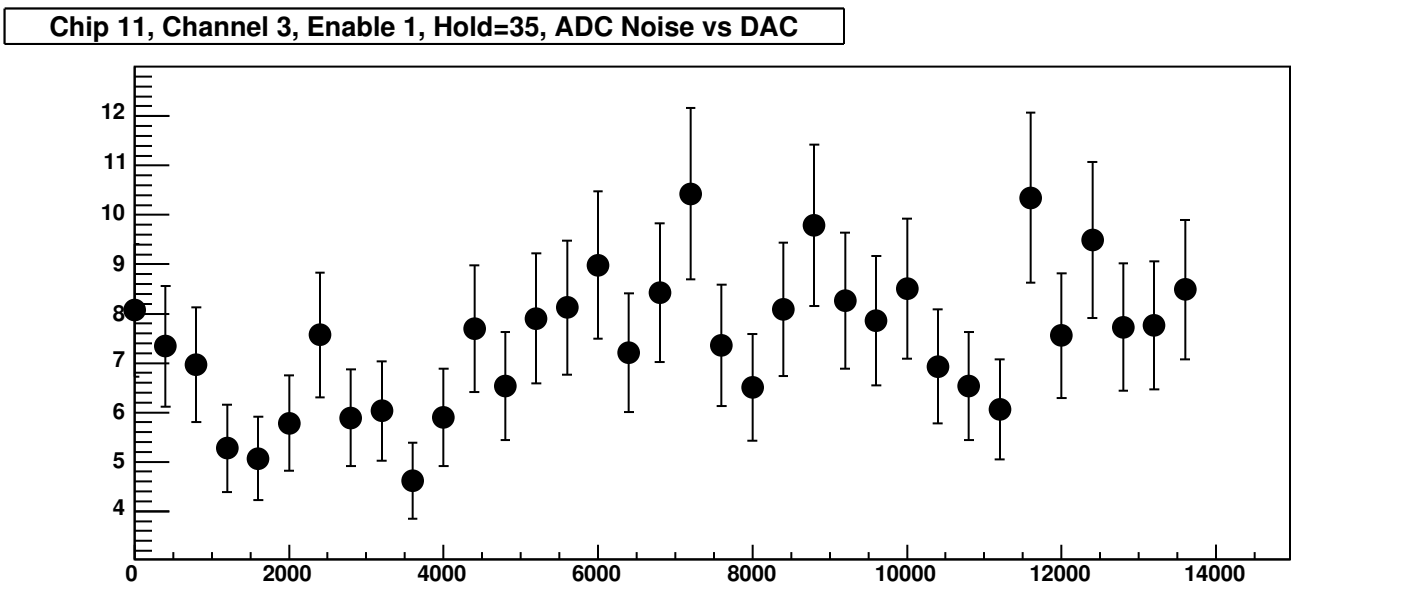

Chip 11, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

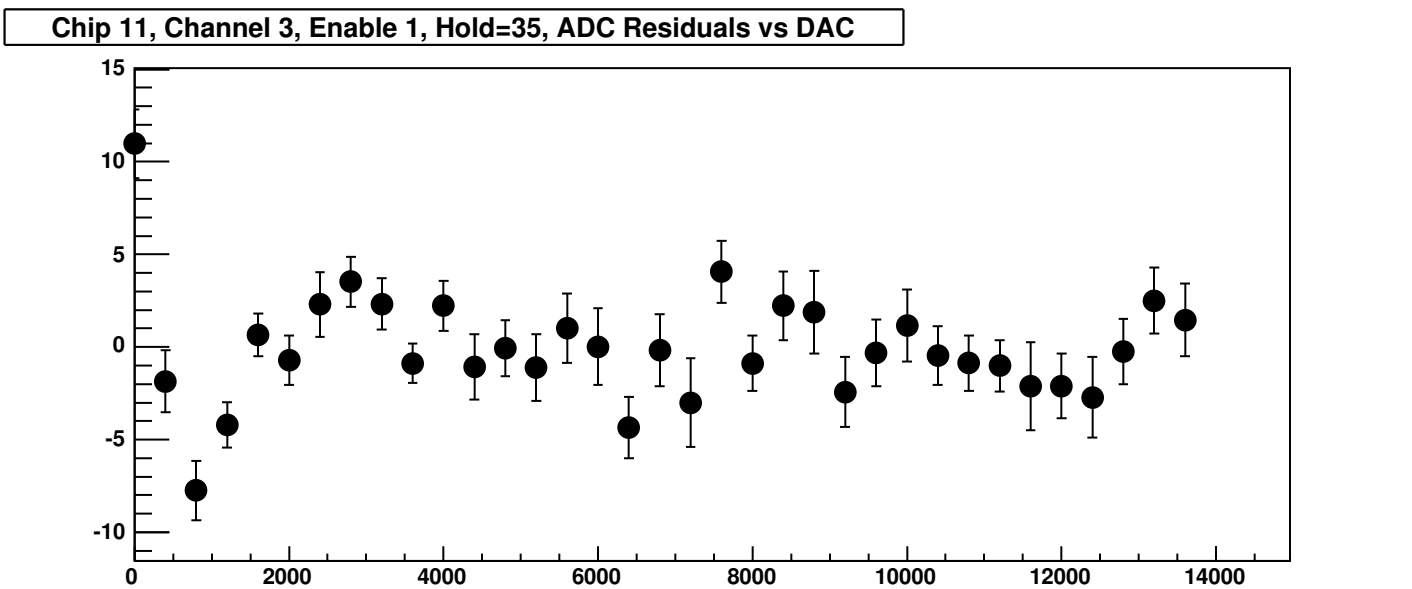

Chip 11, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.086e+05 / 23
p0 -928.5 ± 0.8705
p1 0.139 ± 0.0001631
1400
1200
1000
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

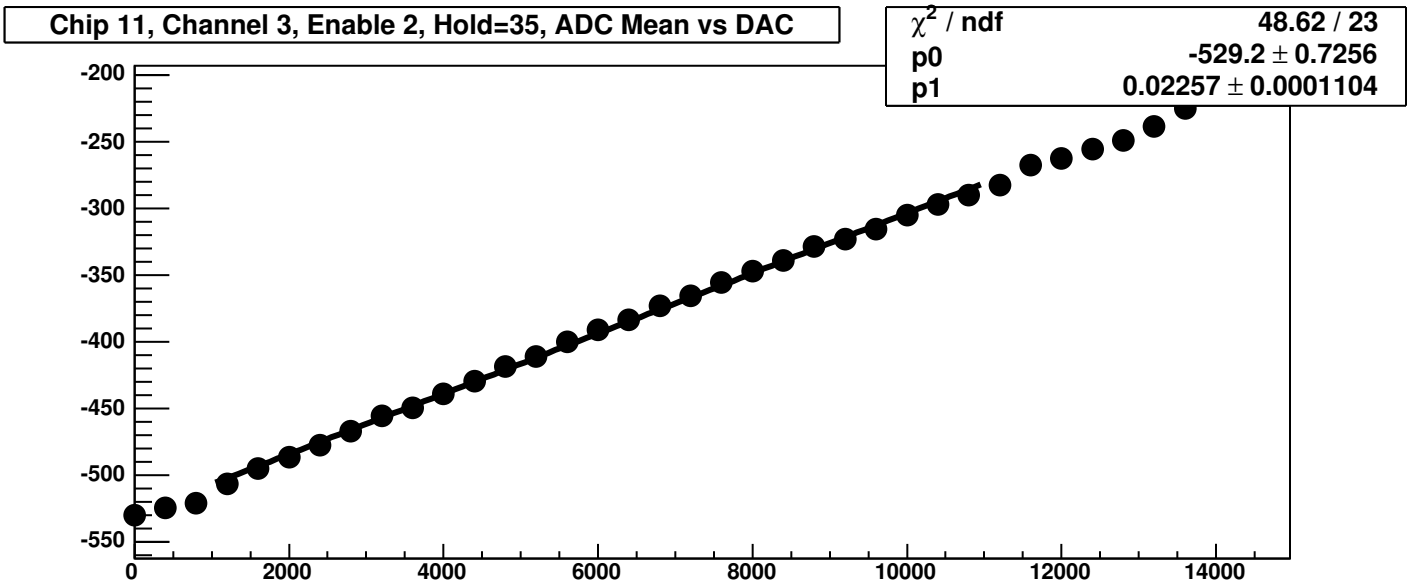

Chip 11, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

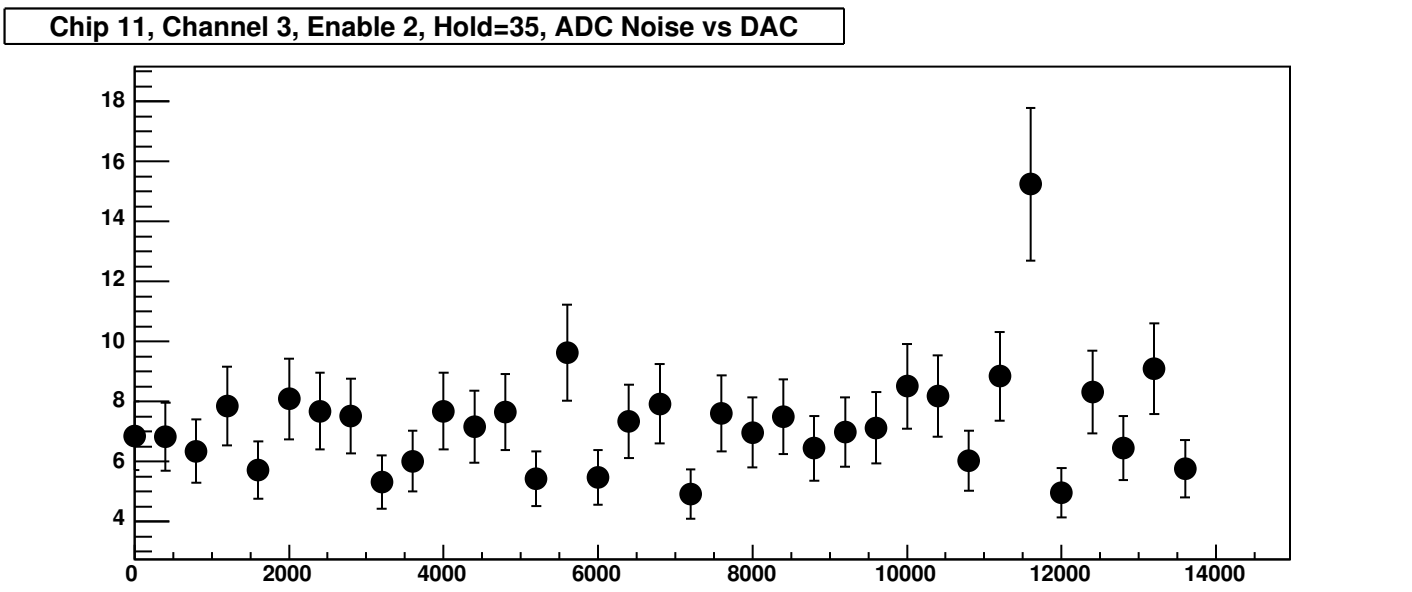

Chip 11, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

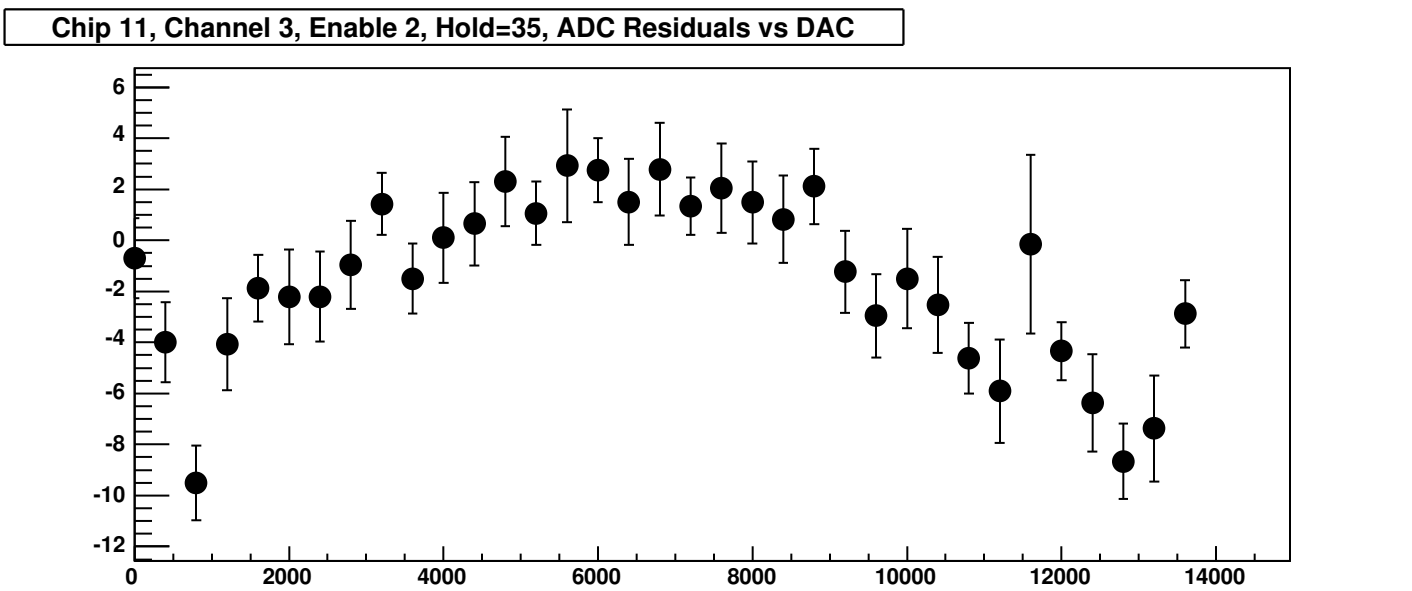

Chip 11, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 47.66 / 23
p0 -532.7 ± 0.6312
p1 0.02524 ± 0.0001044
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

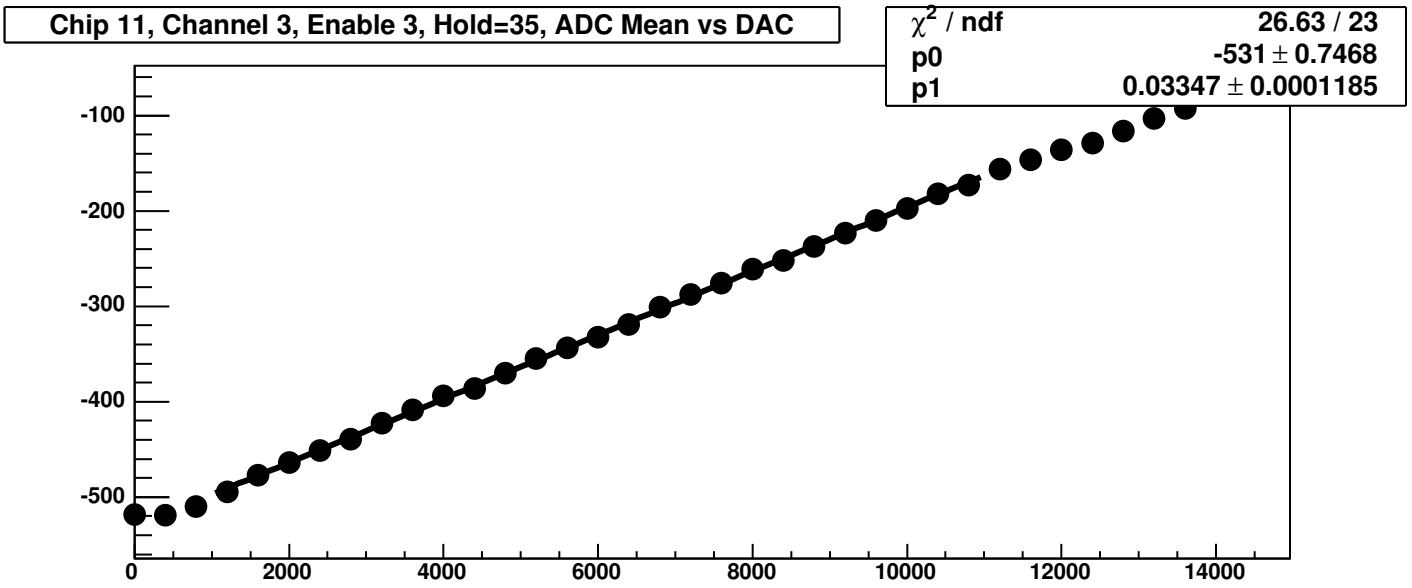

Chip 11, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

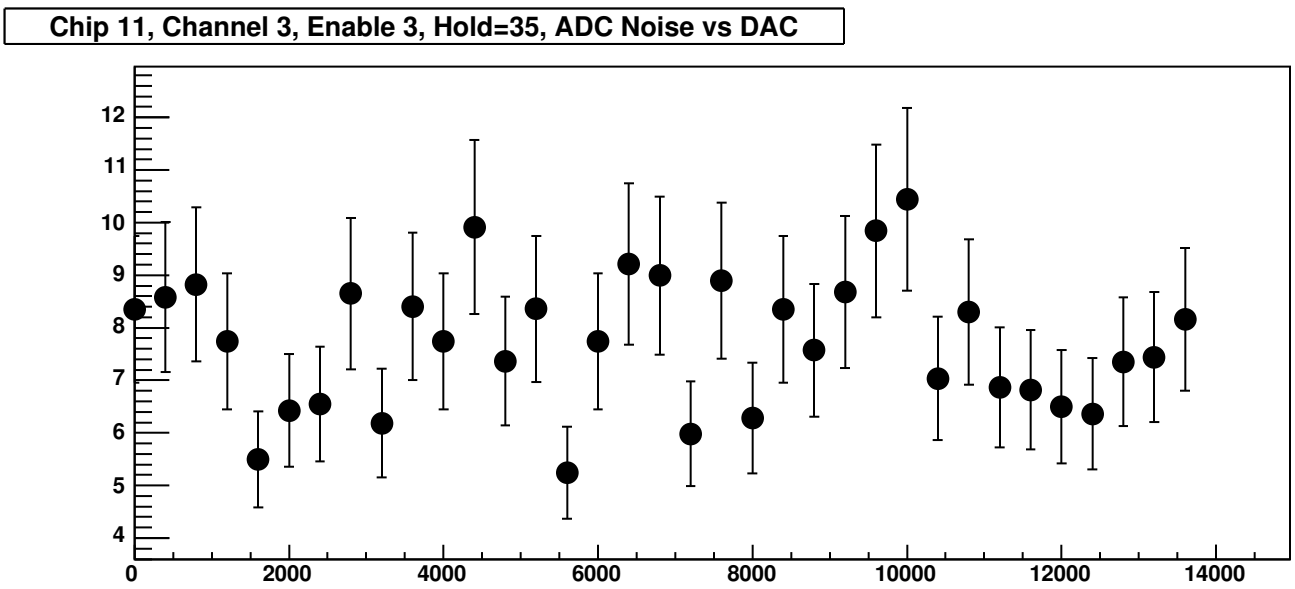

Chip 11, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

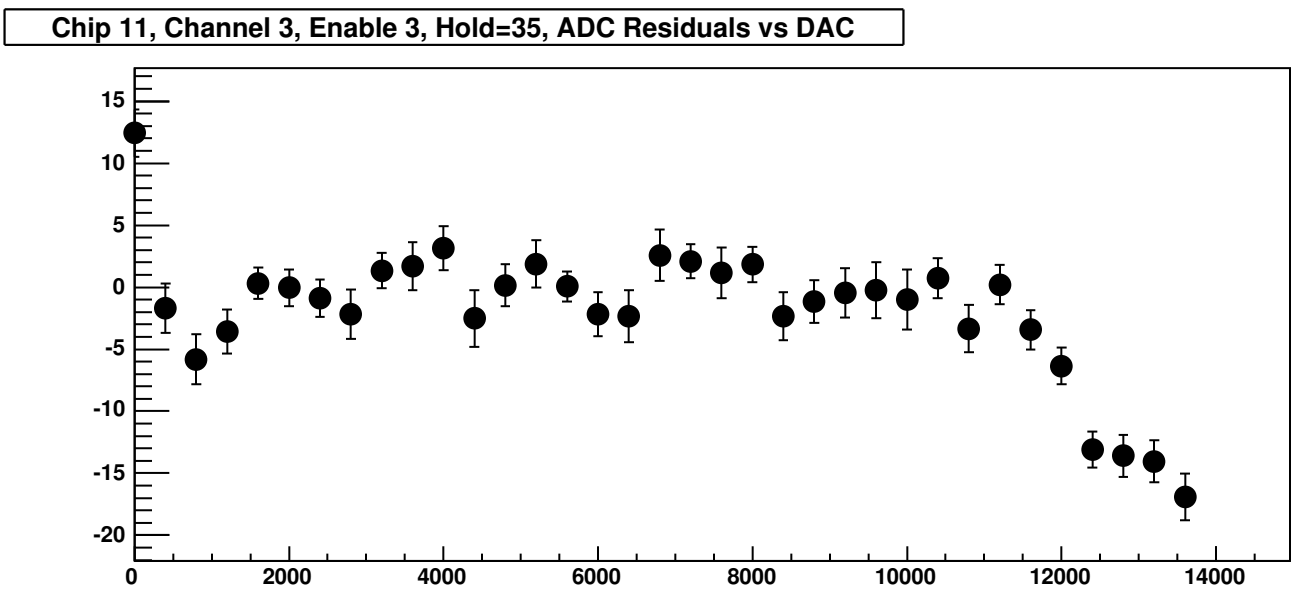

Chip 11, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 48.62 / 23
p0 -529.2 ± 0.7256
p1 0.02257 ± 0.0001104
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

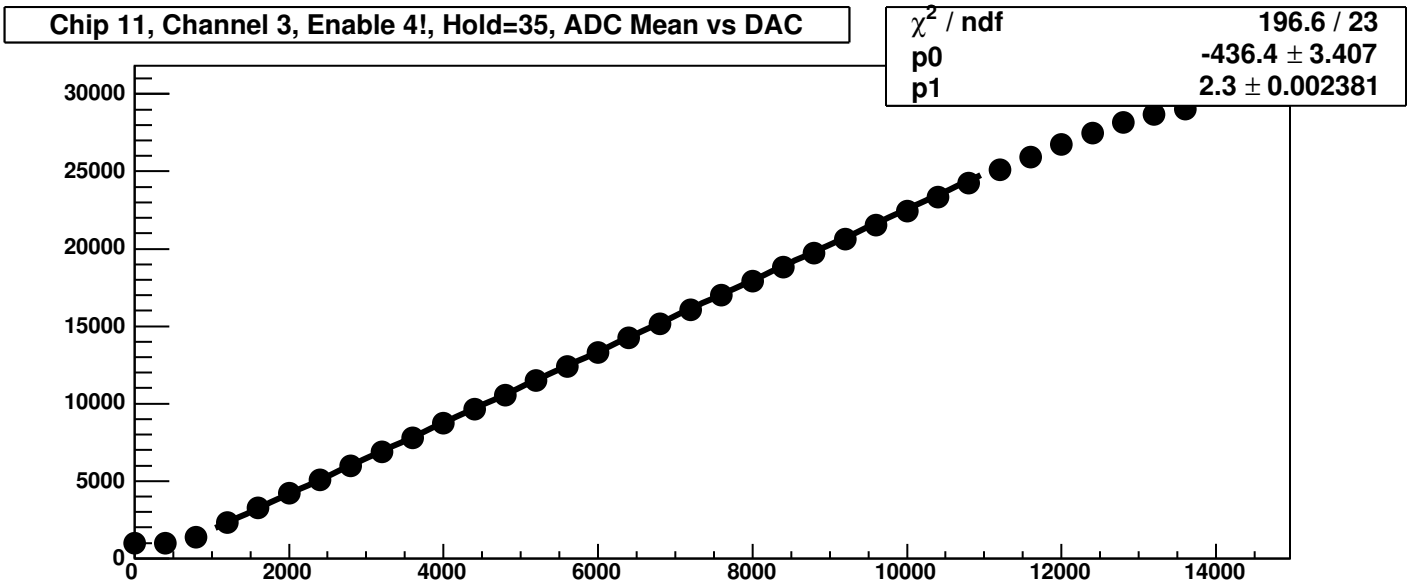

Chip 11, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

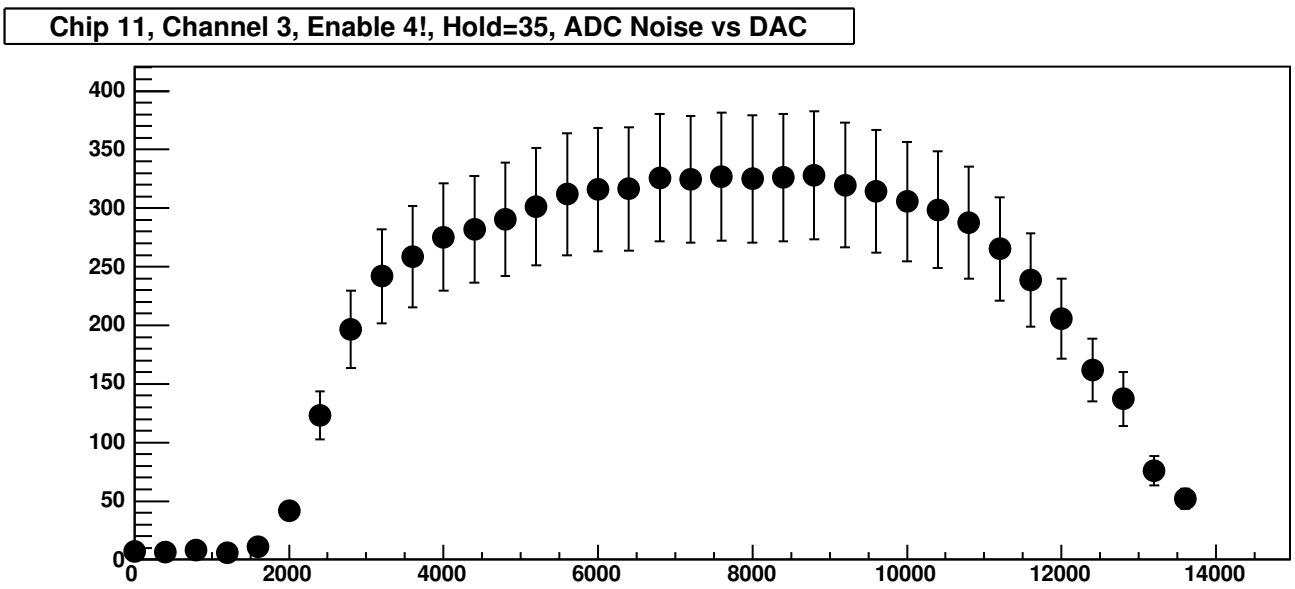

Chip 11, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
0
2000
4000
6000
8000
10000
12000
14000

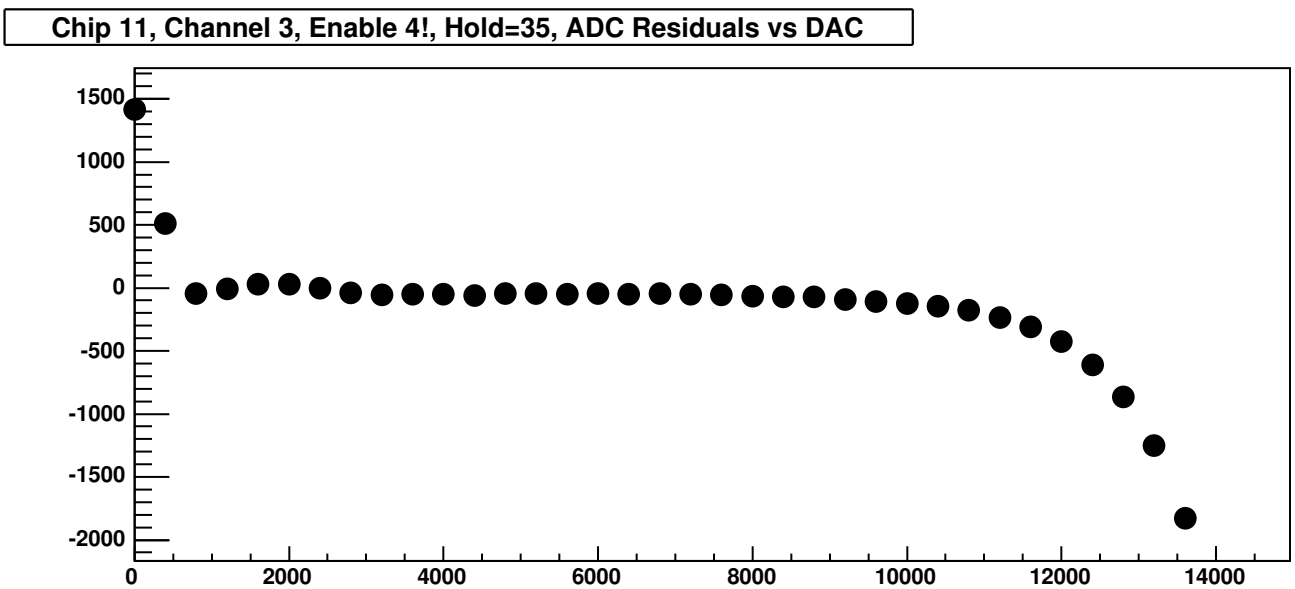

Chip 11, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.63 / 23
p0 -531 ± 0.7468
p1 0.03347 ± 0.0001185
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000

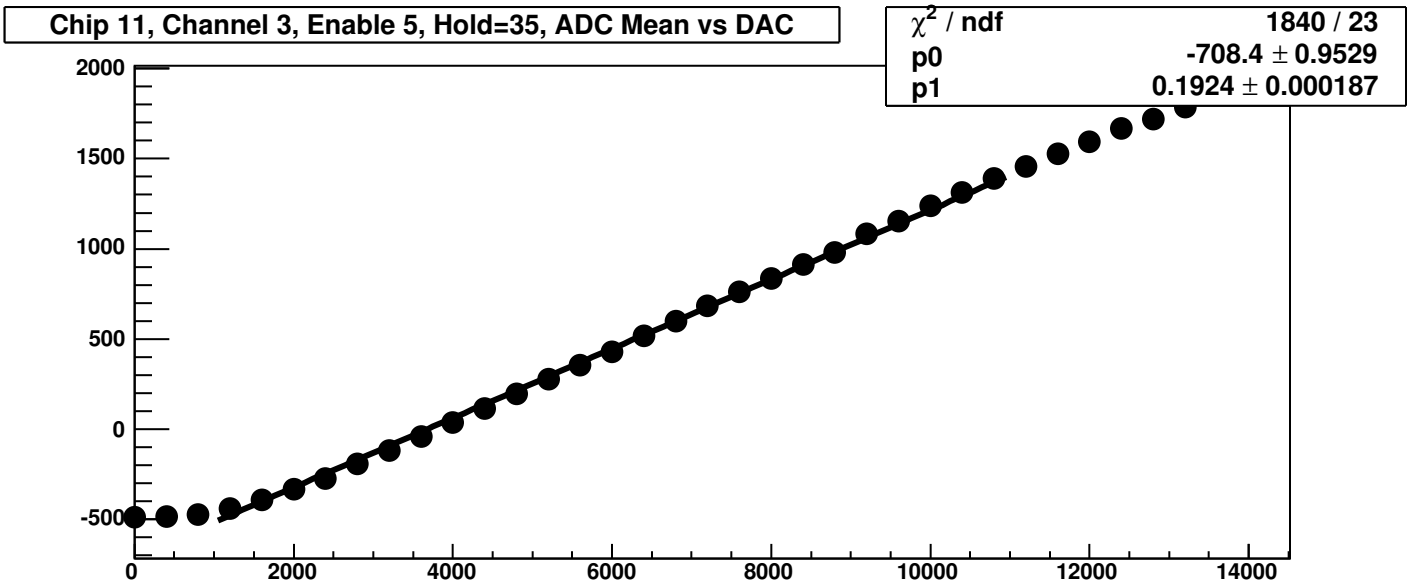

Chip 11, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

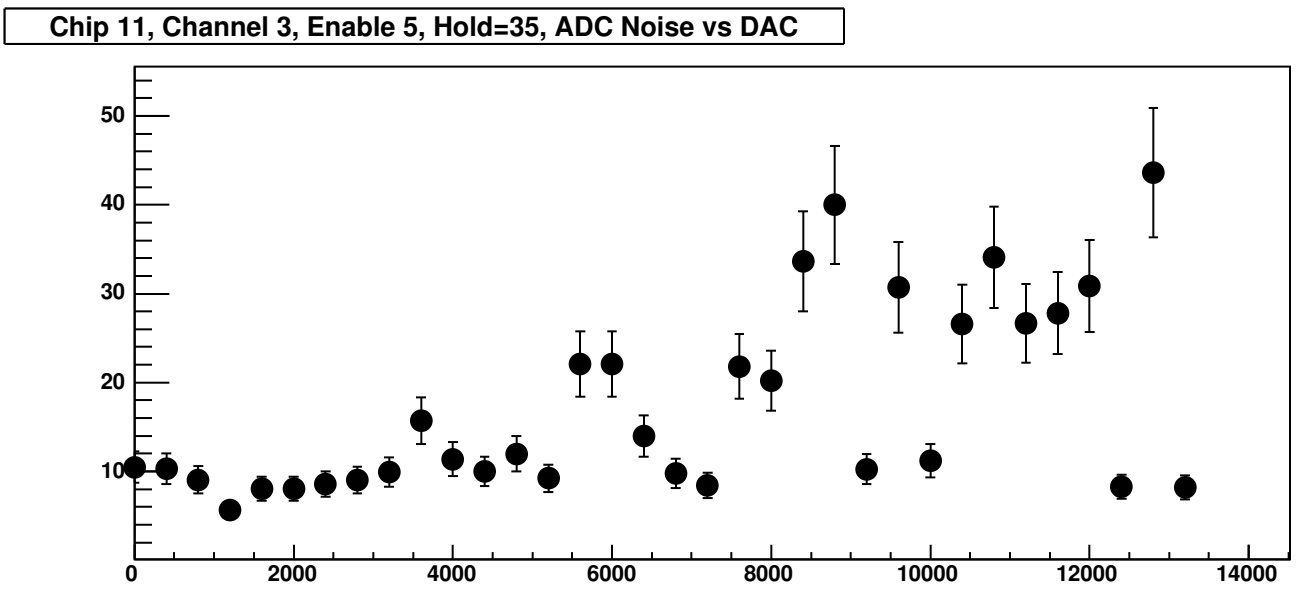

Chip 11, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
2000
4000
6000
8000
10000
12000
14000

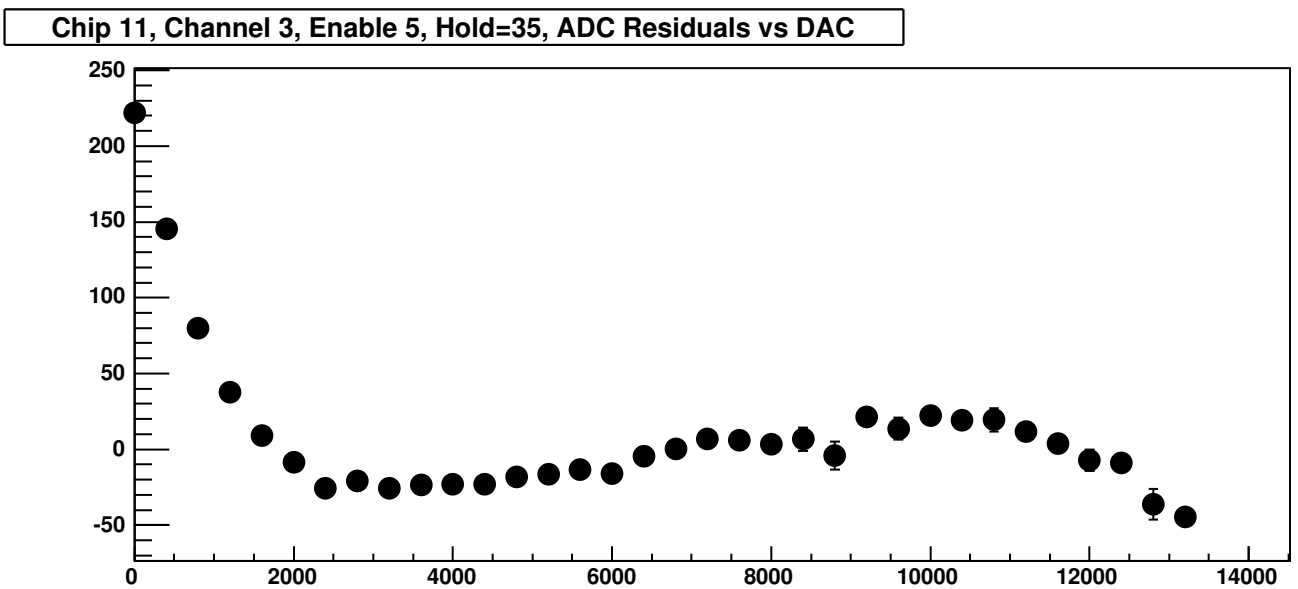

Chip 11, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 196.6 / 23
p0 -436.4 ± 3.407
p1 2.3 ± 0.002381
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

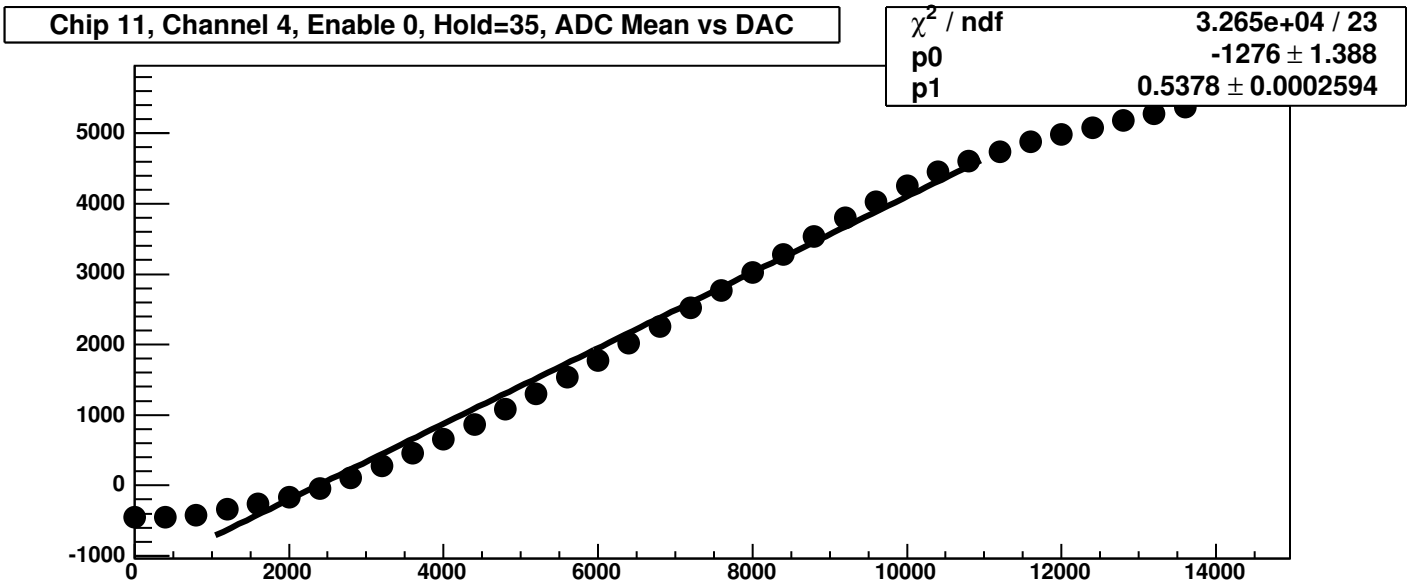

Chip 11, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

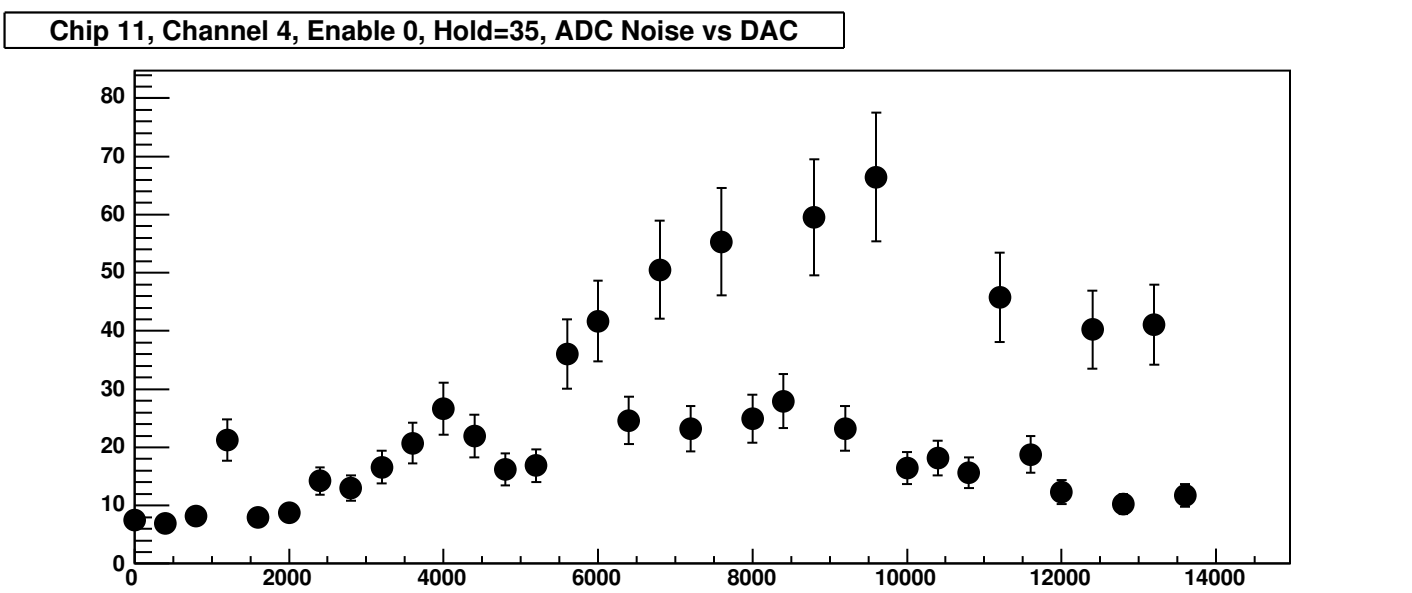

Chip 11, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

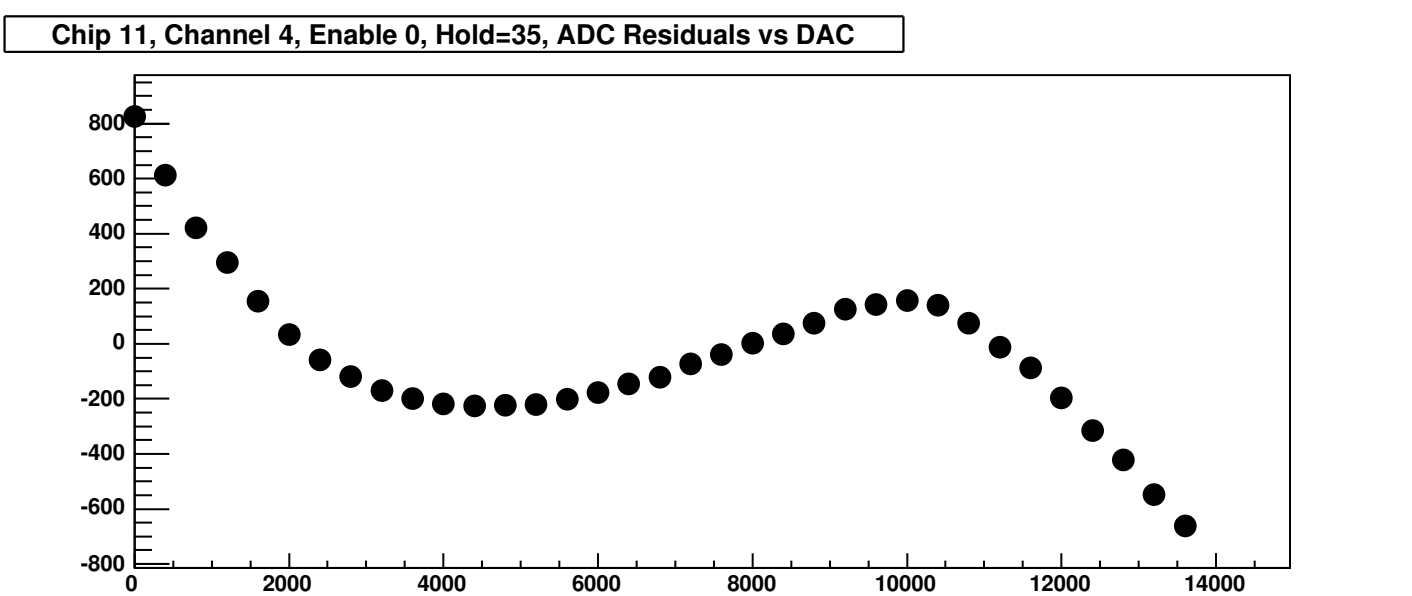

Chip 11, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1840 / 23
p0 -708.4 ± 0.9529
p1 0.1924 ± 0.000187
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

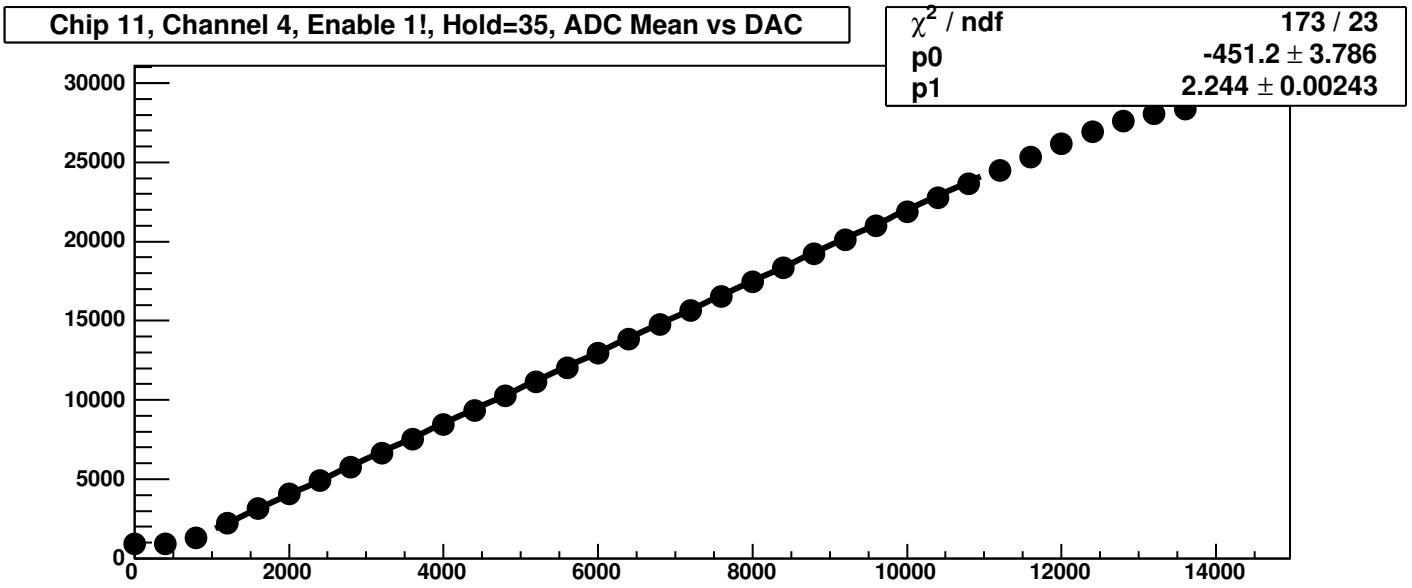

Chip 11, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

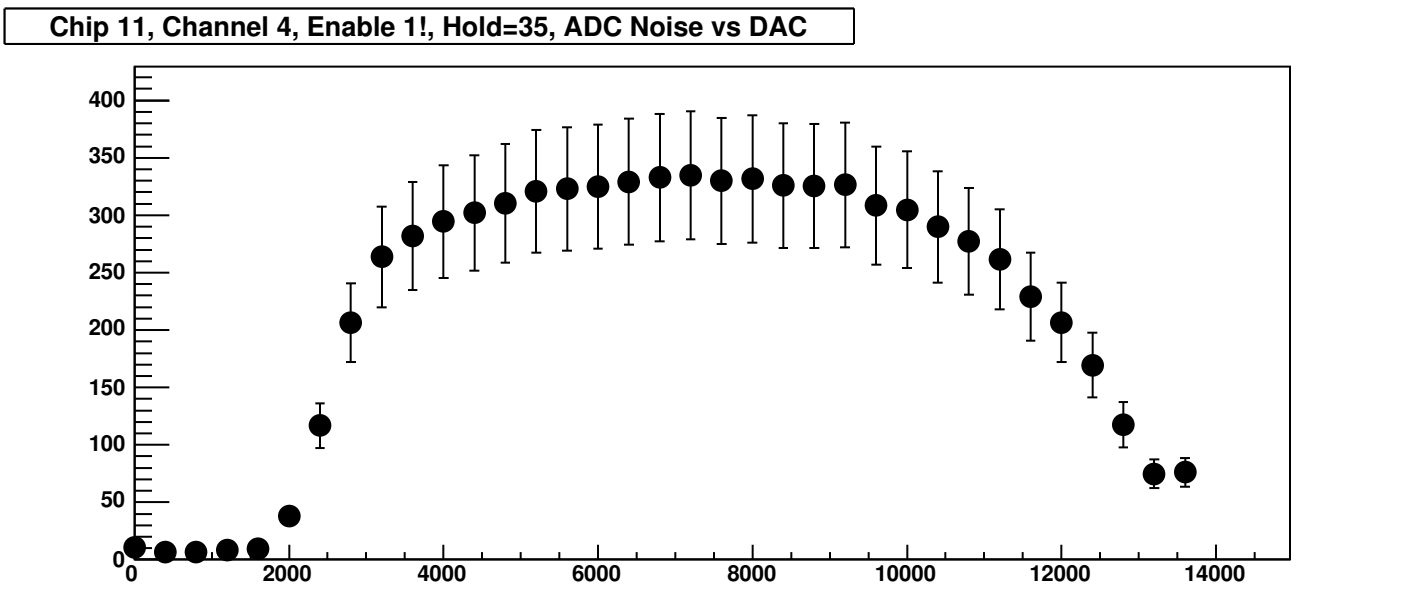

Chip 11, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

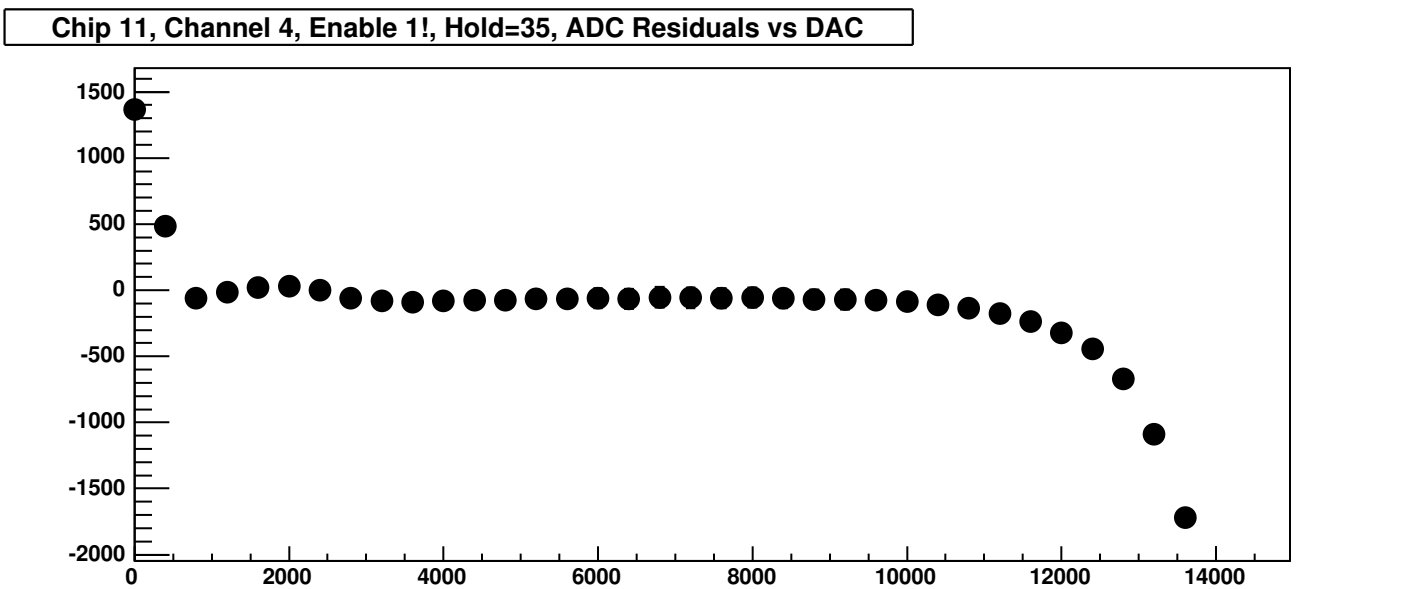

Chip 11, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.265e+04 / 23
p0 -1276 ± 1.388
p1 0.5378 ± 0.0002594
5000
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

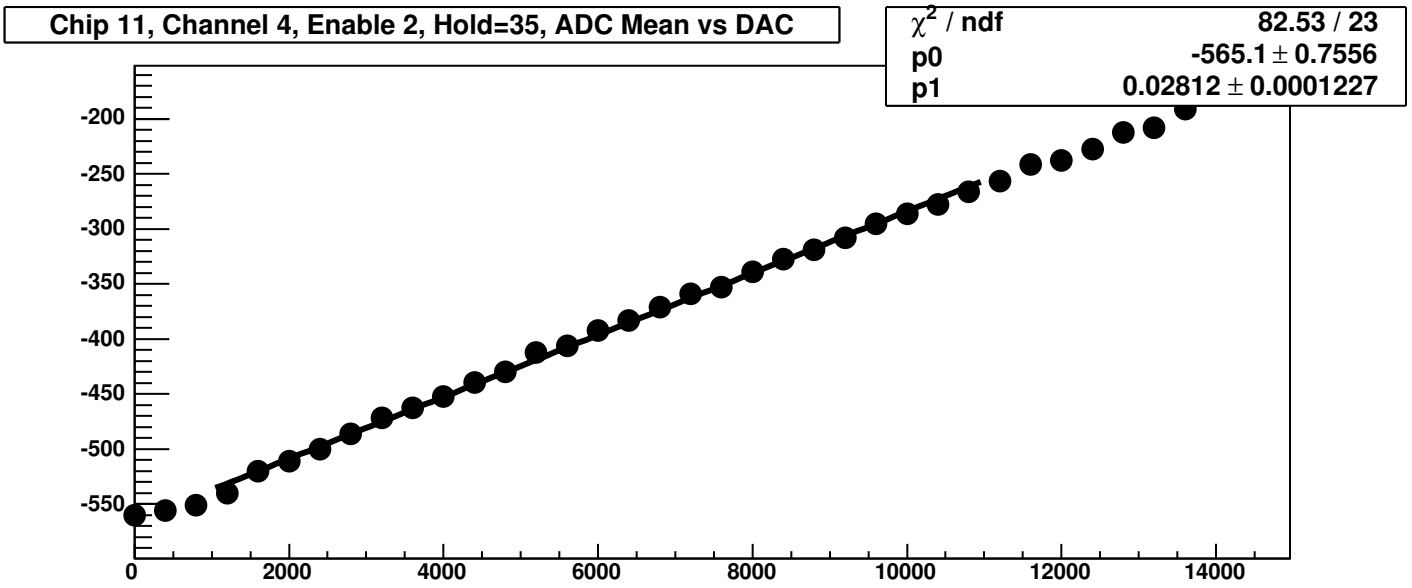

Chip 11, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

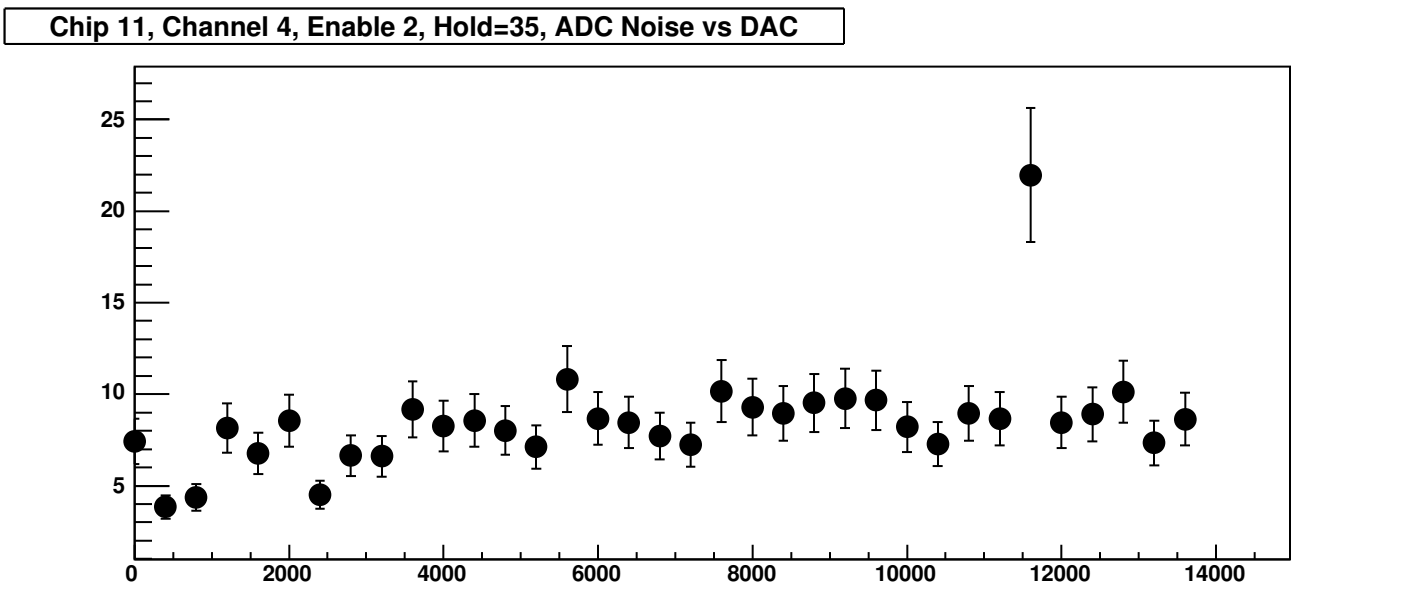

Chip 11, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

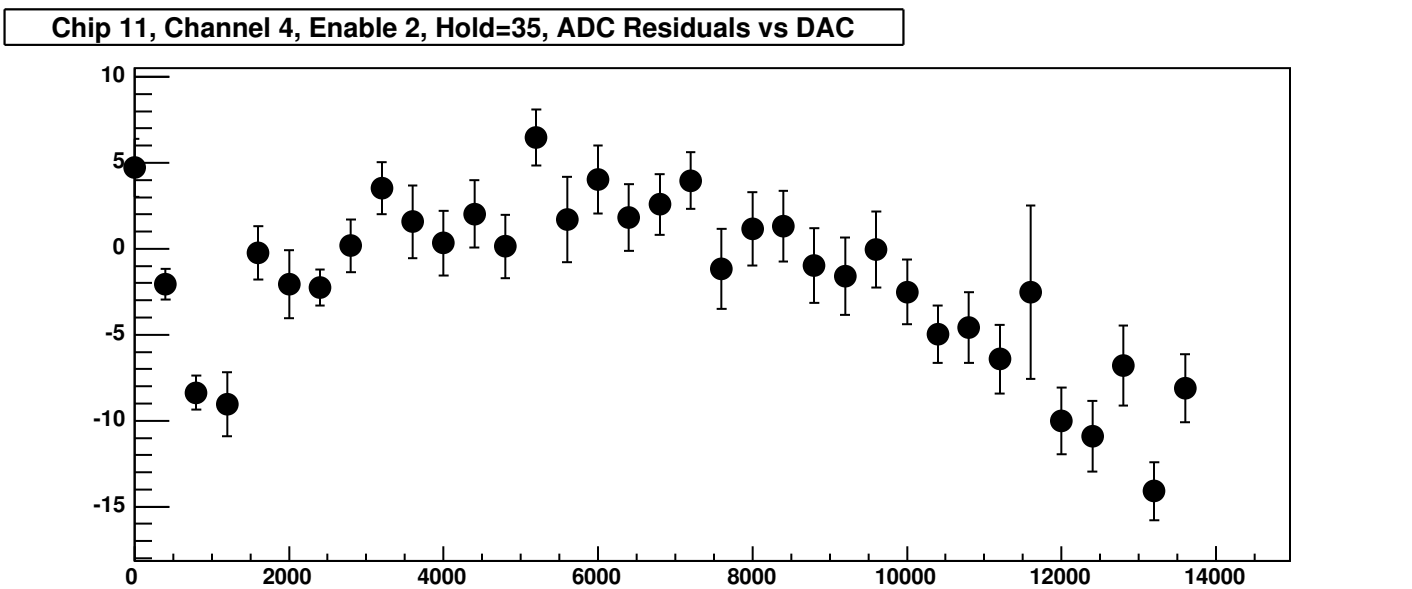

Chip 11, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 173 / 23
p0 -451.2 ± 3.786
p1 2.244 ± 0.00243
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

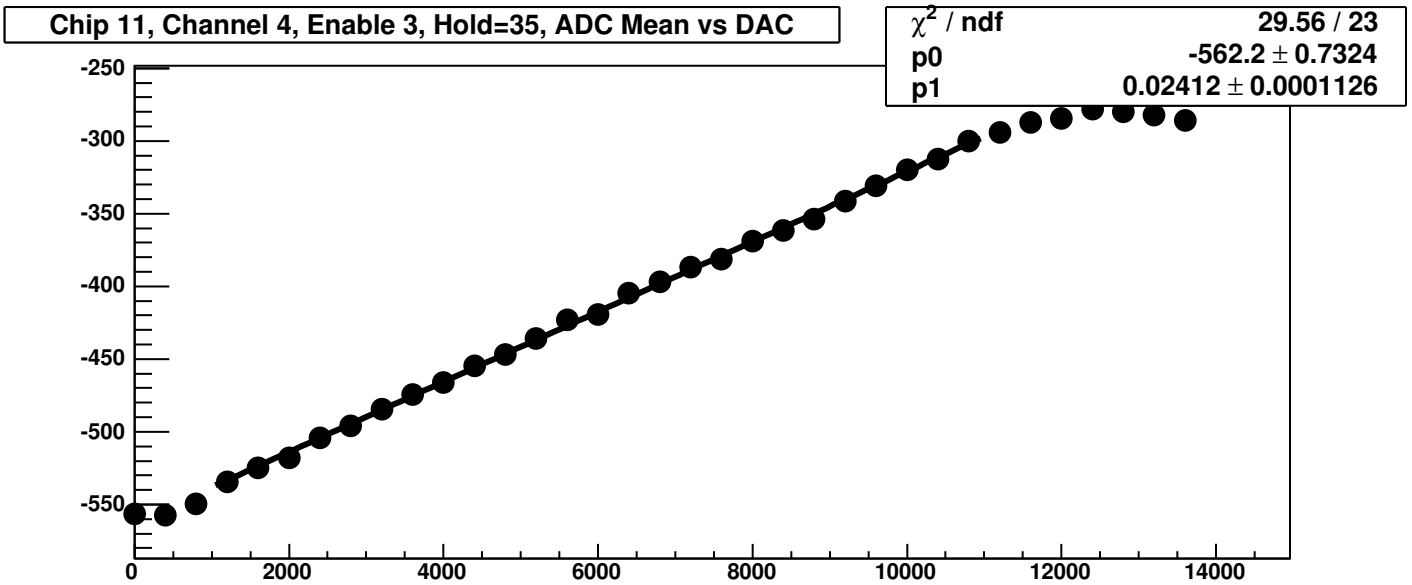

Chip 11, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

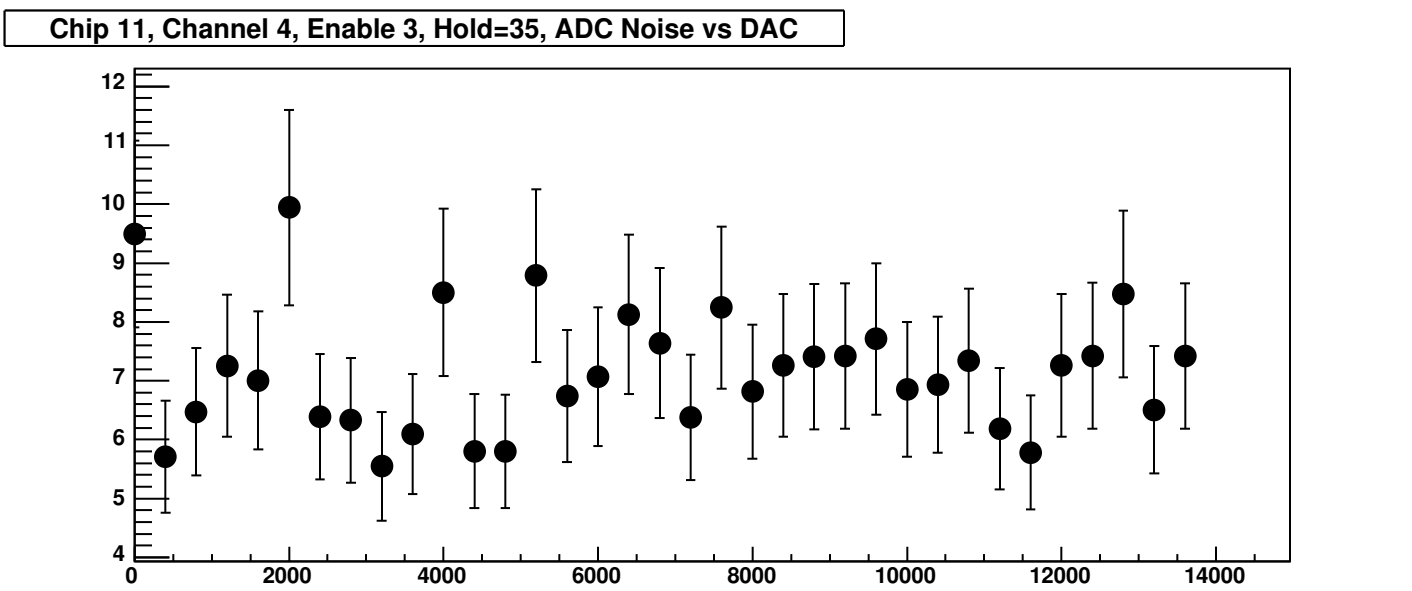

Chip 11, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

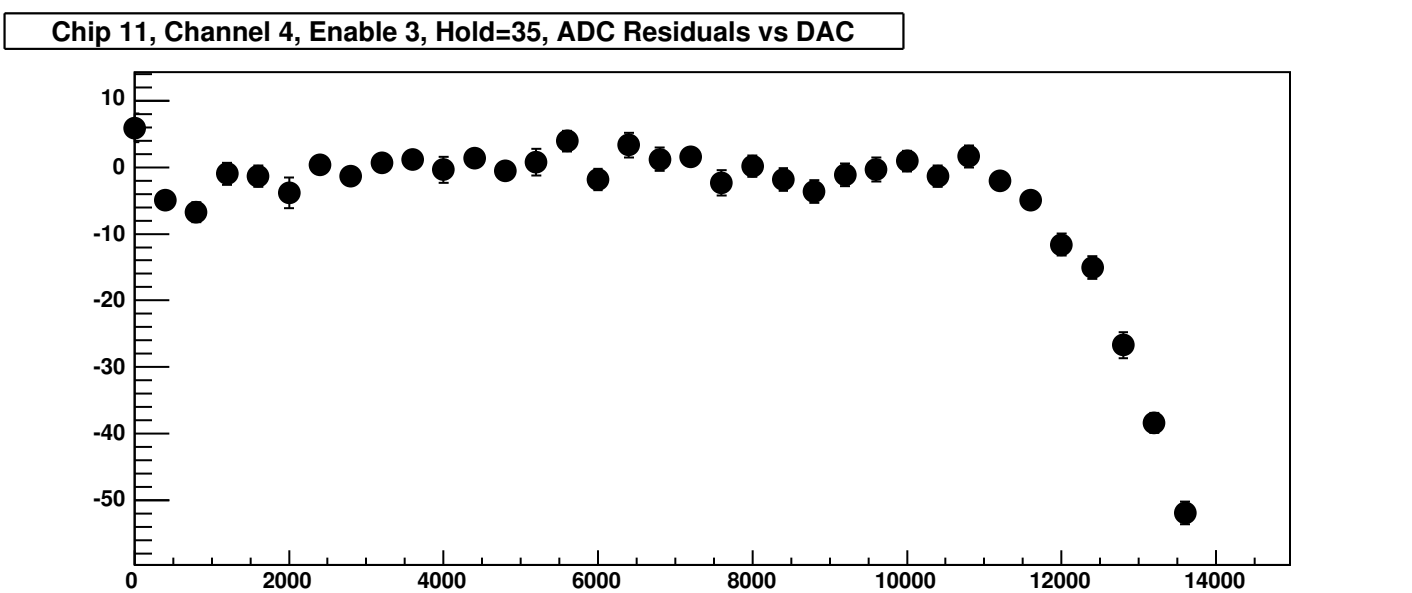

Chip 11, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 82.53 / 23
p0 -565.1 ± 0.7556
p1 0.02812 ± 0.0001227
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

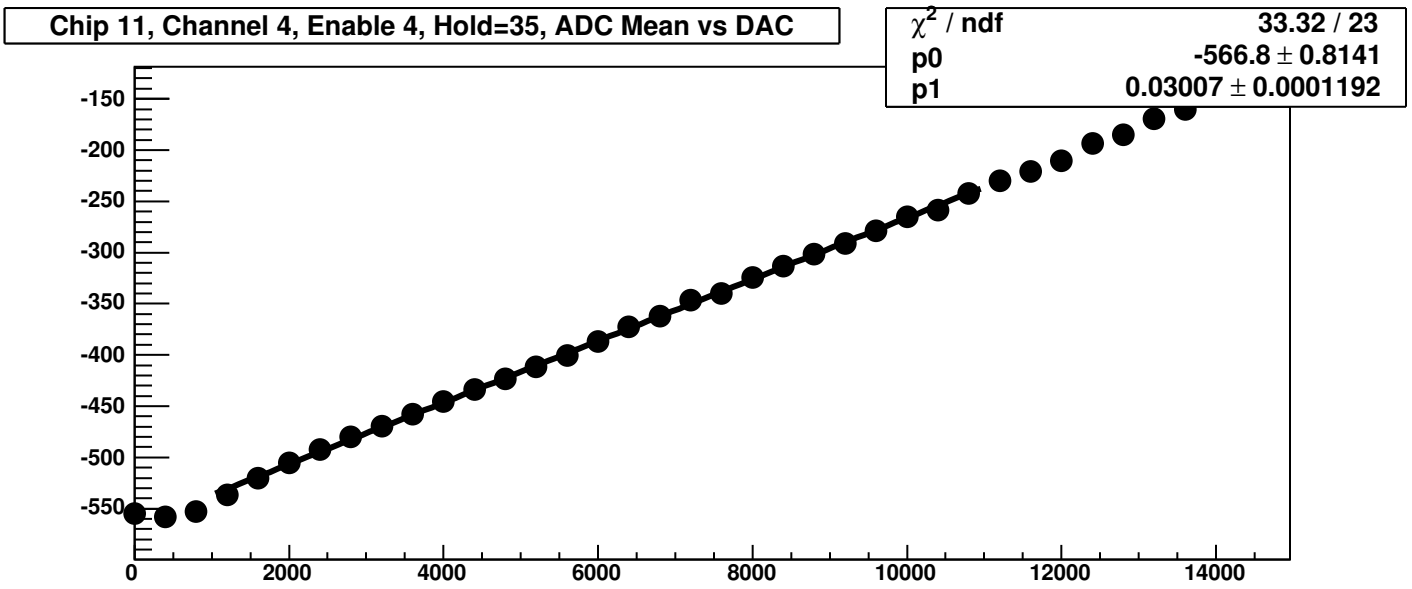

Chip 11, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

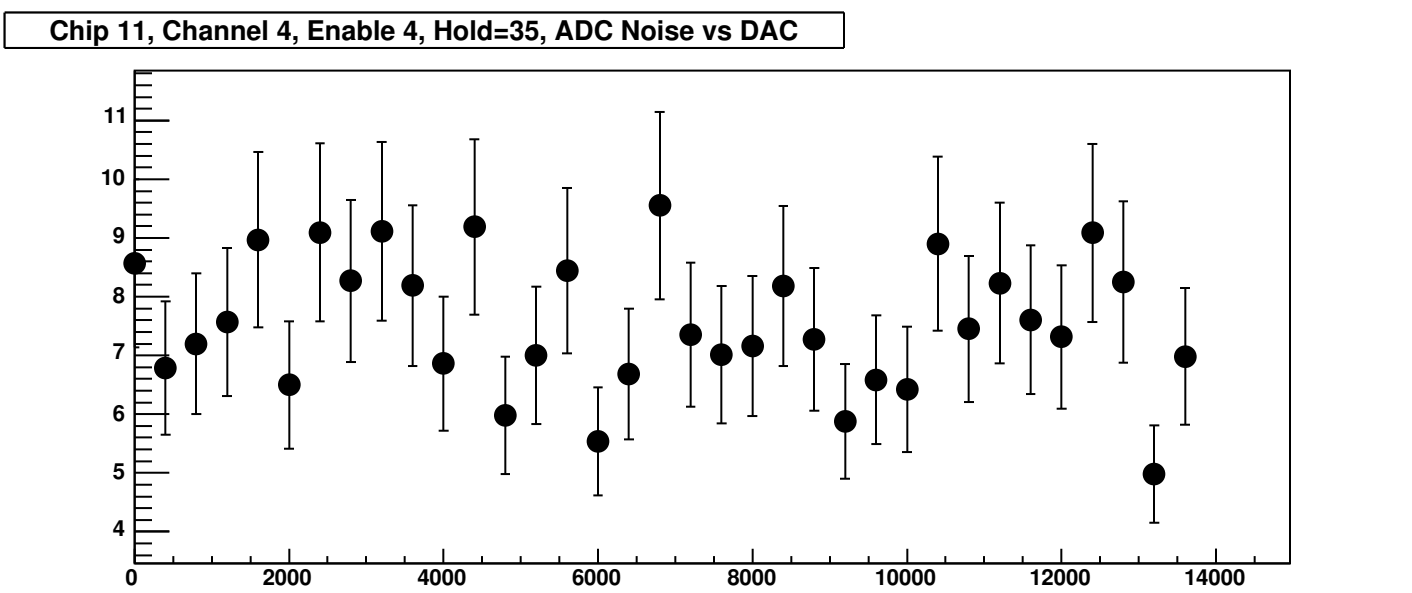

Chip 11, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

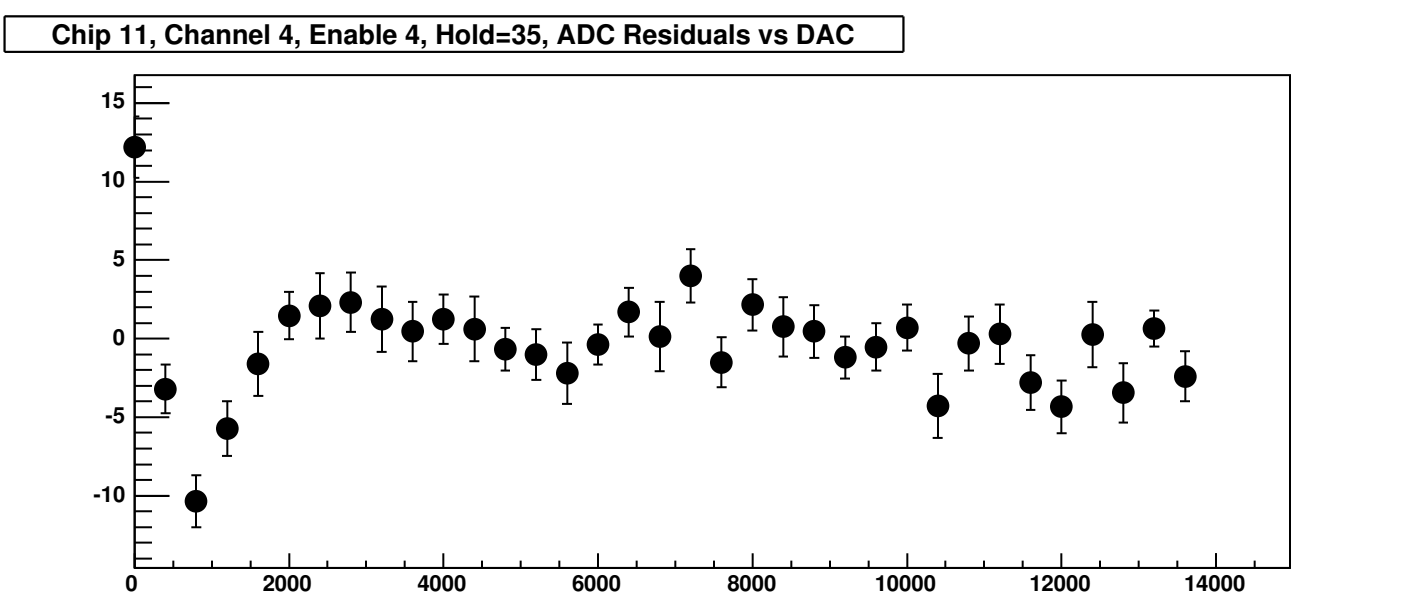

Chip 11, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 29.56 / 23
p0 -562.2 ± 0.7324
p1 0.02412 ± 0.0001126
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

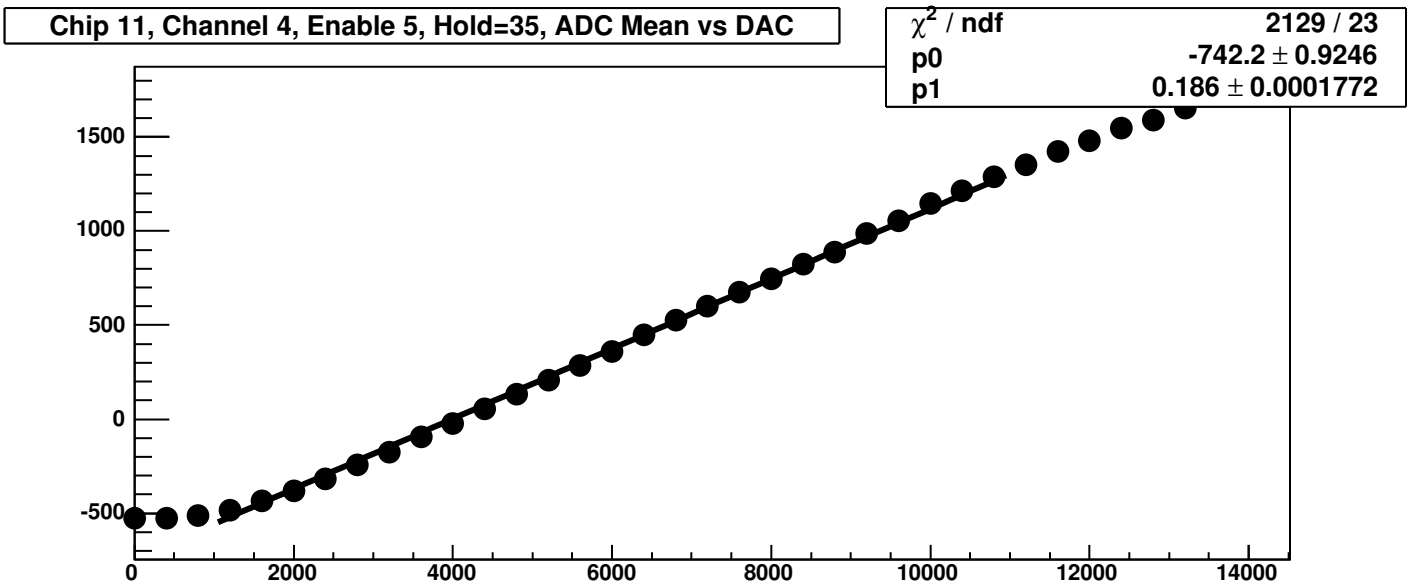

Chip 11, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

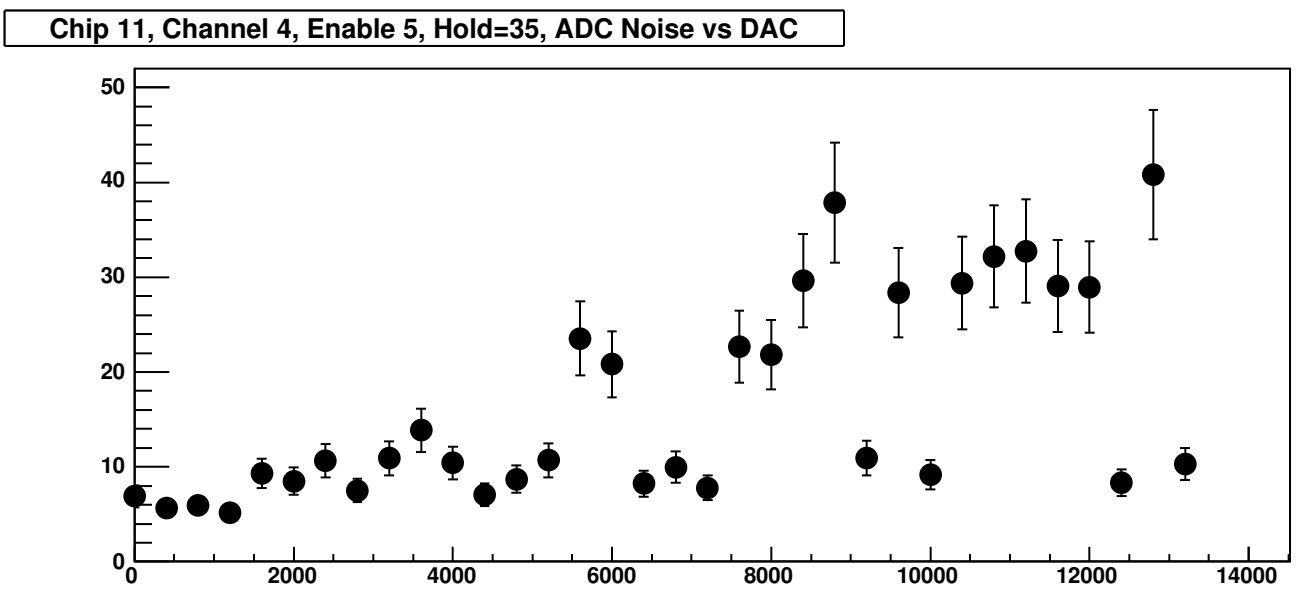

Chip 11, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

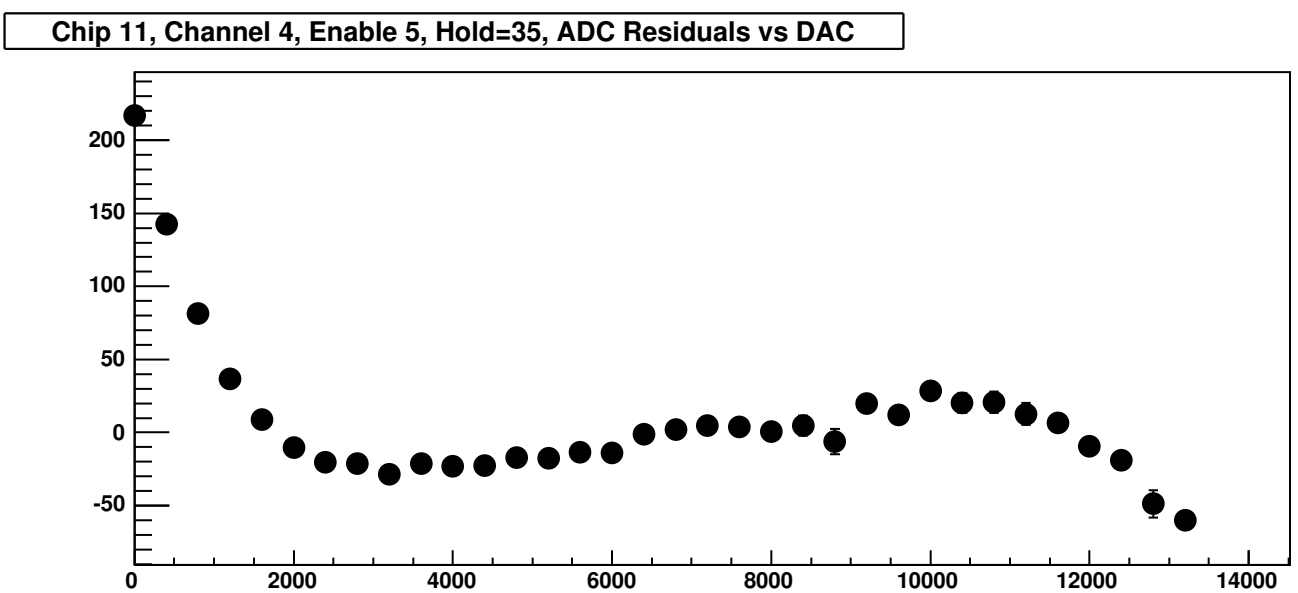

Chip 11, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 33.32 / 23
p0 -566.8 ± 0.8141
p1 0.03007 ± 0.0001192
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

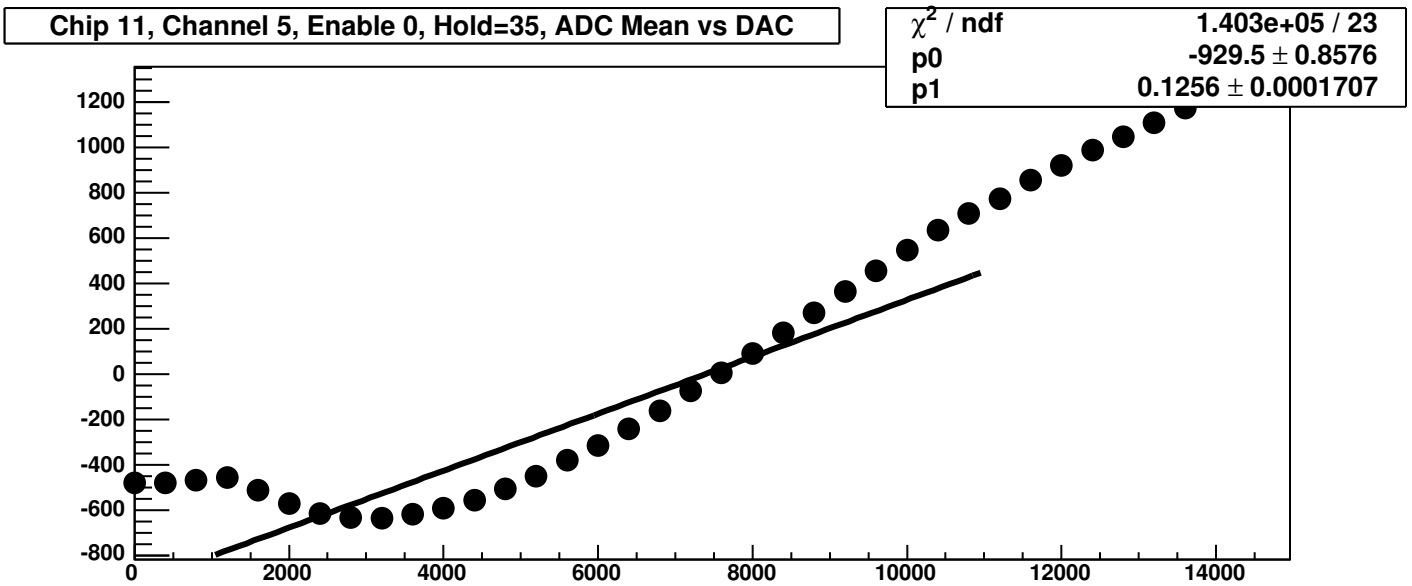

Chip 11, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

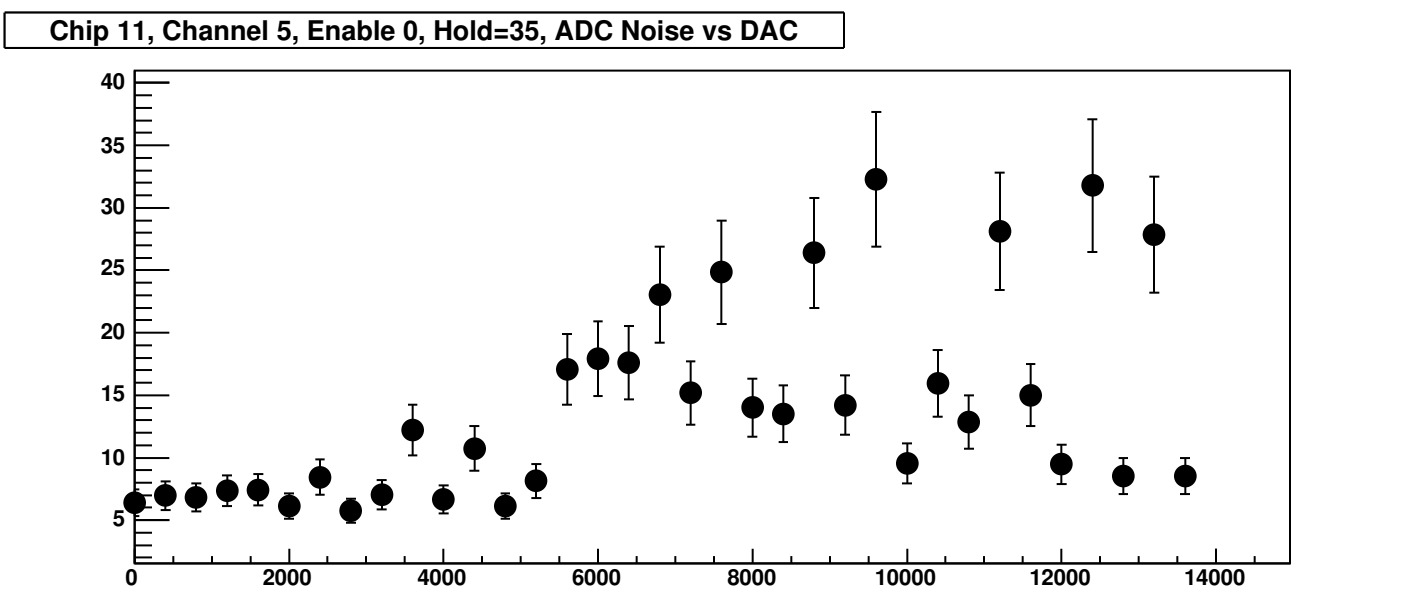

Chip 11, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

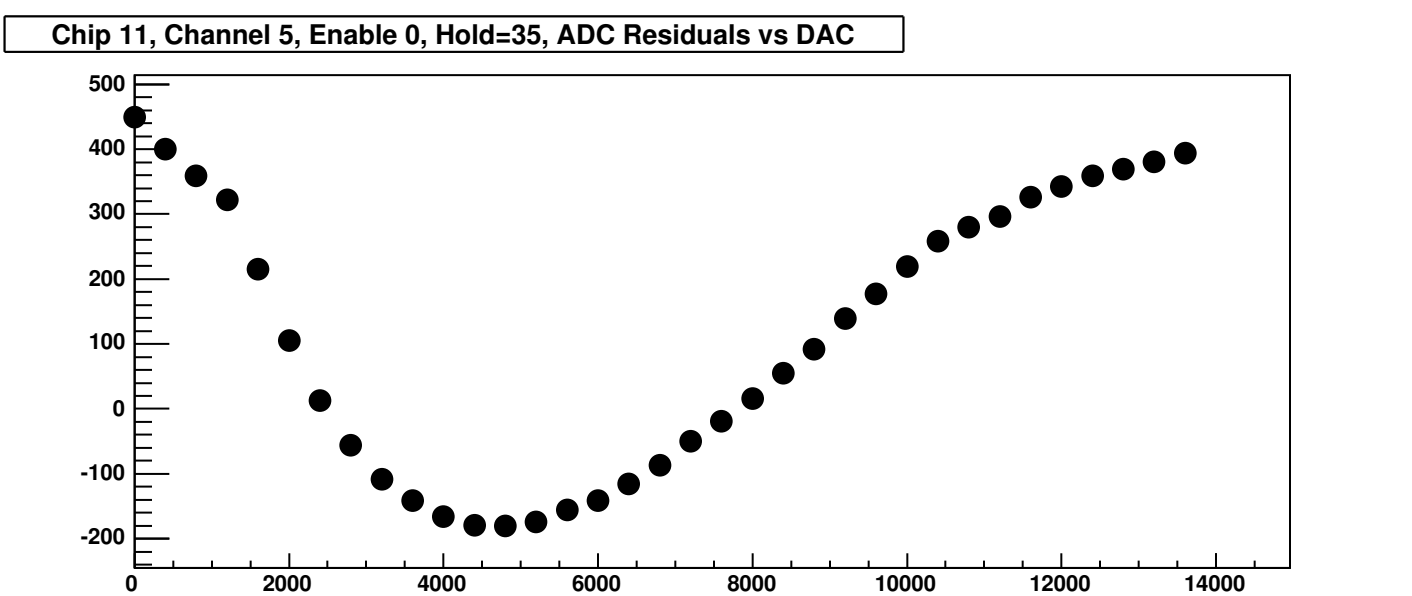

Chip 11, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2129 / 23
p0 -742.2 ± 0.9246
p1 0.186 ± 0.0001772
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

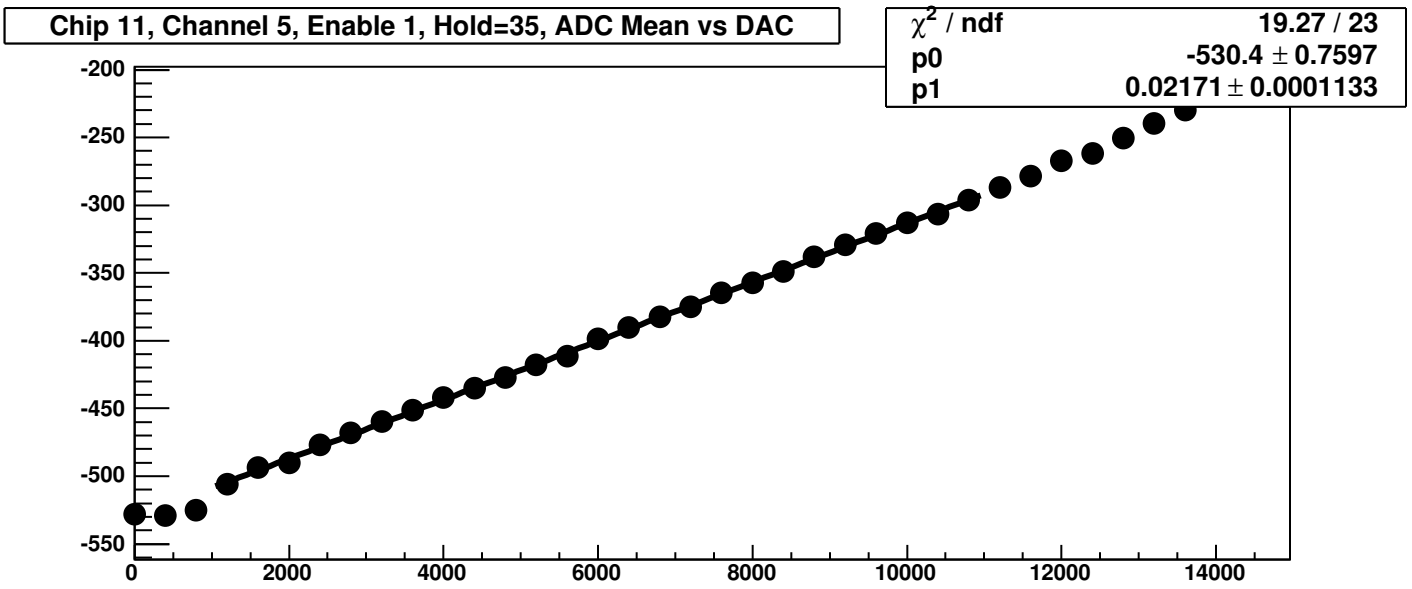

Chip 11, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

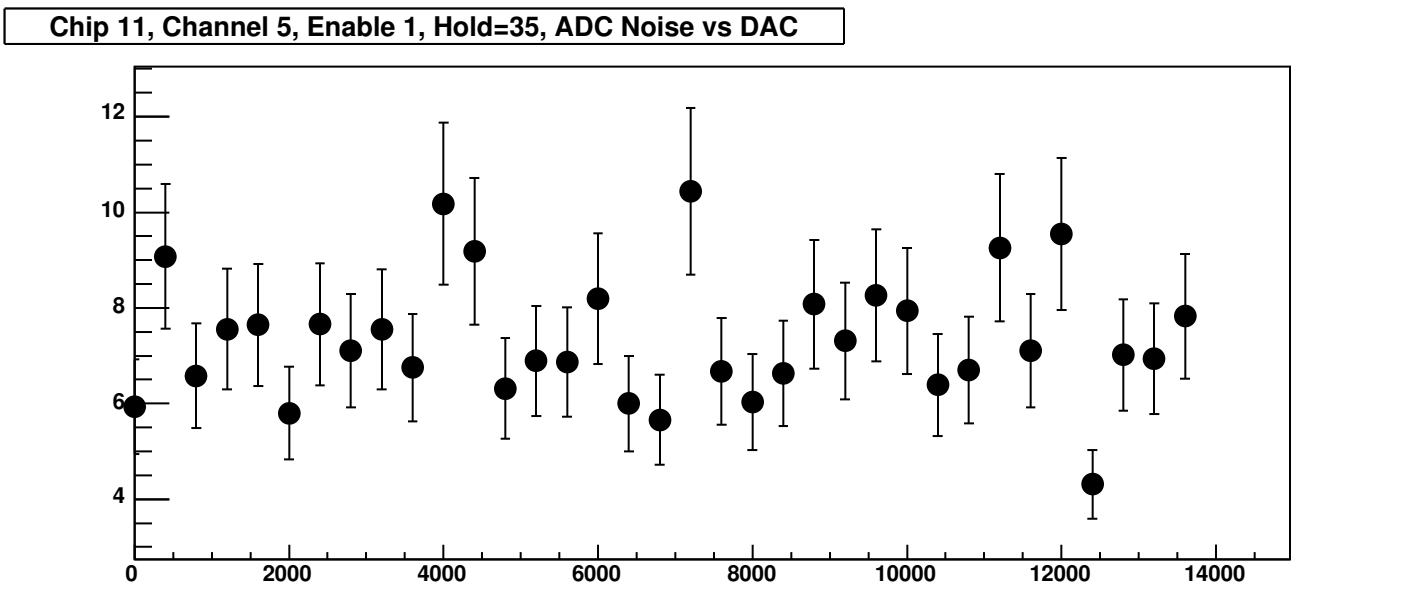

Chip 11, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

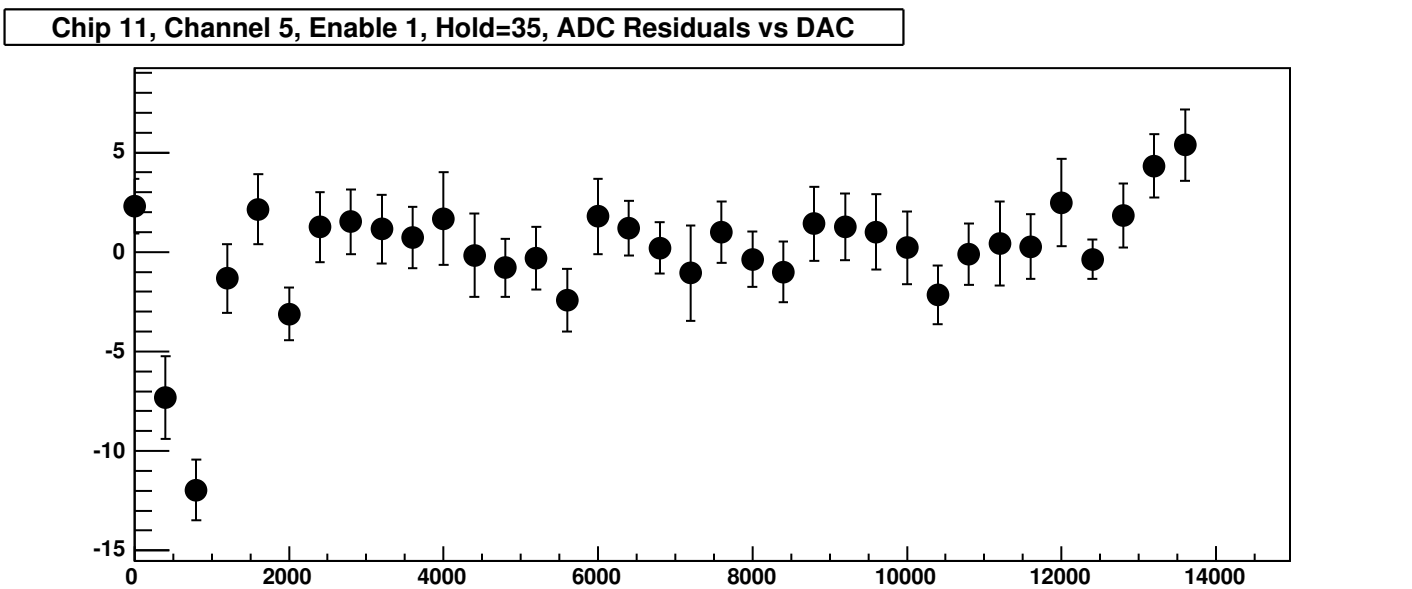

Chip 11, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.403e+05 / 23
p0 -929.5 ± 0.8576
p1 0.1256 ± 0.0001707
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

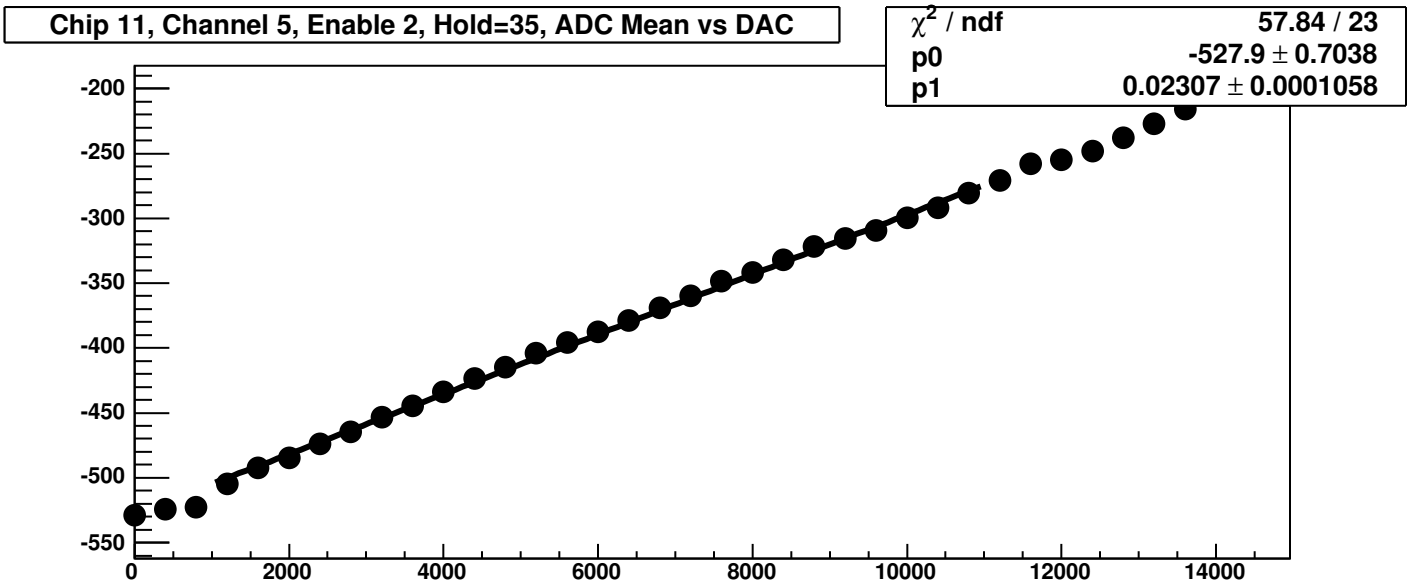

Chip 11, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

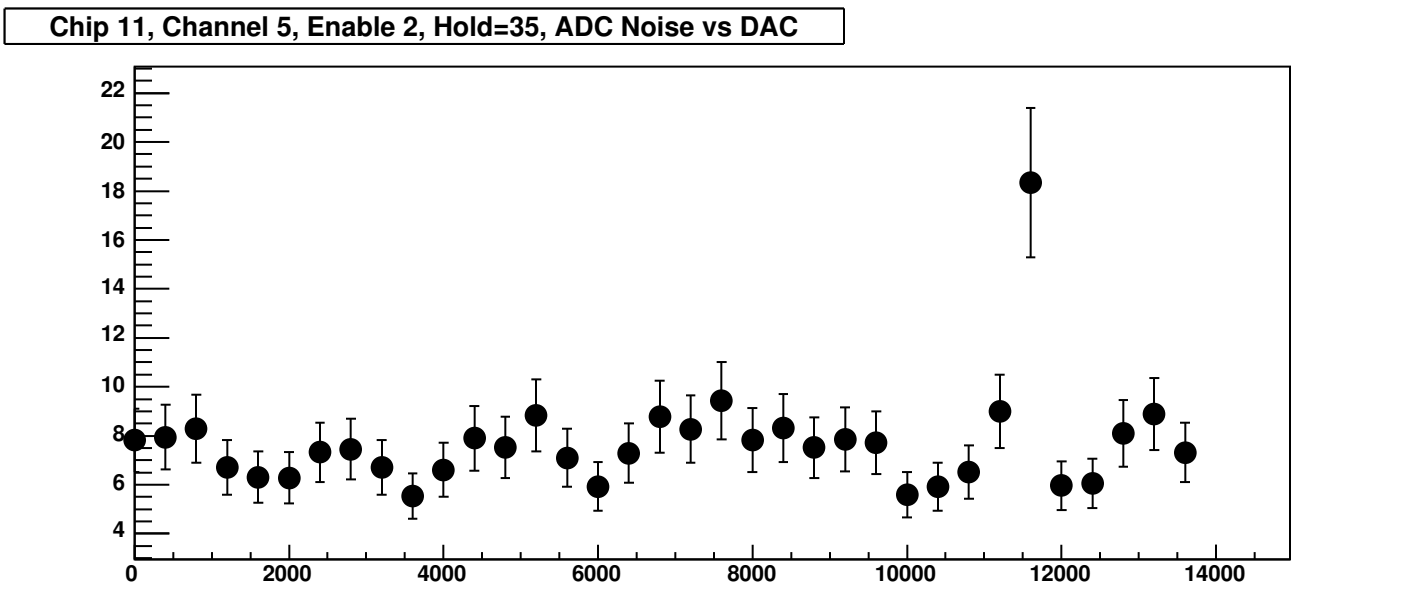

Chip 11, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

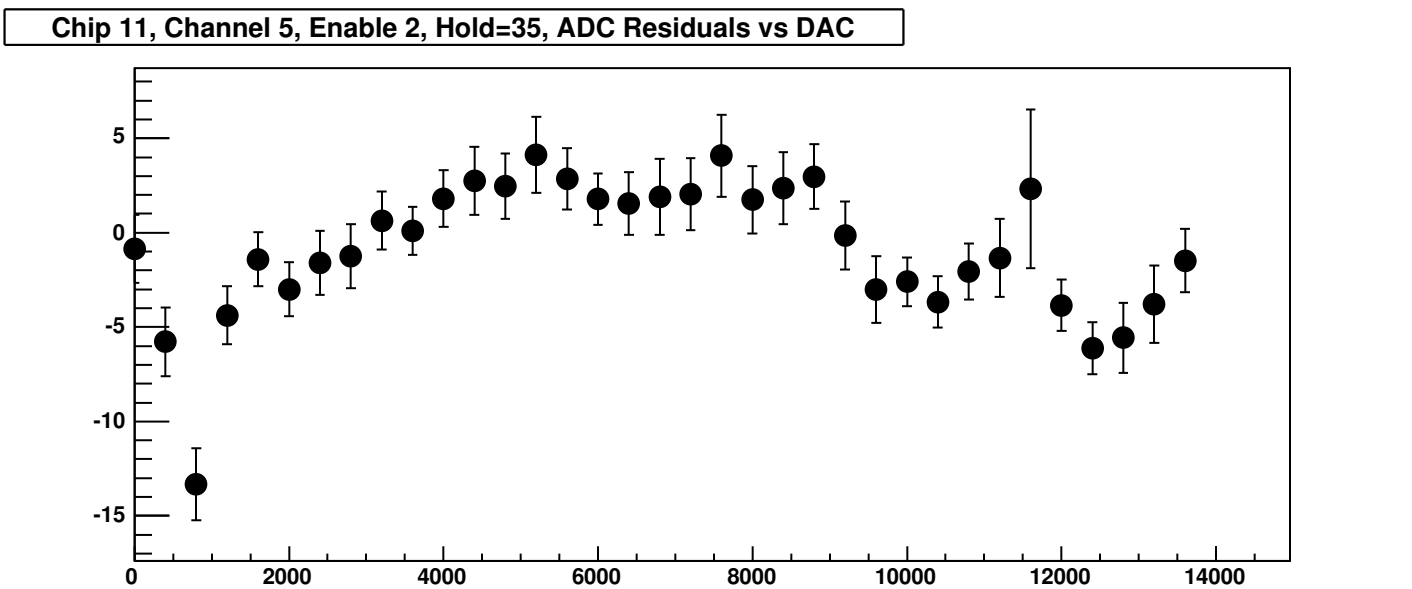

Chip 11, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 19.27 / 23
p0 -530.4 ± 0.7597
p1 0.02171 ± 0.0001133
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

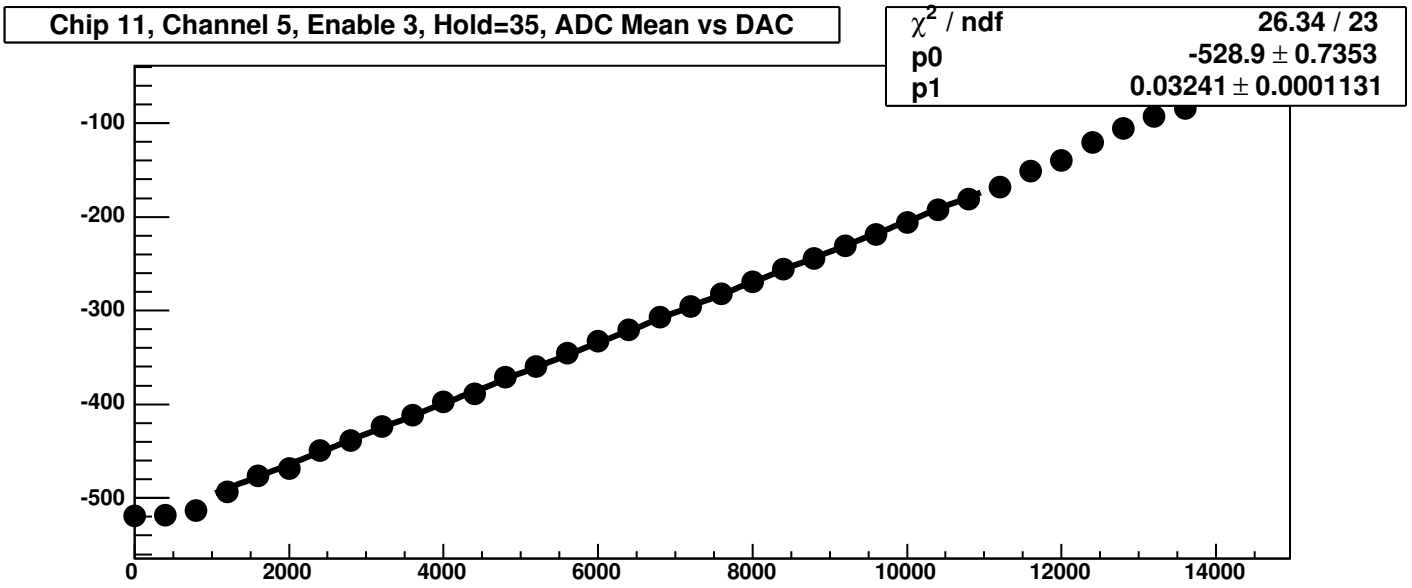

Chip 11, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

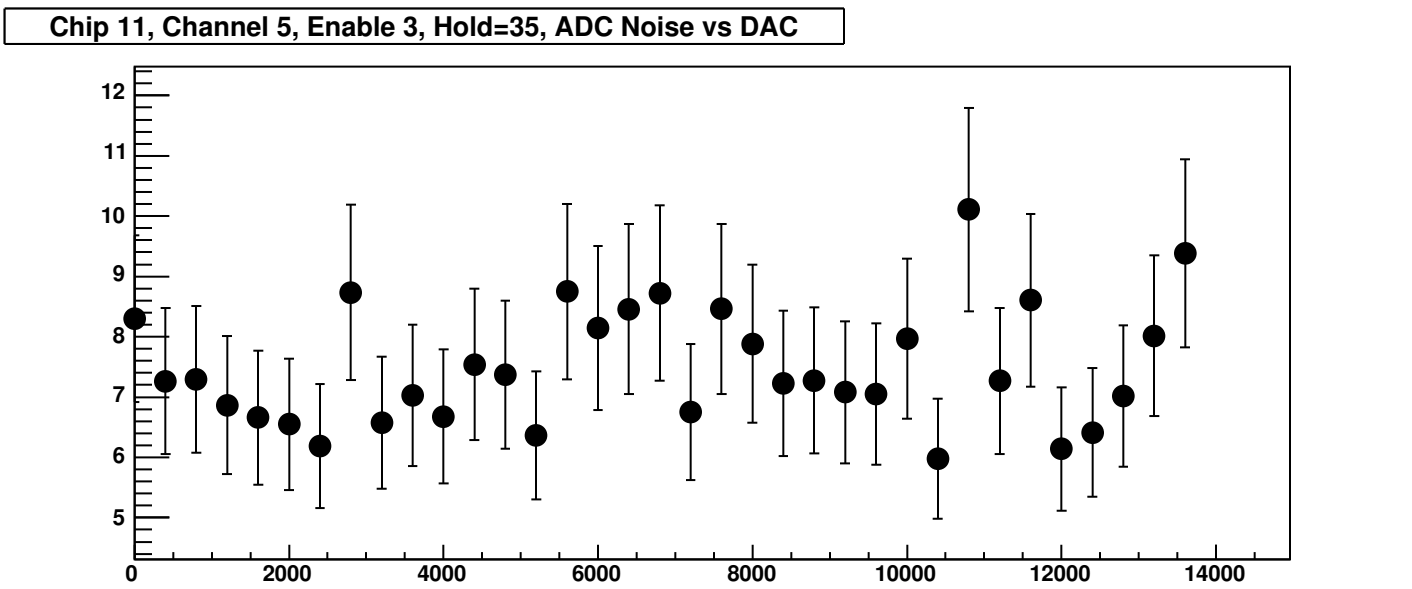

Chip 11, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

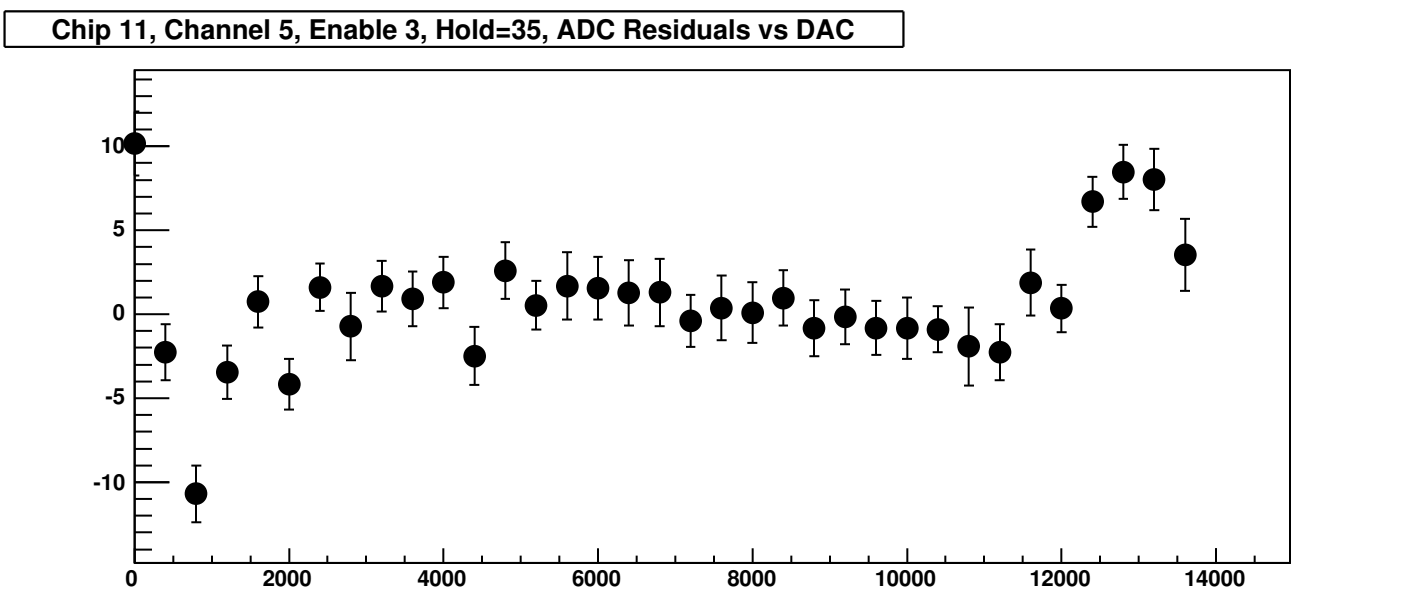

Chip 11, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 57.84 / 23
p0 -527.9 ± 0.7038
p1 0.02307 ± 0.0001058
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

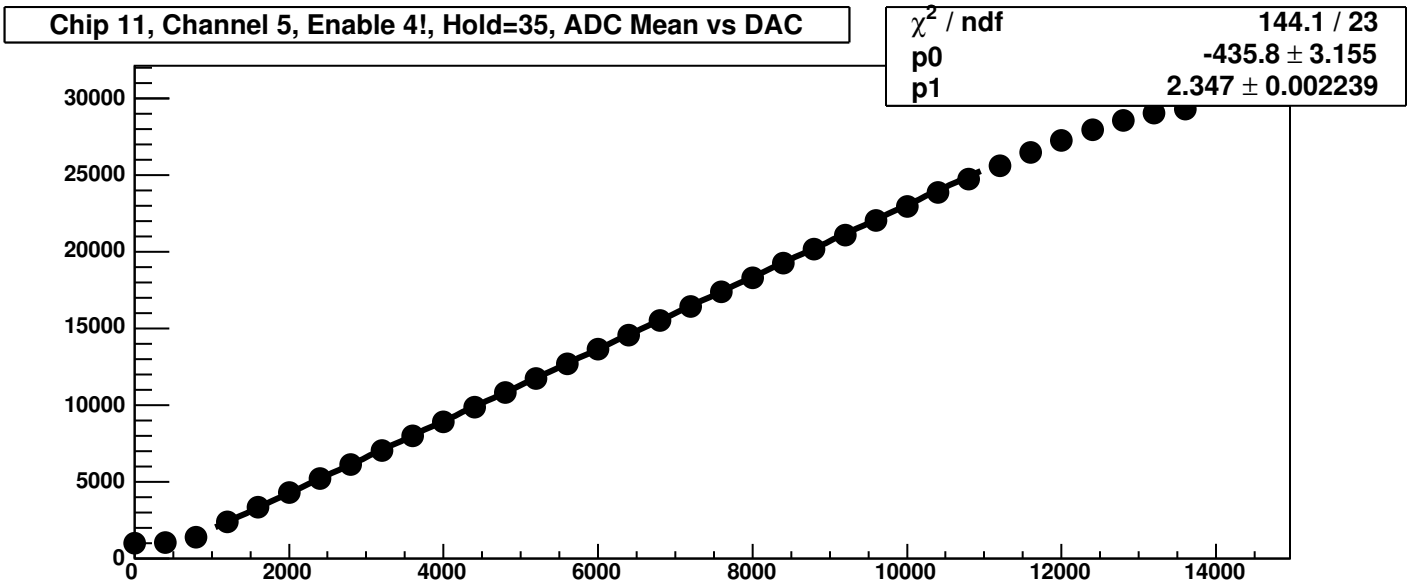

Chip 11, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

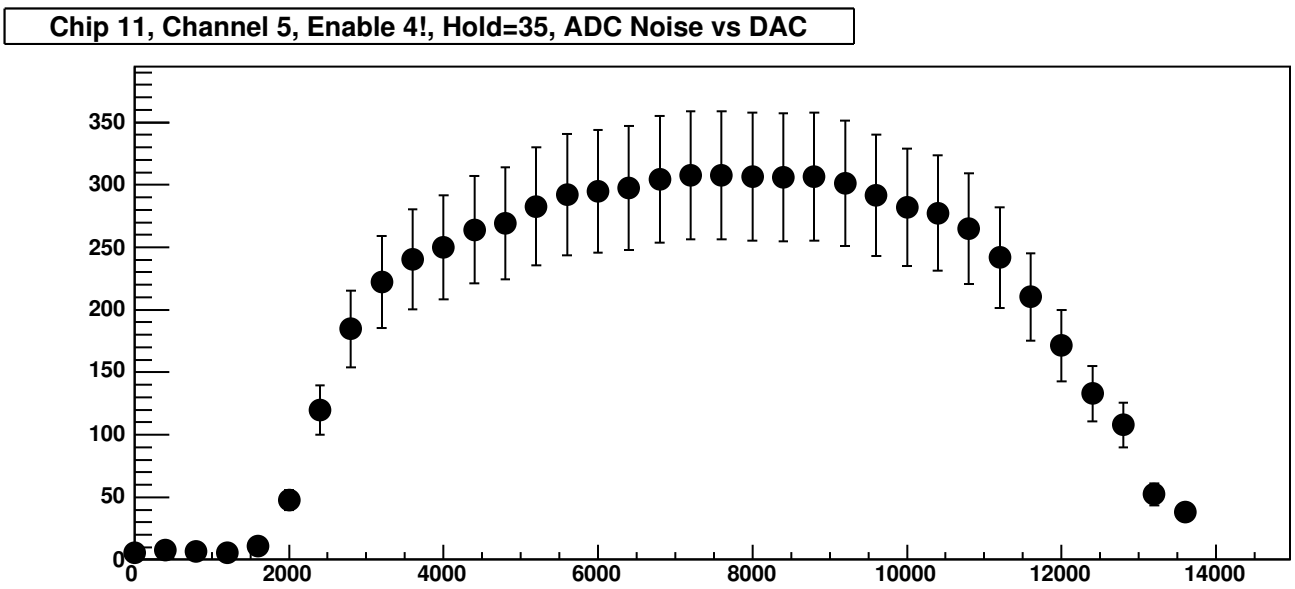

Chip 11, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

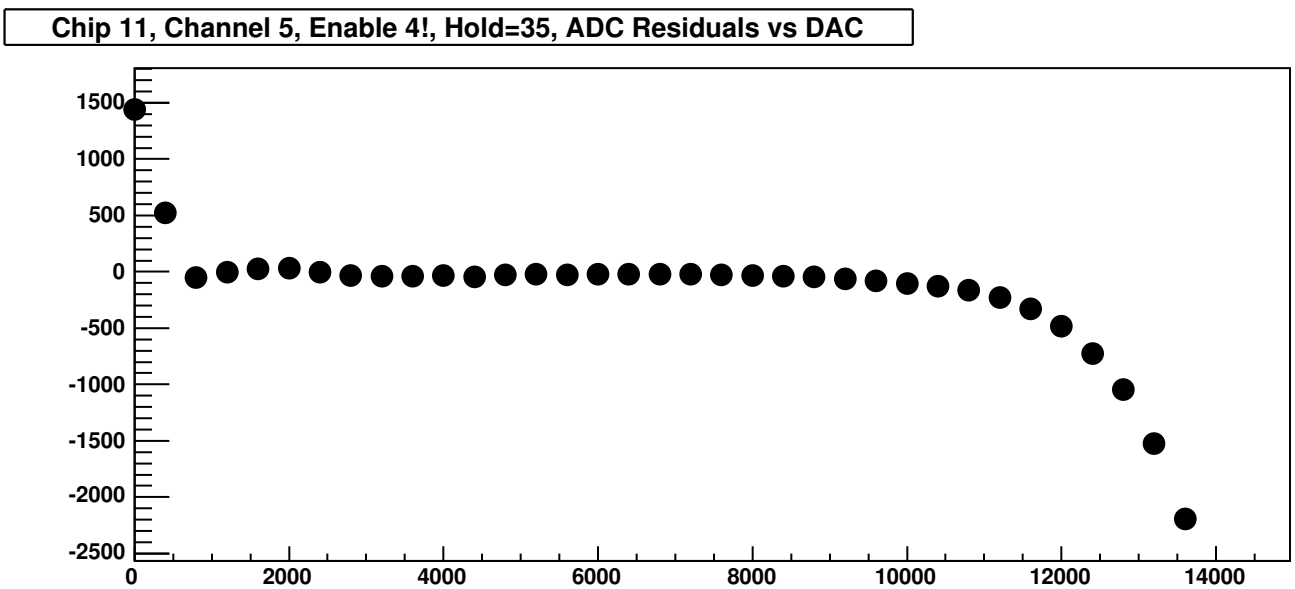

Chip 11, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.34 / 23
p0 -528.9 ± 0.7353
p1 0.03241 ± 0.0001131
-100
-200
-300
-400
-500
0
2000
4000
6000
8000
10000
12000
14000

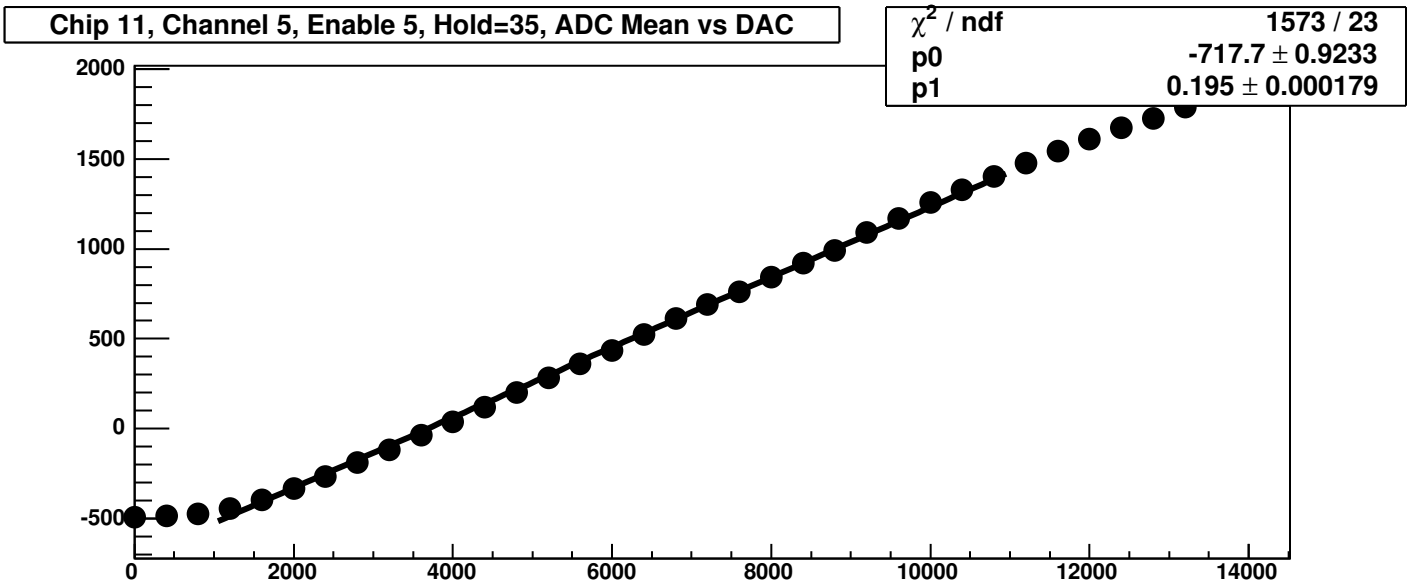

Chip 11, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
2000
4000
6000
8000
10000
12000
14000

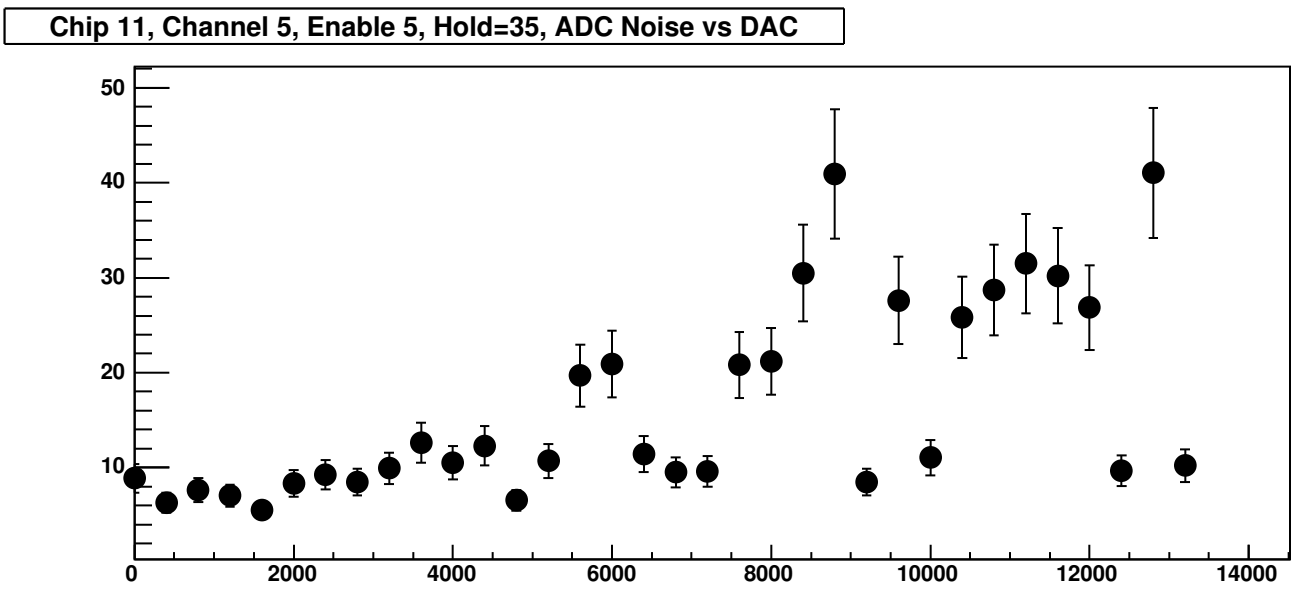

Chip 11, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

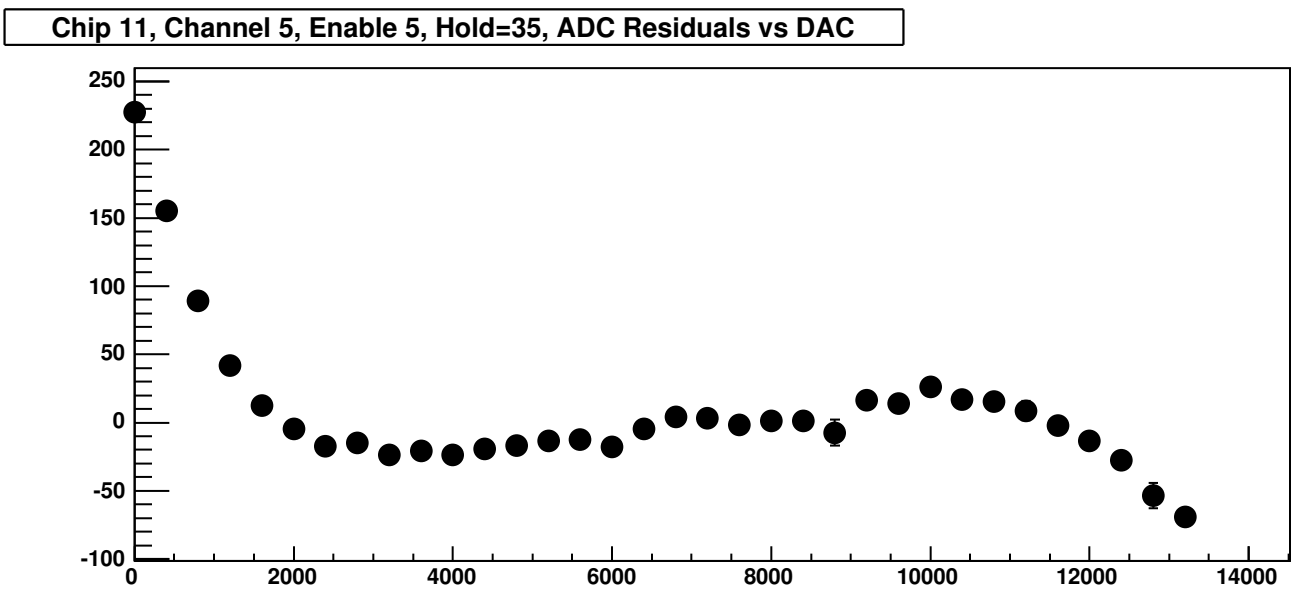

Chip 11, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 144.1 / 23
p0 -435.8 ± 3.155
p1 2.347 ± 0.002239
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

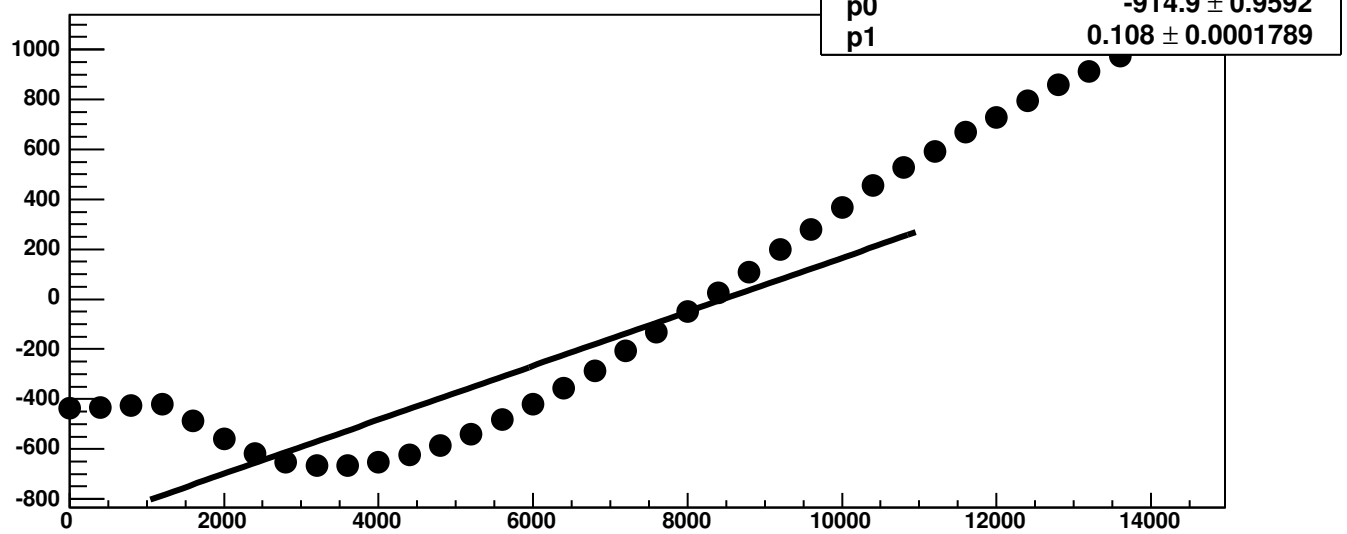

Chip 11, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1573 / 23
p0 -717.7 ± 0.9233
p1 0.195 ± 0.000179
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

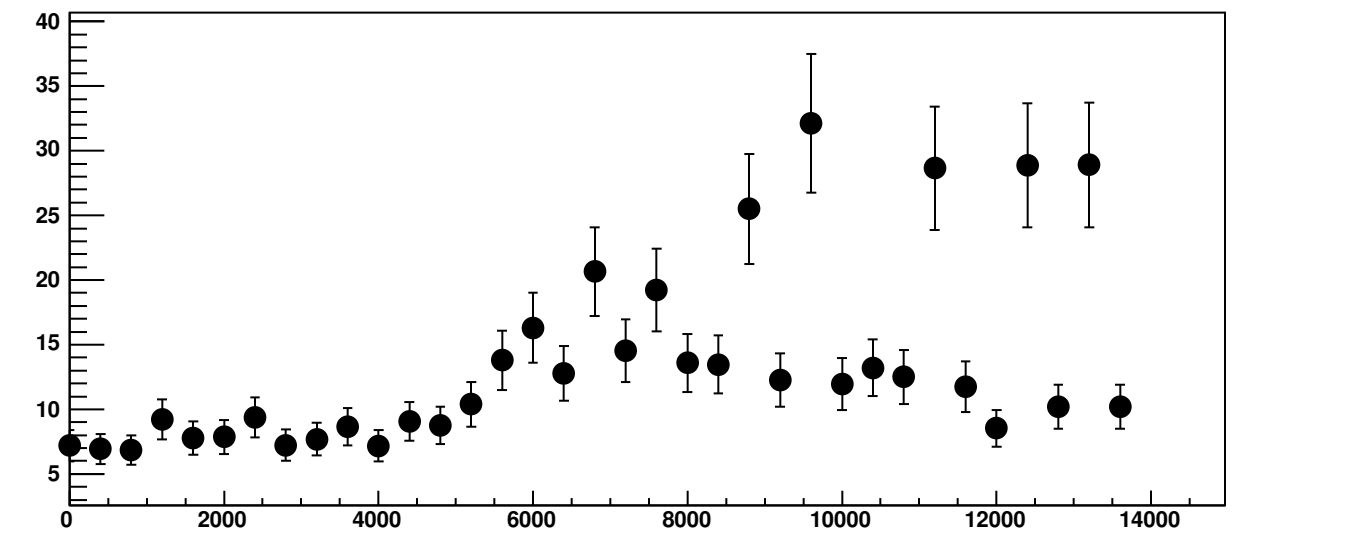

Chip 11, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

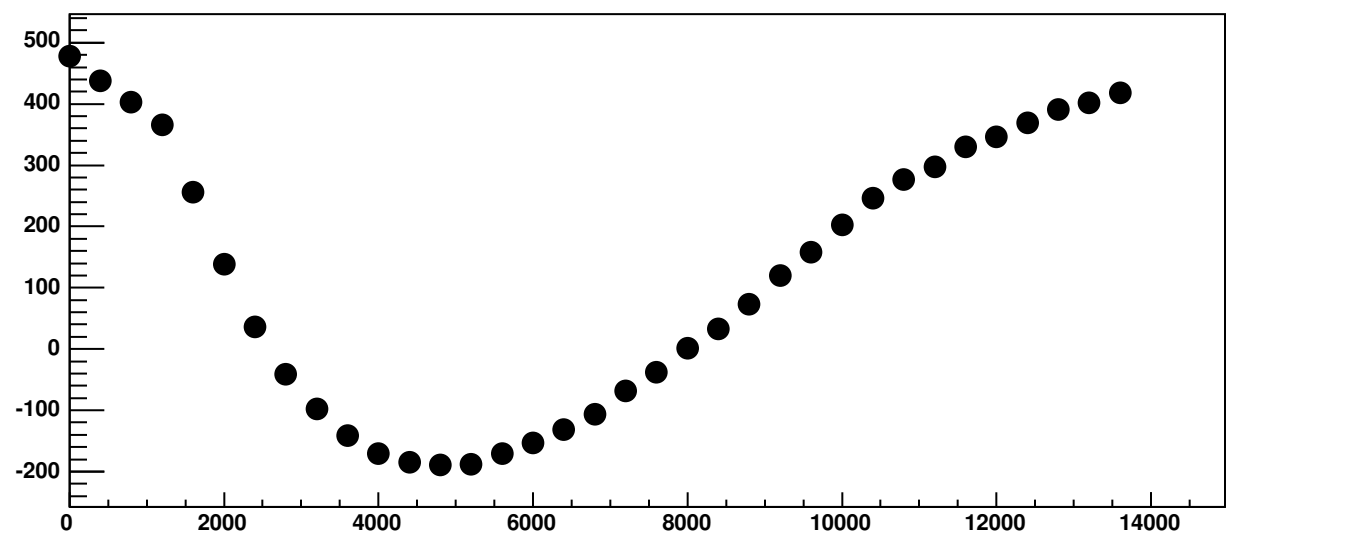
Chip 11, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC



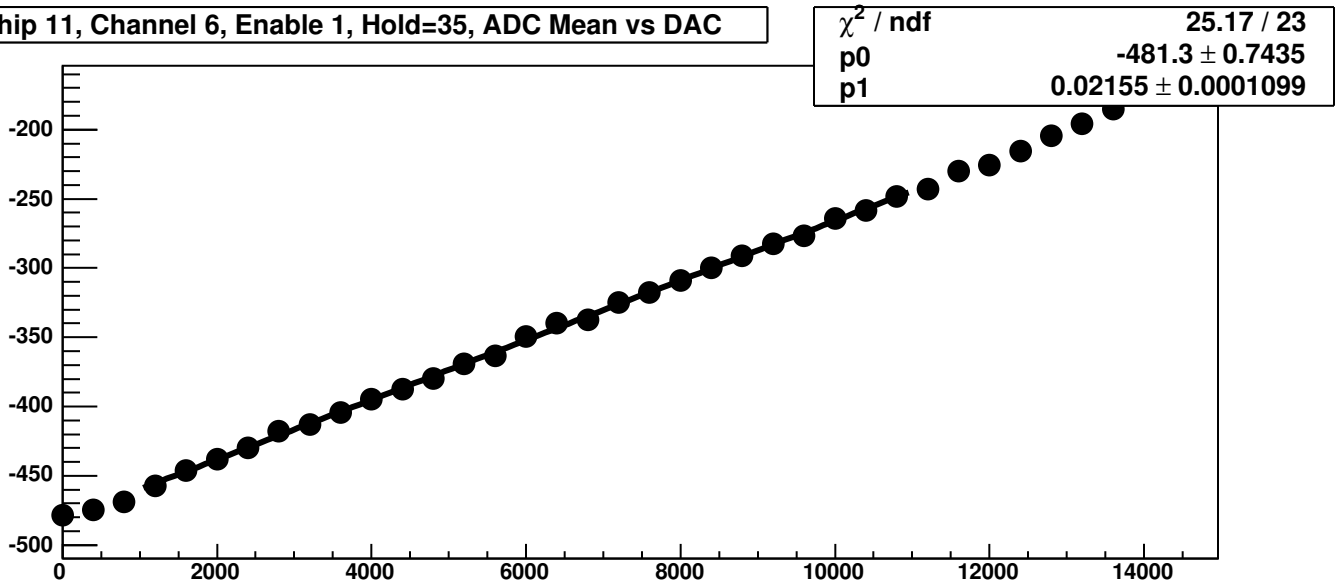
Chip 11, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC



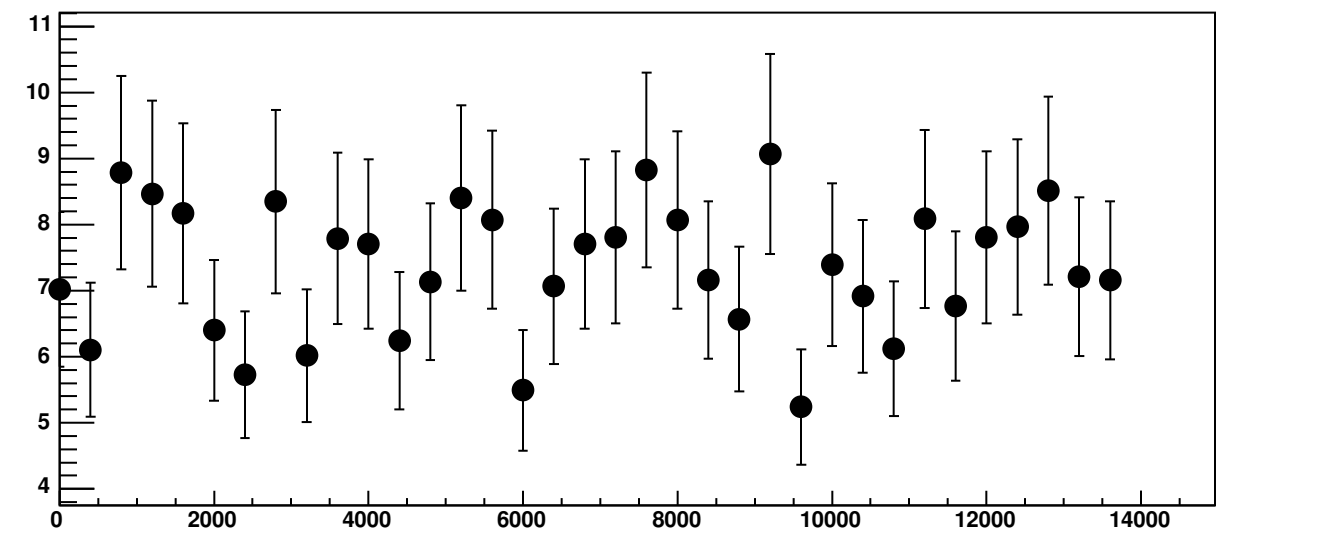
Chip 11, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC



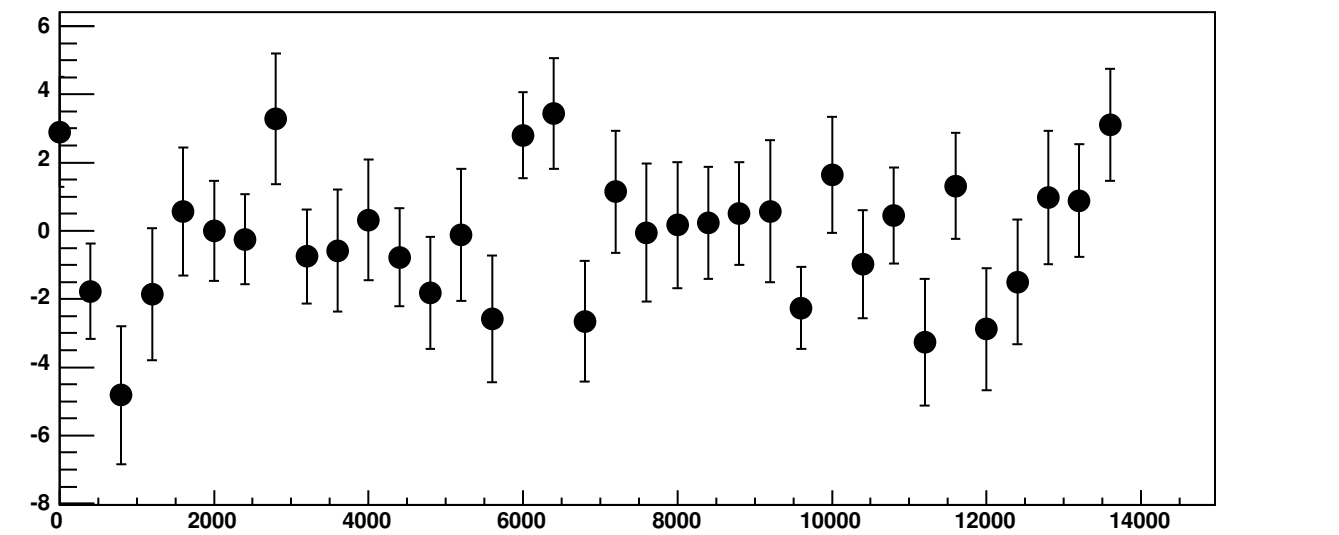
Chip 11, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC

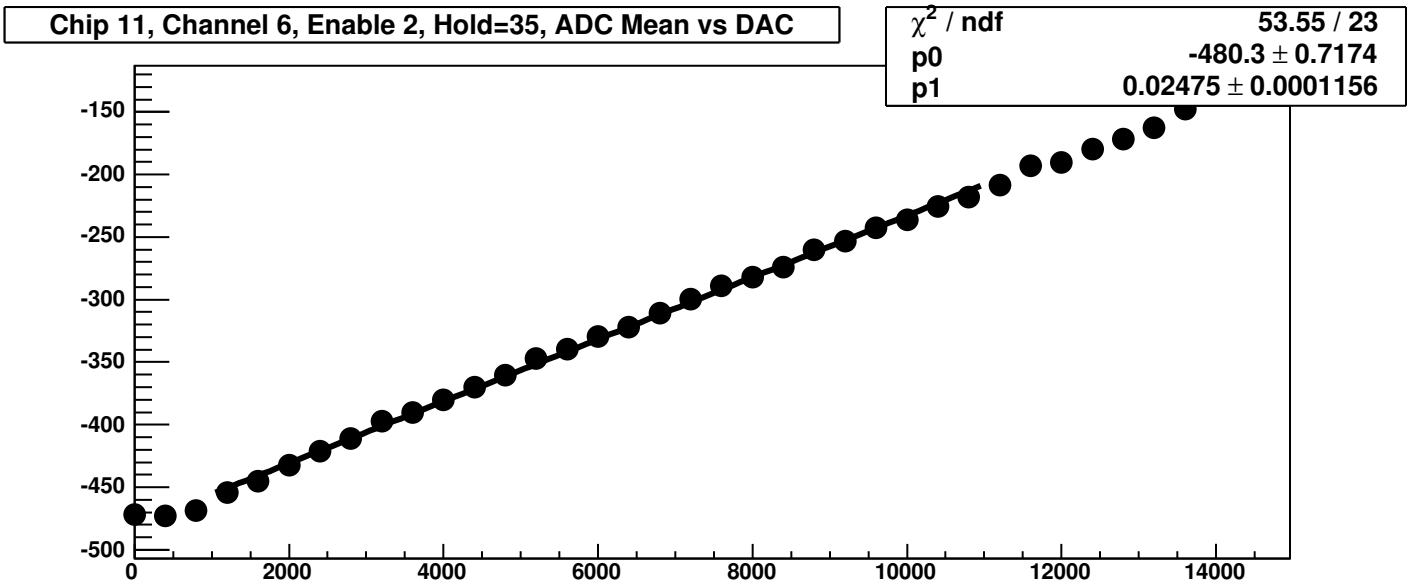


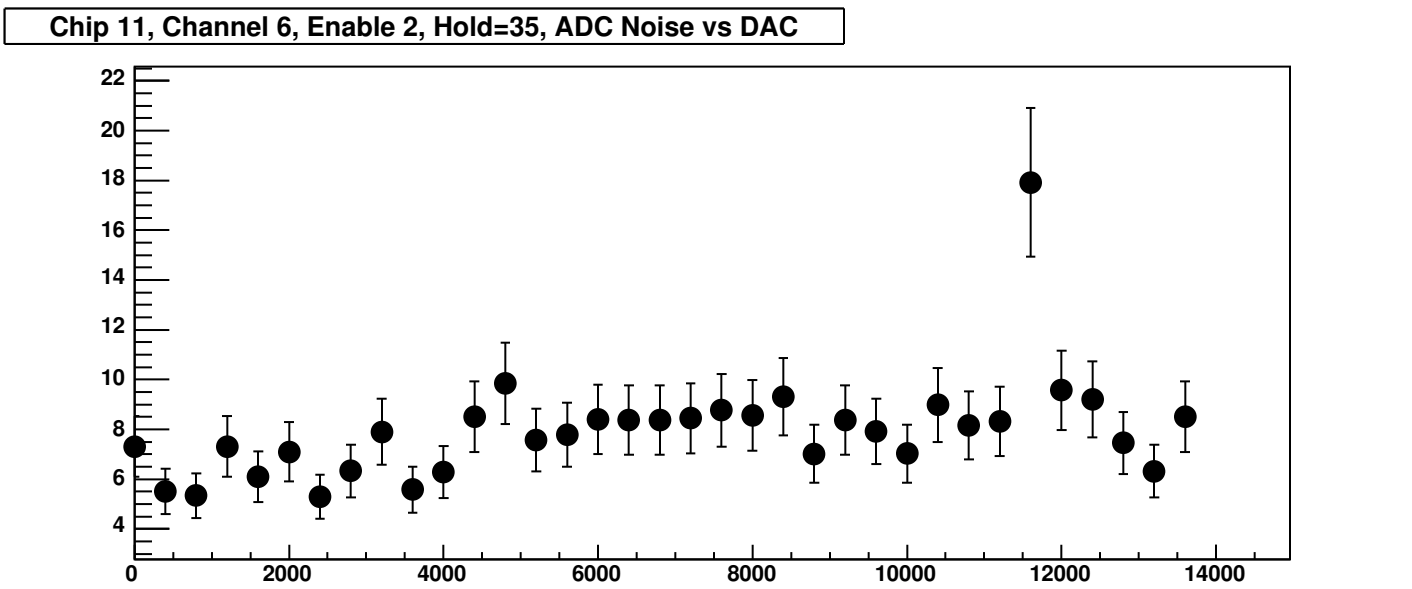
Chip 11, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC

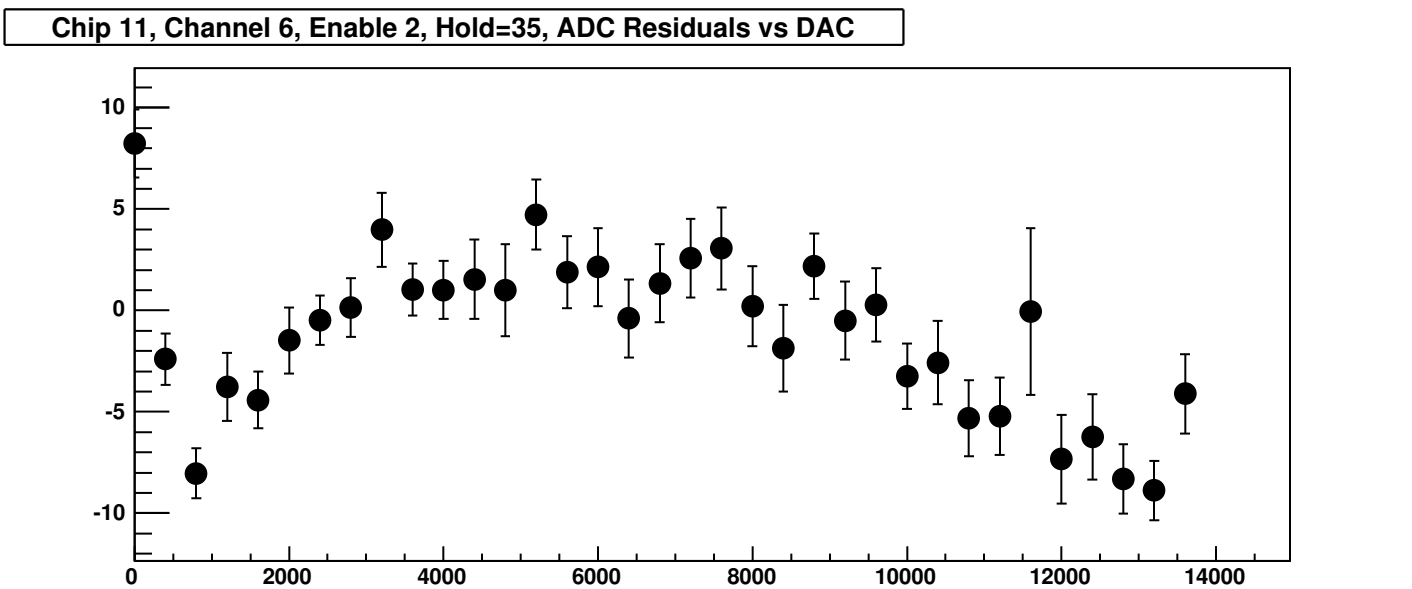


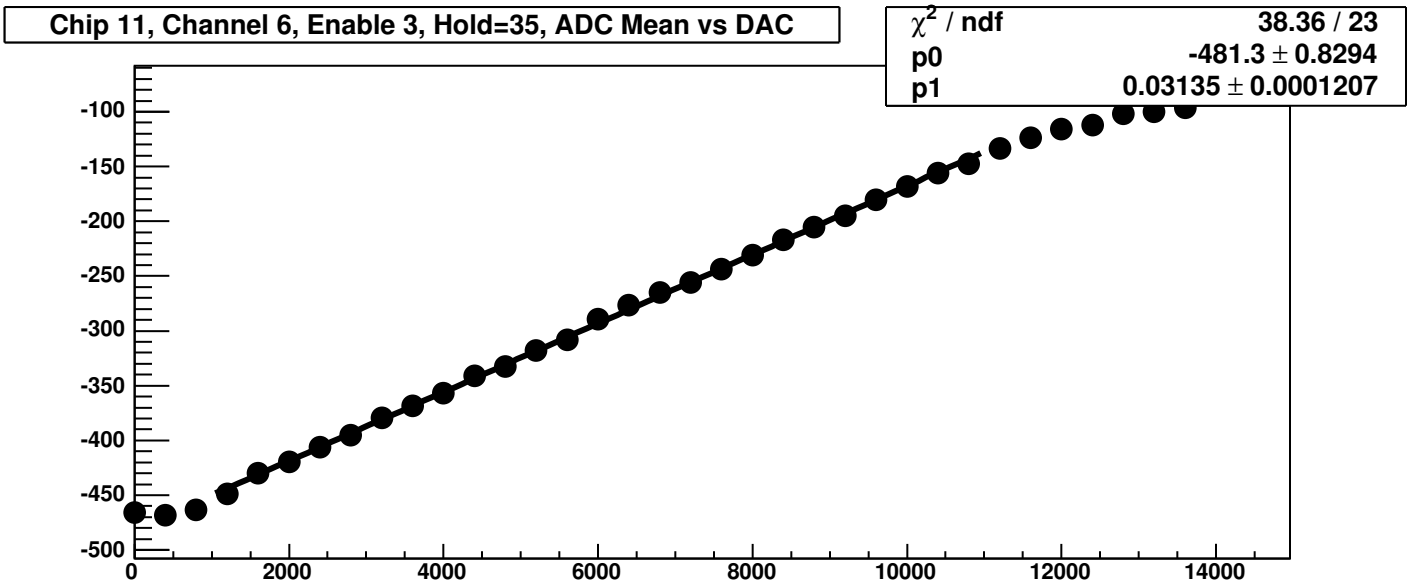
Chip 11, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC

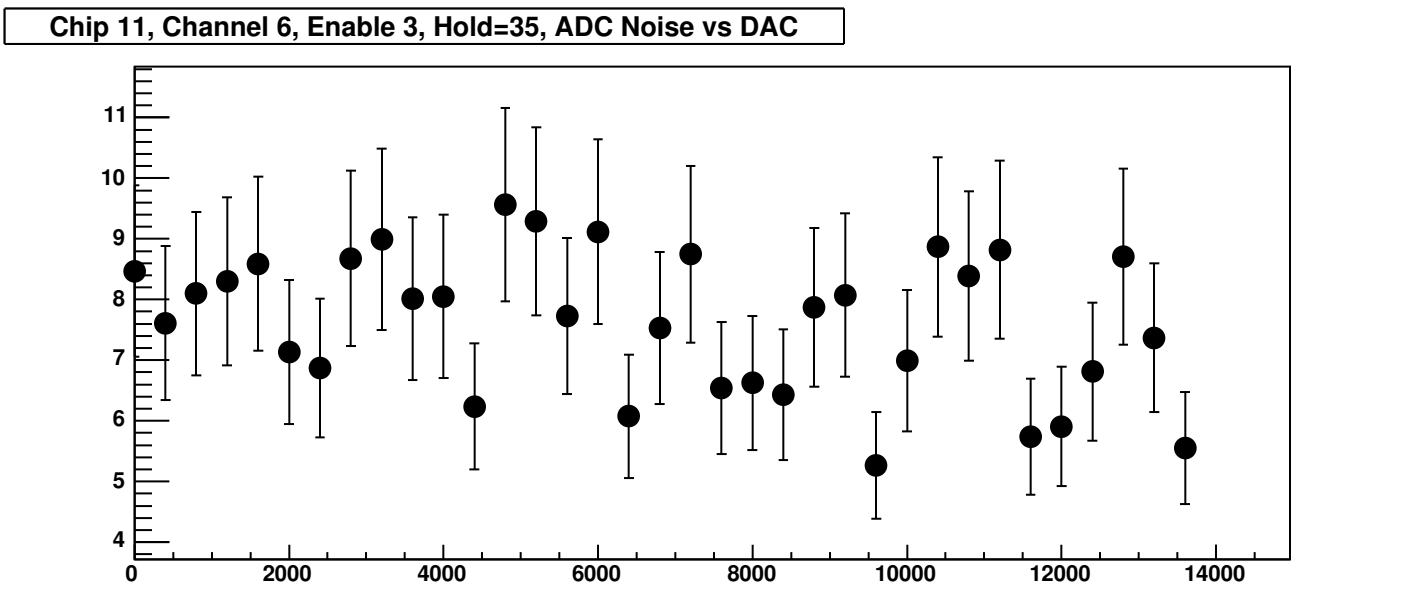


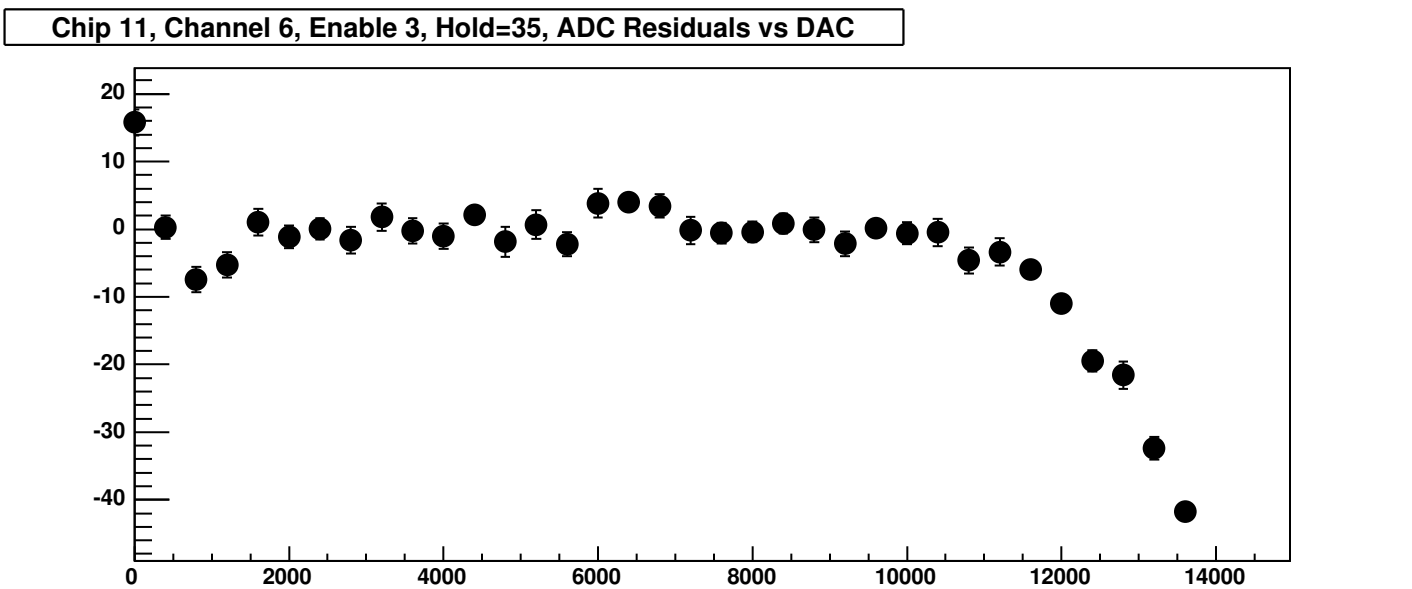

Chip 11, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 53.55 / 23
p0 -480.3 ± 0.7174
p1 0.02475 ± 0.0001156
-150
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

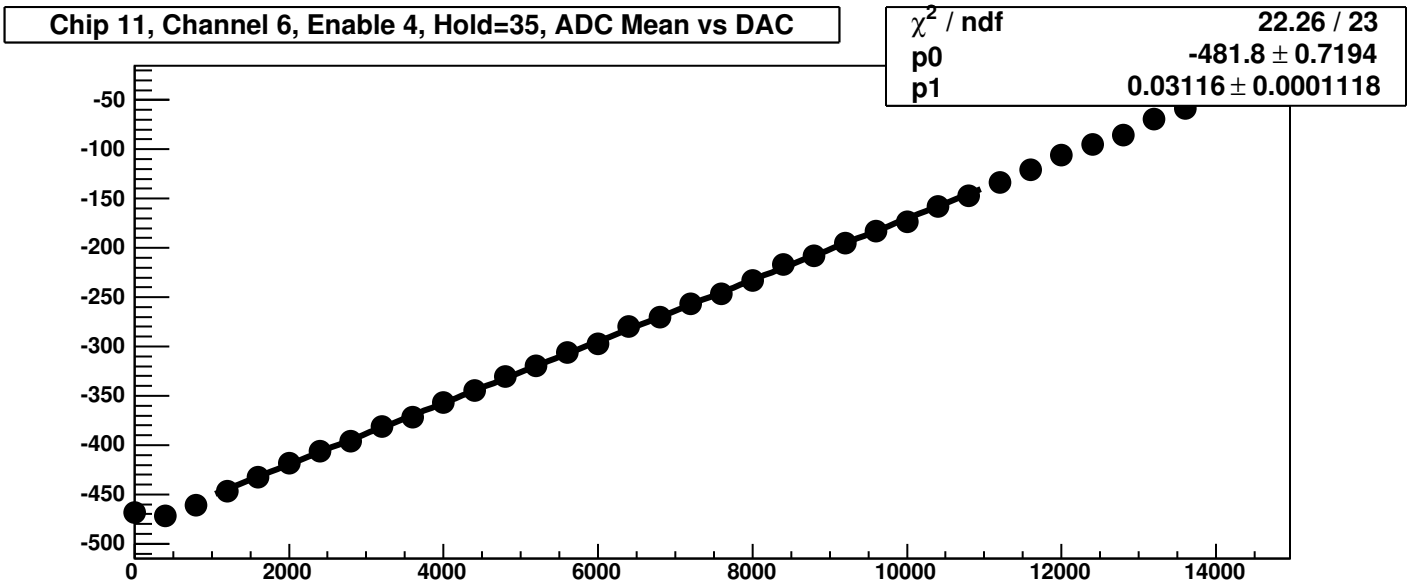

Chip 11, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

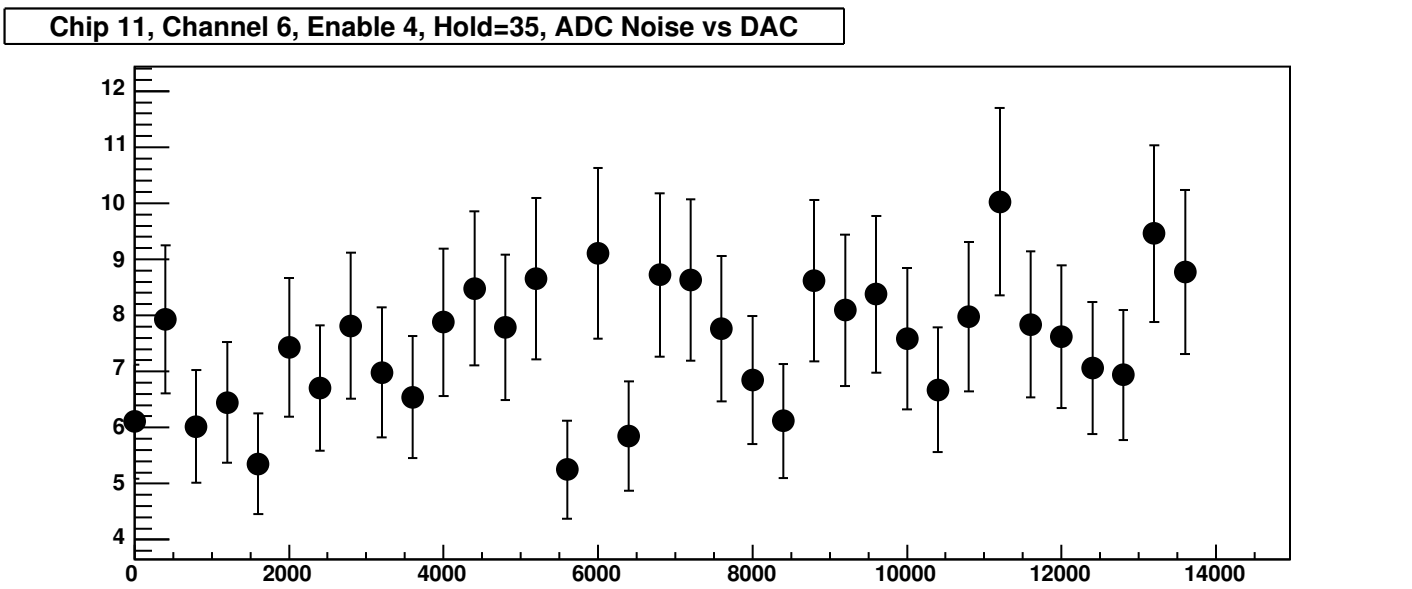

Chip 11, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

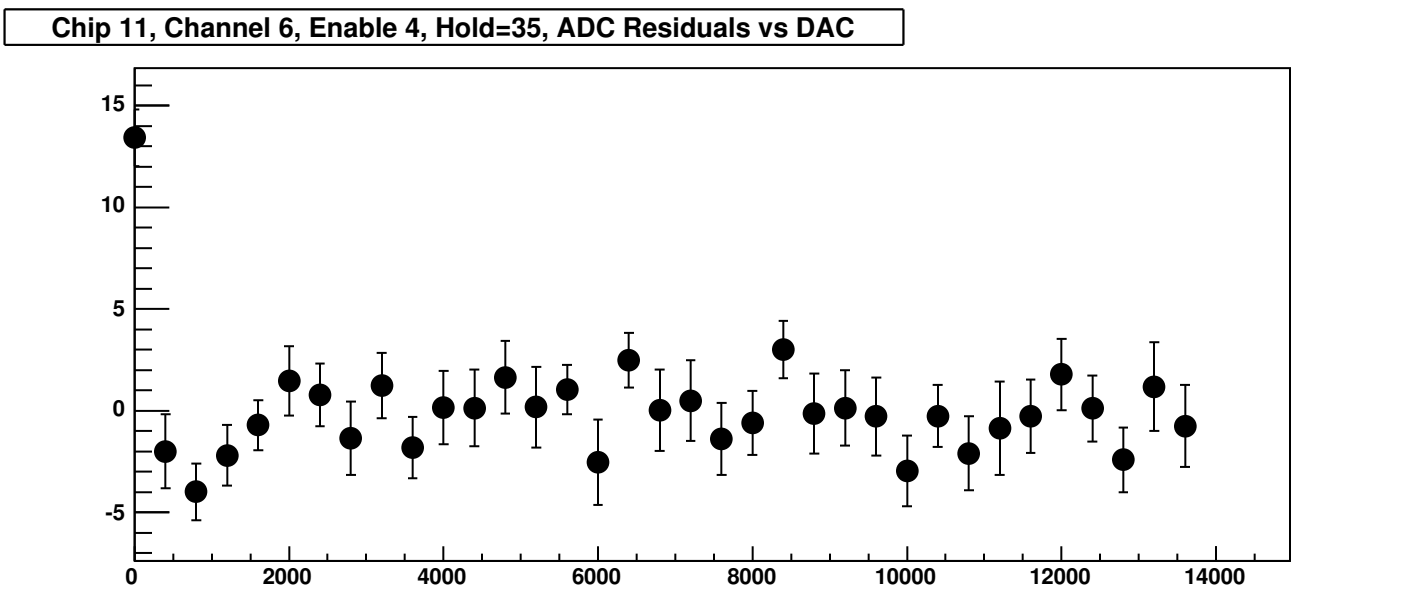

Chip 11, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.36 / 23
p0 -481.3 ± 0.8294
p1 0.03135 ± 0.0001207
-100
-150
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

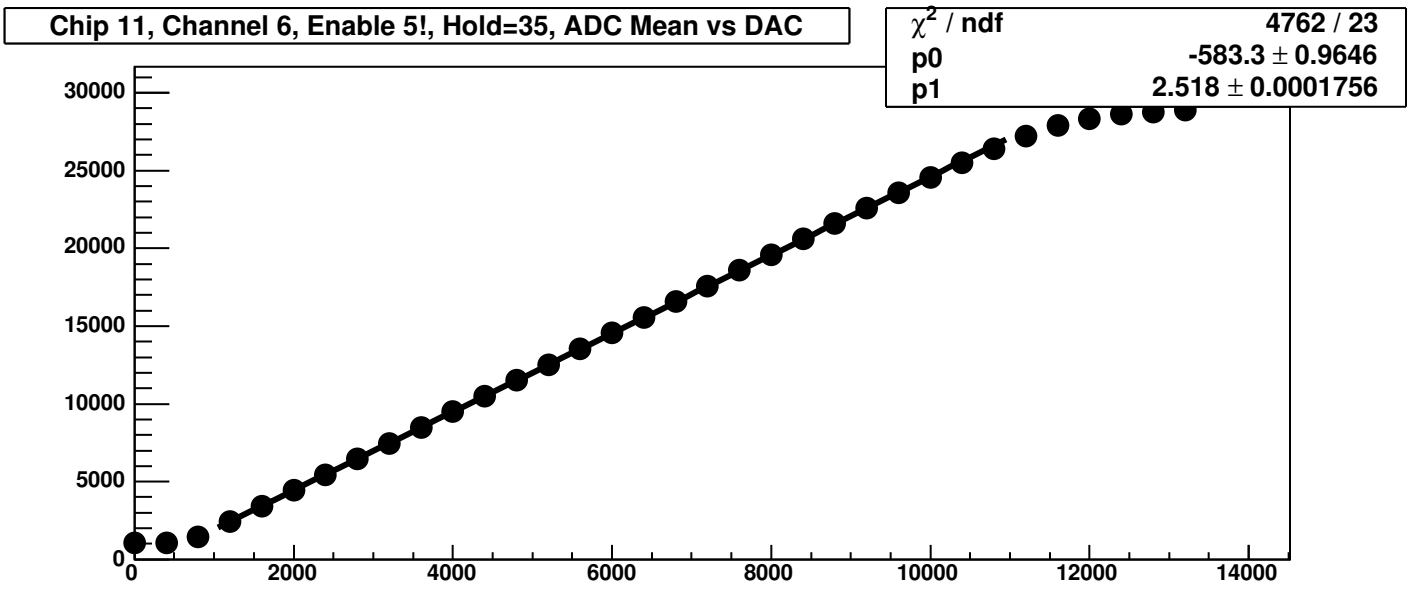

Chip 11, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

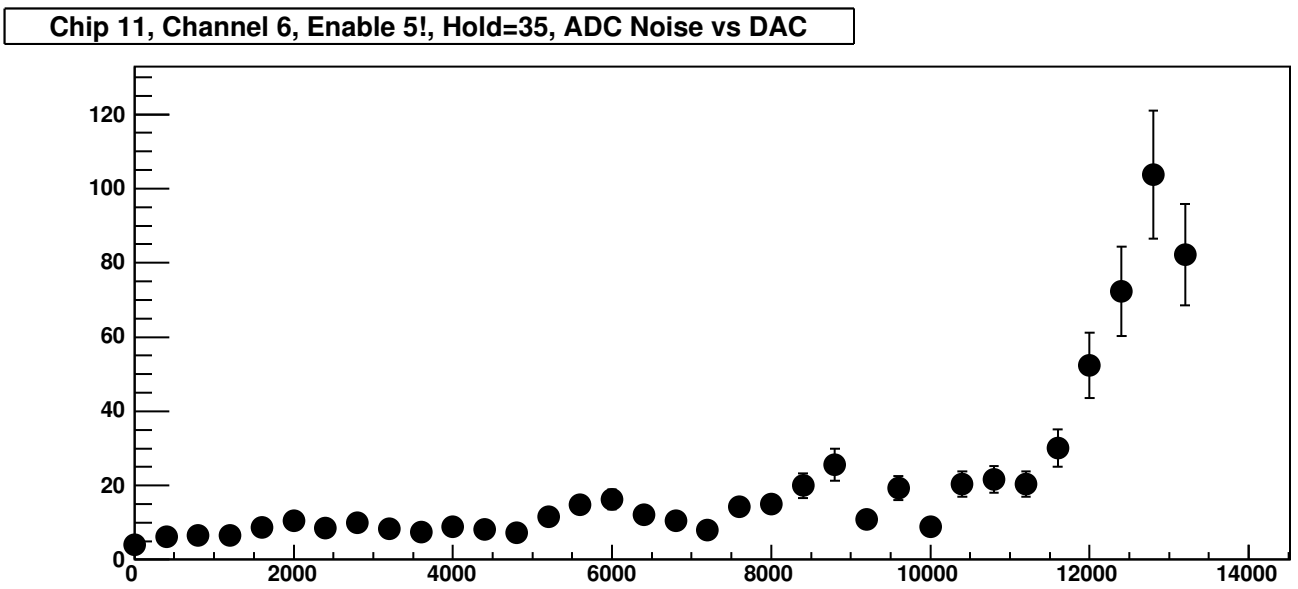

Chip 11, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

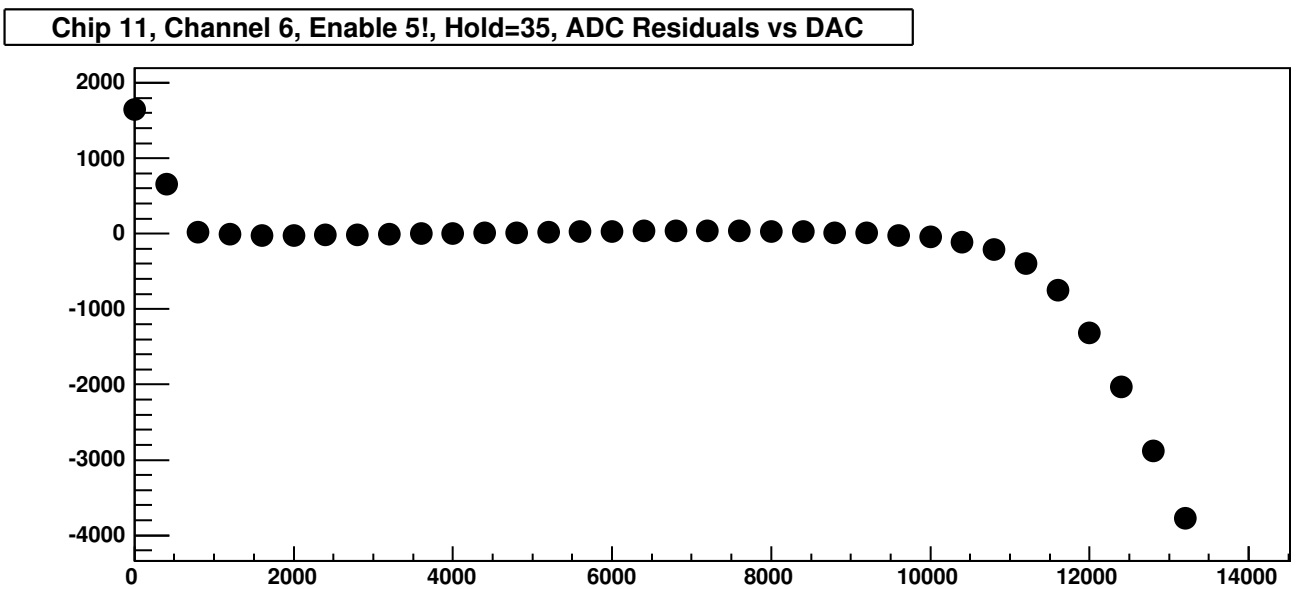

Chip 11, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 22.26 / 23
p0 -481.8 ± 0.7194
p1 0.03116 ± 0.0001118
-50
-100
-150
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

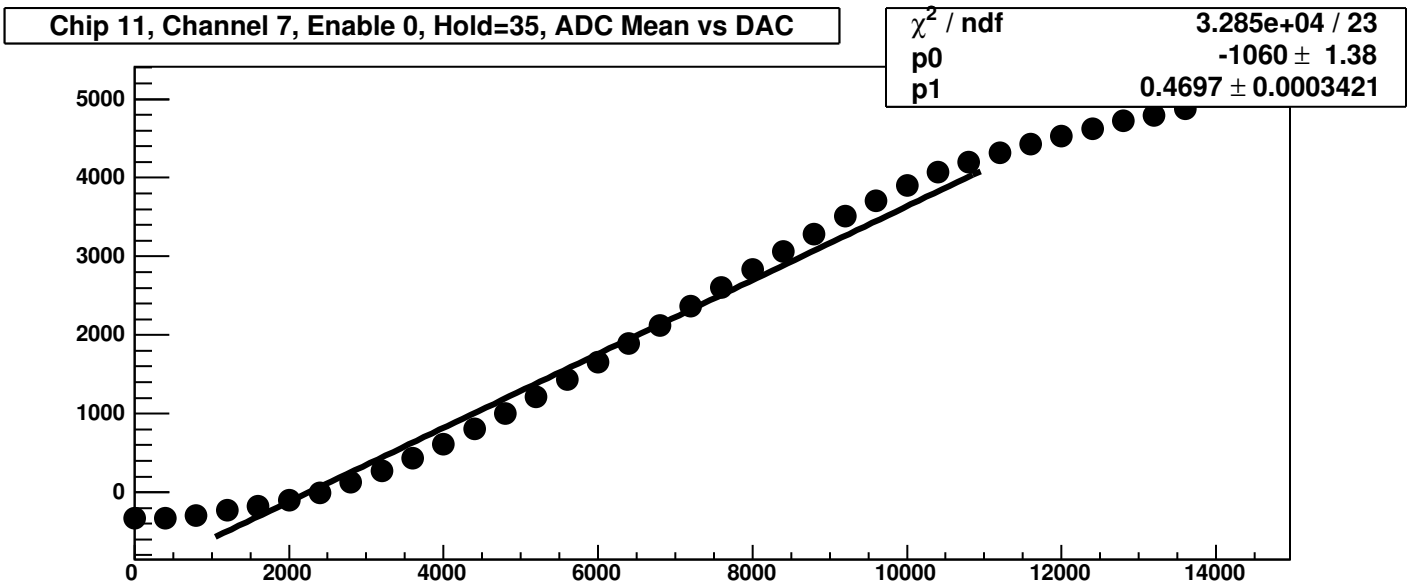

Chip 11, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

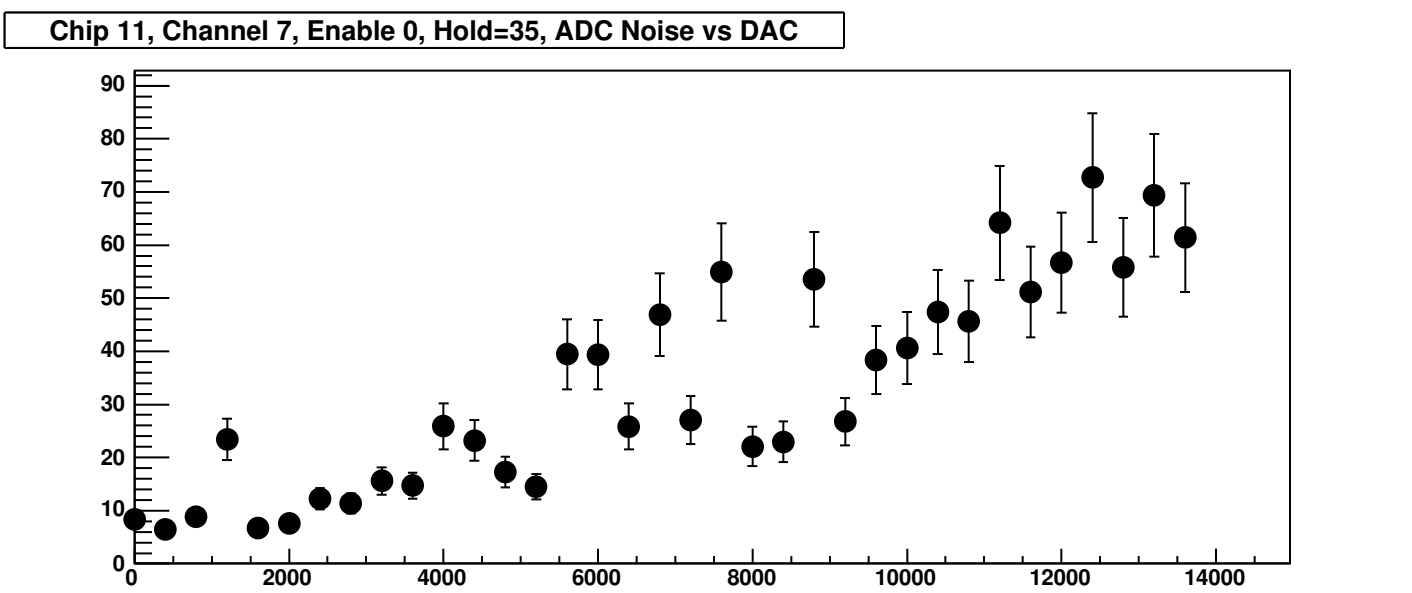

Chip 11, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

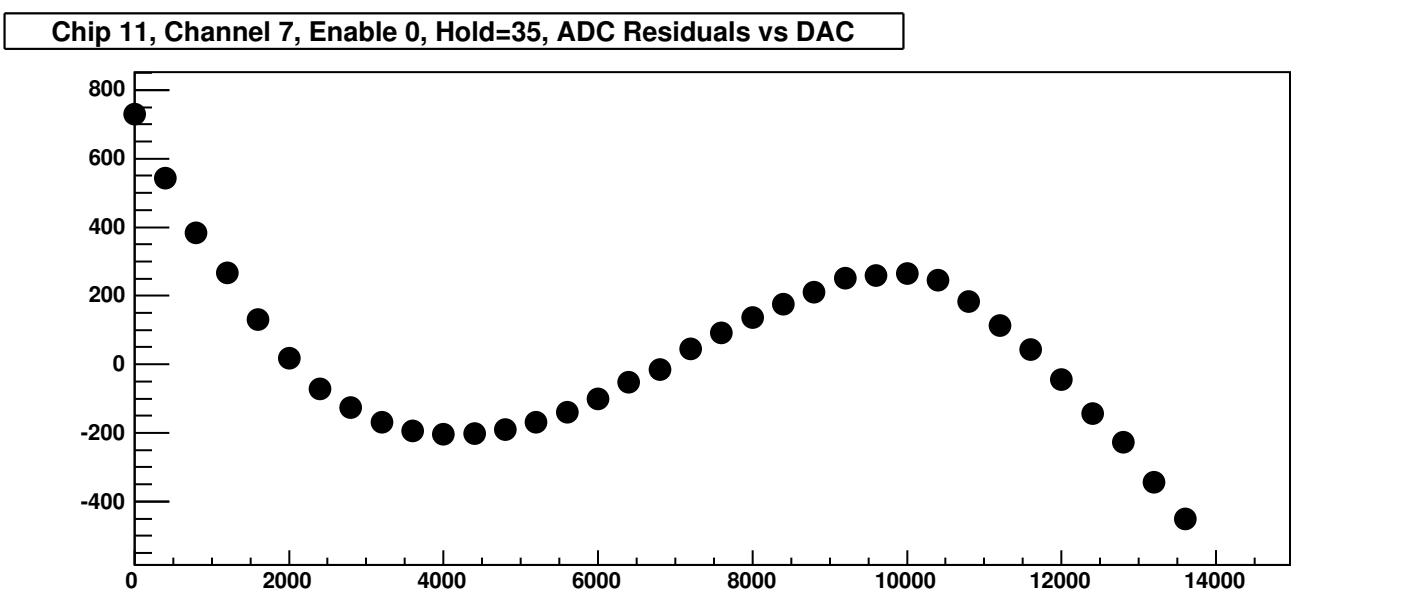

Chip 11, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4762 / 23
p0 -583.3 ± 0.9646
p1 2.518 ± 0.0001756
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

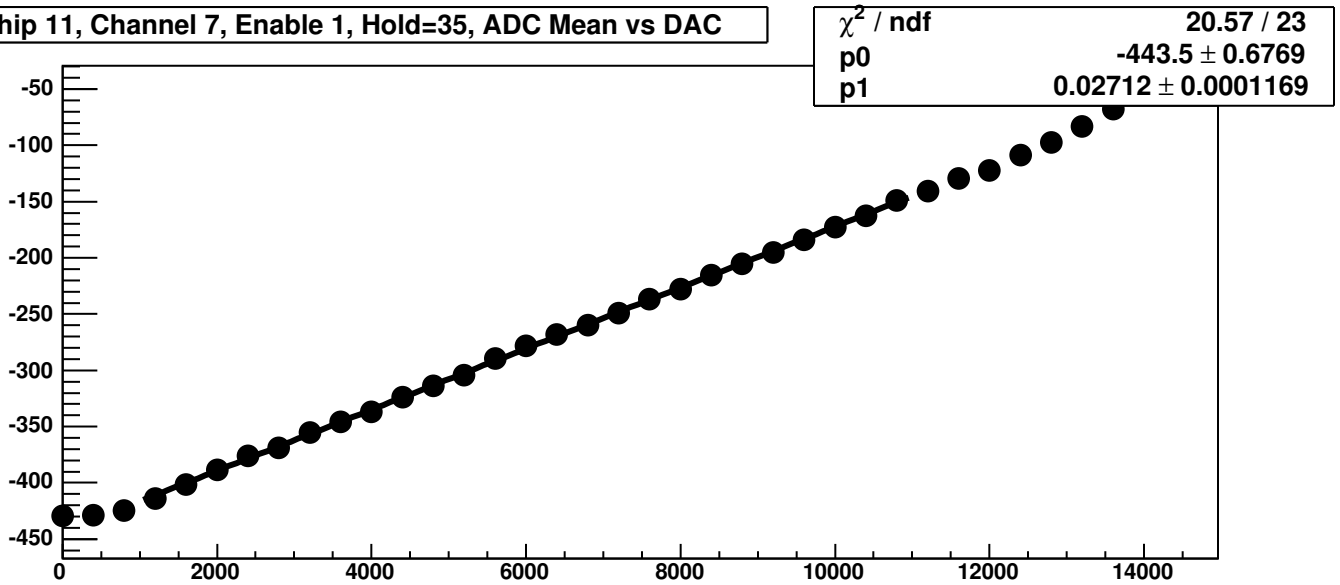

Chip 11, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
-3000
-4000
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.285e+04 / 23
p0 -1060 ± 1.38
p1 0.4697 ± 0.0003421
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

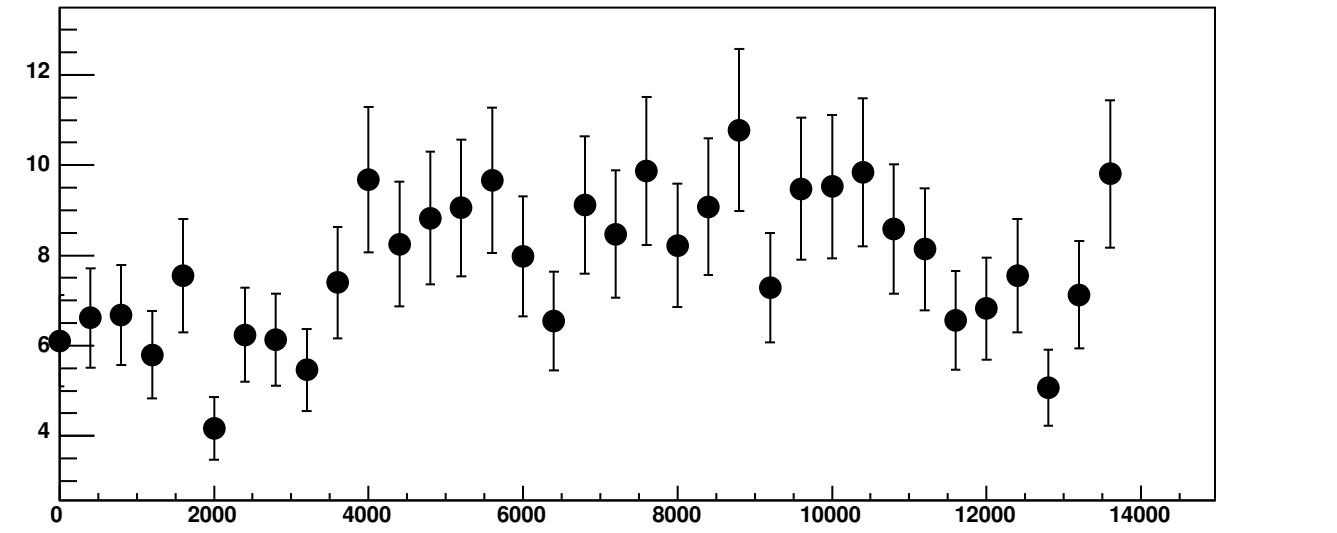

Chip 11, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

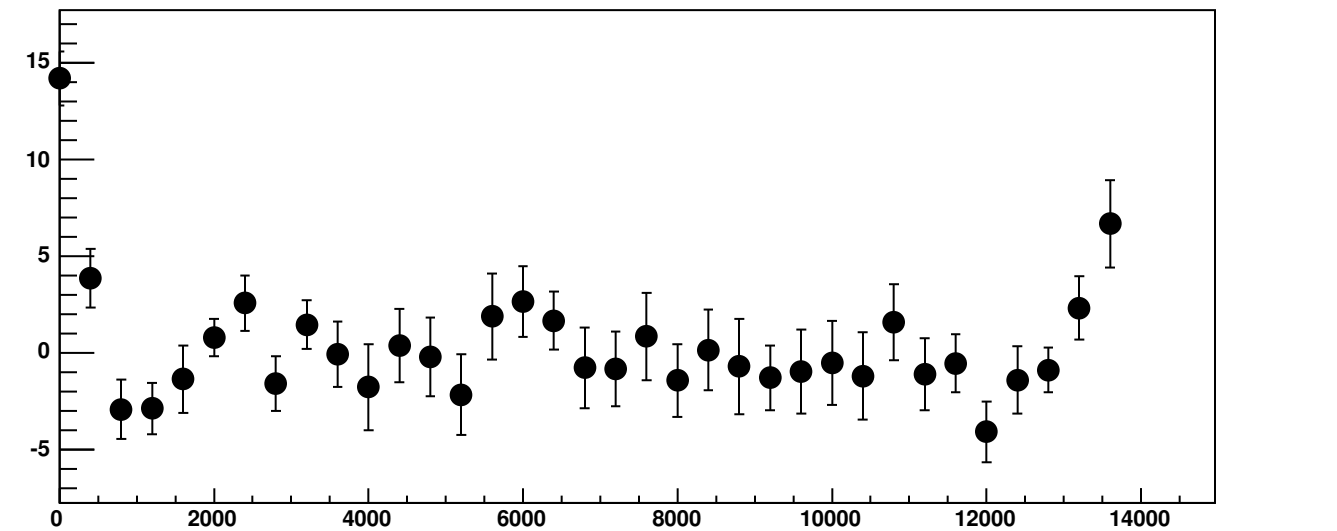
Chip 11, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

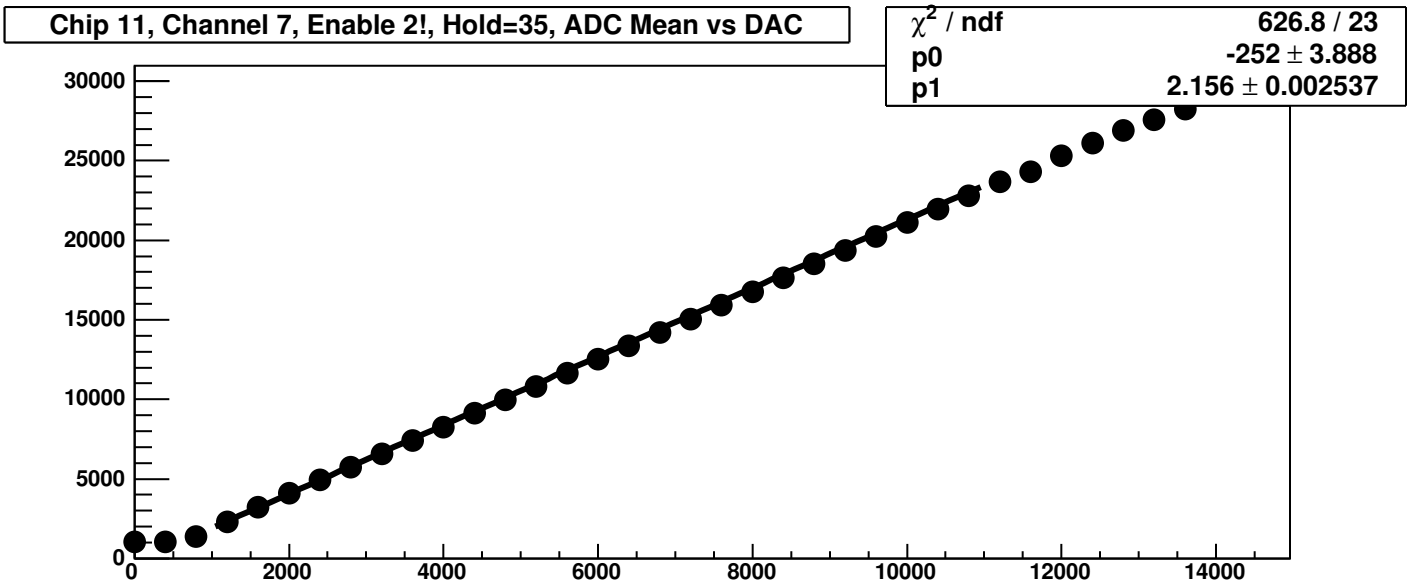


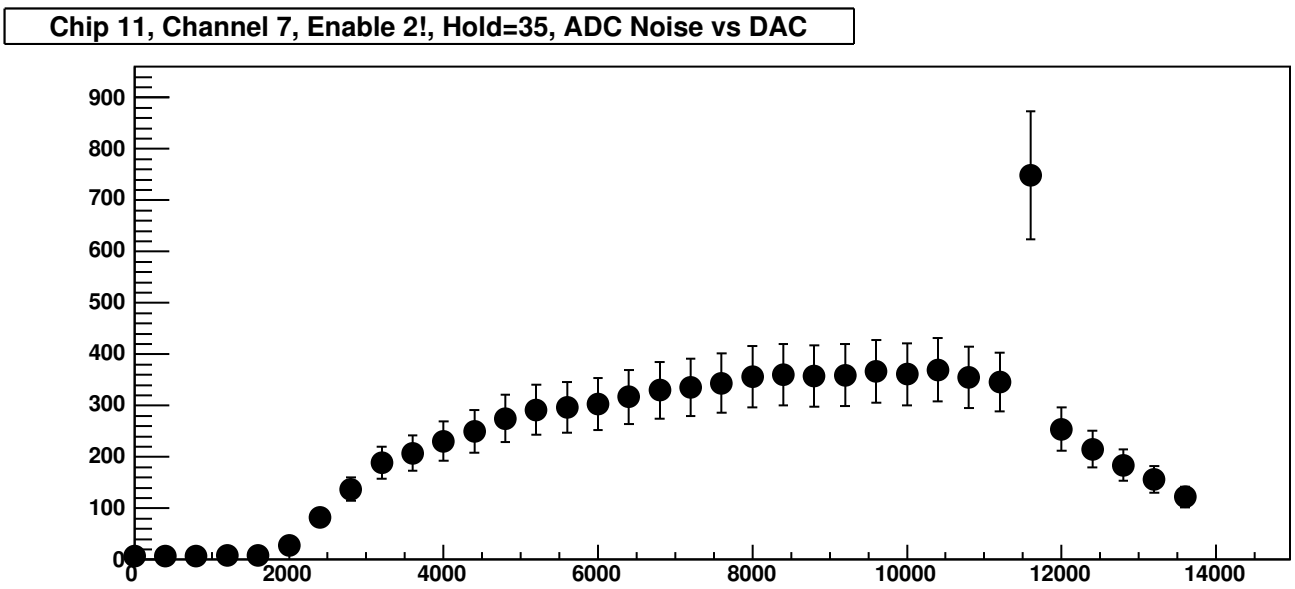
Chip 11, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC

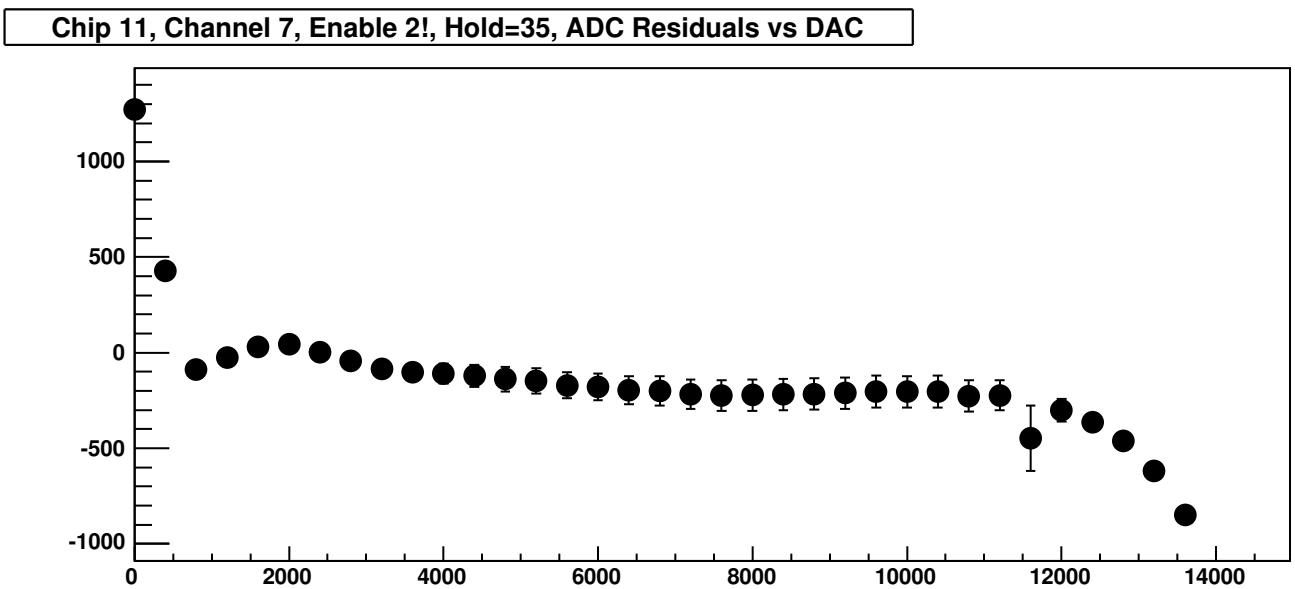


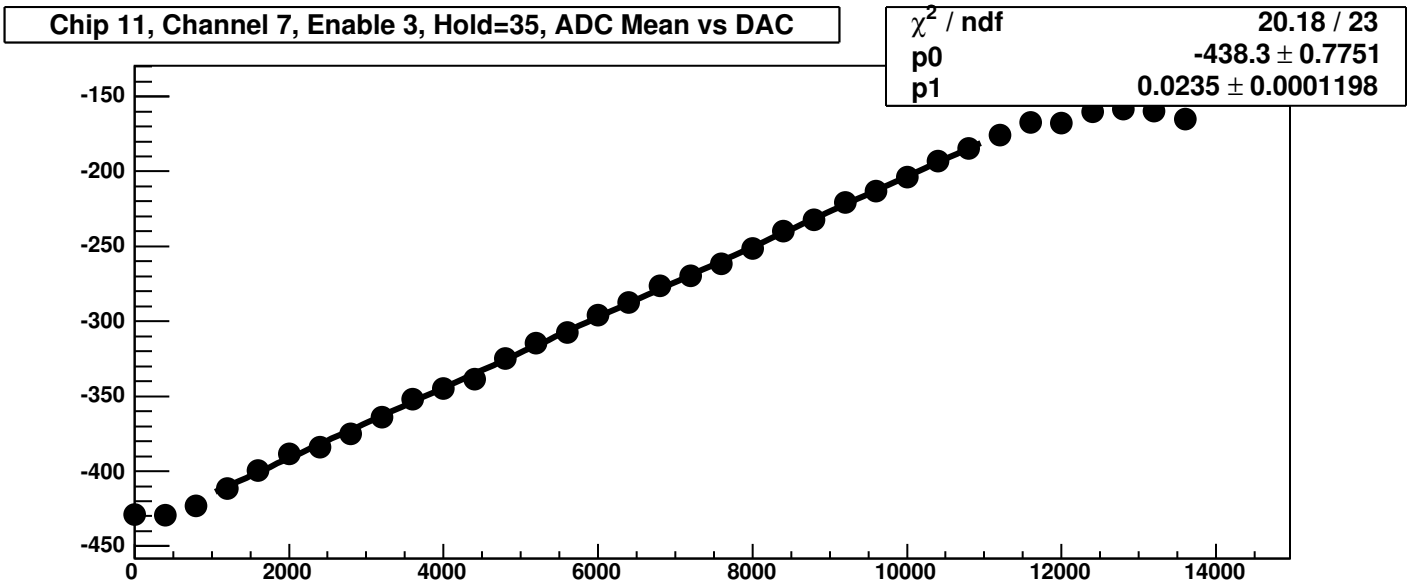
Chip 11, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

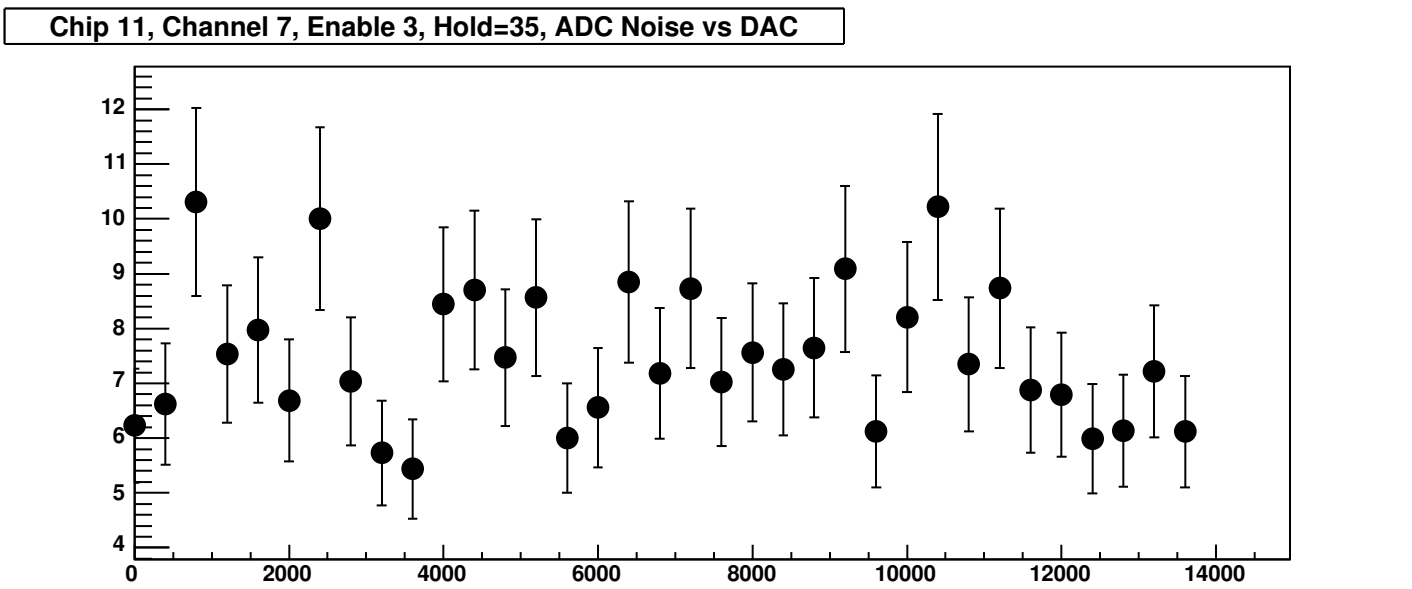


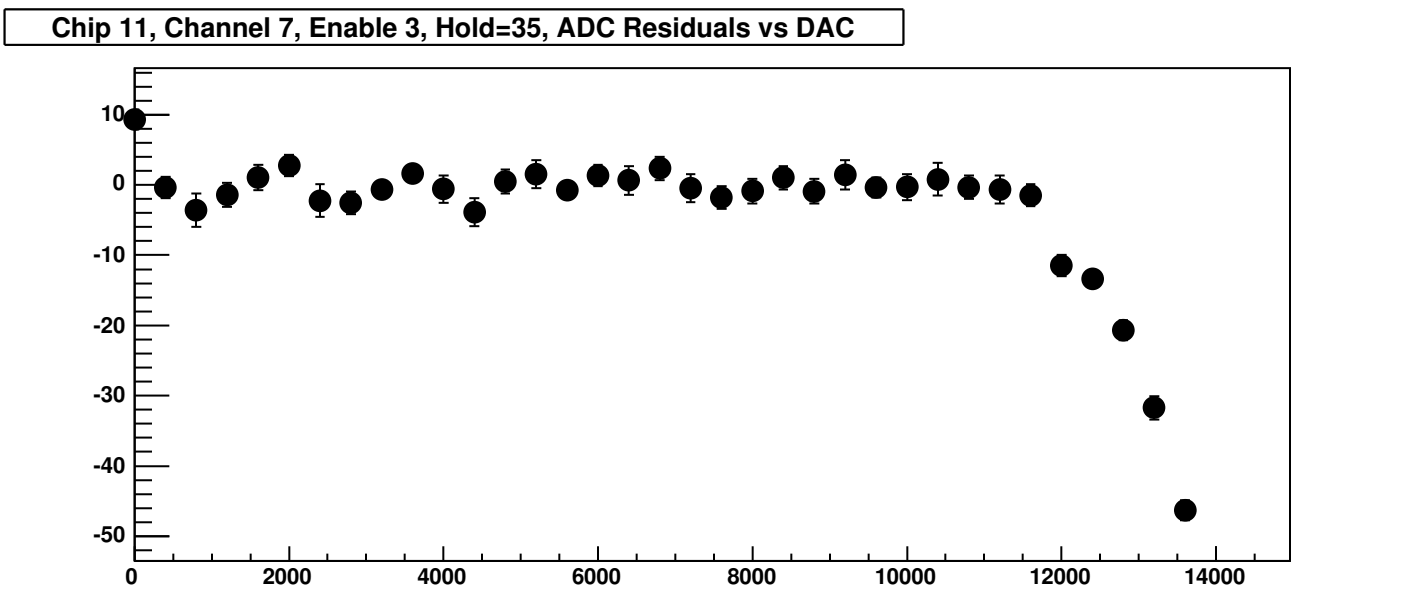

Chip 11, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 626.8 / 23
p0 -252 ± 3.888
p1 2.156 ± 0.002537
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

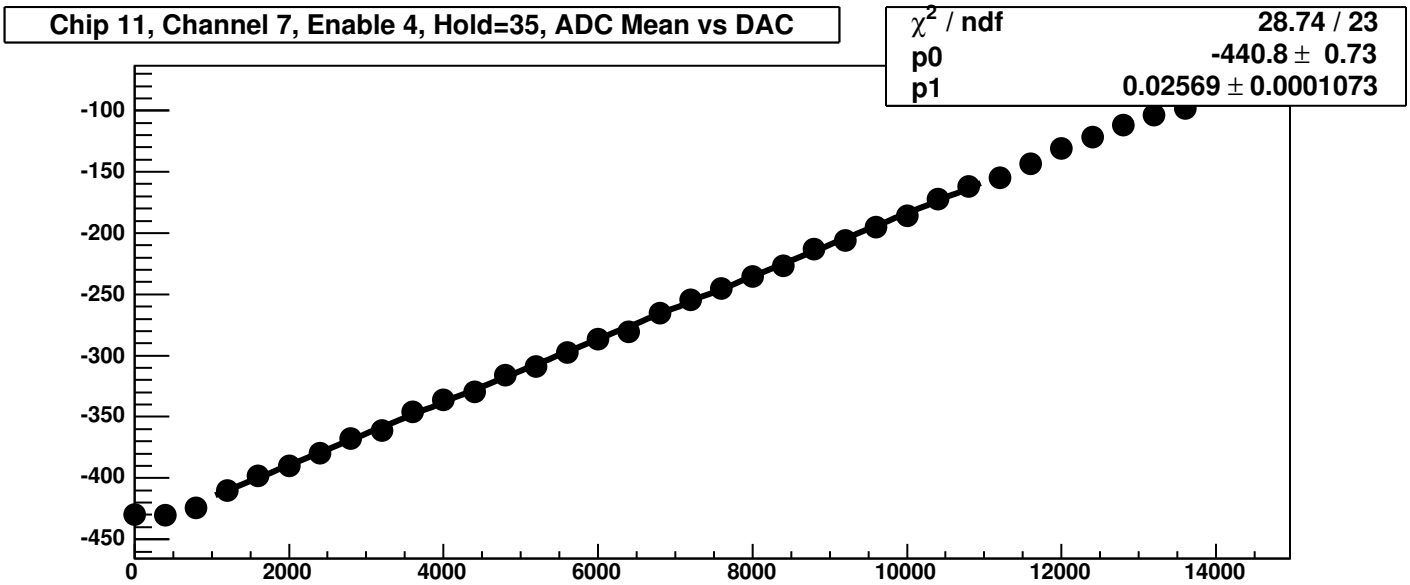

Chip 11, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
900
800
700
600
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

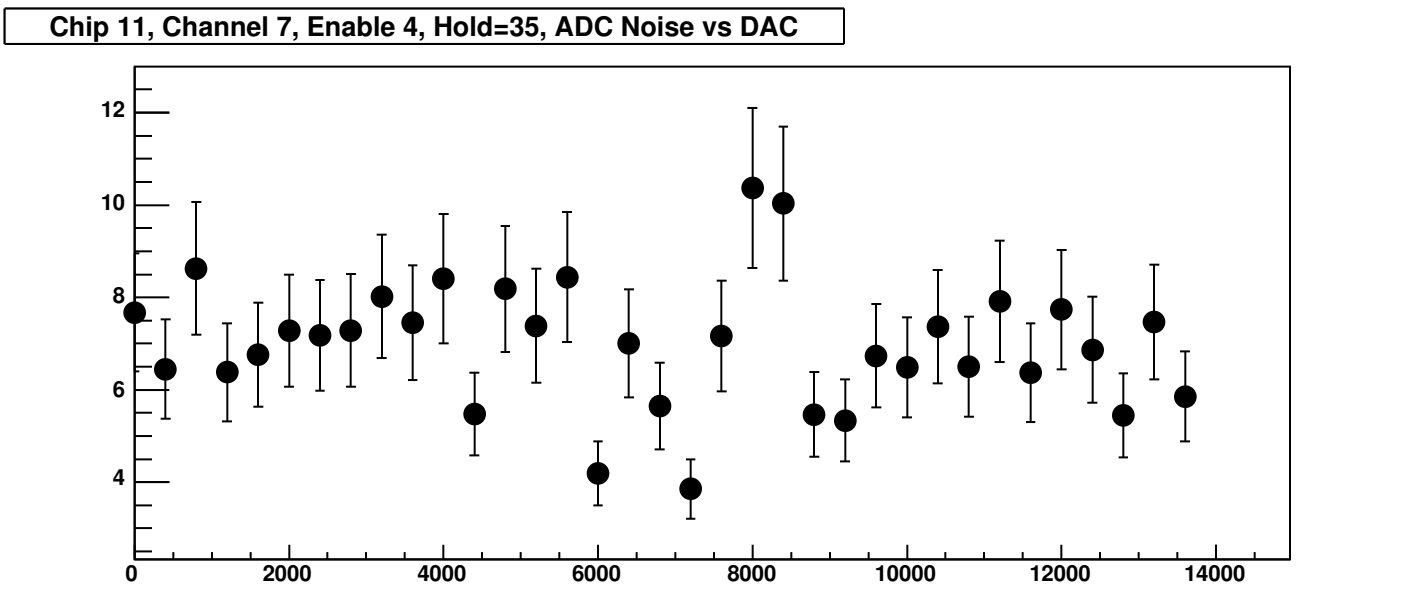

Chip 11, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

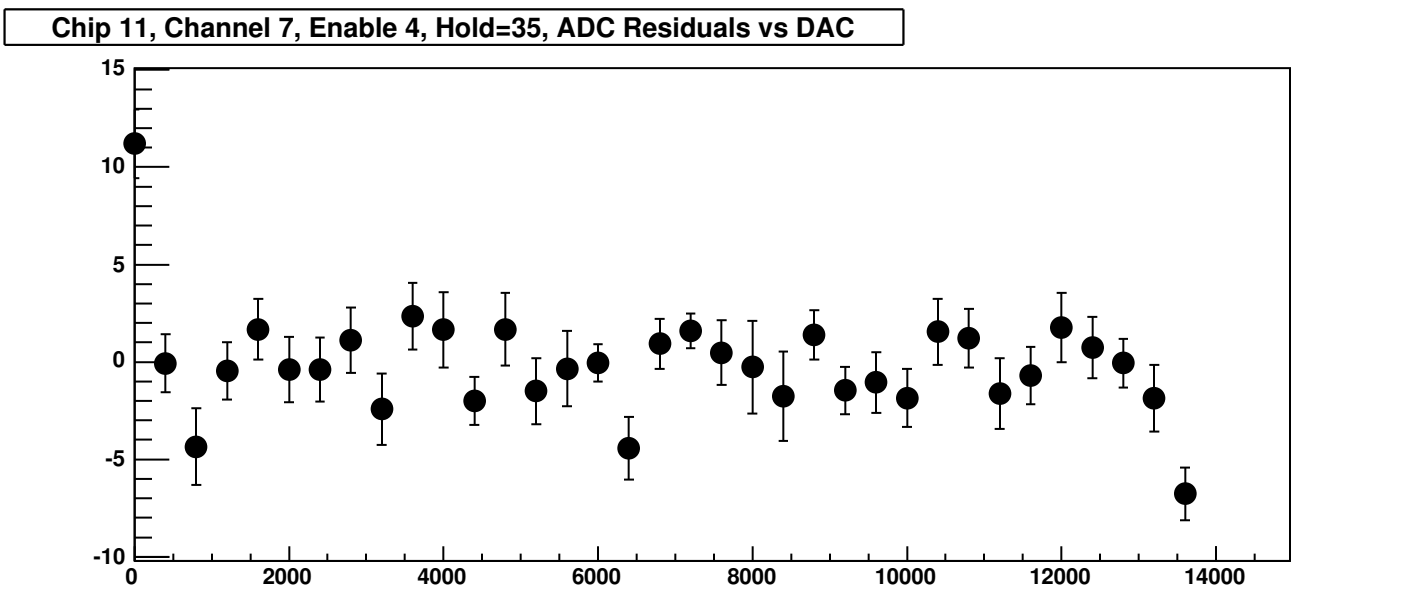

Chip 11, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 20.18 / 23
p0 -438.3 ± 0.7751
p1 0.0235 ± 0.0001198
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

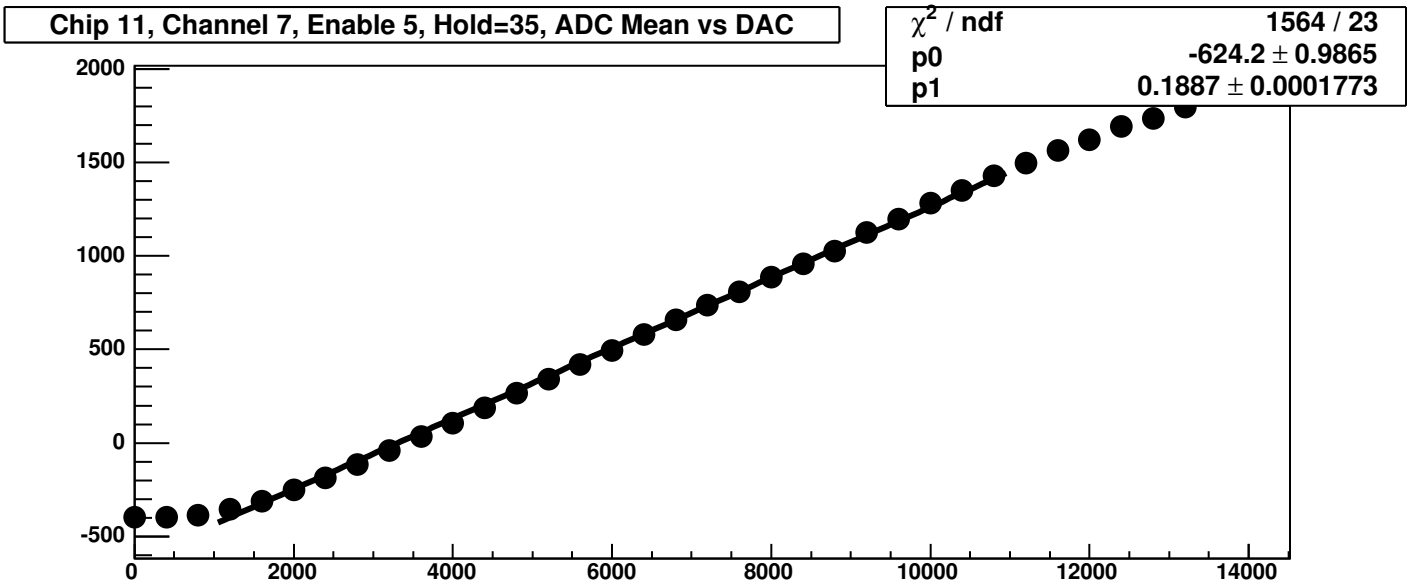

Chip 11, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

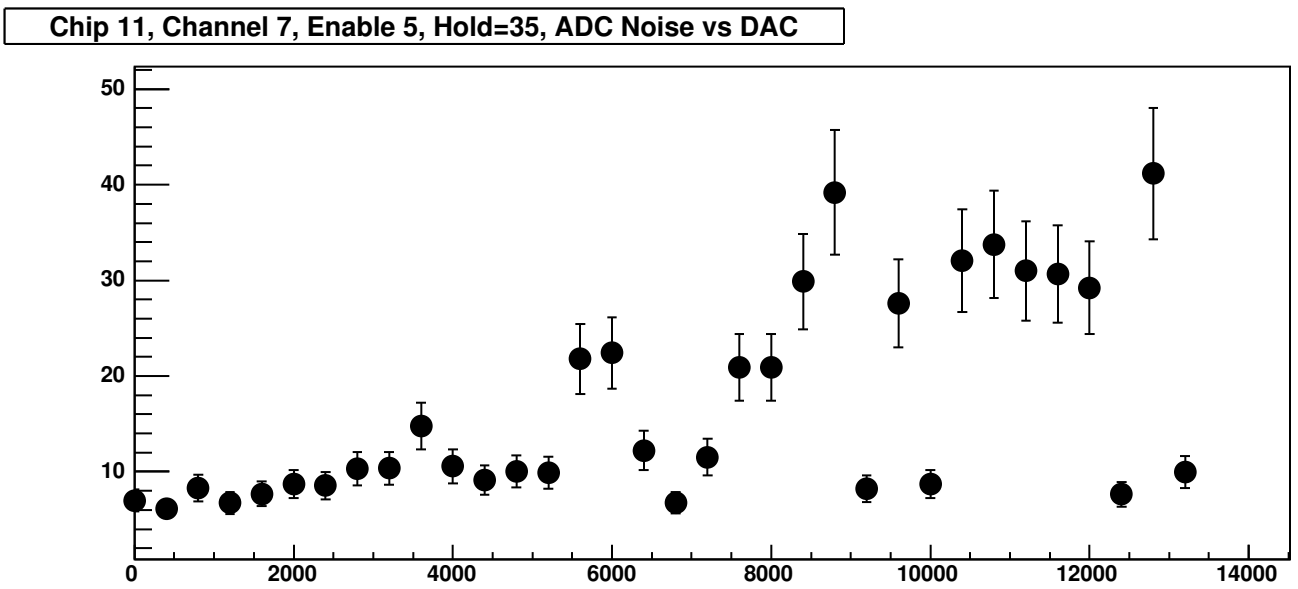

Chip 11, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
0
2000
4000
6000
8000
10000
12000
14000

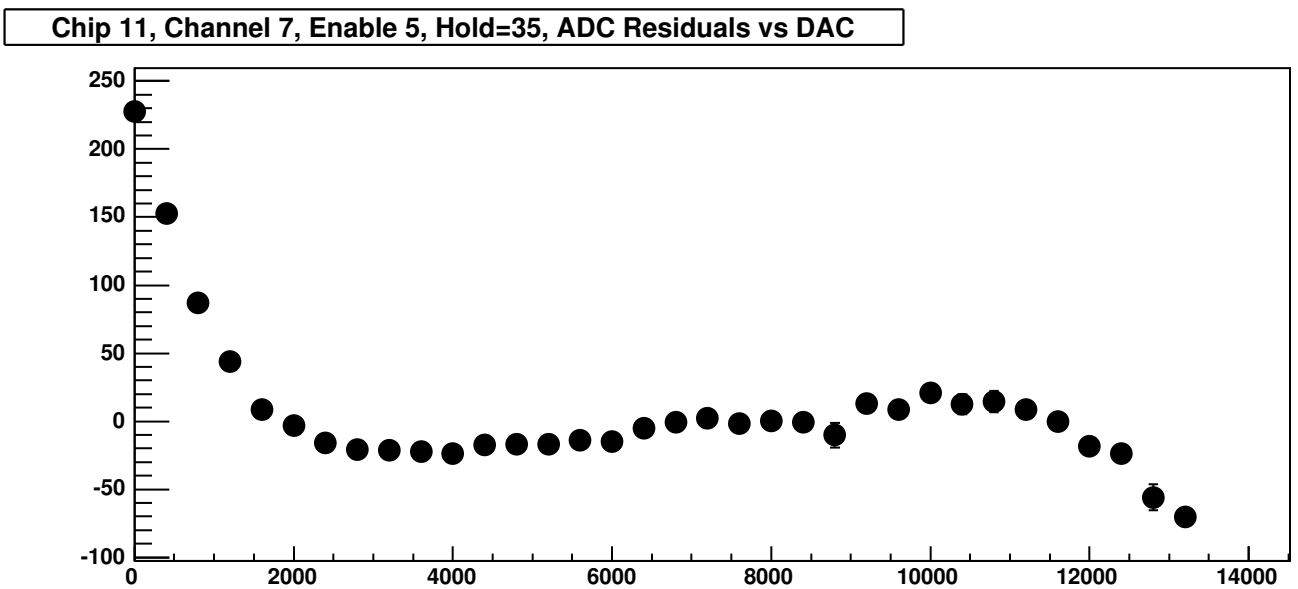

Chip 11, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 28.74 / 23
p0 -440.8 ± 0.73
p1 0.02569 ± 0.0001073
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

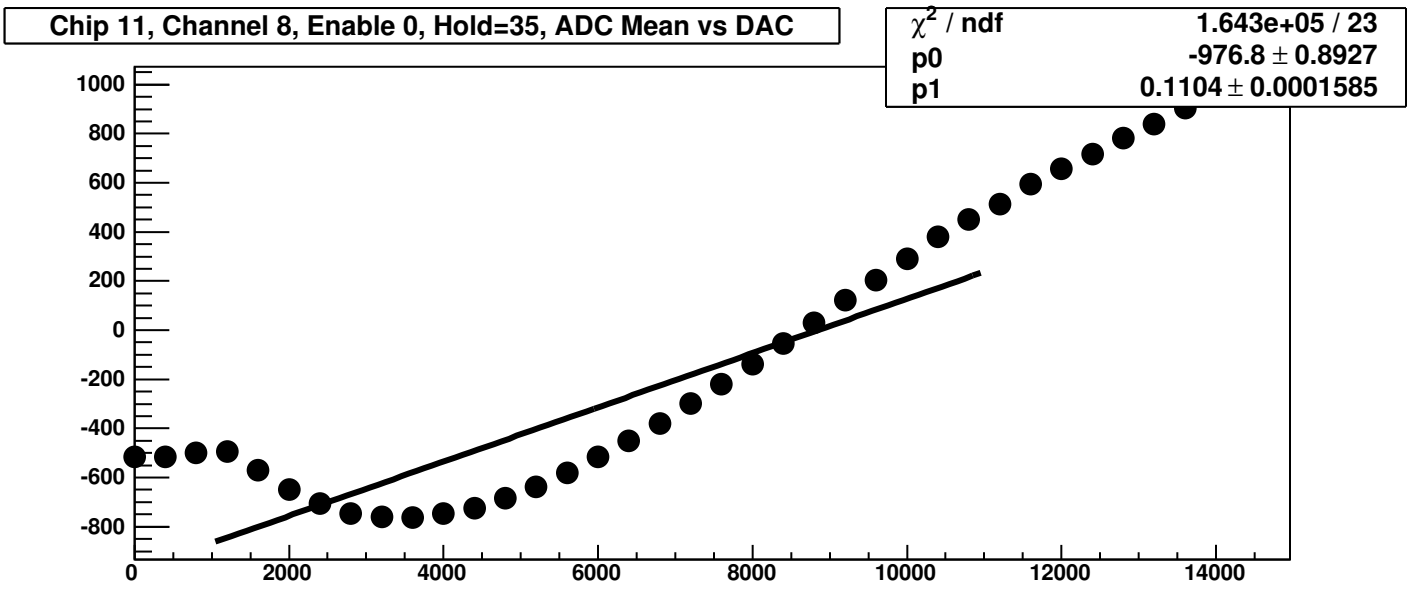

Chip 11, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

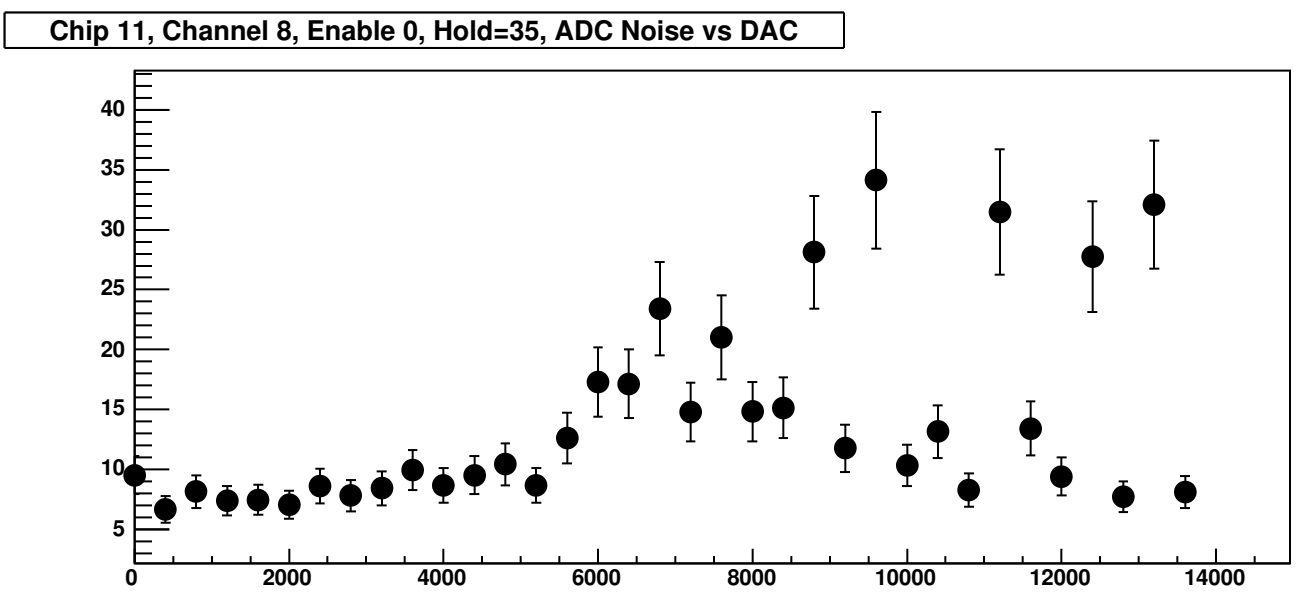

Chip 11, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

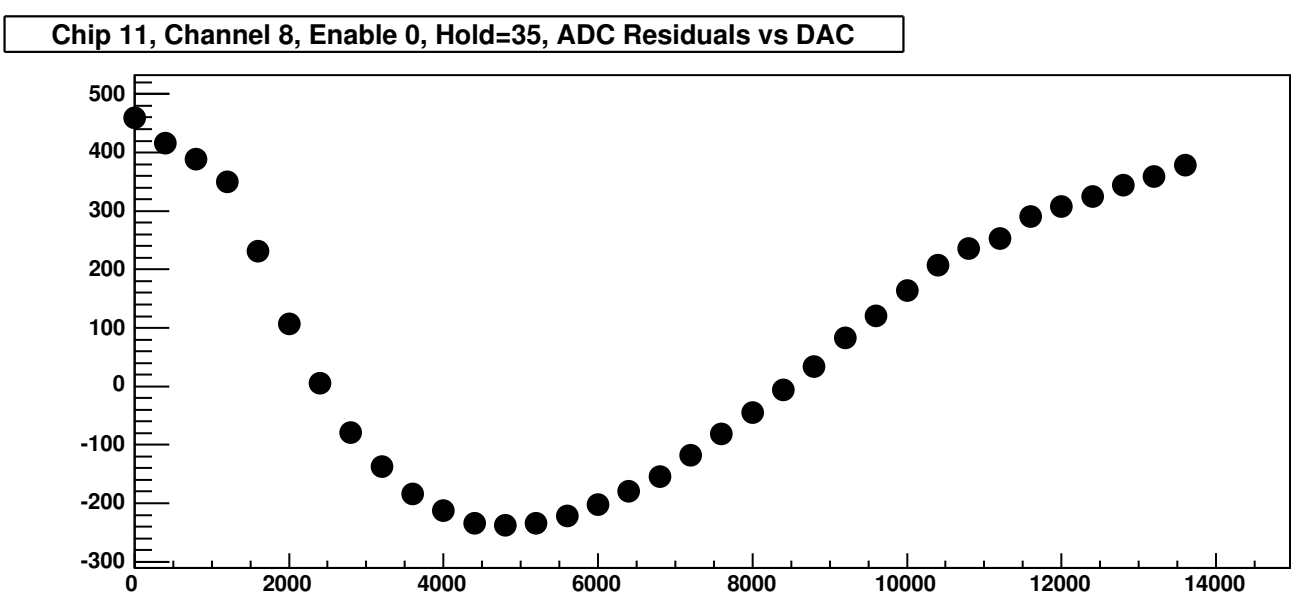

Chip 11, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1564 / 23
p0 -624.2 ± 0.9865
p1 0.1887 ± 0.0001773
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

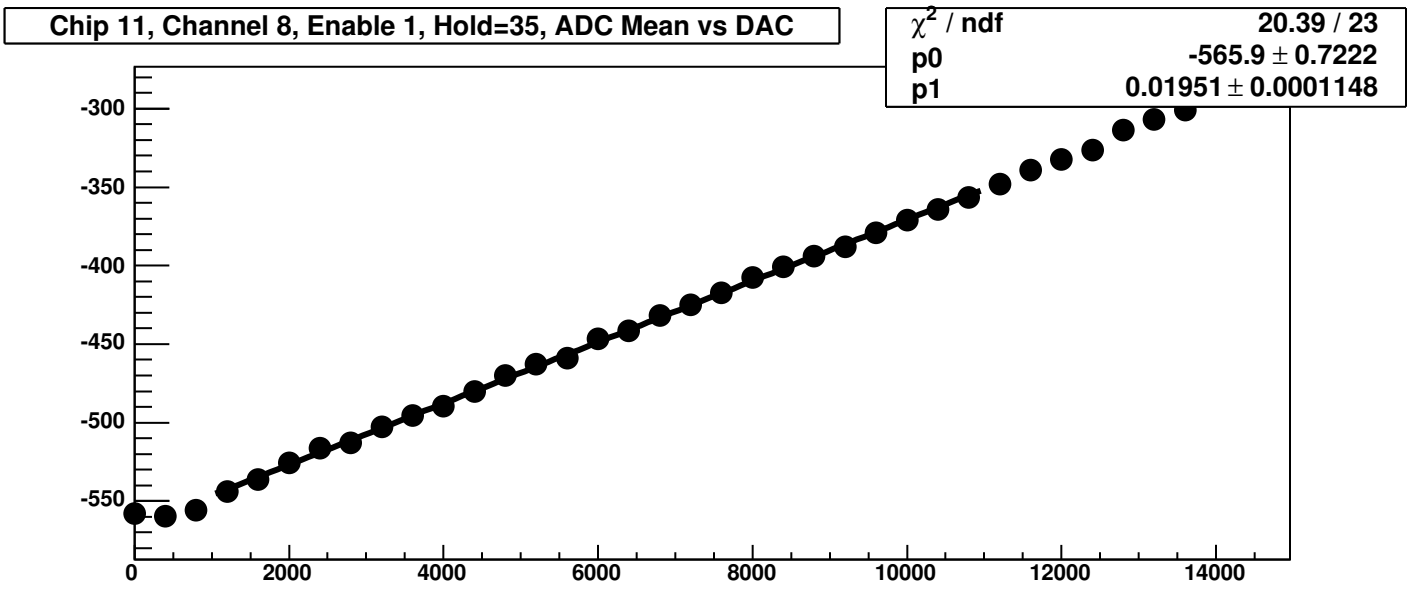

Chip 11, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

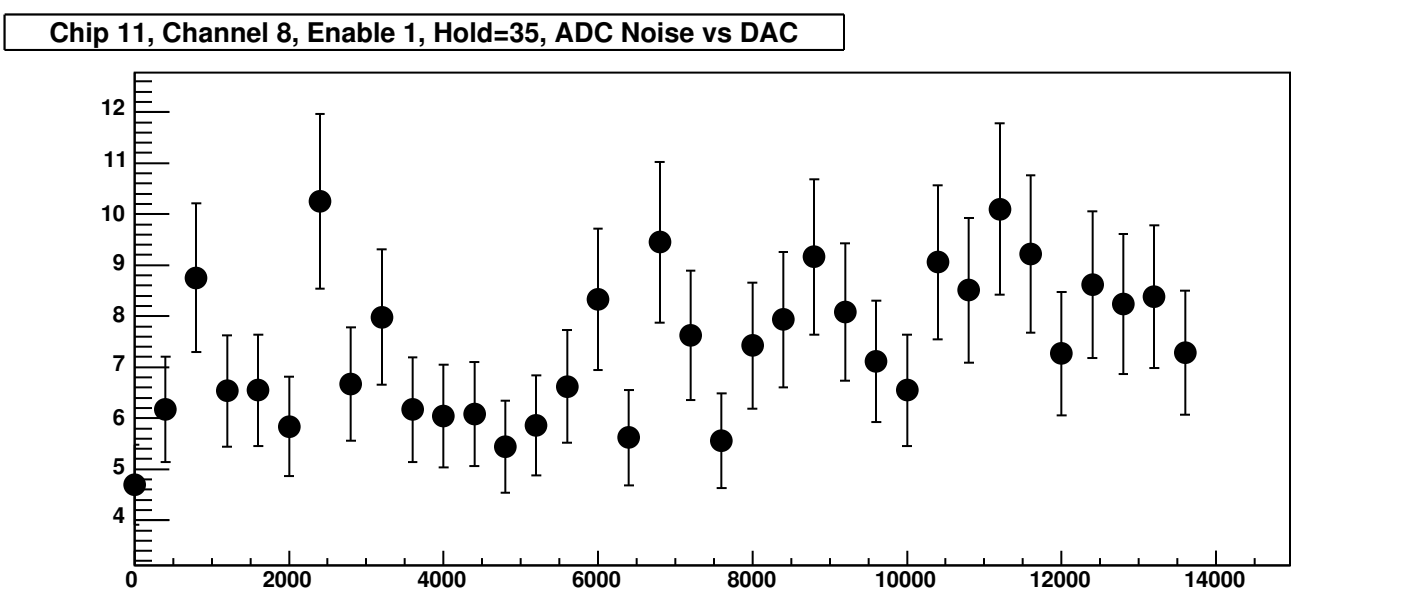

Chip 11, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

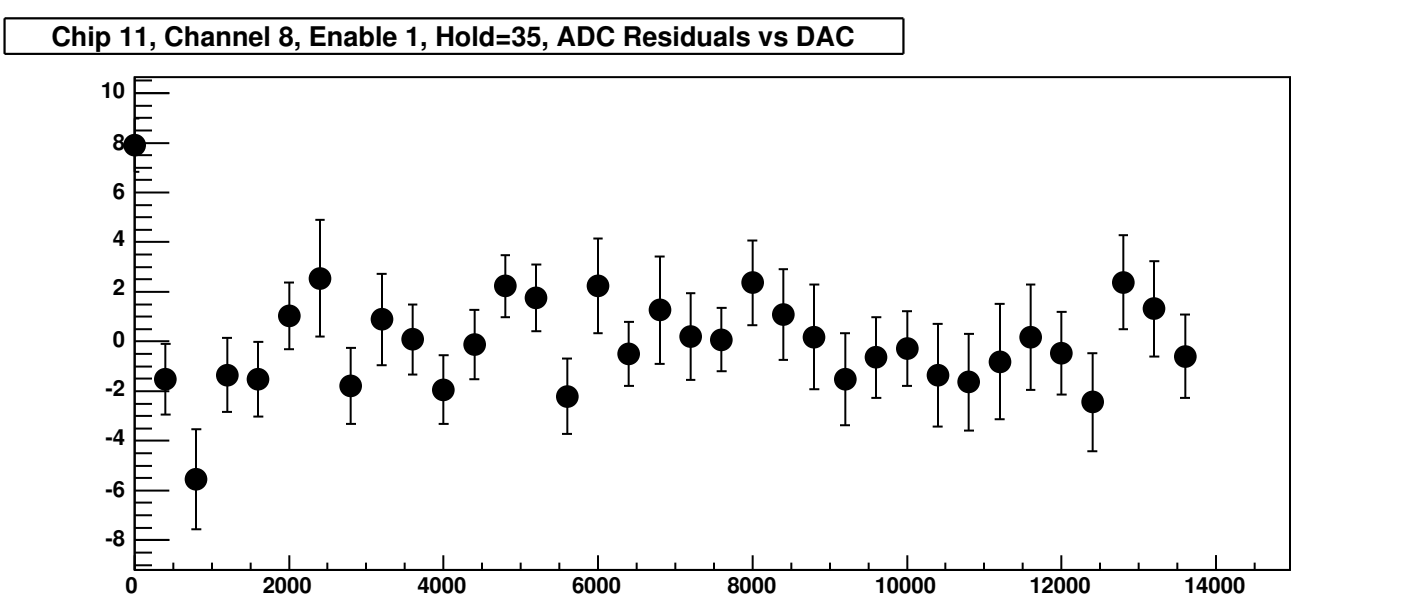

Chip 11, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.643e+05 / 23
p0 -976.8 ± 0.8927
p1 0.1104 ± 0.0001585
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

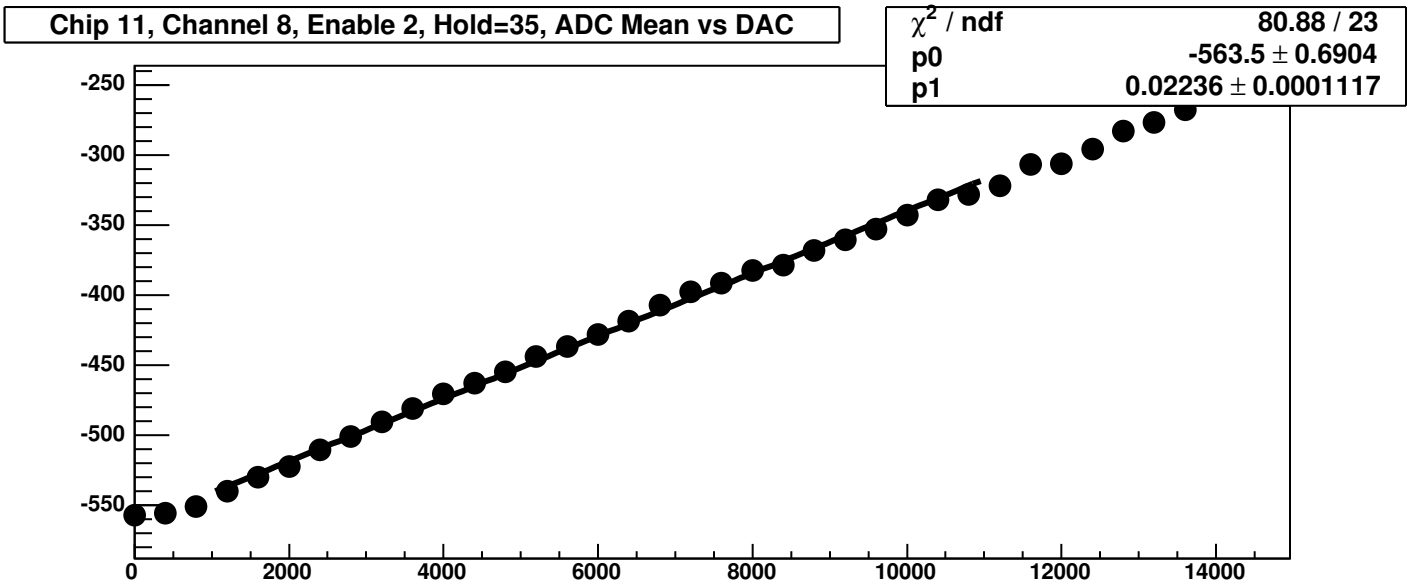

Chip 11, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

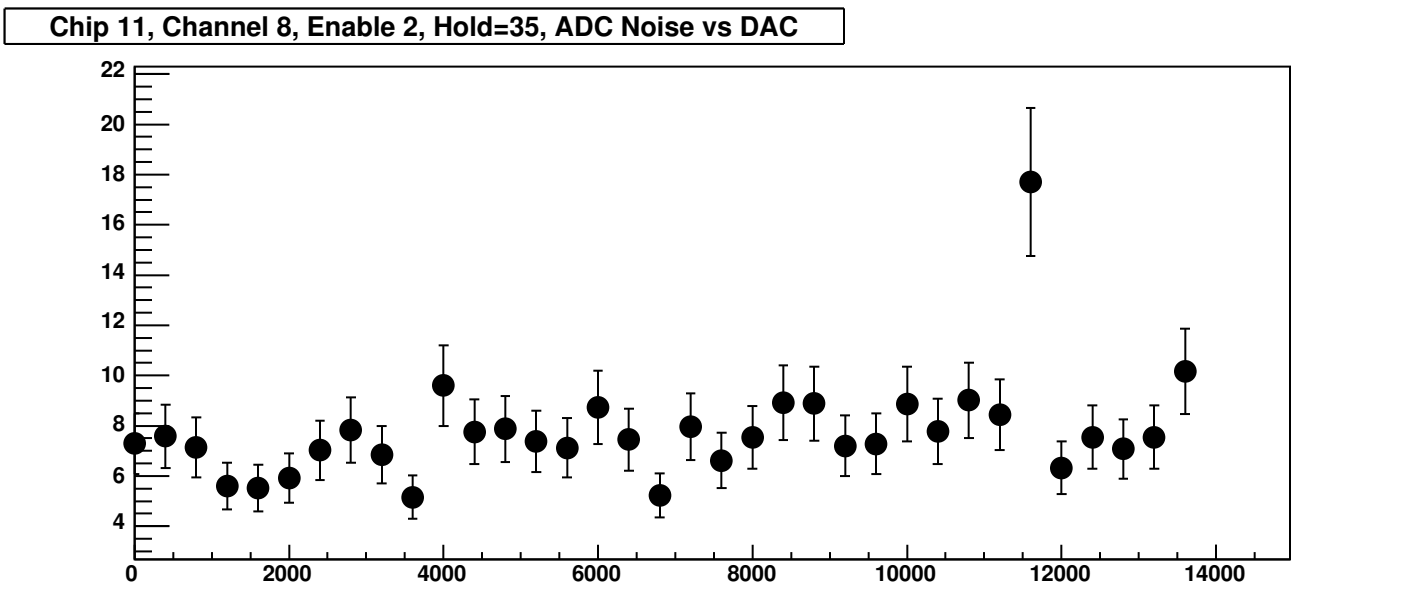

Chip 11, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
500
400
300
200
100
0
-100
-200
-300
0
2000
4000
6000
8000
10000
12000
14000

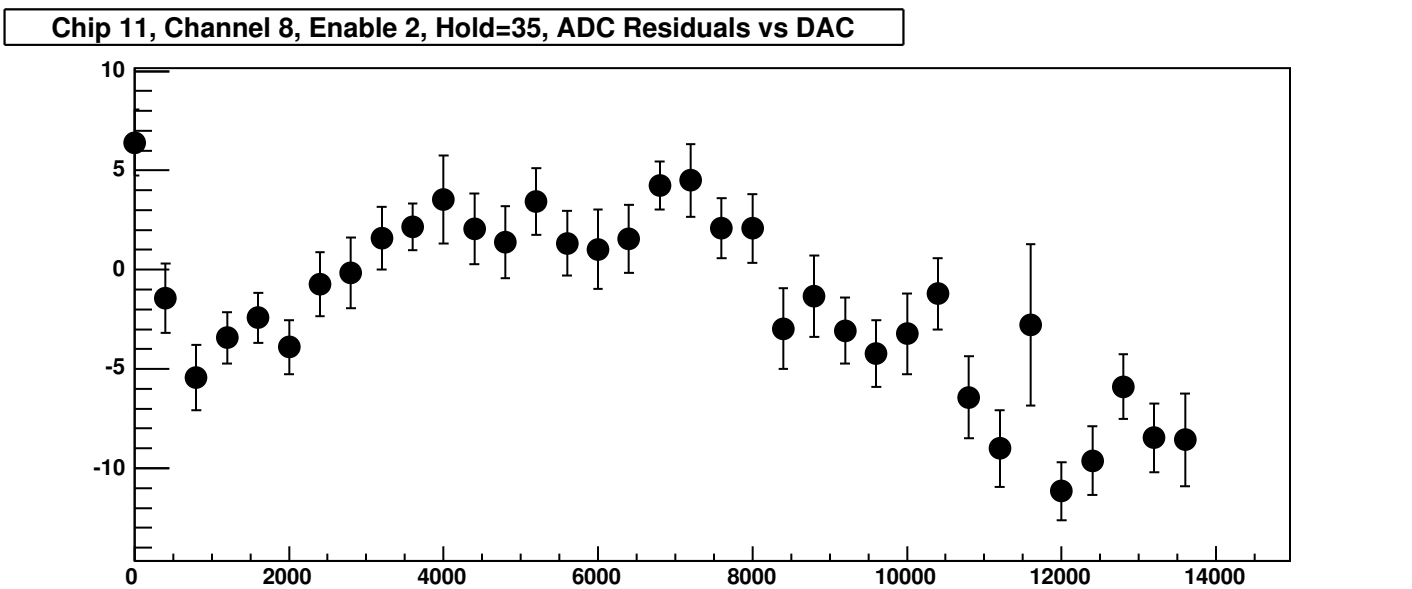

Chip 11, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 20.39 / 23
p0 -565.9 ± 0.7222
p1 0.01951 ± 0.0001148
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

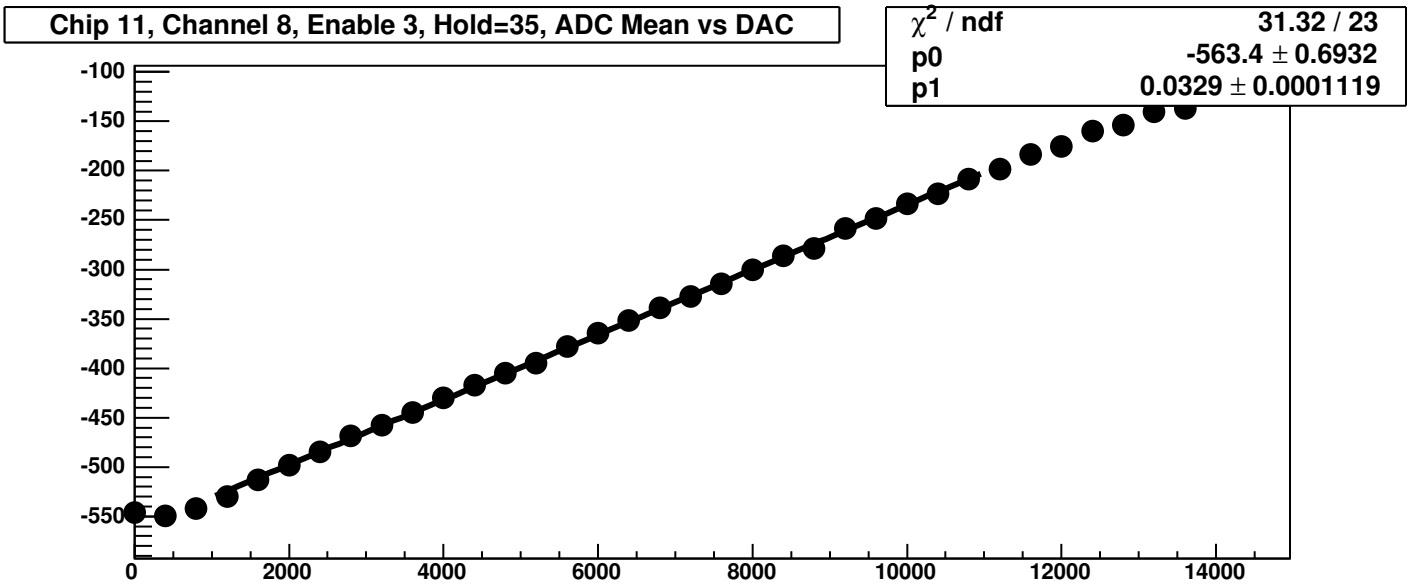

Chip 11, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

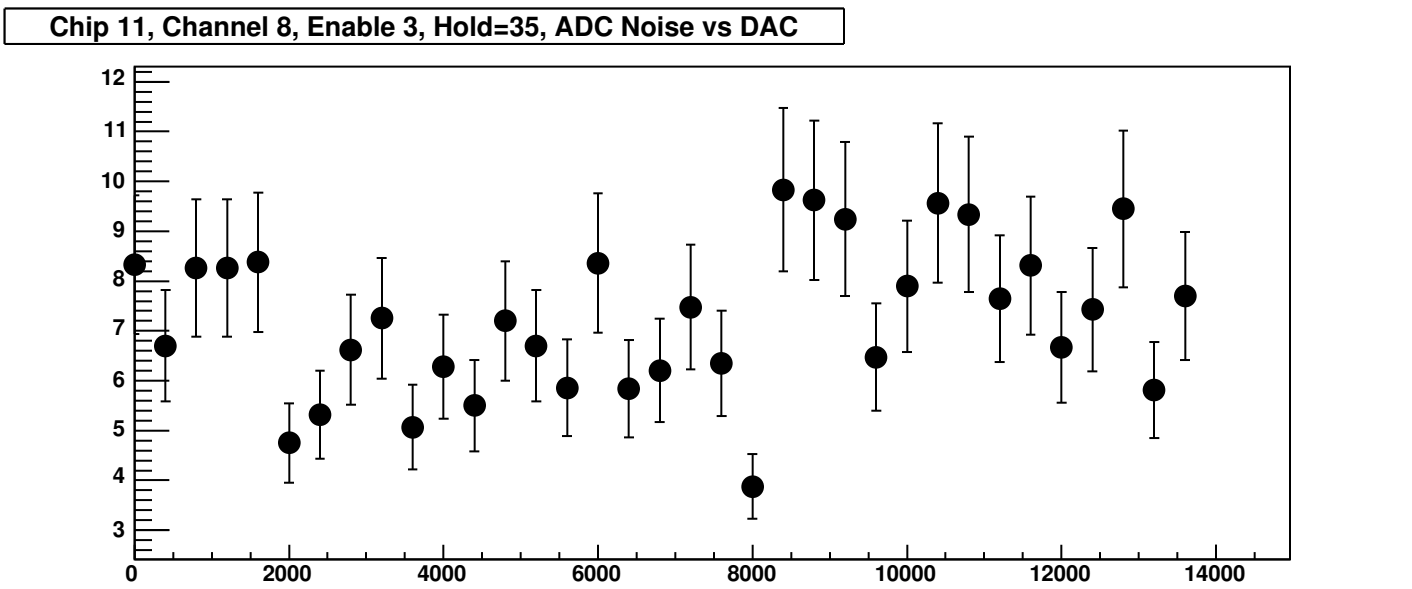

Chip 11, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

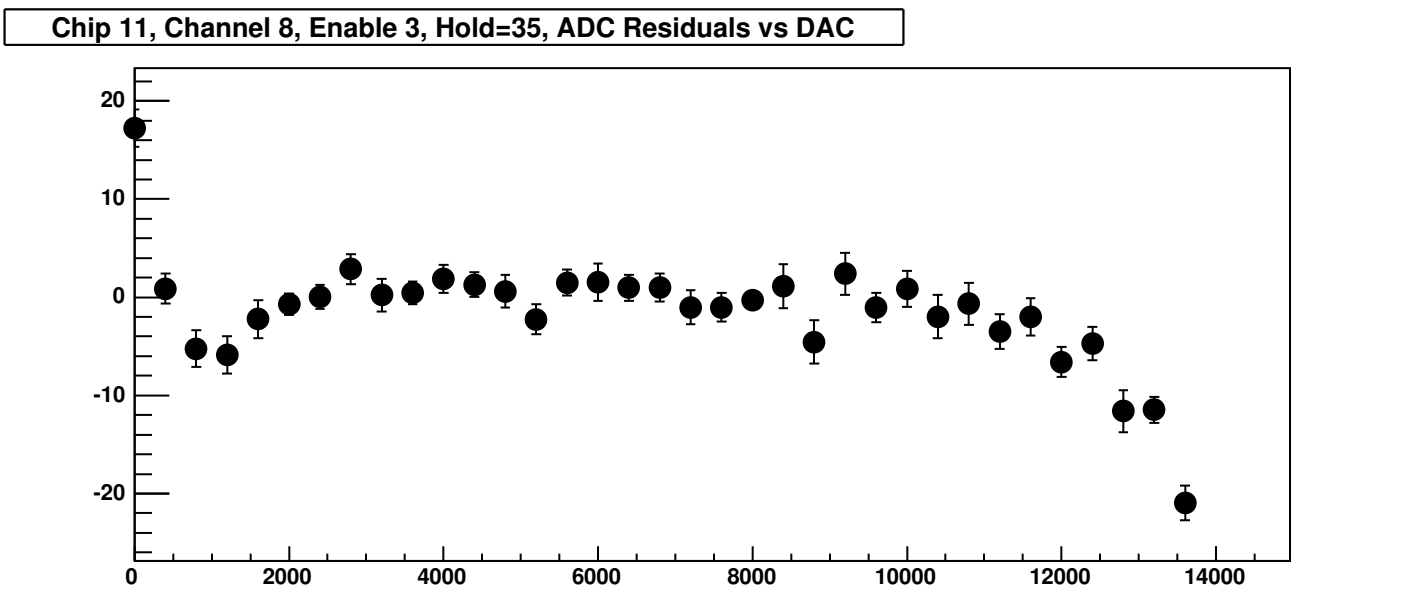

Chip 11, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 80.88 / 23
p0 -563.5 ± 0.6904
p1 0.02236 ± 0.0001117
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

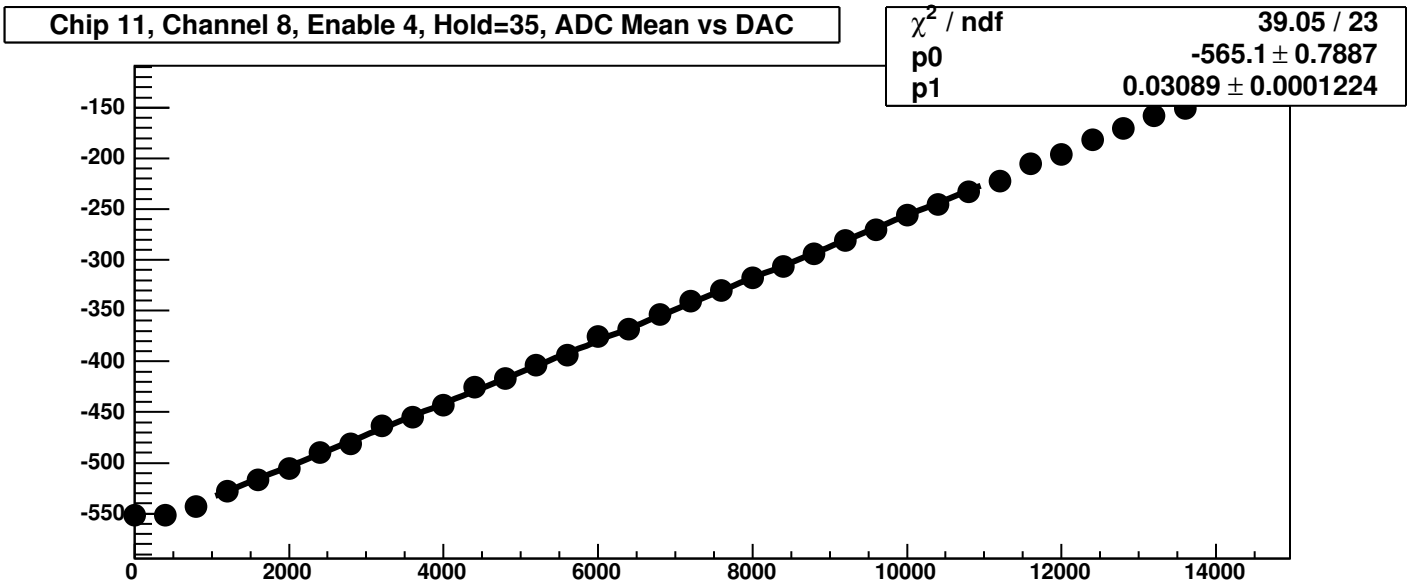

Chip 11, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

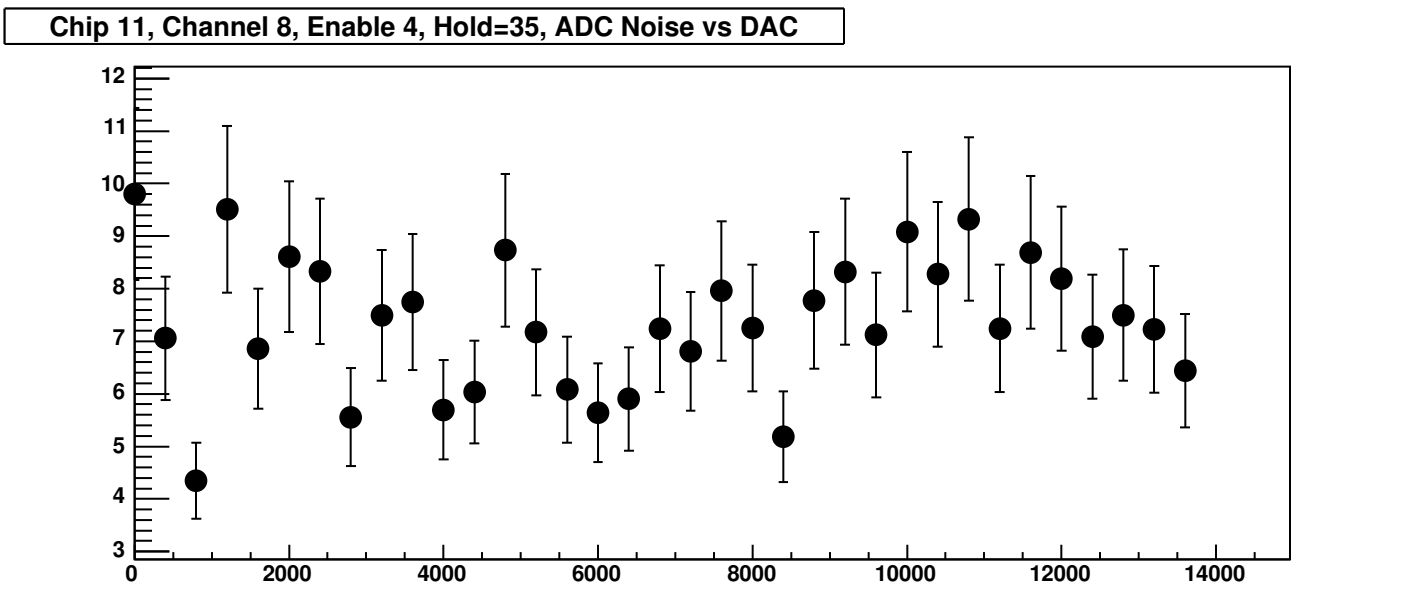

Chip 11, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

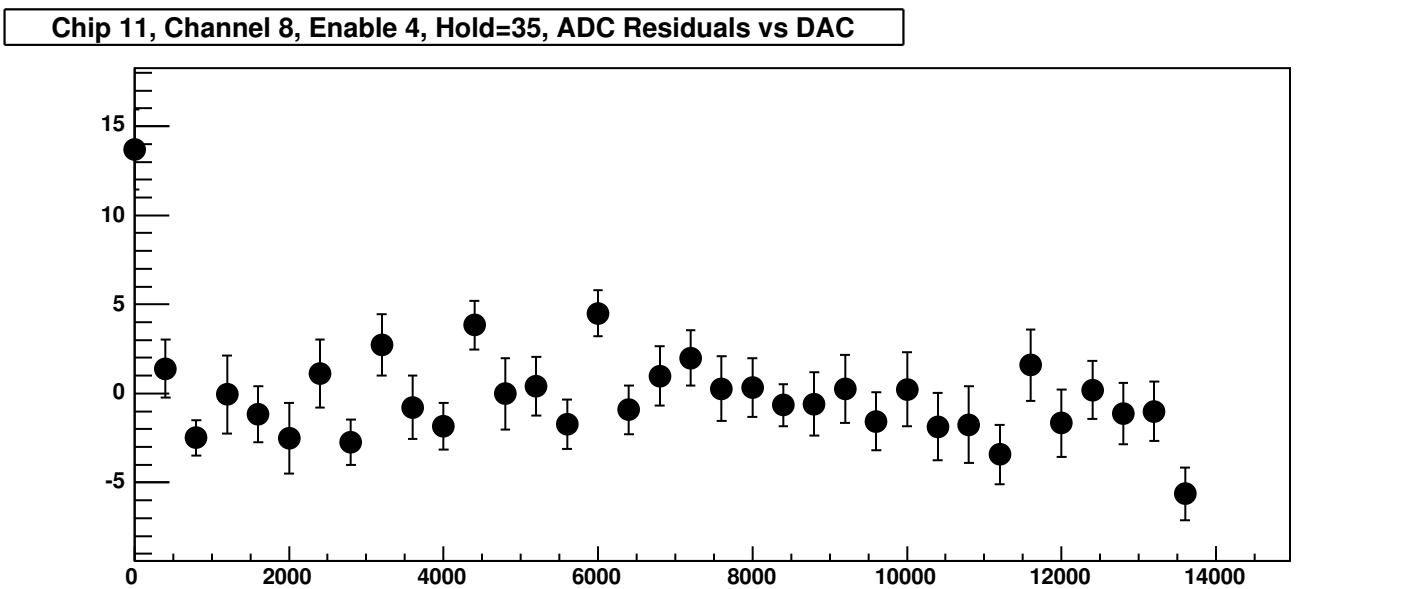

Chip 11, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.32 / 23
p0 -563.4 ± 0.6932
p1 0.0329 ± 0.0001119
-100
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

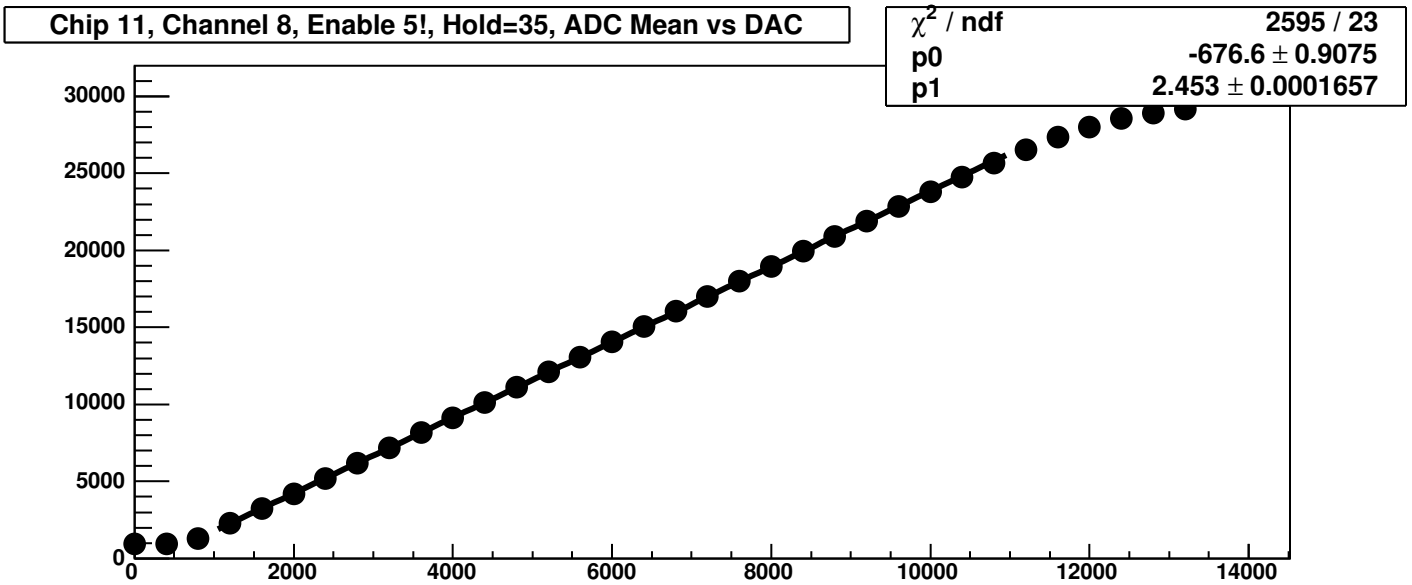

Chip 11, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

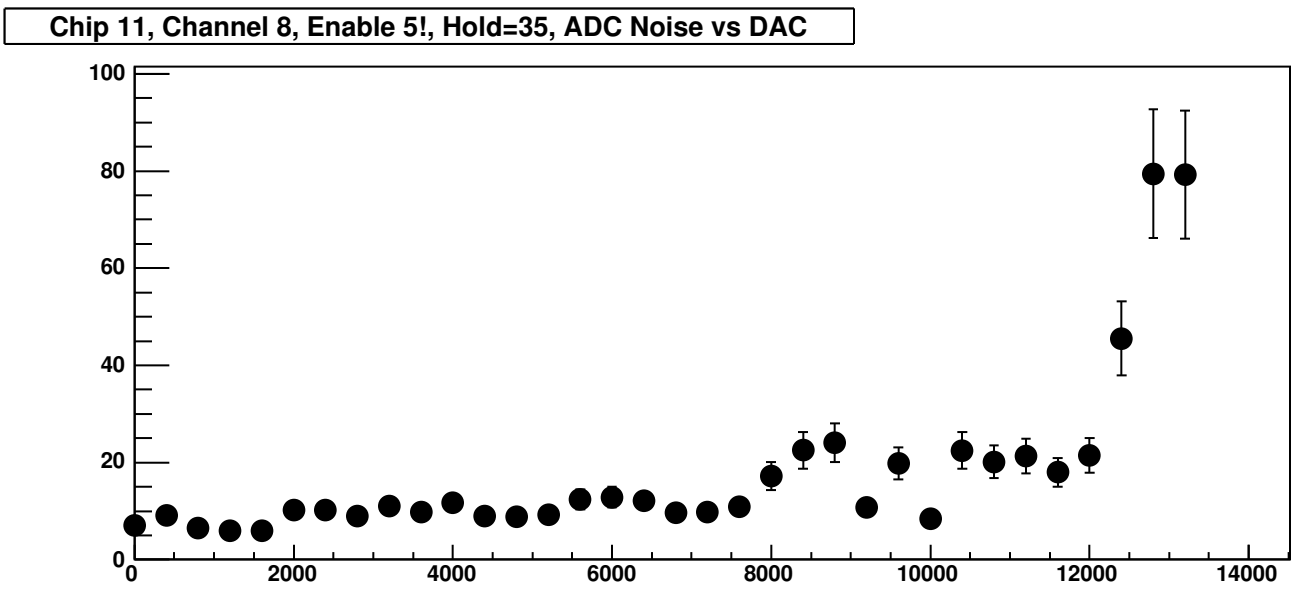

Chip 11, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
0
2000
4000
6000
8000
10000
12000
14000

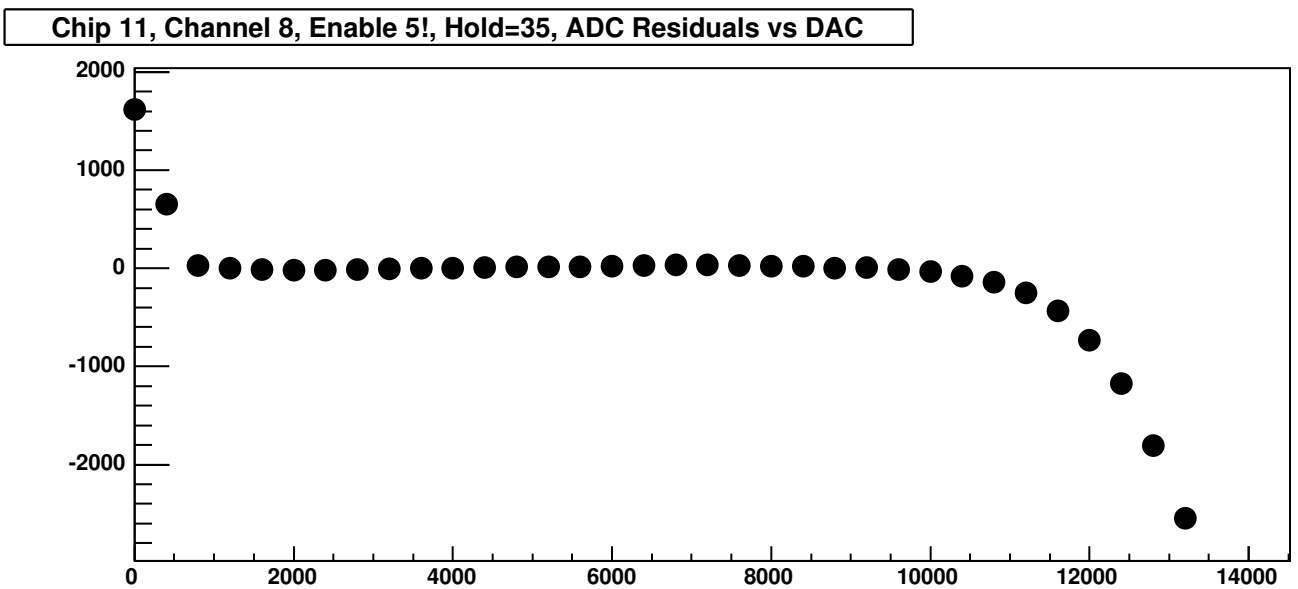

Chip 11, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 39.05 / 23
p0 -565.1 ± 0.7887
p1 0.03089 ± 0.0001224
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

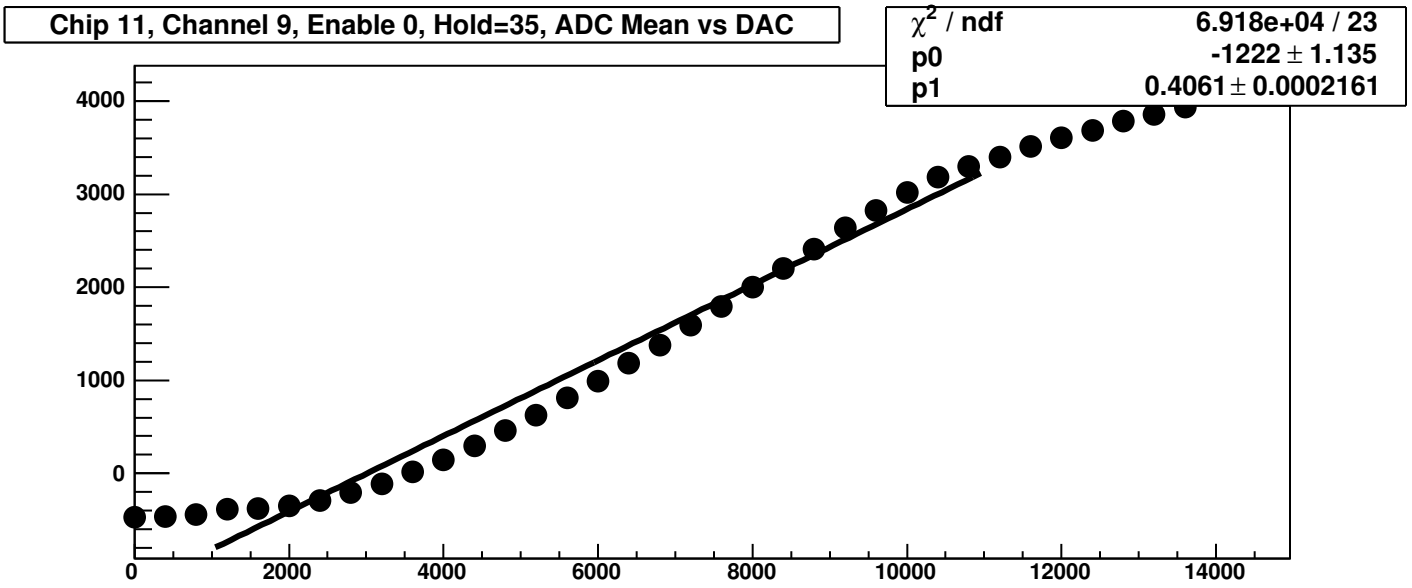

Chip 11, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

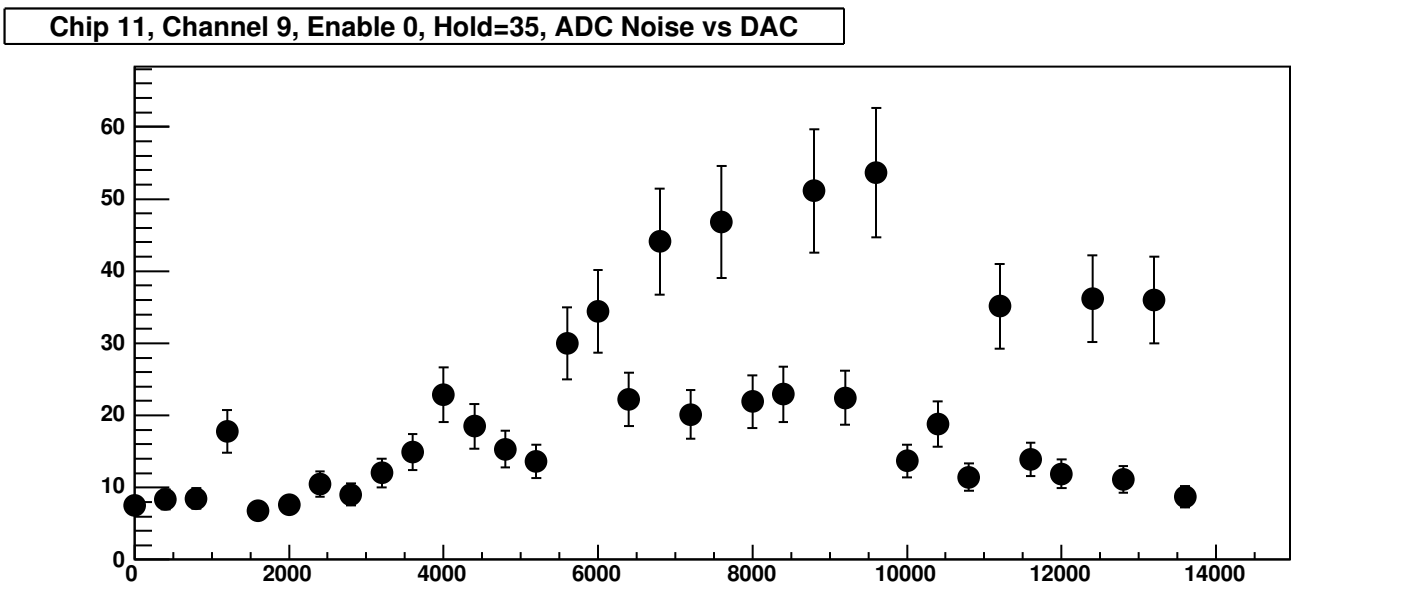

Chip 11, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

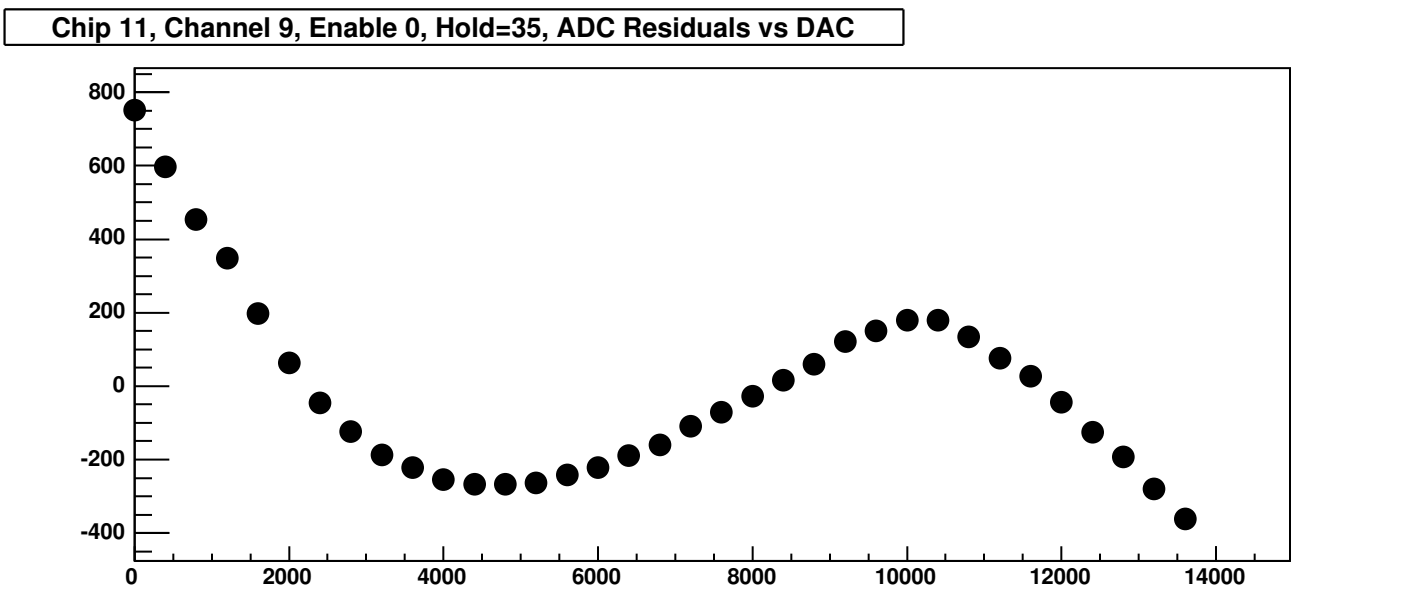

Chip 11, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2595 / 23
p0 -676.6 ± 0.9075
p1 2.453 ± 0.0001657
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

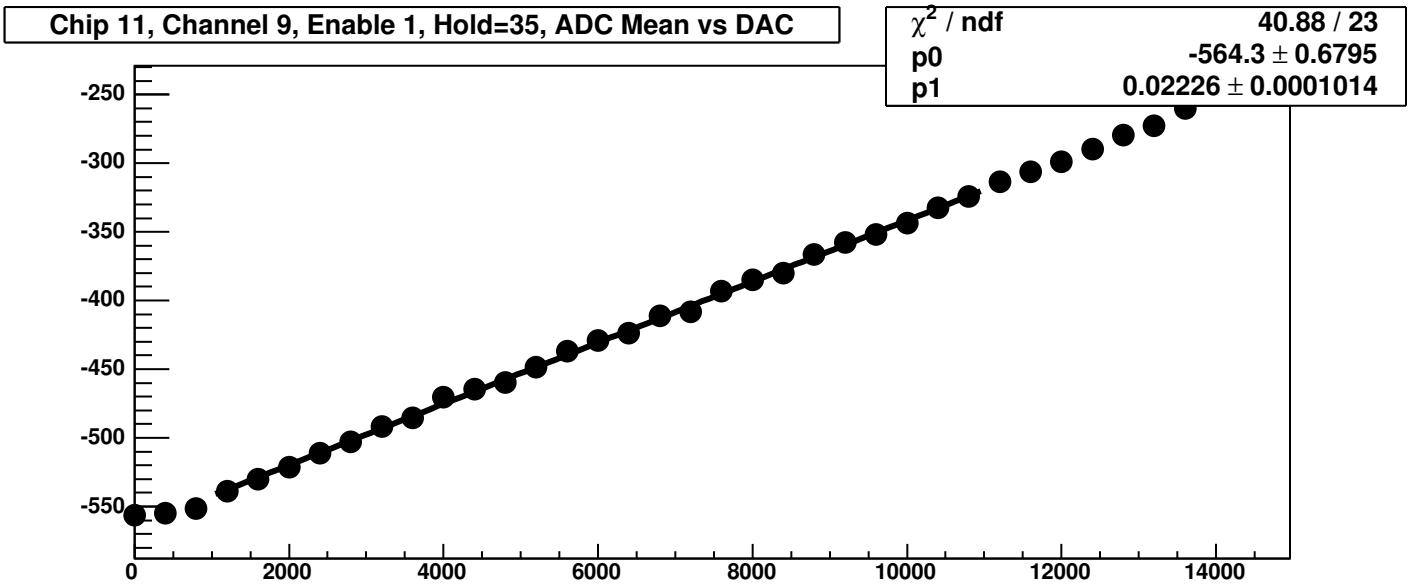

Chip 11, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
2000
4000
6000
8000
10000
12000
14000

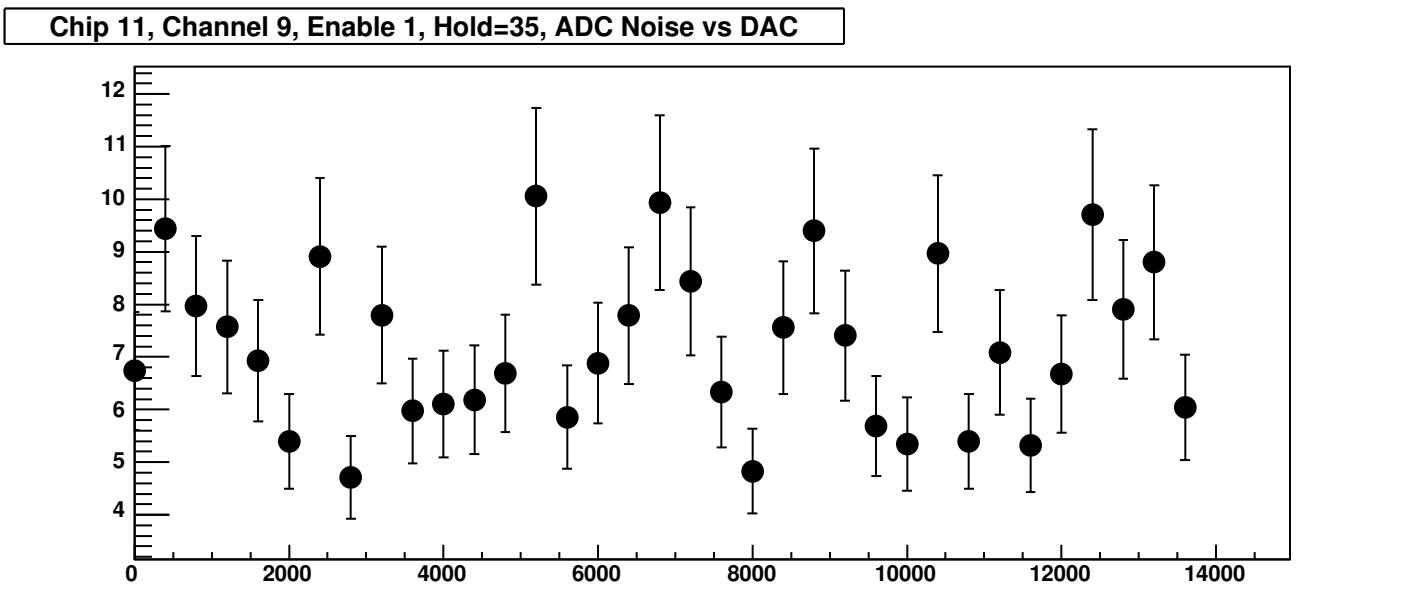

Chip 11, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1000
0
-1000
-2000
0
2000
4000
6000
8000
10000
12000
14000

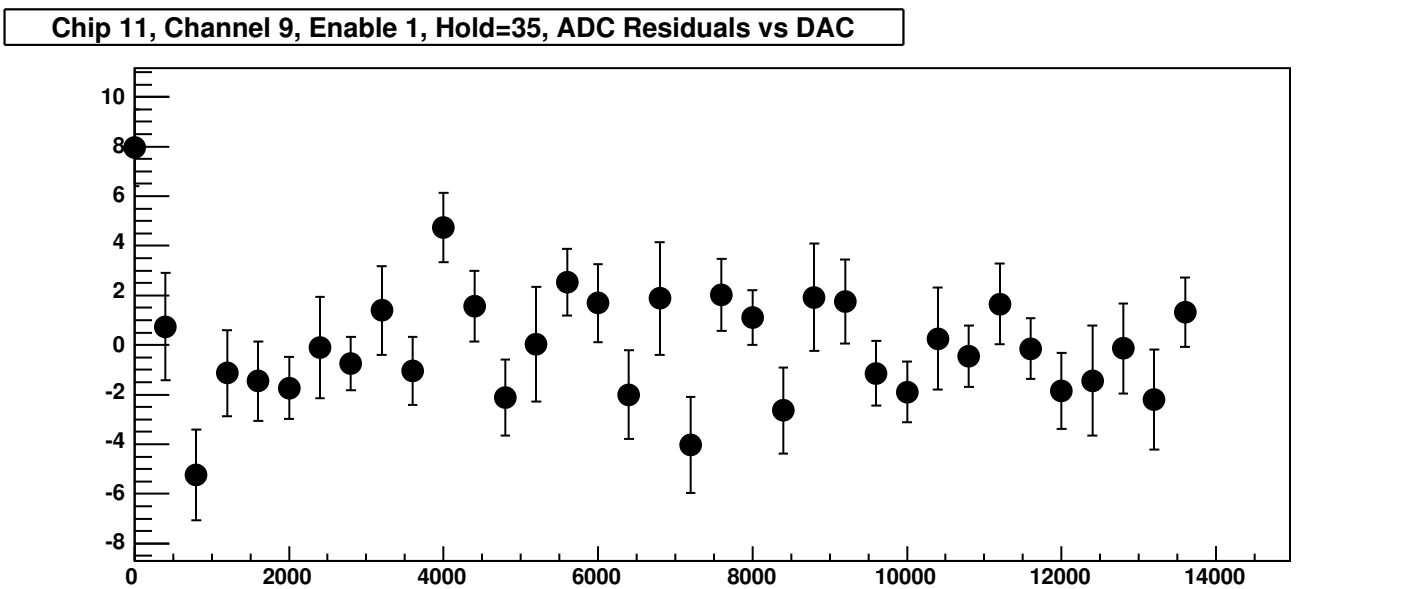

Chip 11, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 6.918e+04 / 23
p0 -1222 ± 1.135
p1 0.4061 ± 0.0002161
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

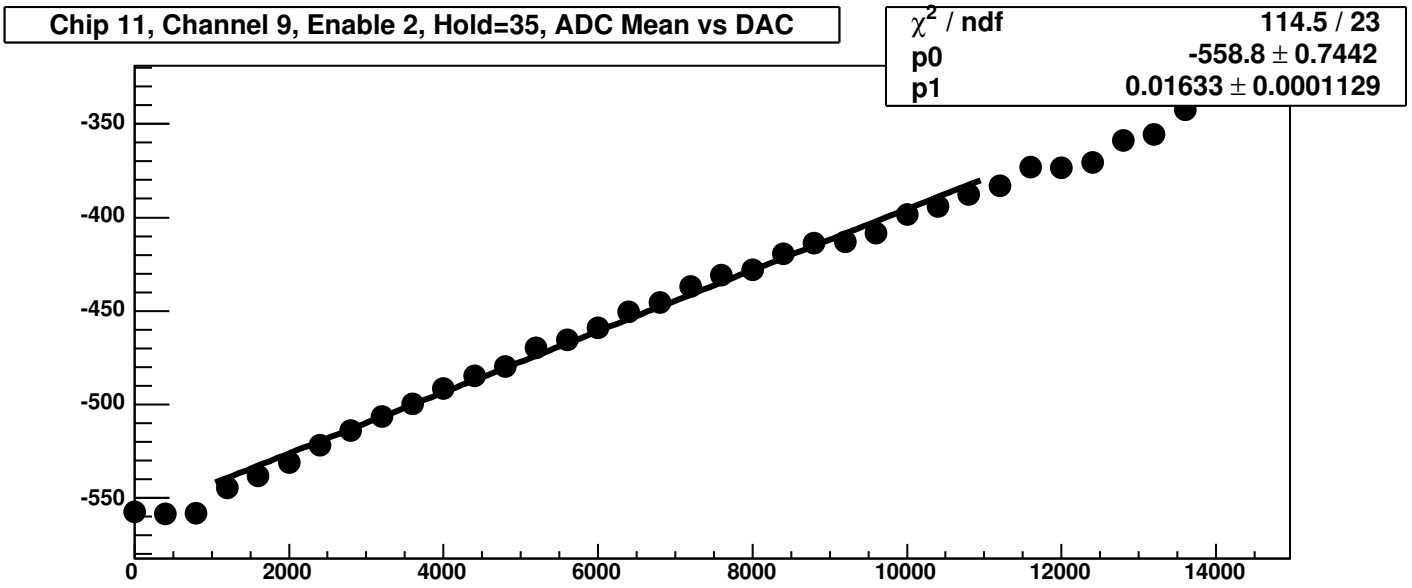

Chip 11, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

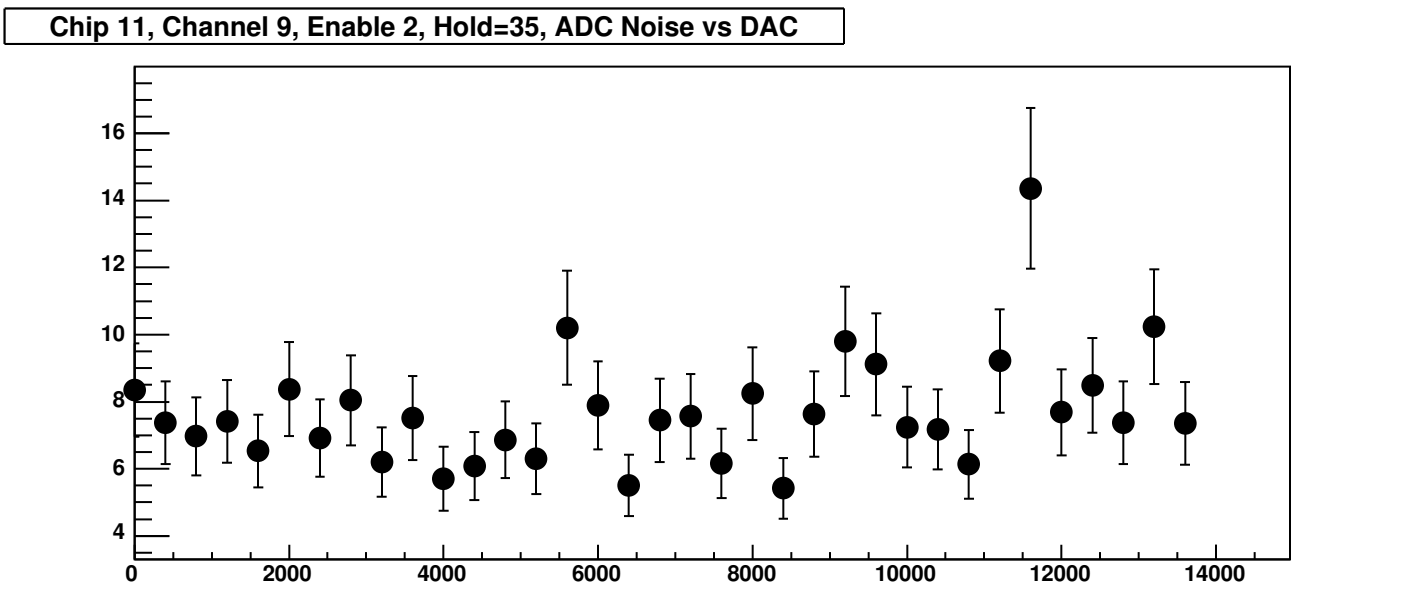

Chip 11, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
0
2000
4000
6000
8000
10000
12000
14000

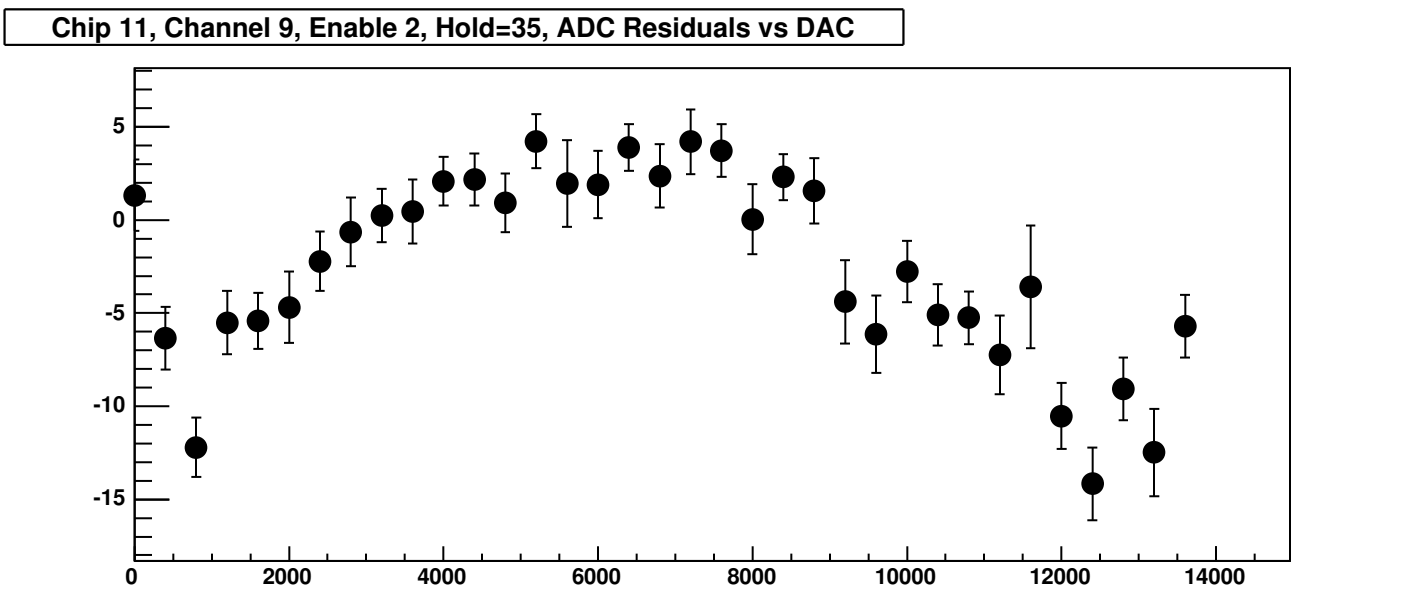

Chip 11, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 40.88 / 23
p0 -564.3 ± 0.6795
p1 0.02226 ± 0.0001014
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

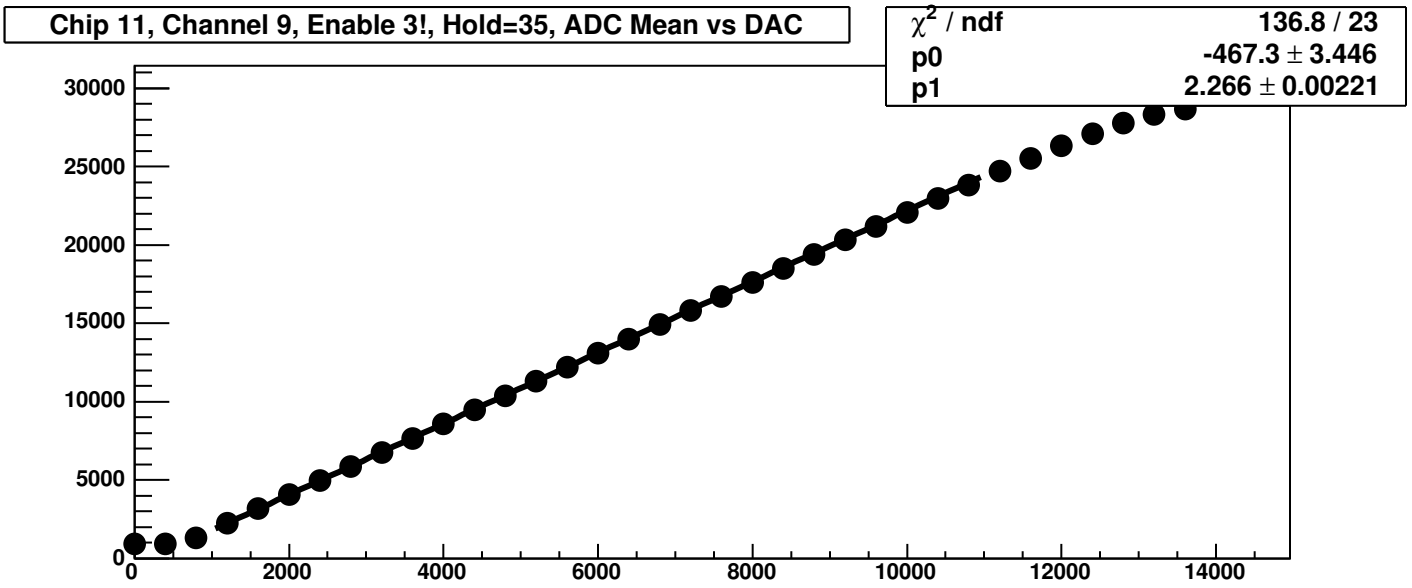

Chip 11, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

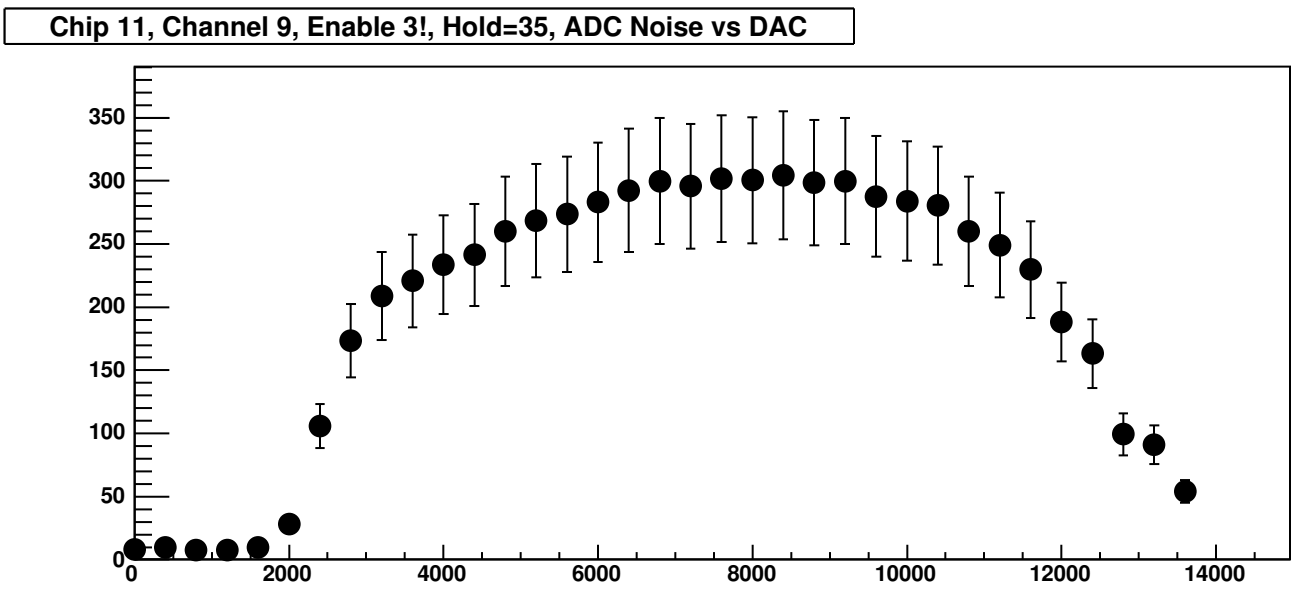

Chip 11, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

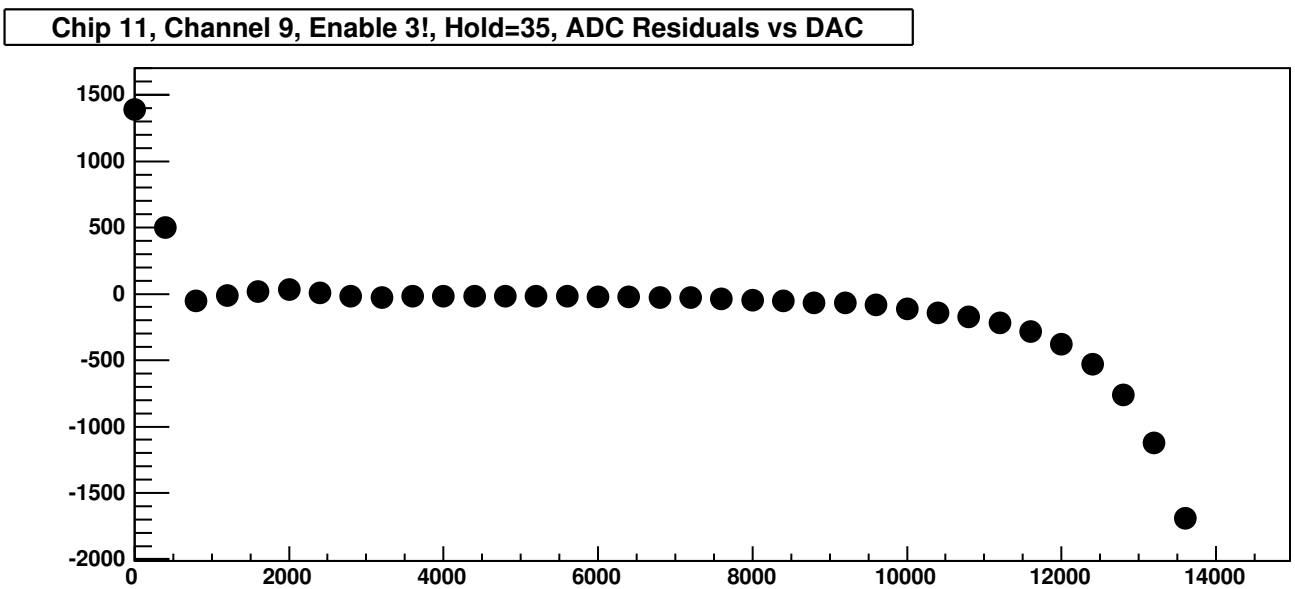

Chip 11, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 114.5 / 23
p0 -558.8 ± 0.7442
p1 0.01633 ± 0.0001129
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

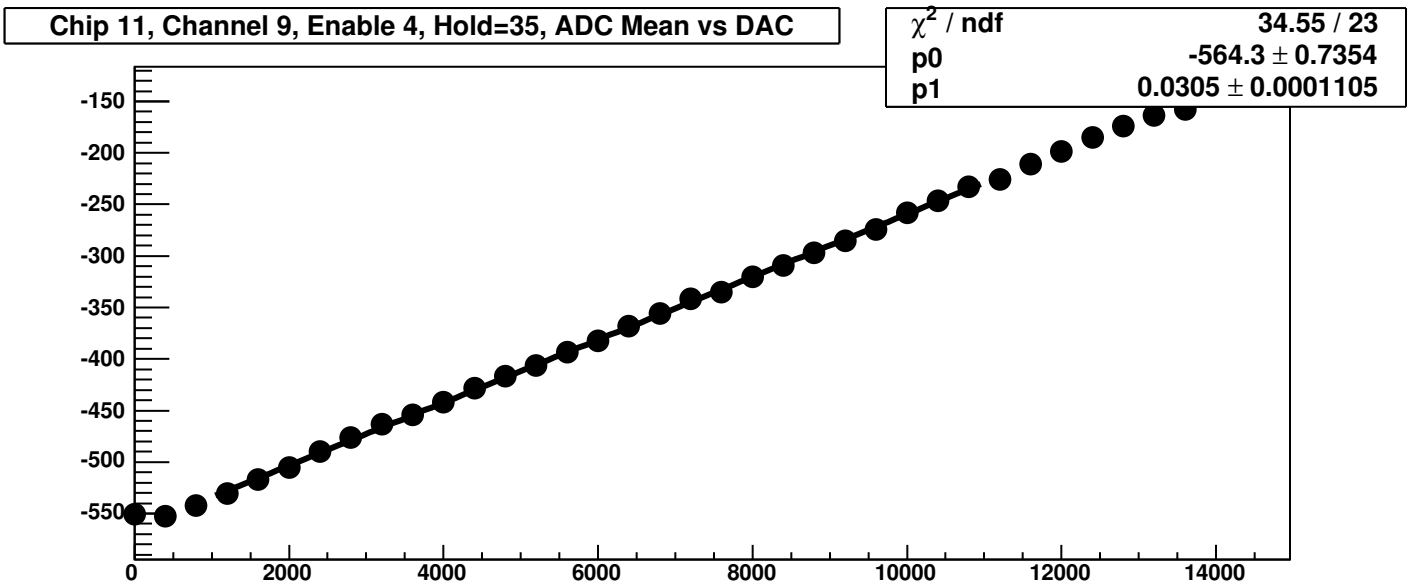

Chip 11, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

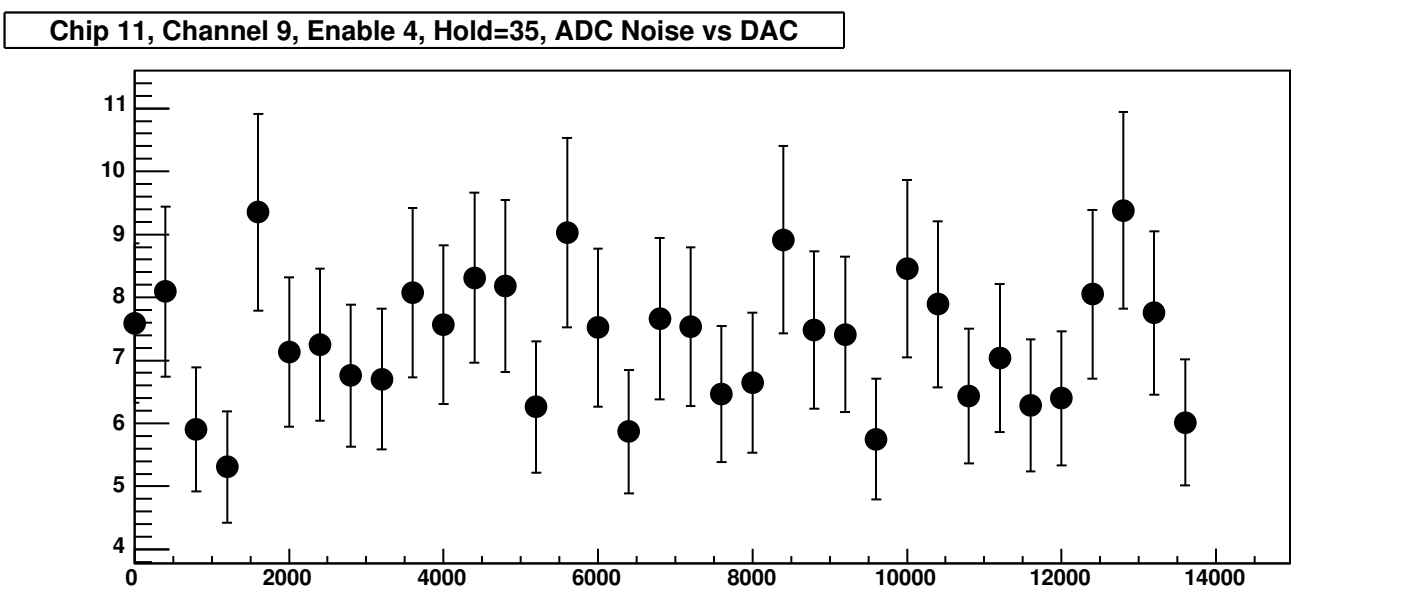

Chip 11, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
2000
4000
6000
8000
10000
12000
14000

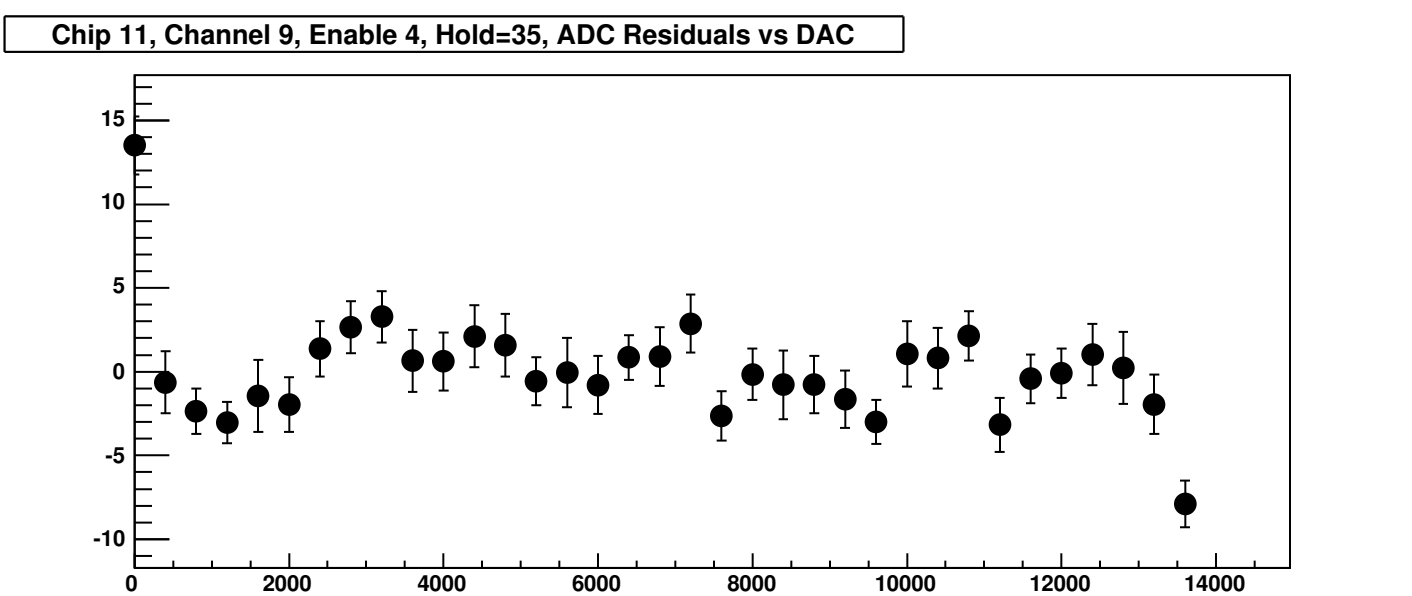

Chip 11, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 136.8 / 23
p0 -467.3 ± 3.446
p1 2.266 ± 0.00221
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

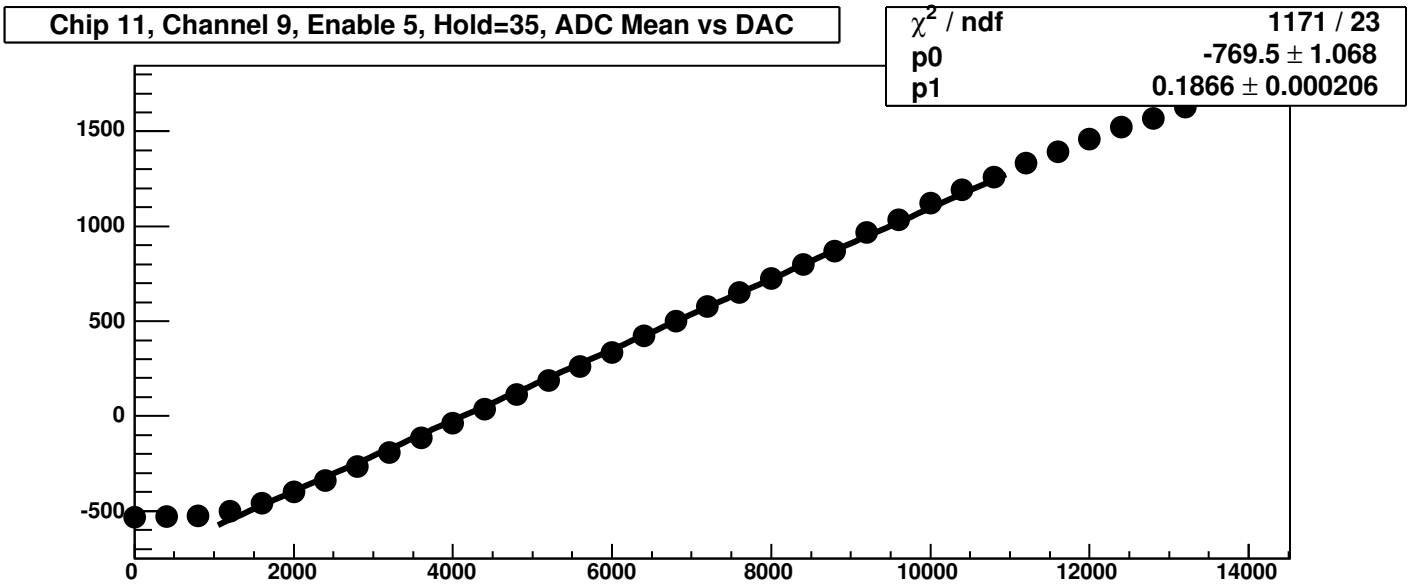

Chip 11, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

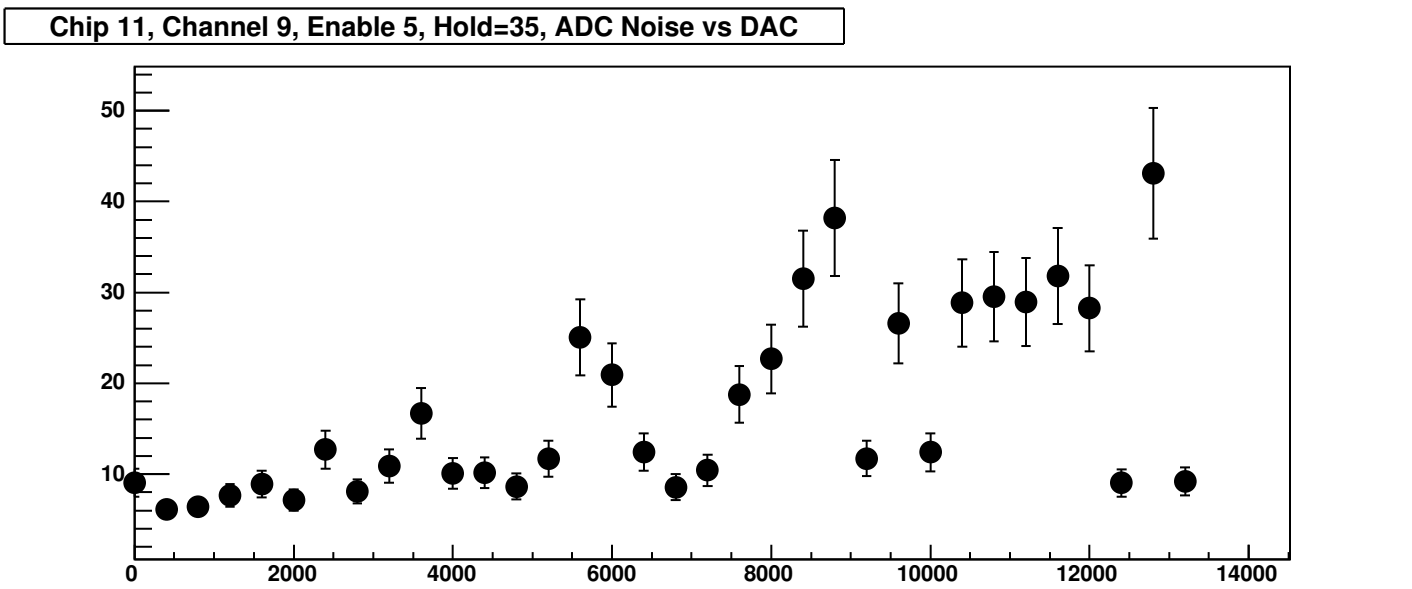

Chip 11, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
-2000
0
2000
4000
6000
8000
10000
12000
14000

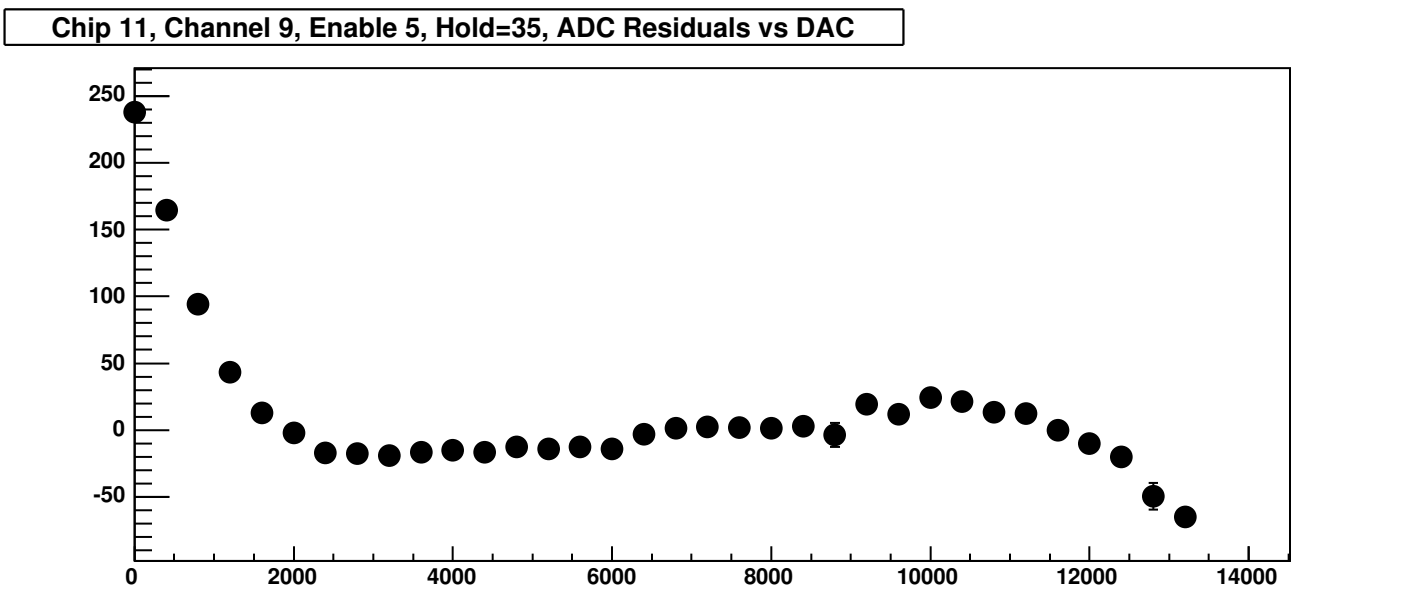

Chip 11, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 34.55 / 23
p0 -564.3 ± 0.7354
p1 0.0305 ± 0.0001105
-150
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

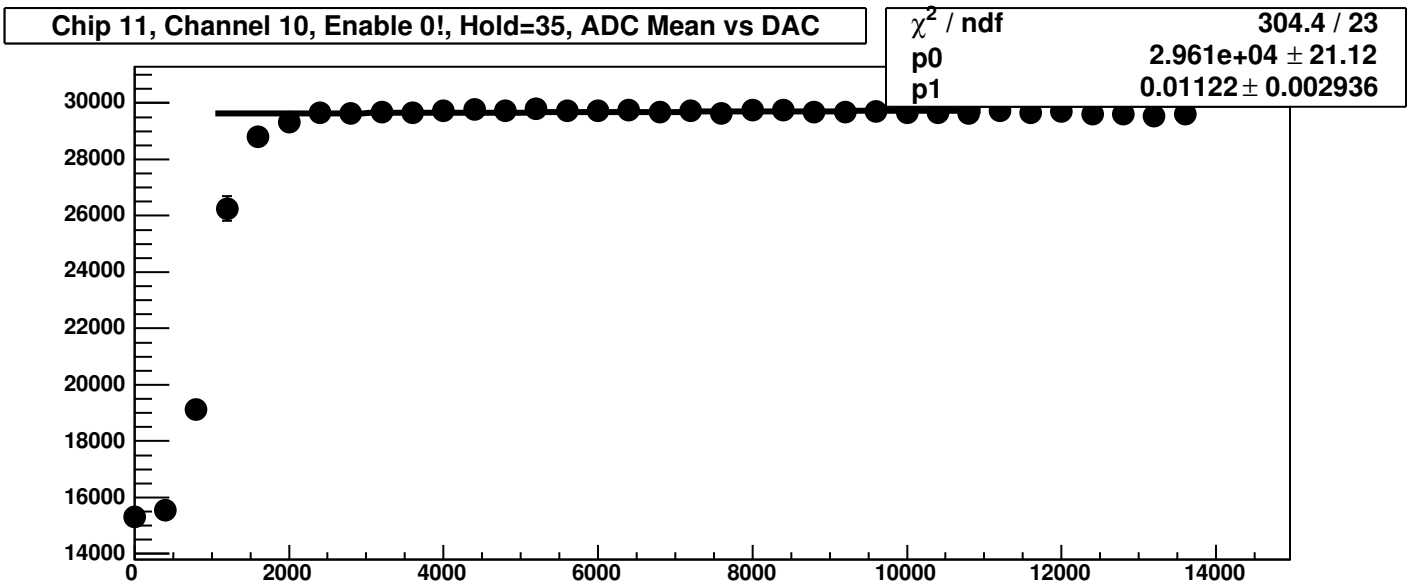

Chip 11, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

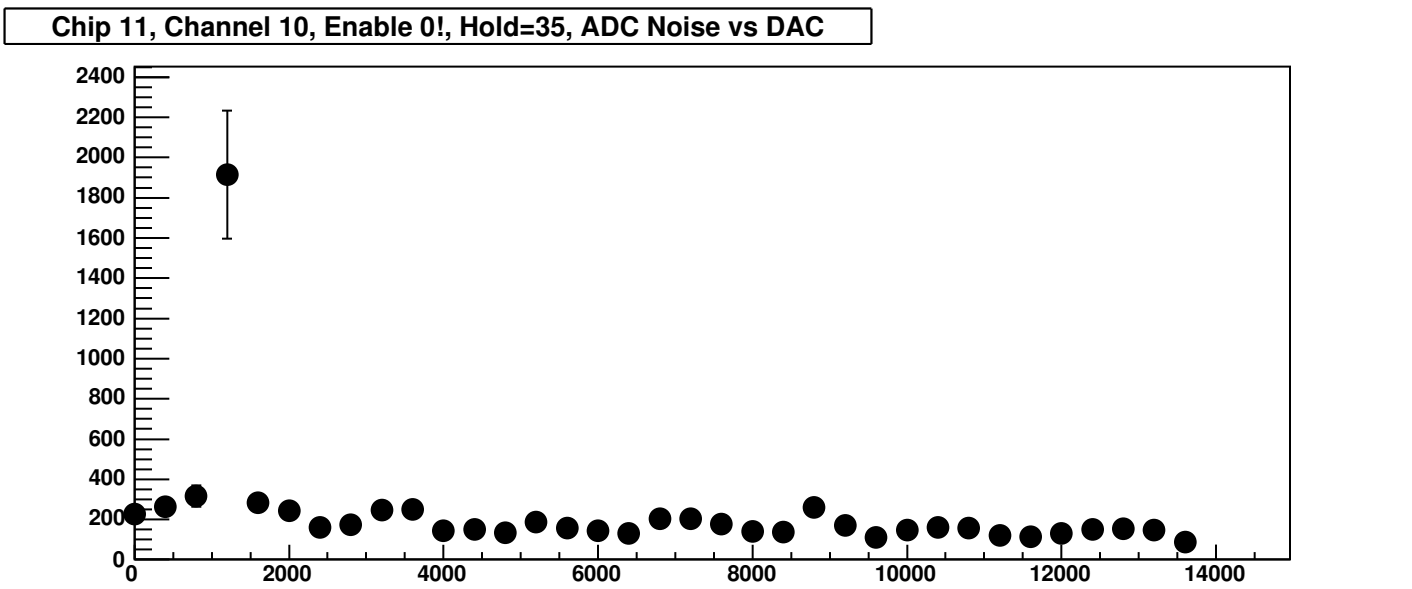

Chip 11, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

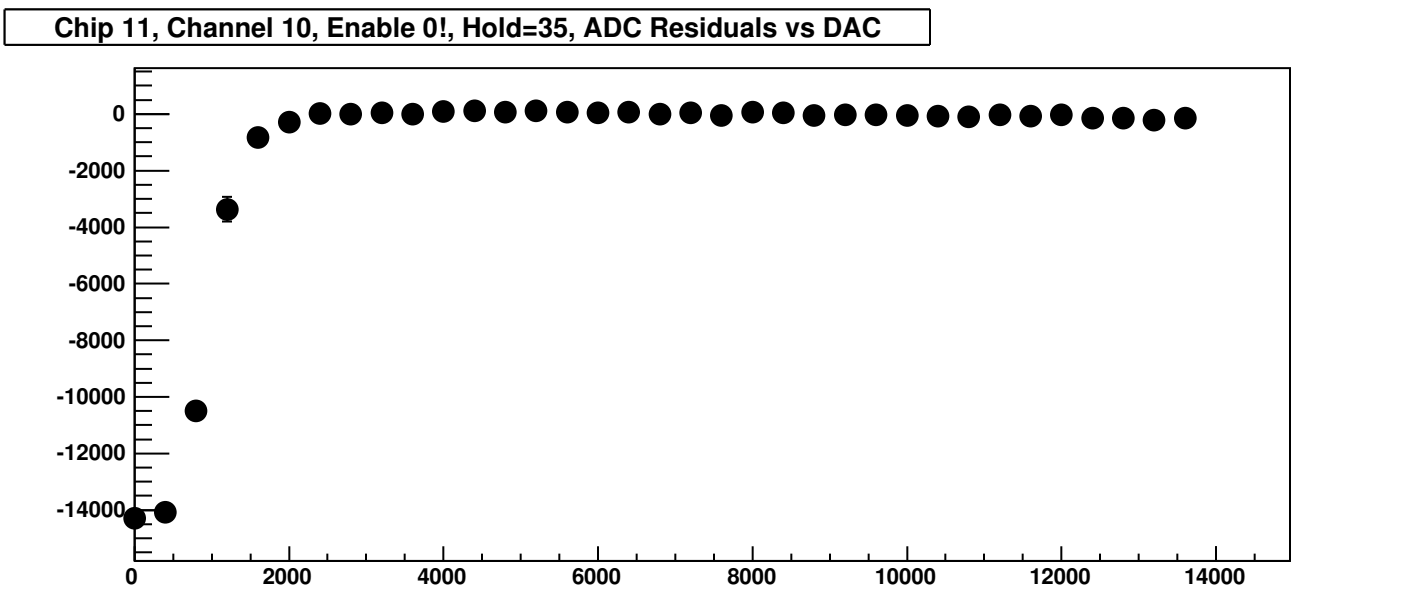

Chip 11, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1171 / 23
p0 -769.5 ± 1.068
p1 0.1866 ± 0.000206
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

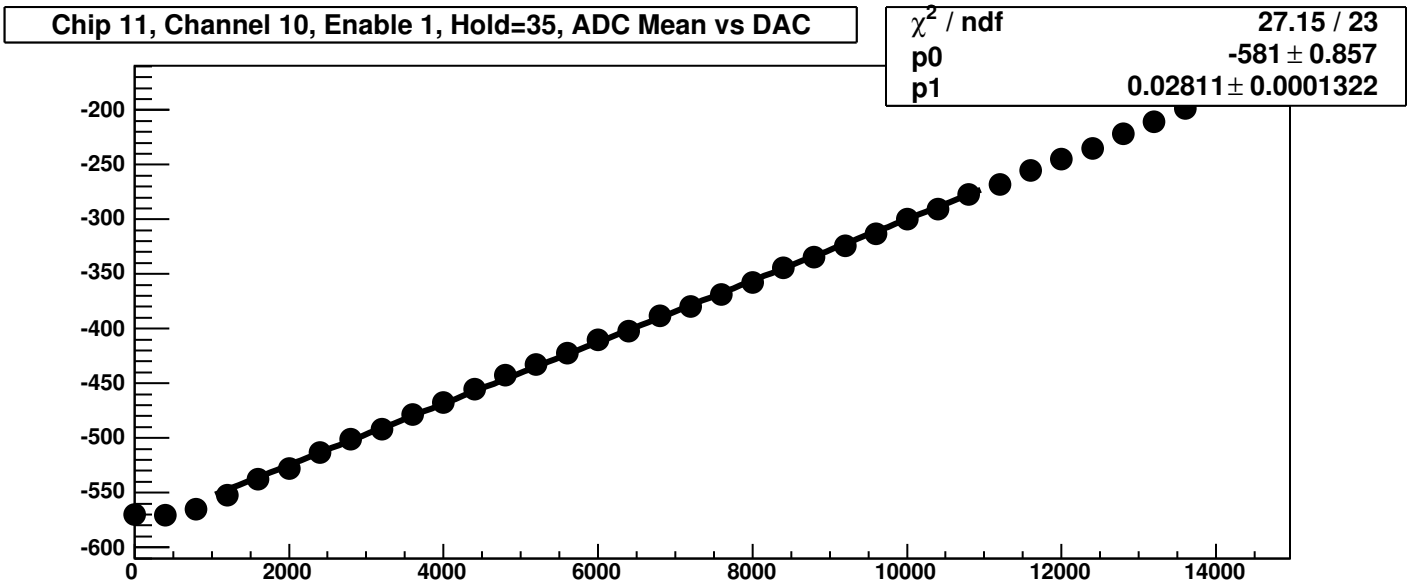

Chip 11, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

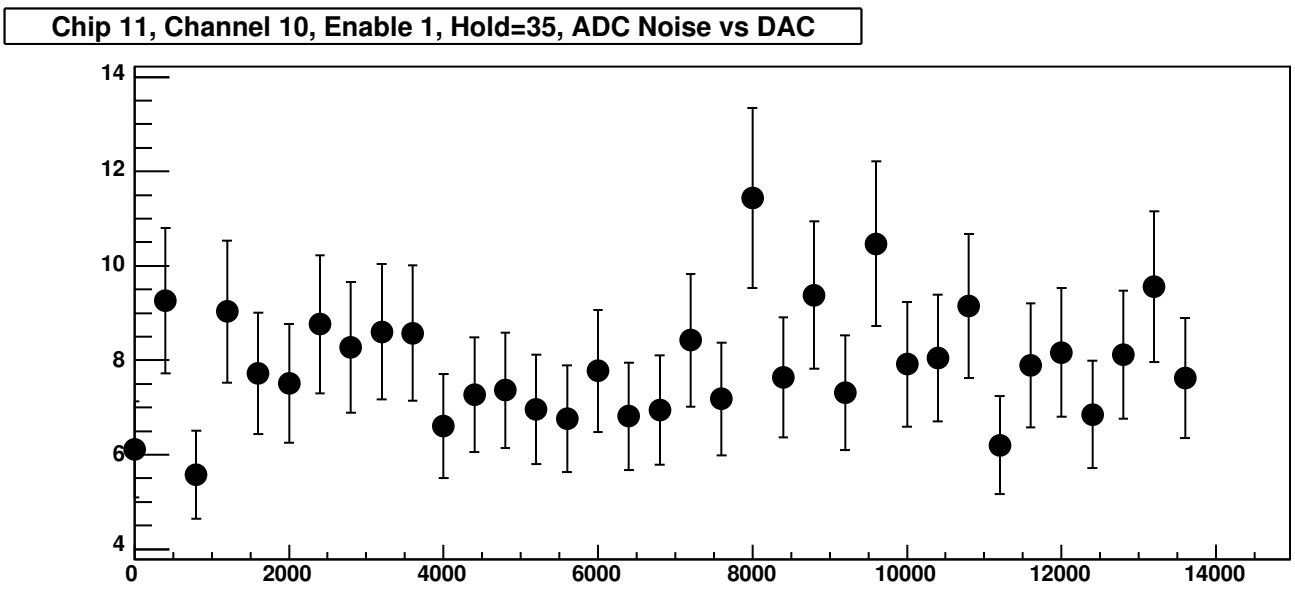

Chip 11, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

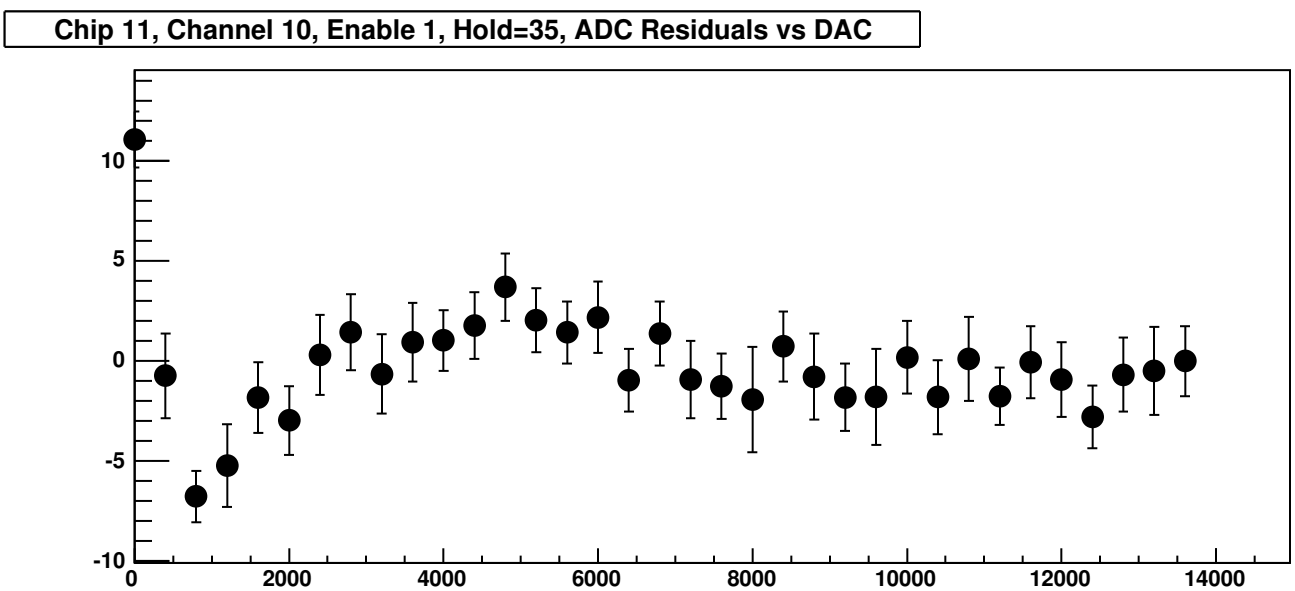

Chip 11, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 304.4 / 23
p0 2.961e+04 ± 21.12
p1 0.01122 ± 0.002936
30000
28000
26000
24000
22000
20000
18000
16000
14000
0
2000
4000
6000
8000
10000
12000
14000

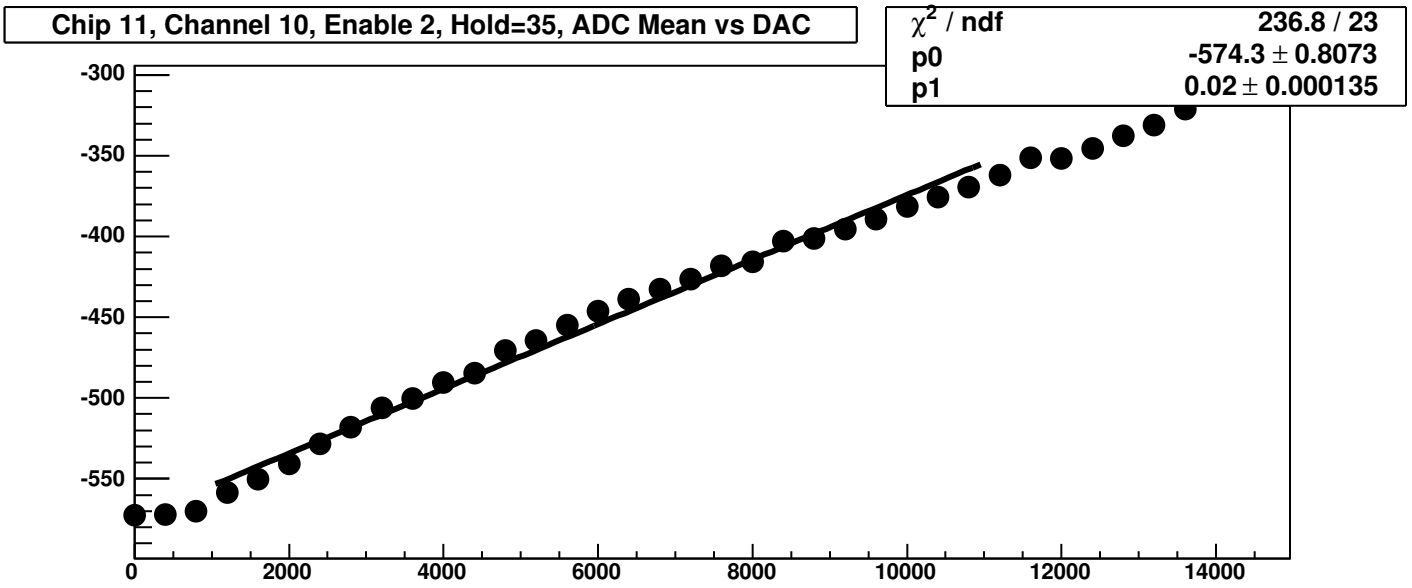

Chip 11, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
2400
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

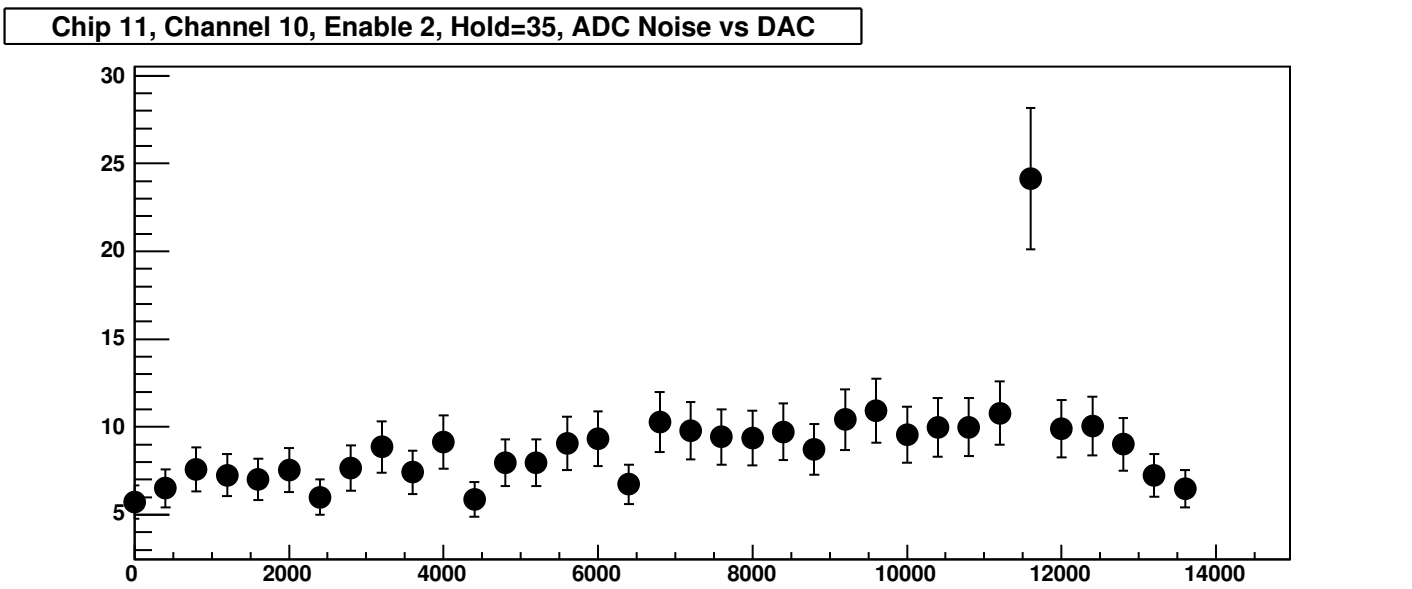

Chip 11, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-2000
-4000
-6000
-8000
-10000
-12000
-14000
0
2000
4000
6000
8000
10000
12000
14000

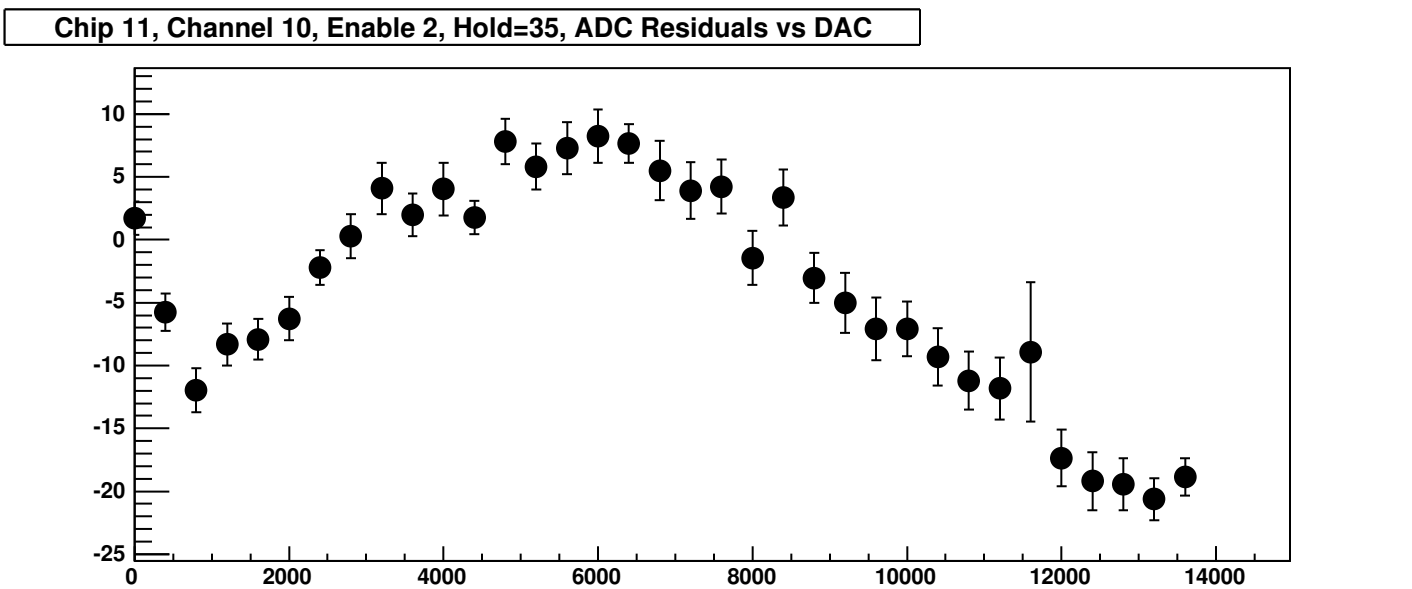

Chip 11, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 27.15 / 23
p0 -581 ± 0.857
p1 0.02811 ± 0.0001322
-200
-250
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

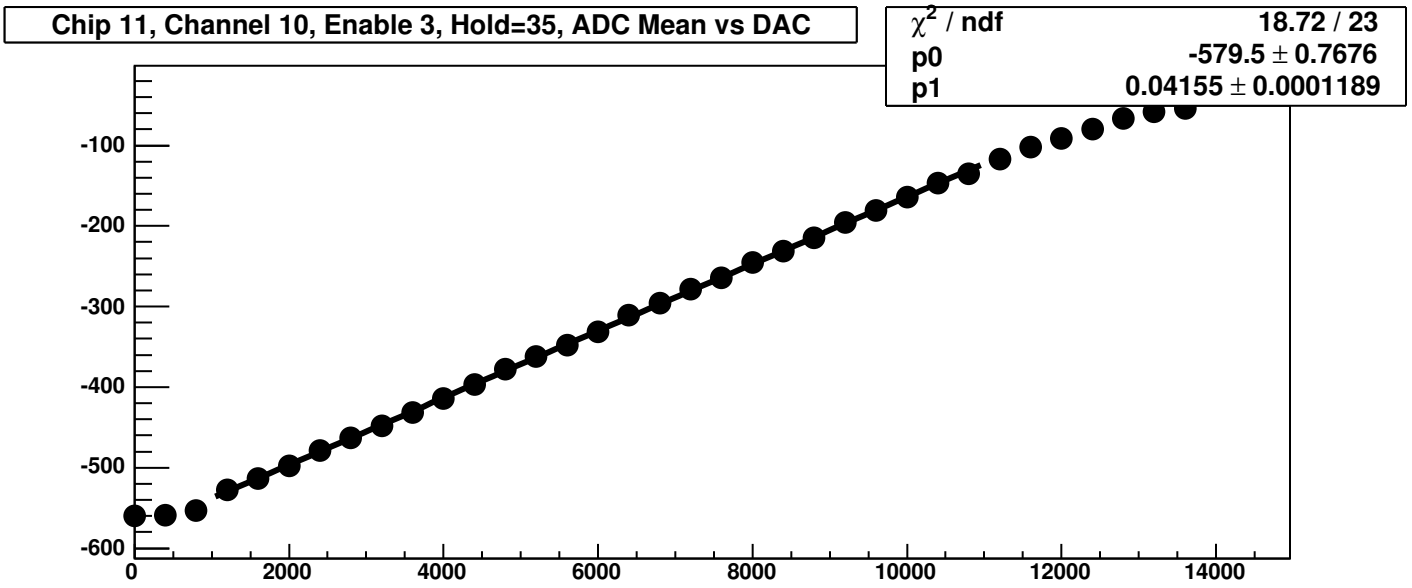

Chip 11, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

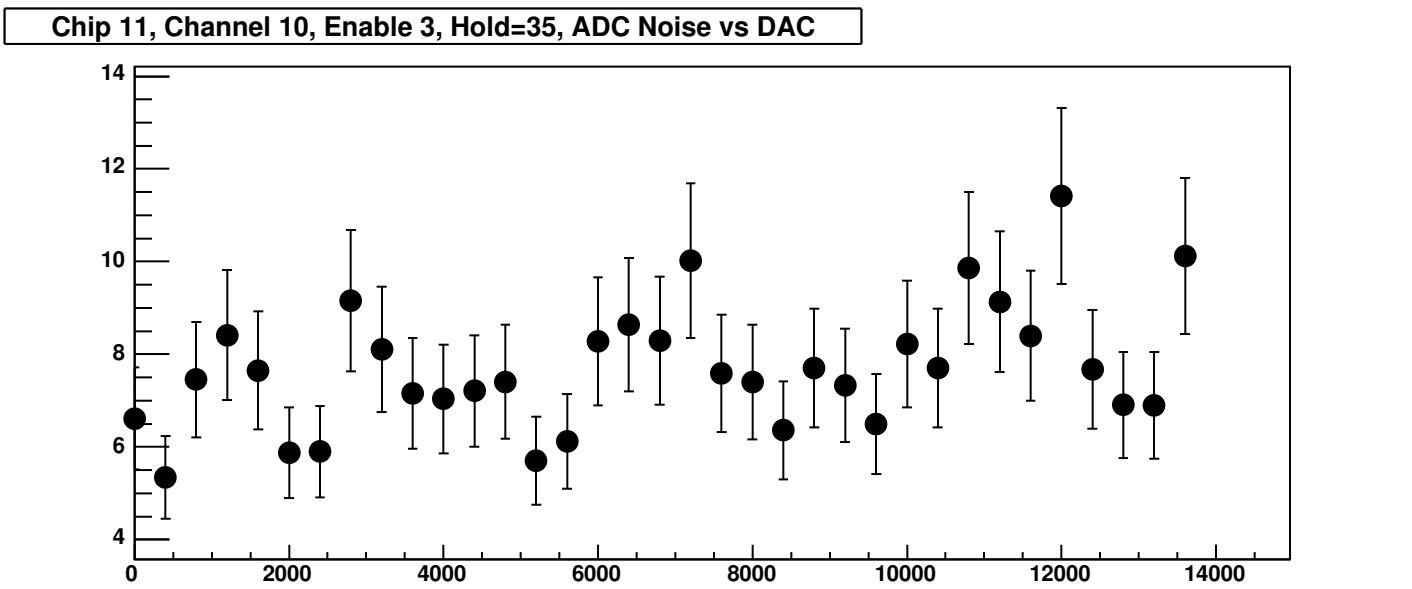

Chip 11, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

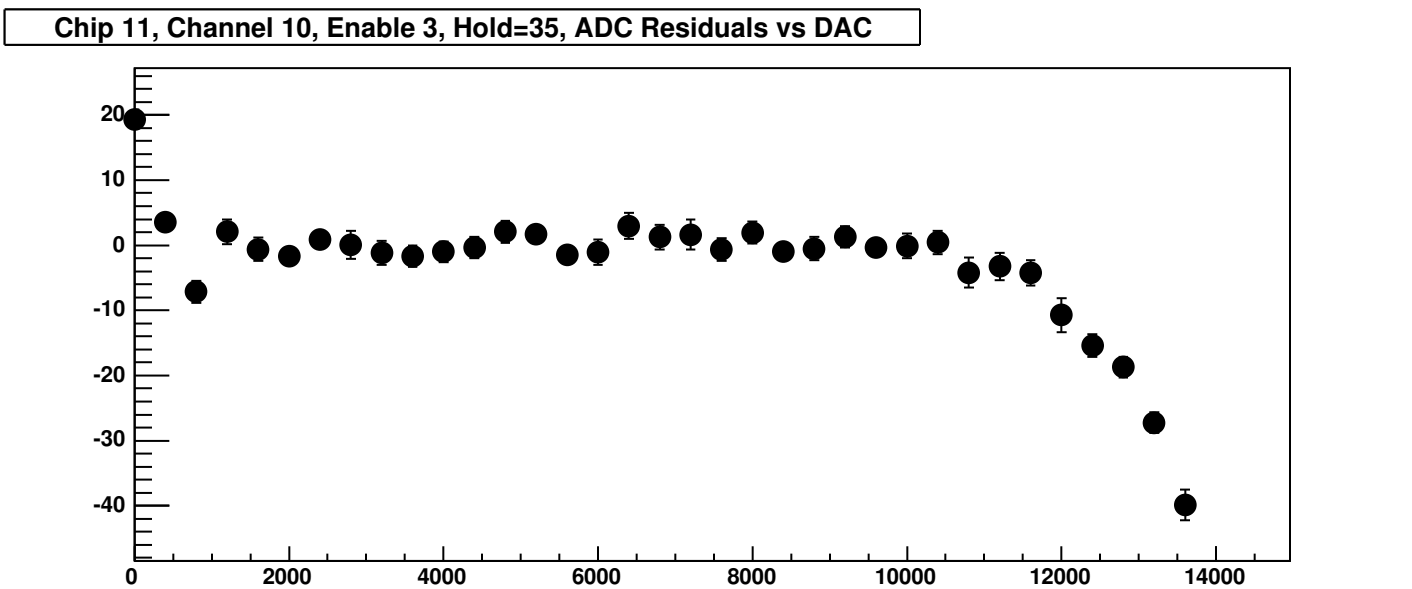

Chip 11, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 236.8 / 23
p0 -574.3 ± 0.8073
p1 0.02 ± 0.000135
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

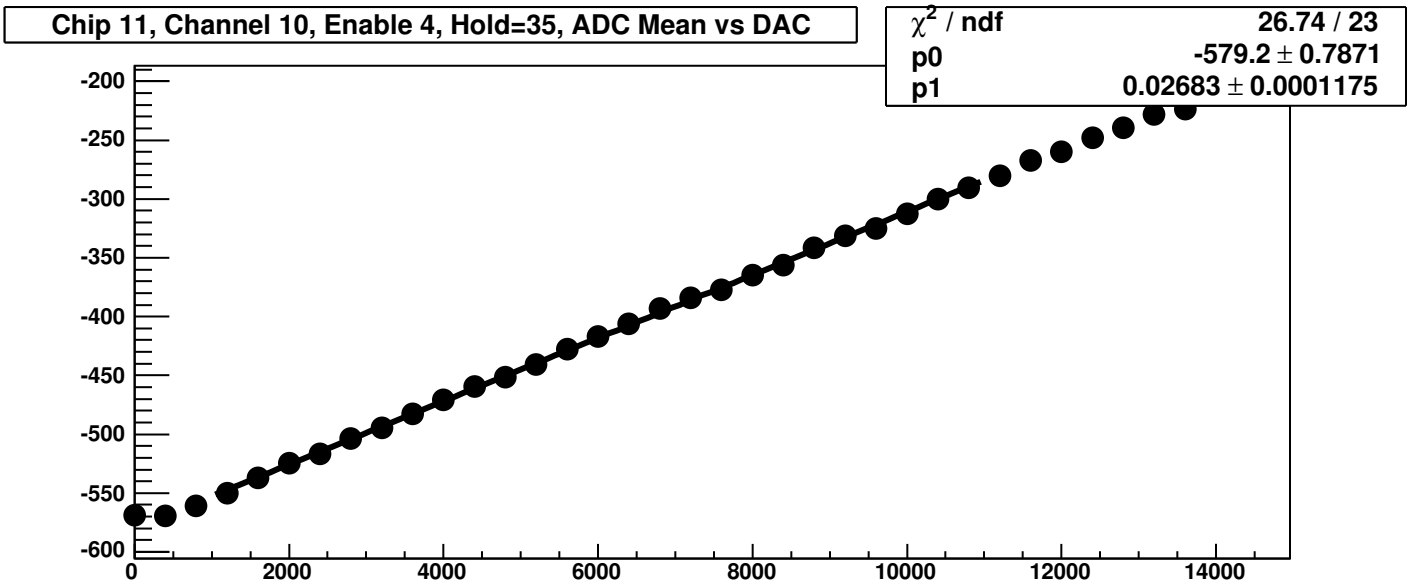

Chip 11, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

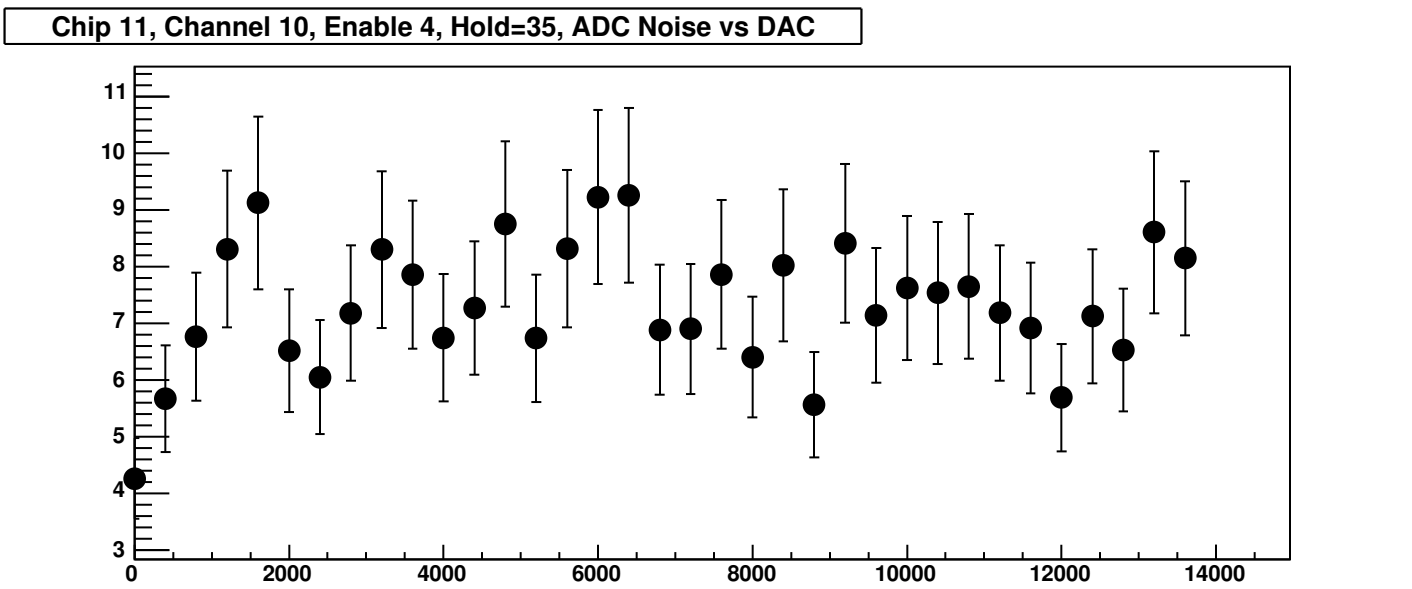

Chip 11, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
2000
4000
6000
8000
10000
12000
14000

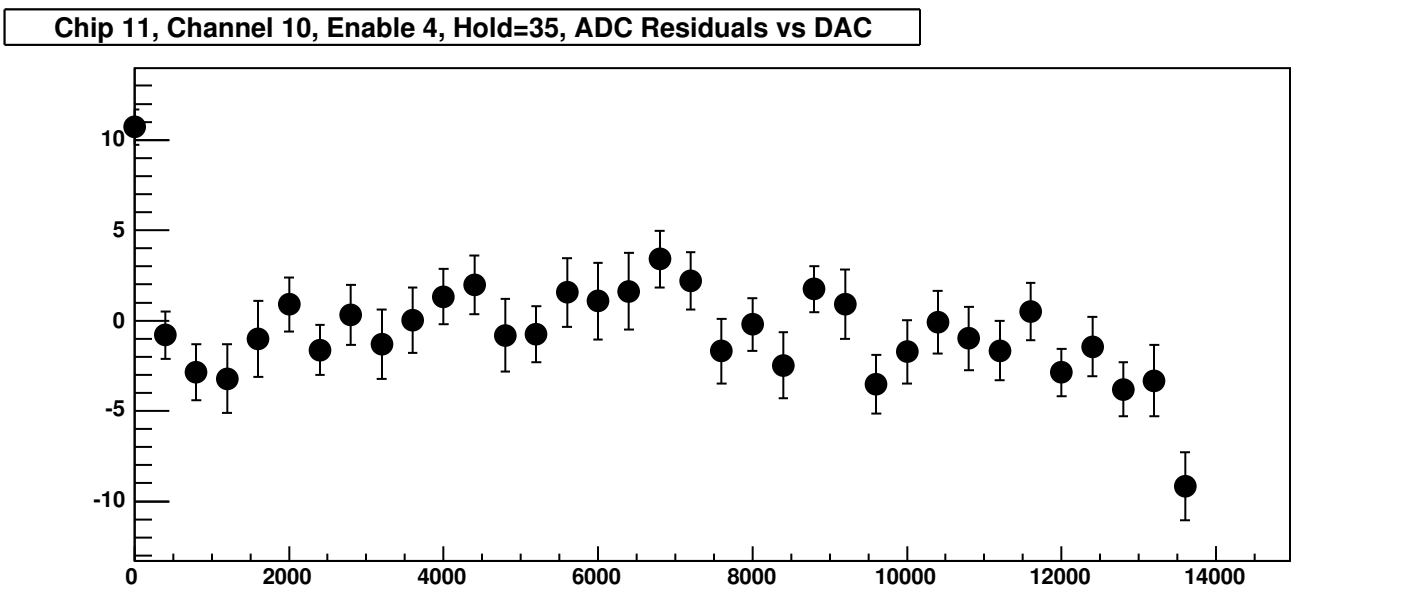

Chip 11, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 18.72 / 23
p0 -579.5 ± 0.7676
p1 0.04155 ± 0.0001189
-100
-200
-300
-400
-500
-600
0
2000
4000
6000
8000
10000
12000
14000

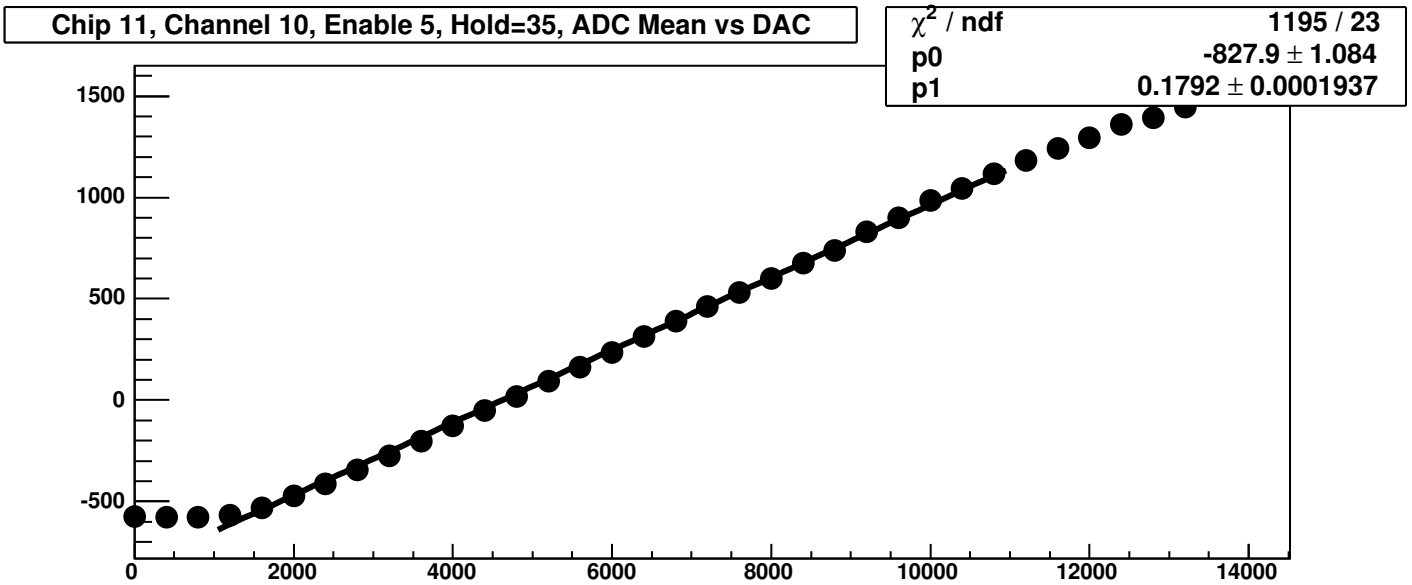

Chip 11, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

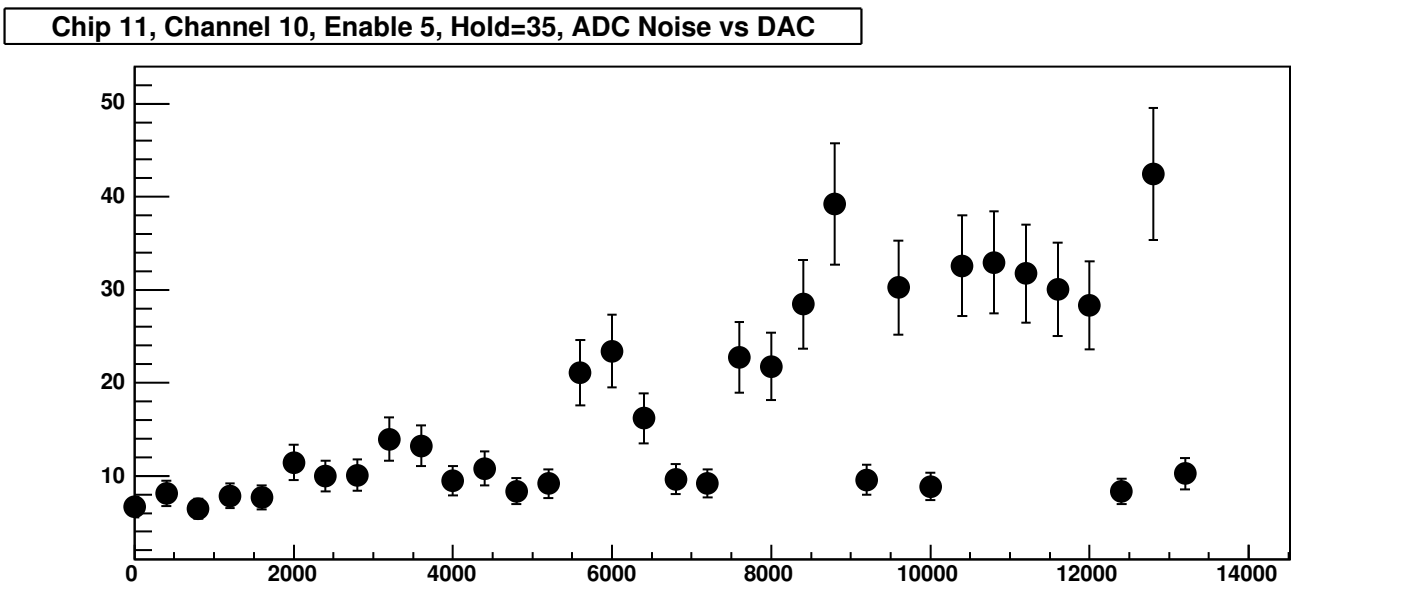

Chip 11, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

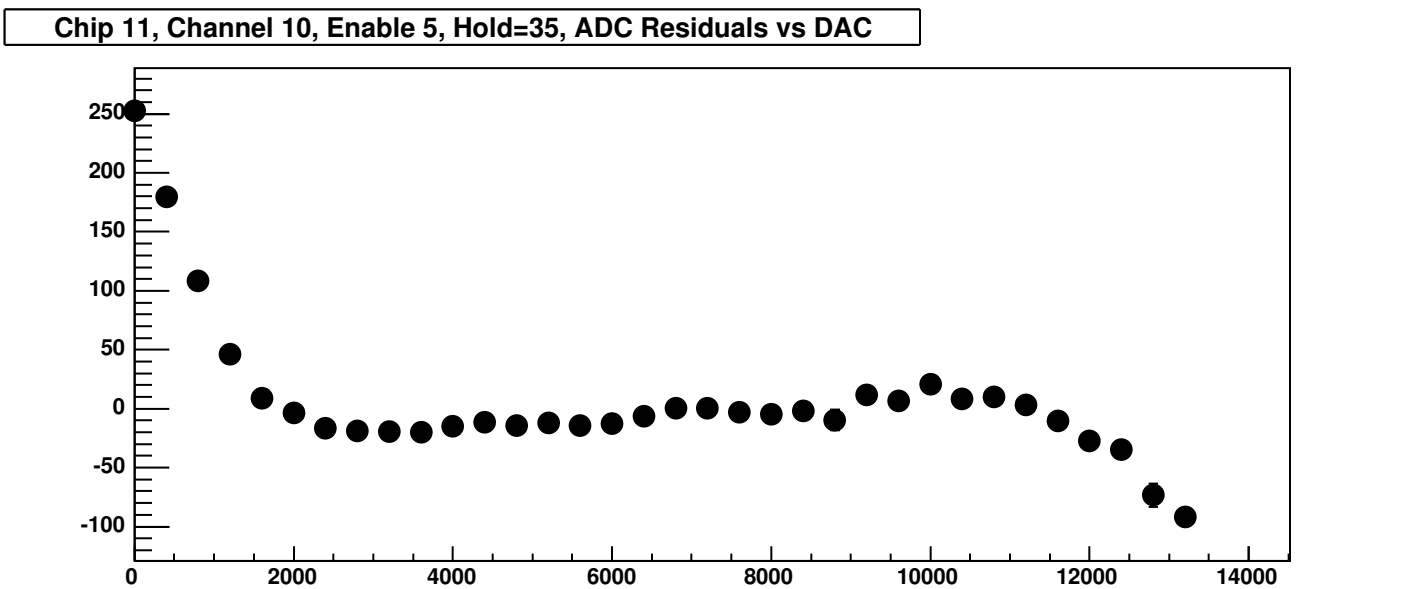

Chip 11, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.74 / 23
p0 -579.2 ± 0.7871
p1 0.02683 ± 0.0001175
-200
-250
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

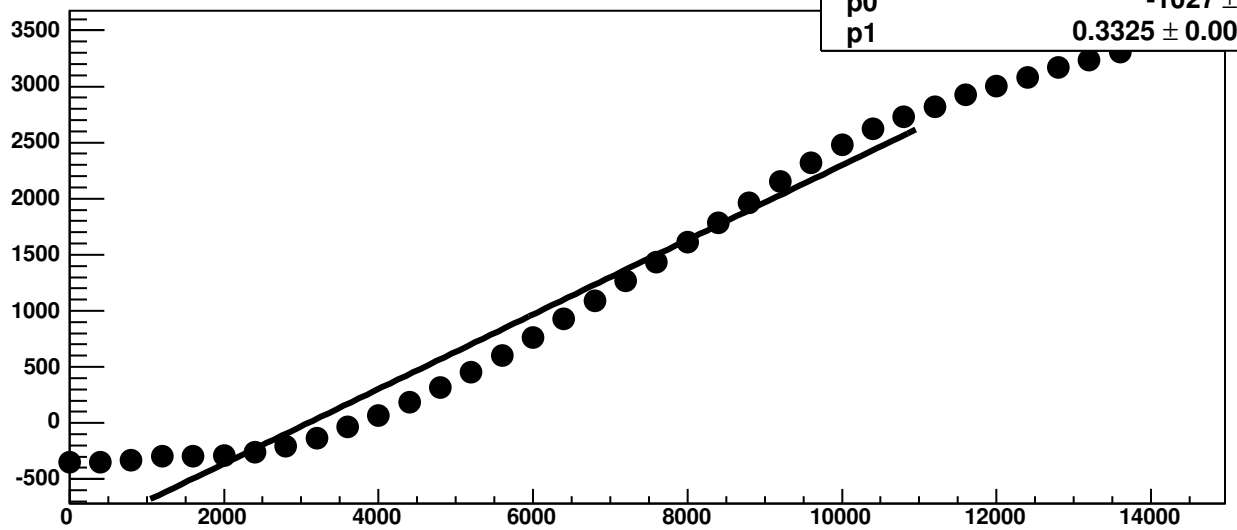

Chip 11, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1195 / 23
p0 -827.9 ± 1.084
p1 0.1792 ± 0.0001937
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

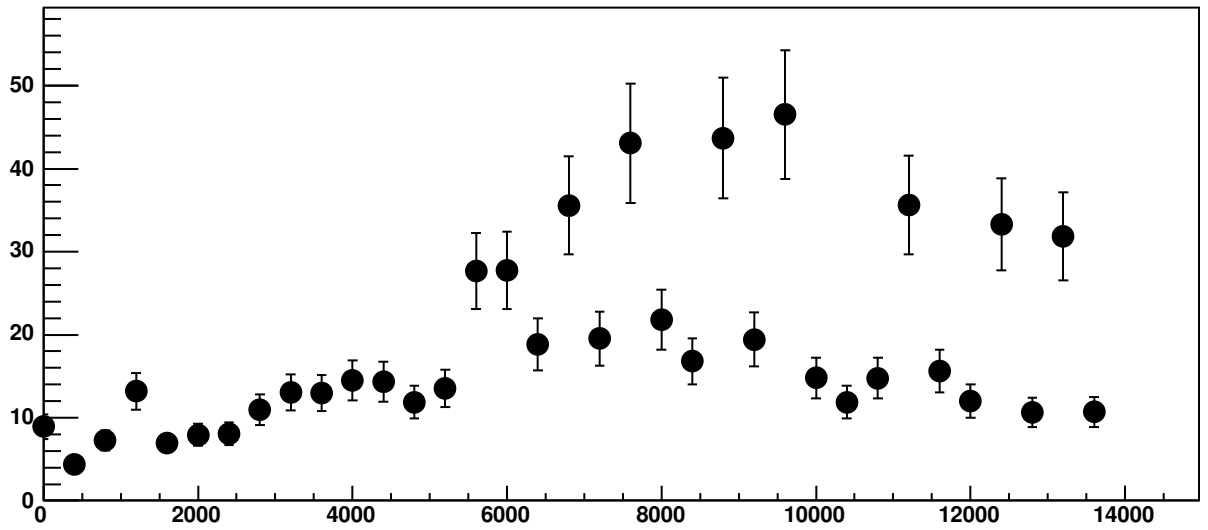

Chip 11, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
-100
0
2000
4000
6000
8000
10000
12000
14000

Chip 11, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC

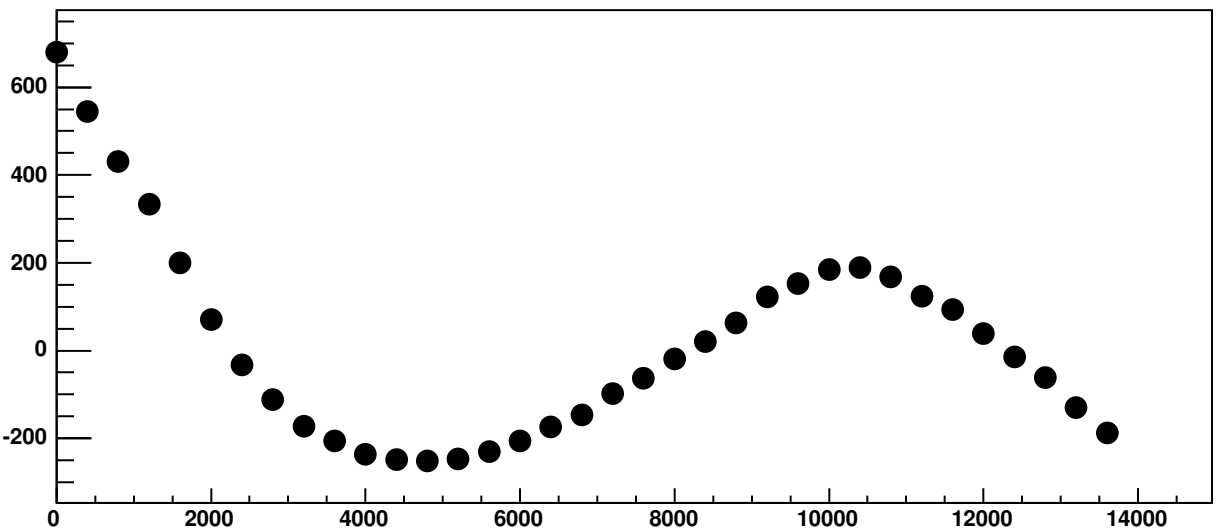
χ^2 / ndf	8.139e+04 / 23
p0	-1027 ± 1.098
p1	0.3325 ± 0.0002082



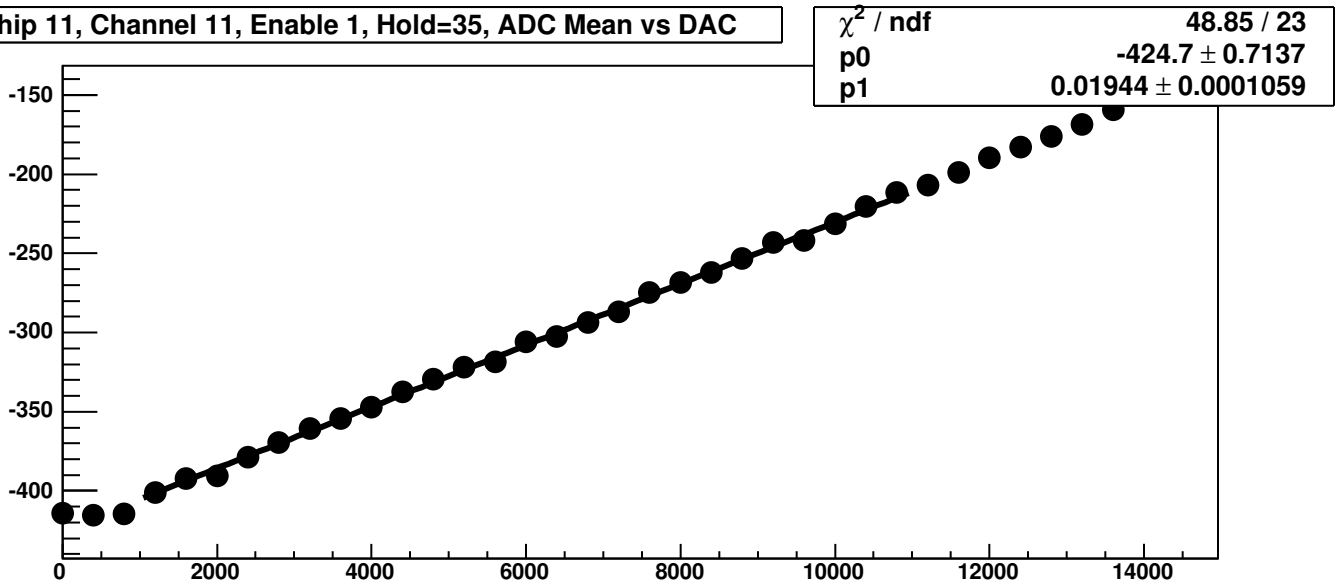
Chip 11, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC



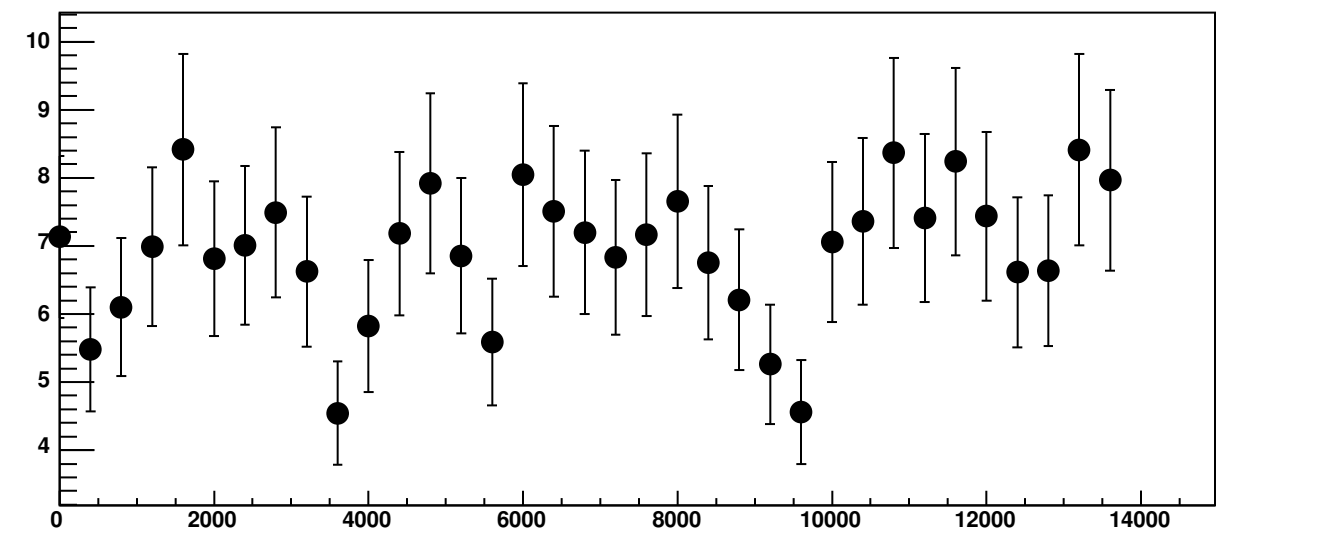
Chip 11, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC



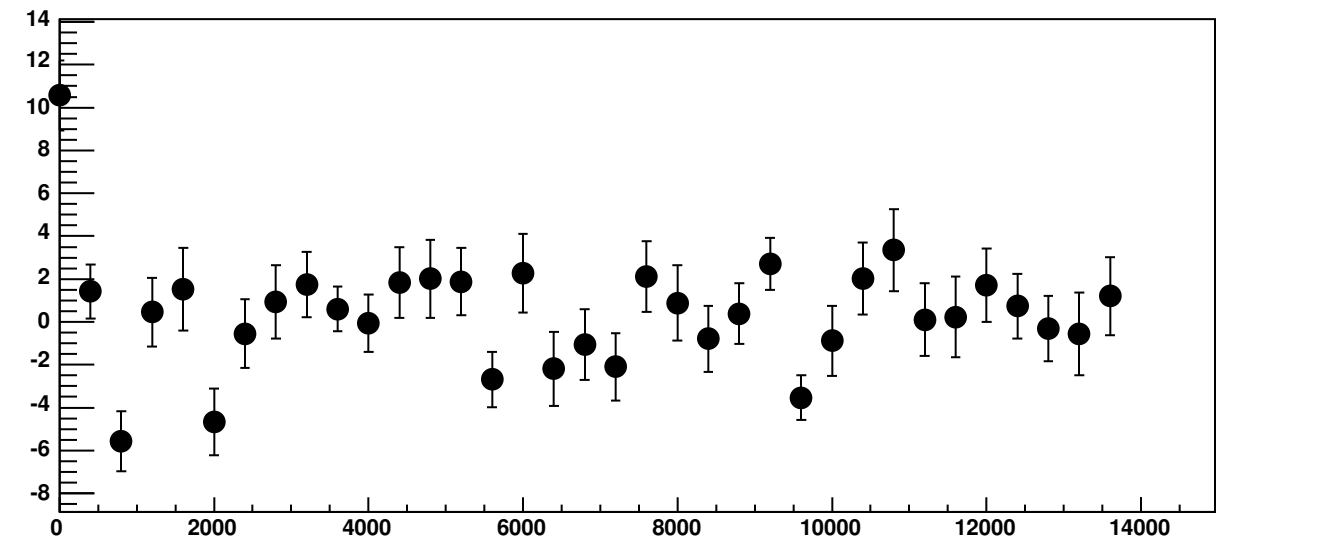
Chip 11, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

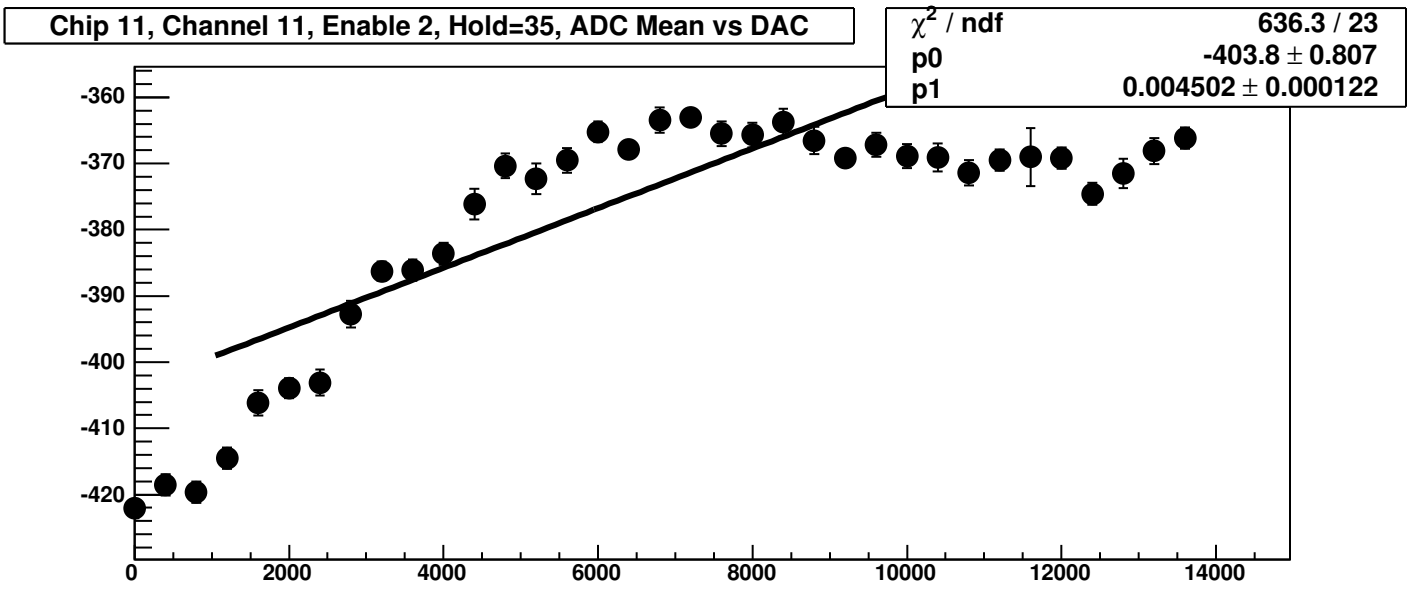


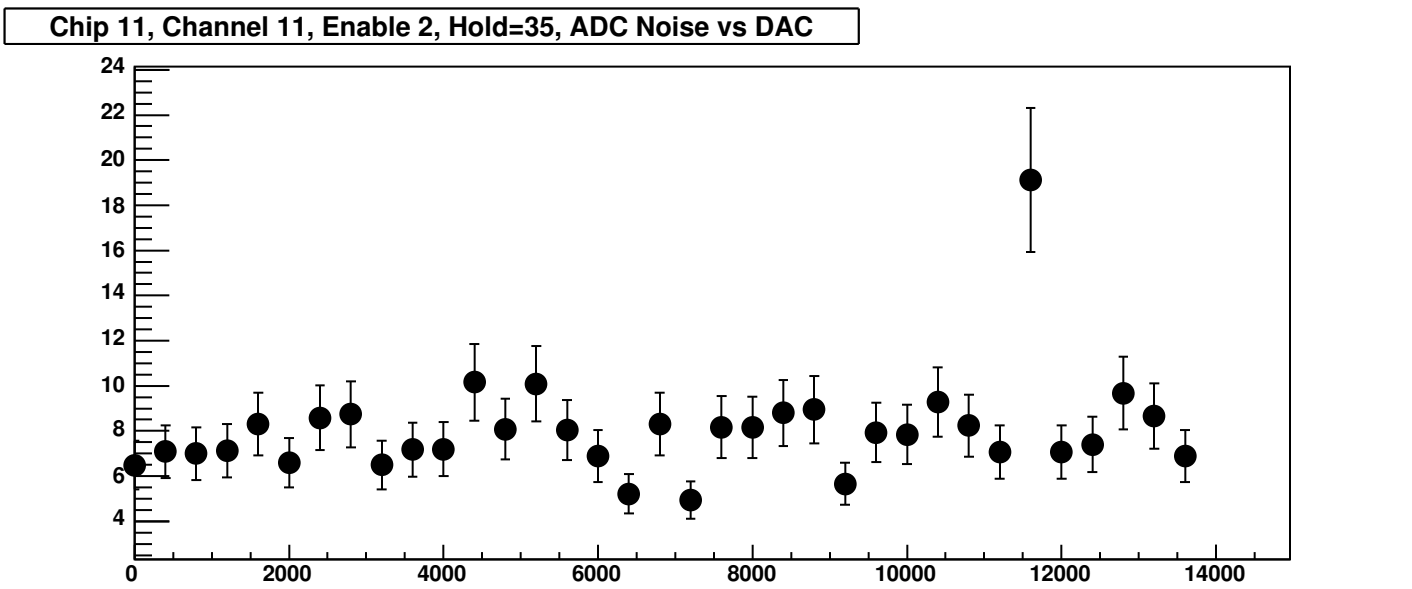
Chip 11, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

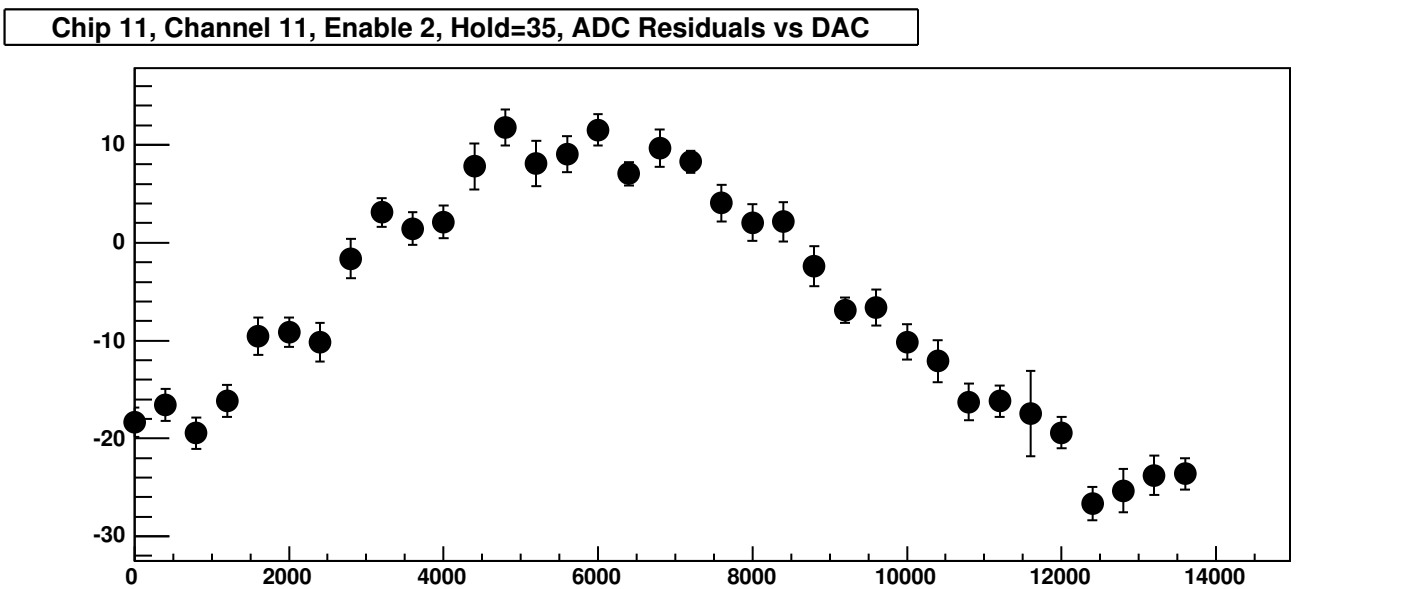


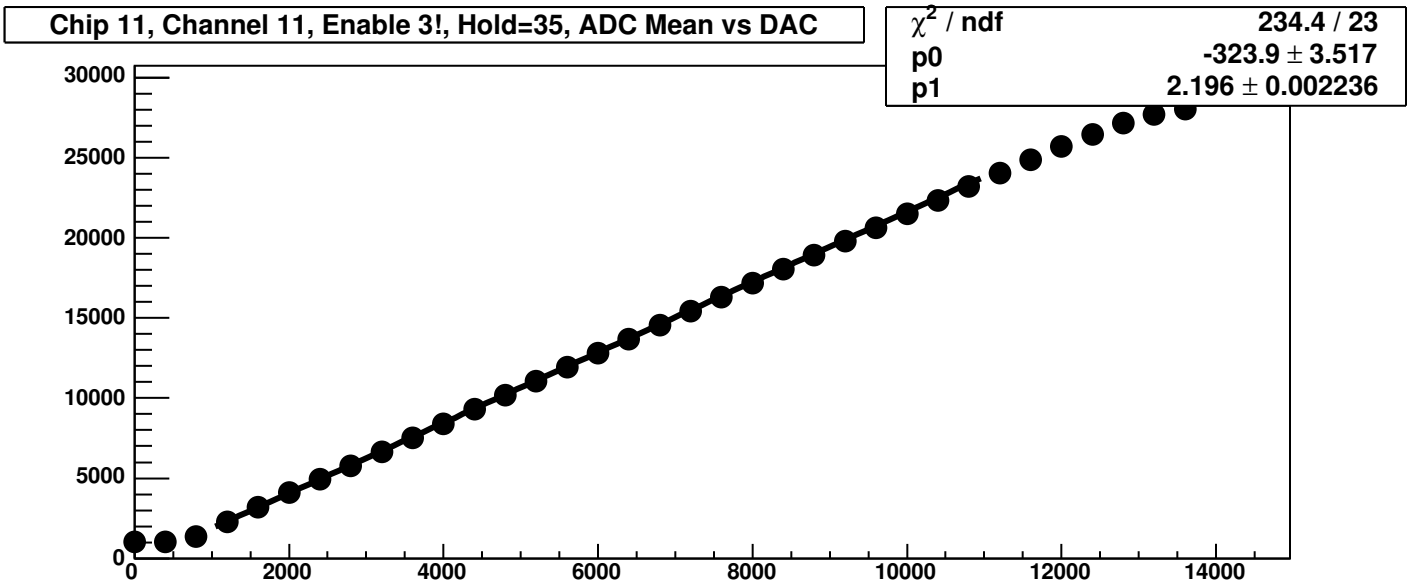
Chip 11, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

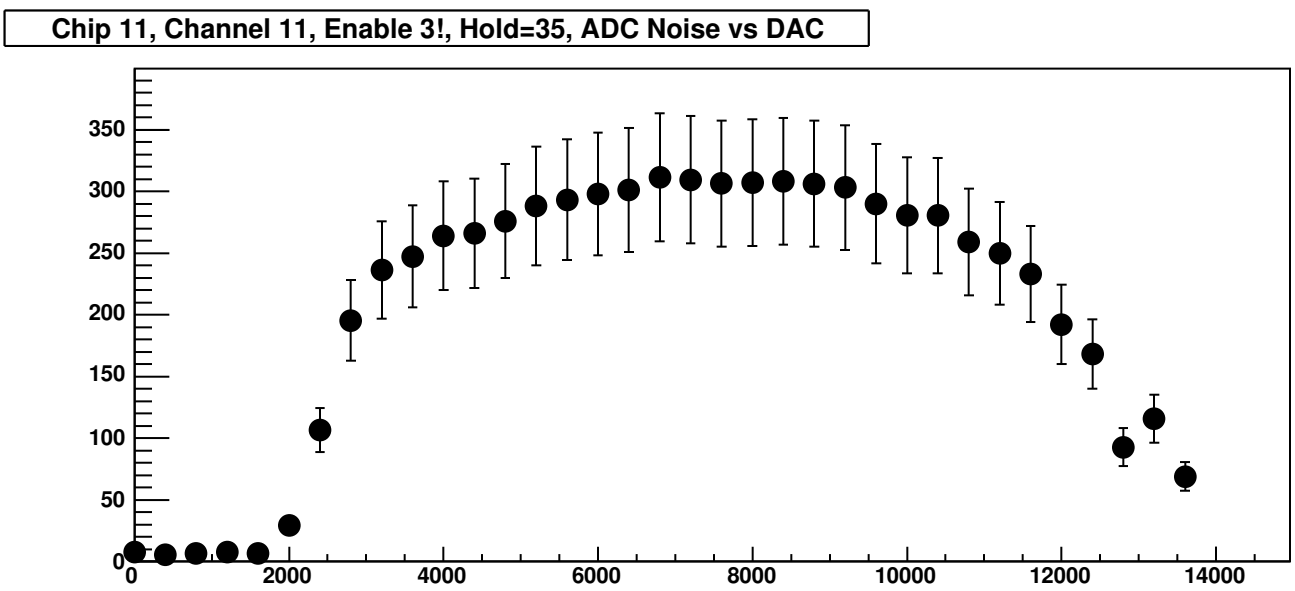


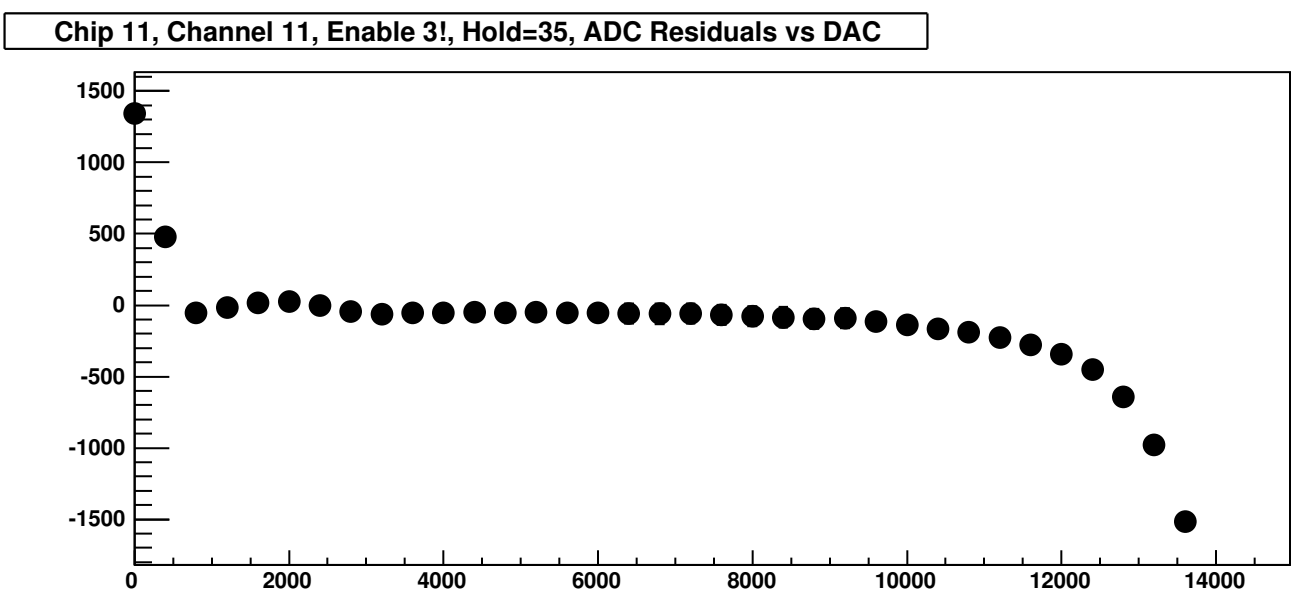

Chip 11, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 636.3 / 23
p0 -403.8 ± 0.807
p1 0.004502 ± 0.000122
-360
-370
-380
-390
-400
-410
-420
0
2000
4000
6000
8000
10000
12000
14000

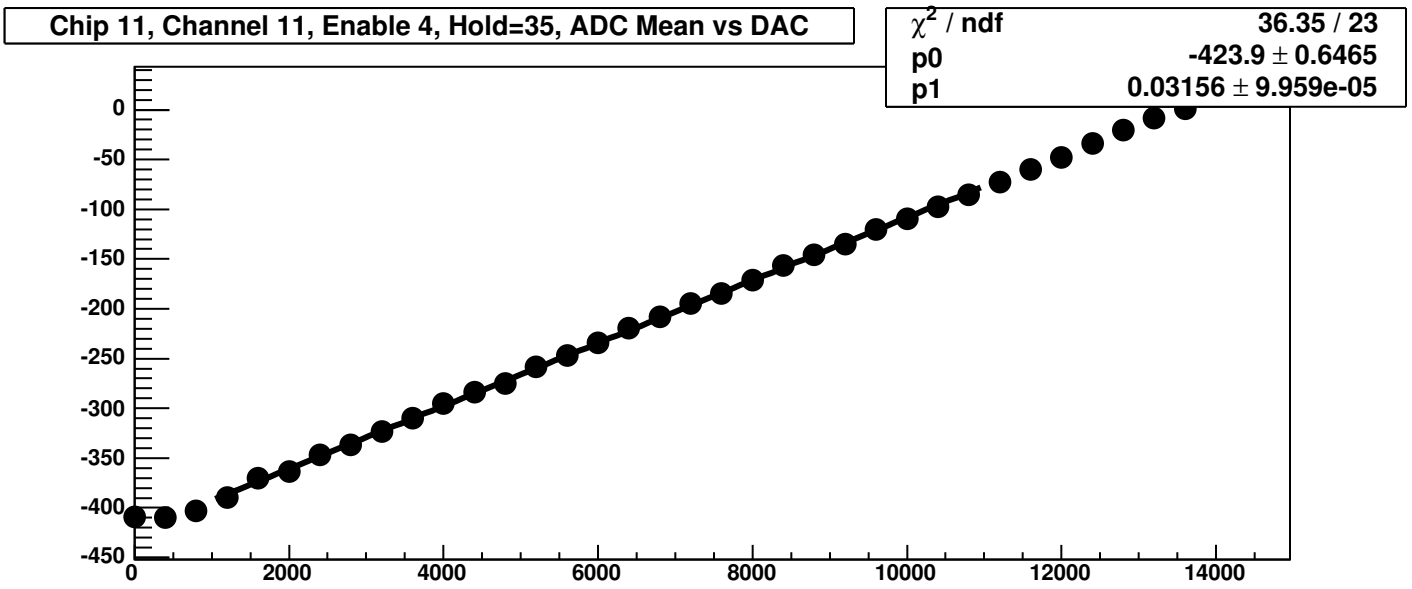

Chip 11, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
24
22
20
18
16
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

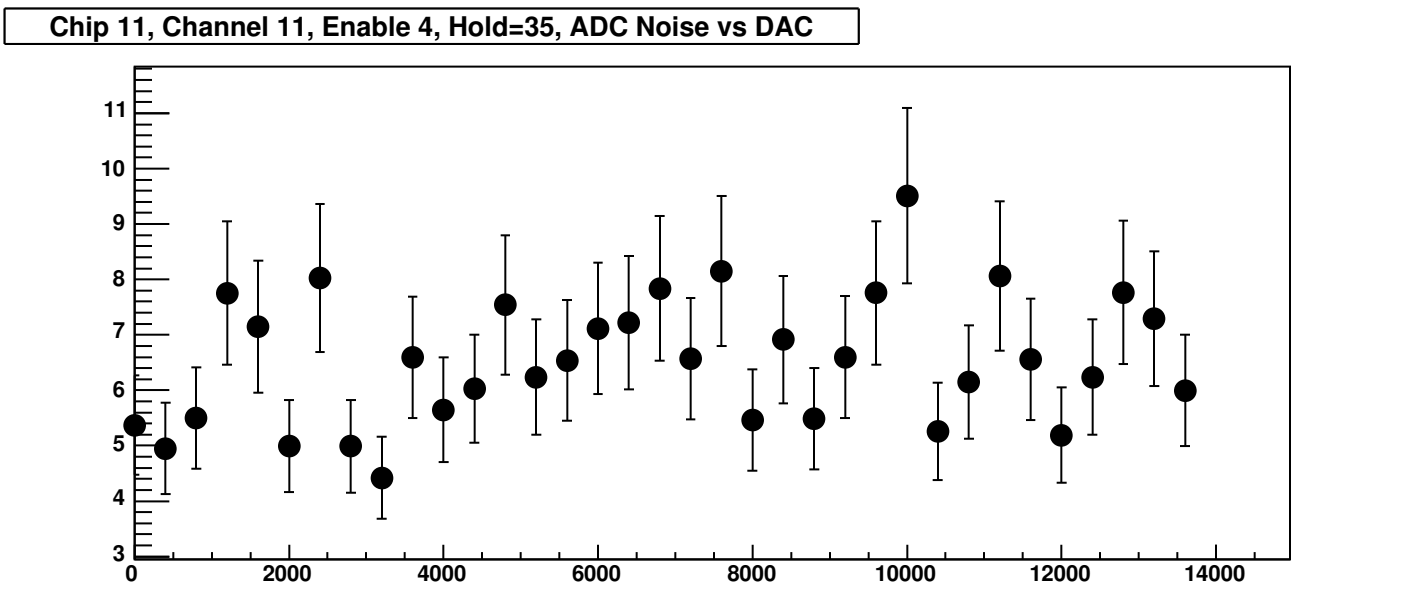

Chip 11, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

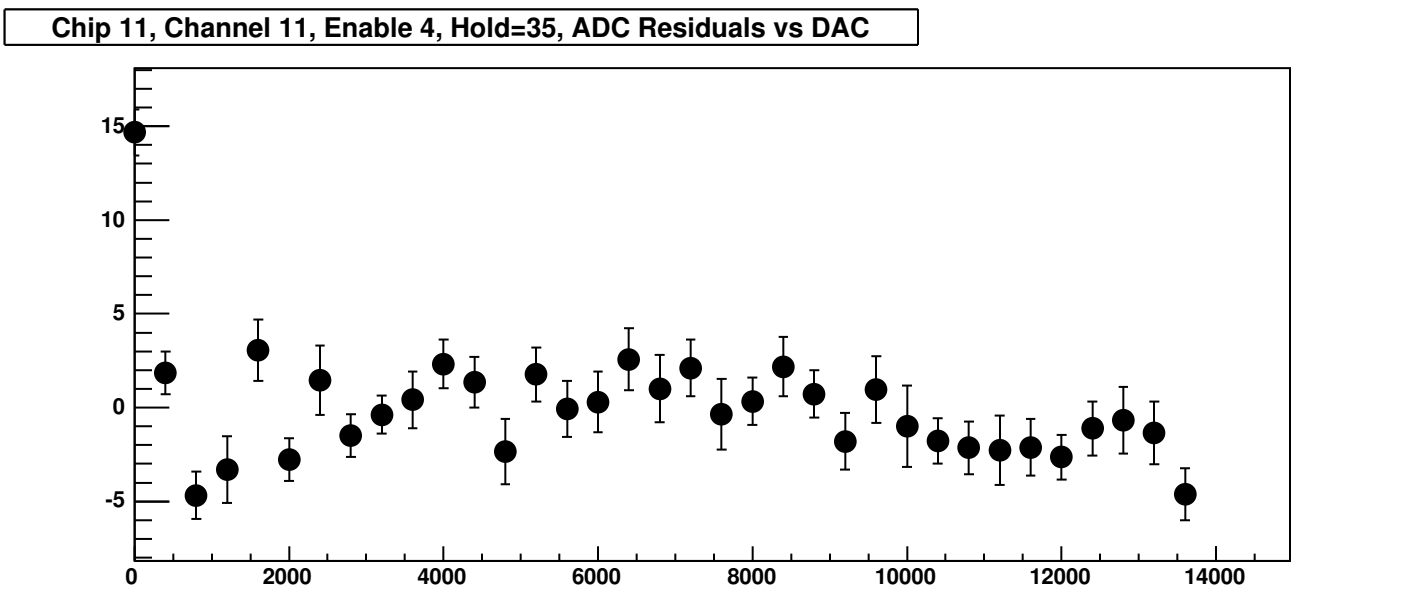

Chip 11, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 234.4 / 23
p0 -323.9 ± 3.517
p1 2.196 ± 0.002236
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

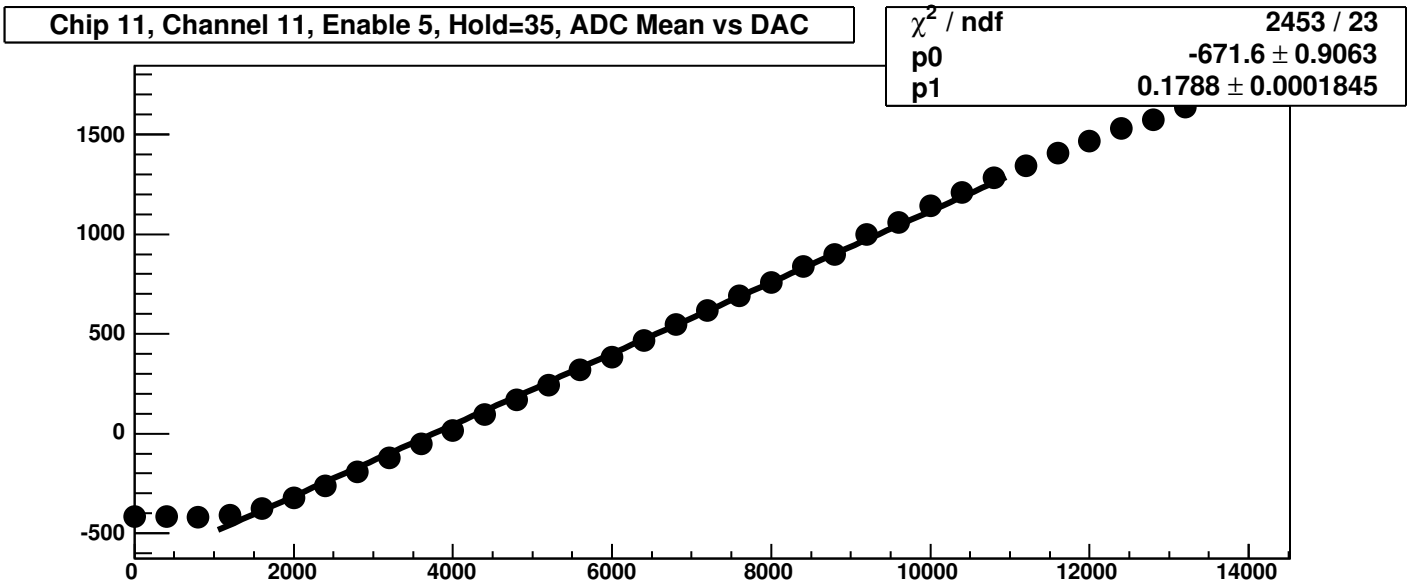

Chip 11, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

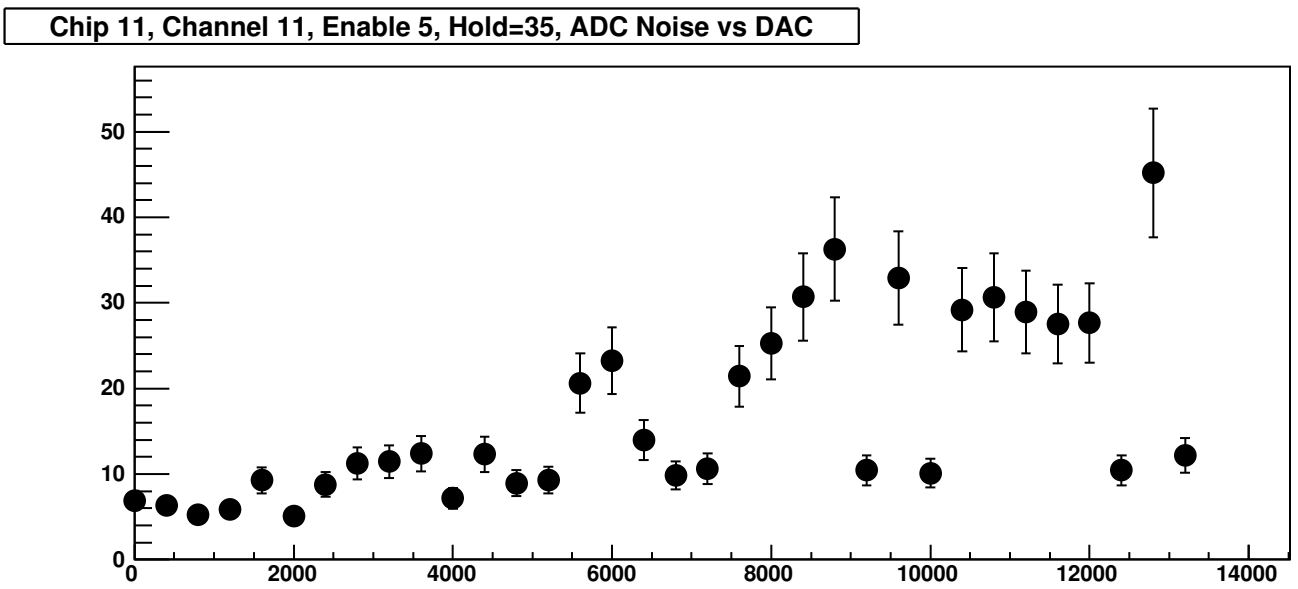

Chip 11, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

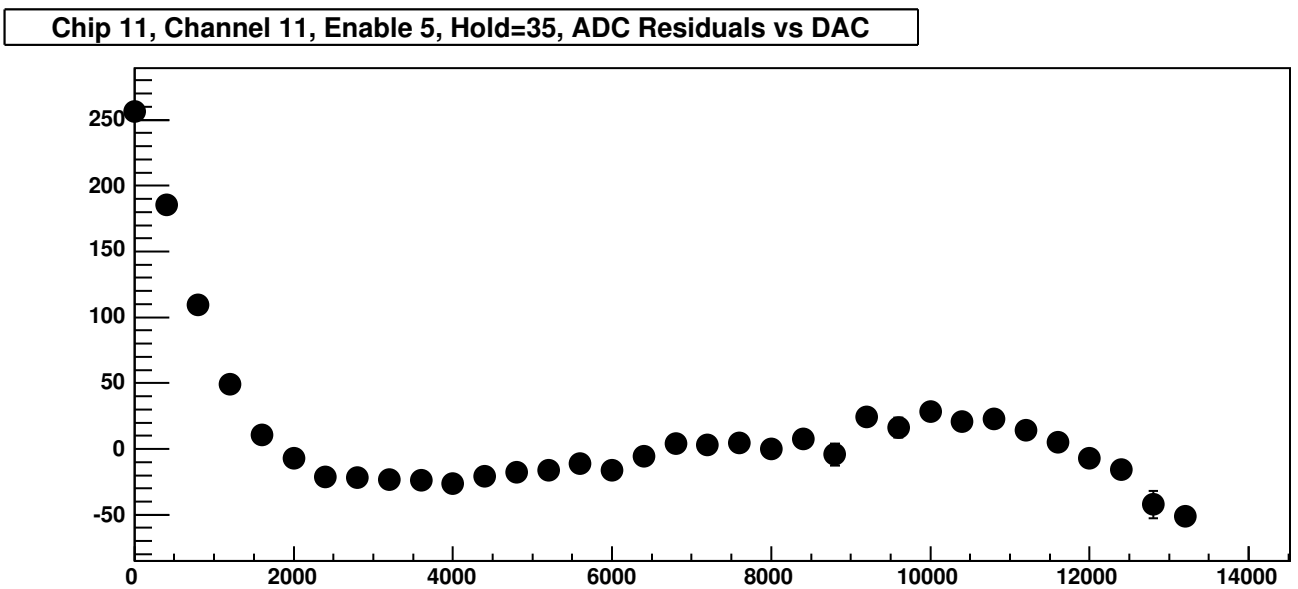

Chip 11, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 36.35 / 23
p0 -423.9 ± 0.6465
p1 0.03156 ± 9.959e-05
0
-50
-100
-150
-200
-250
-300
-350
-400
-450
0
2000
4000
6000
8000
10000
12000
14000

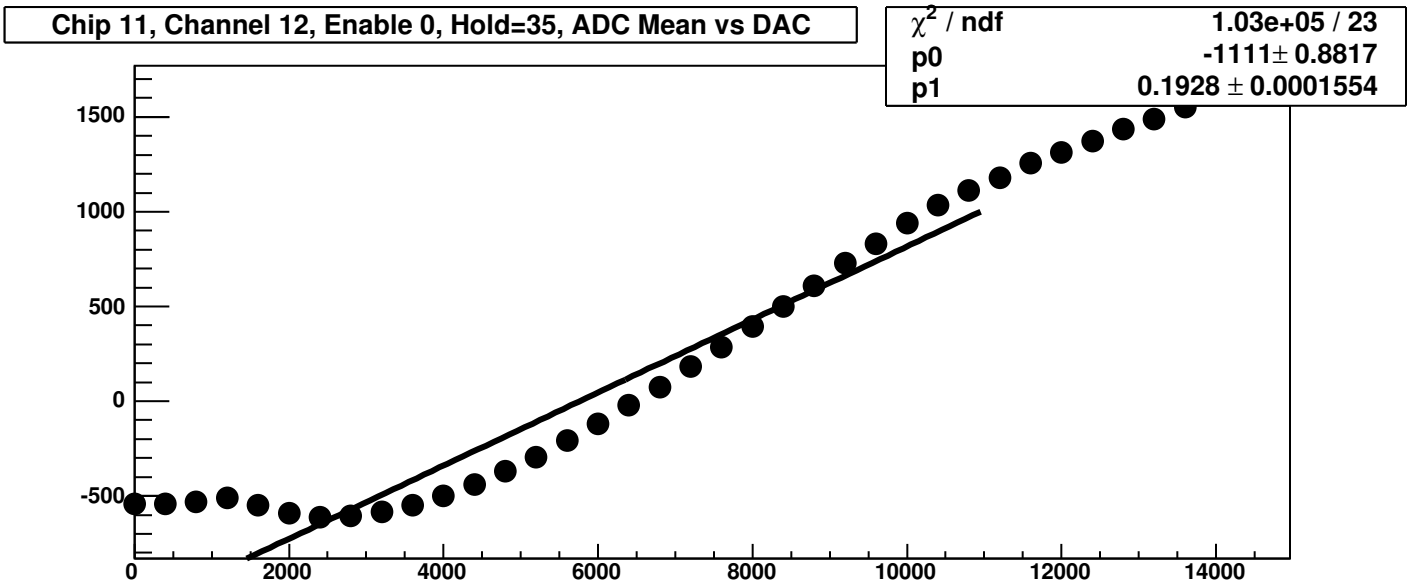

Chip 11, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

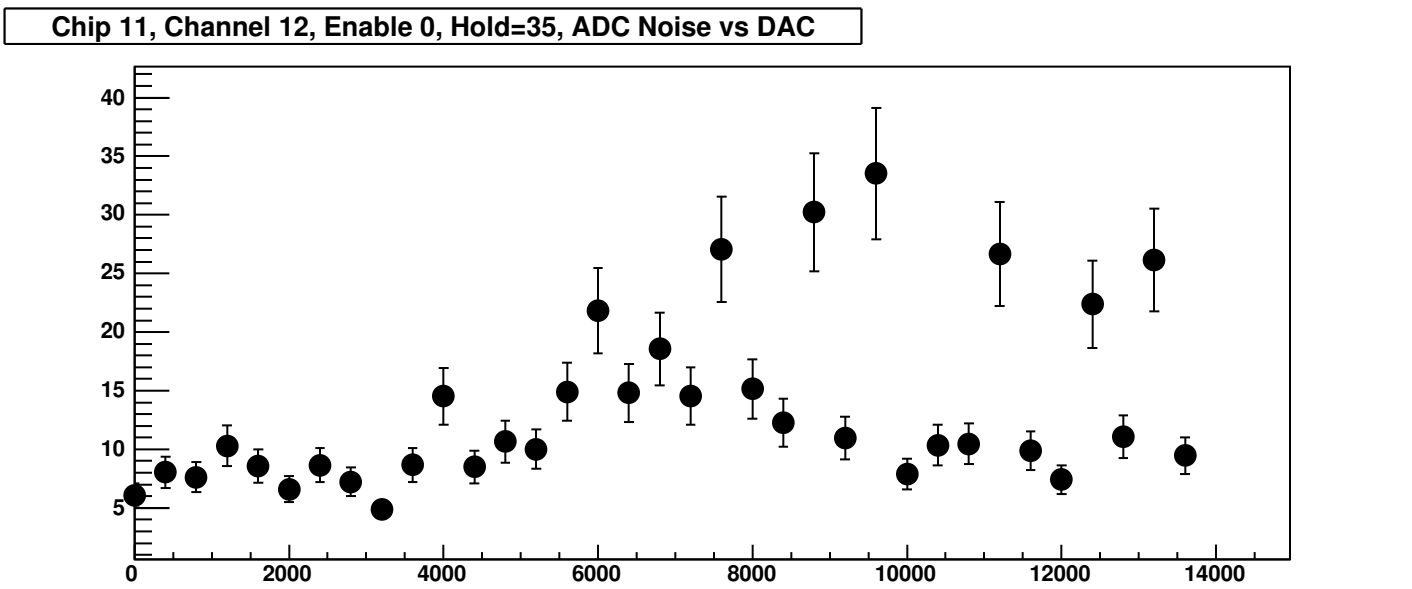

Chip 11, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
2000
4000
6000
8000
10000
12000
14000

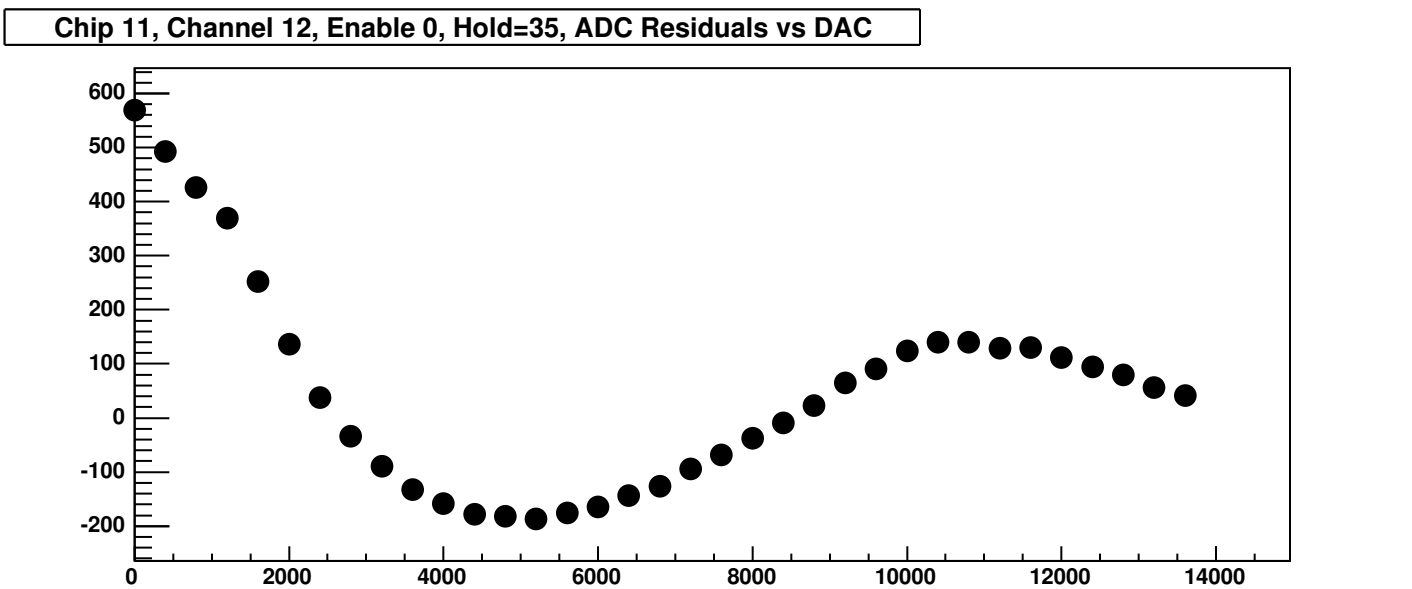

Chip 11, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2453 / 23
p0 -671.6 ± 0.9063
p1 0.1788 ± 0.0001845
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

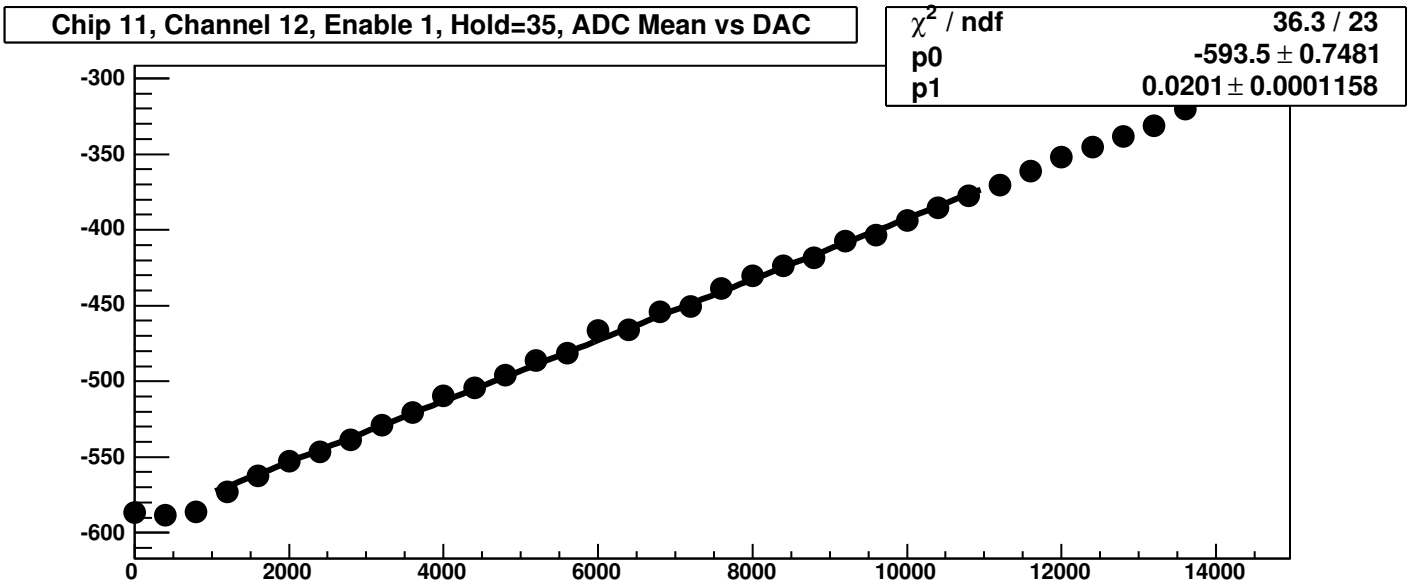

Chip 11, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

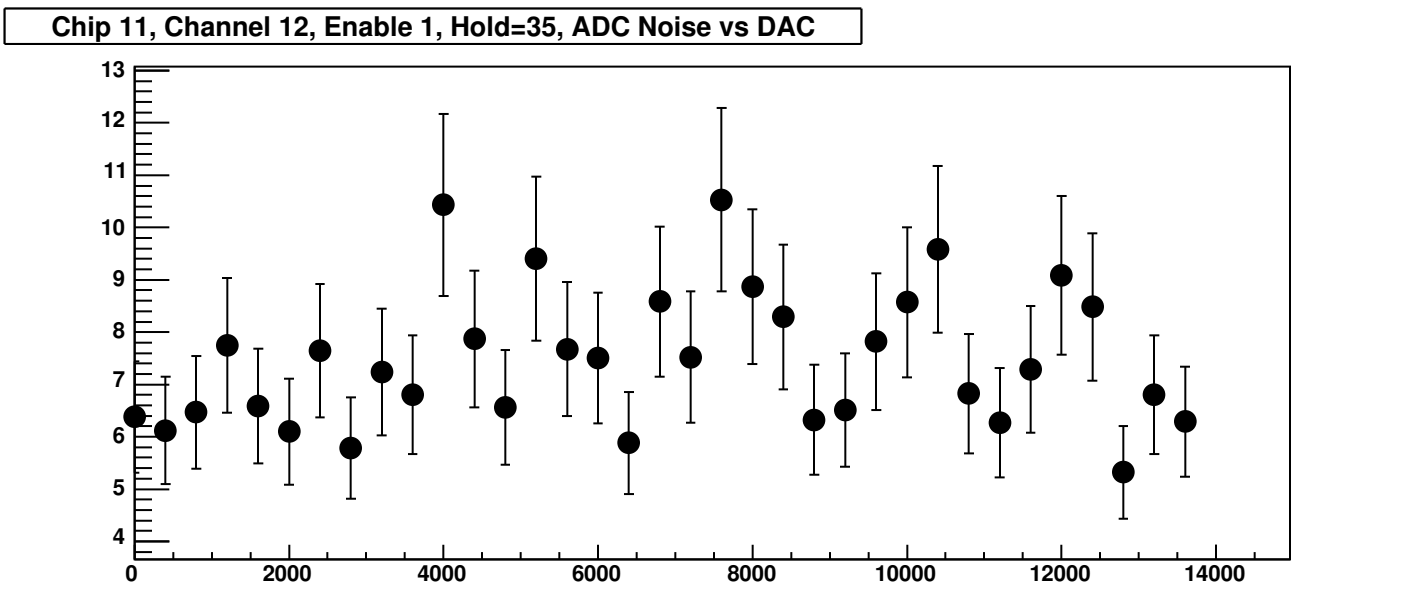

Chip 11, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

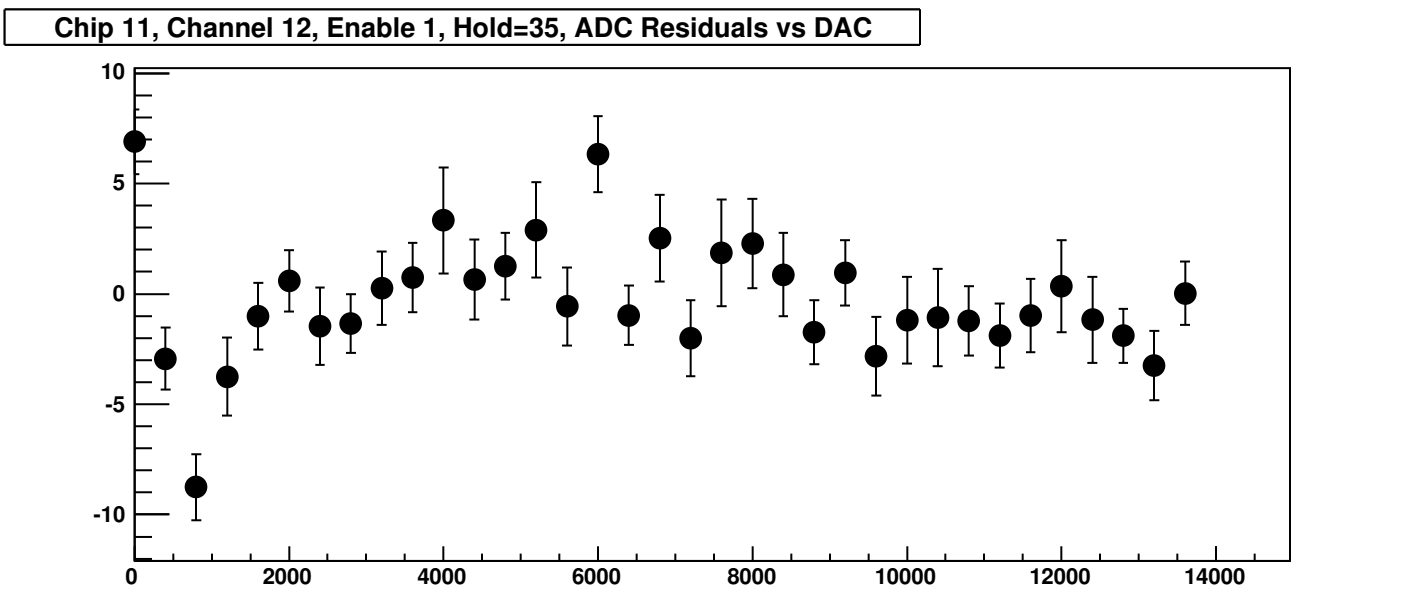

Chip 11, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 1.03e+05 / 23
p0 -1111 ± 0.8817
p1 0.1928 ± 0.0001554
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

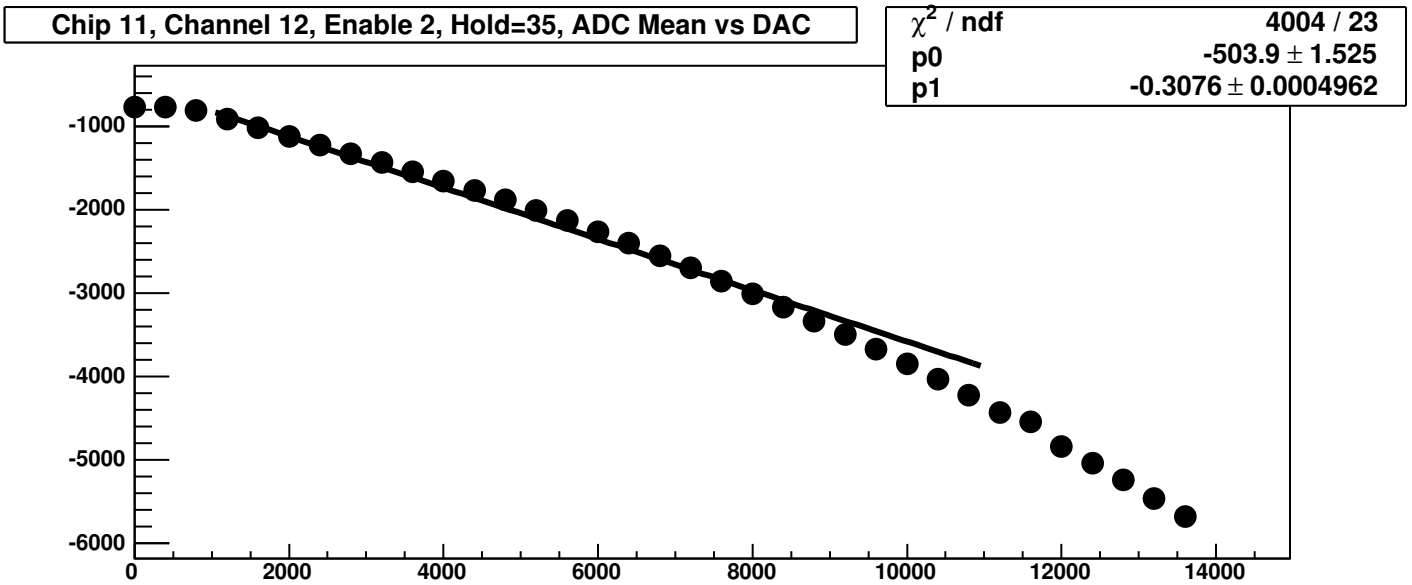

Chip 11, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

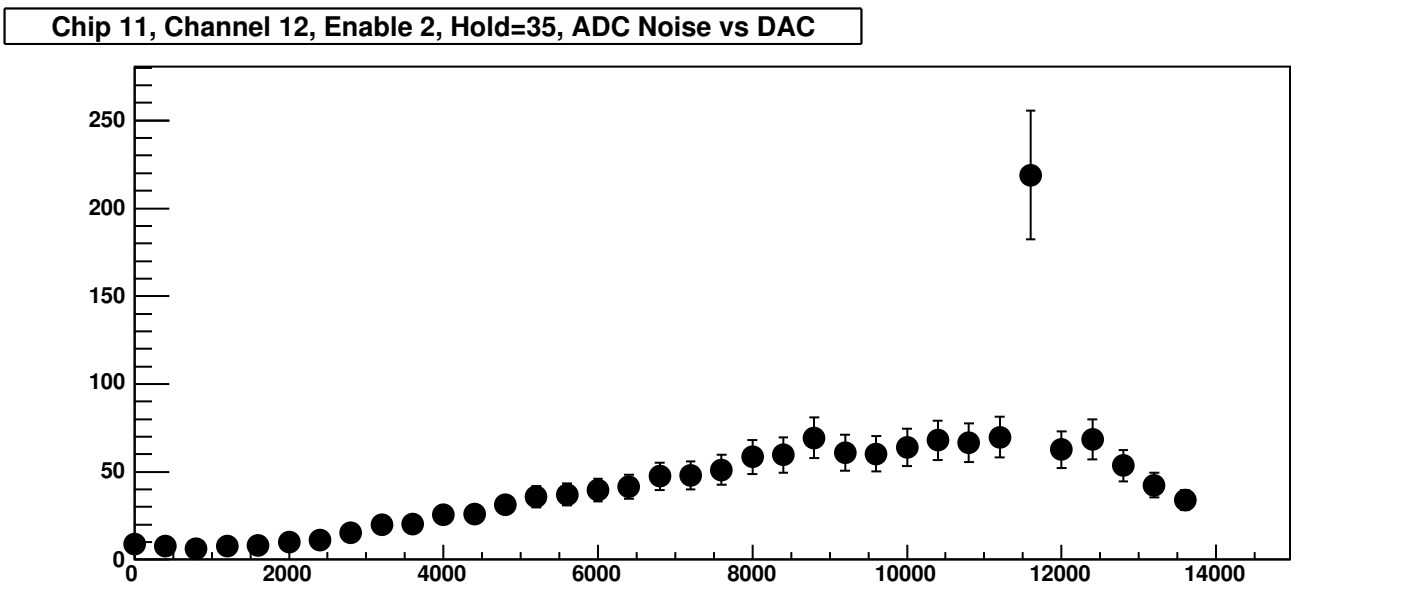

Chip 11, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

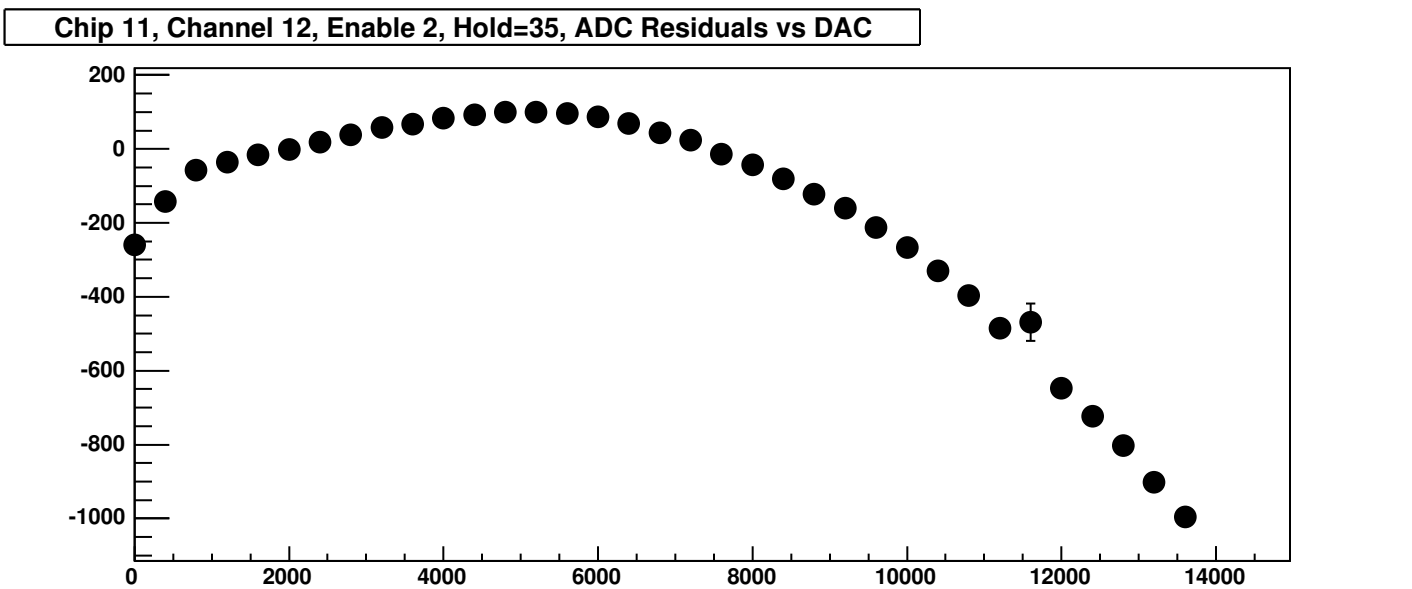

Chip 11, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 36.3 / 23
p0 -593.5 ± 0.7481
p1 0.0201 ± 0.0001158
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

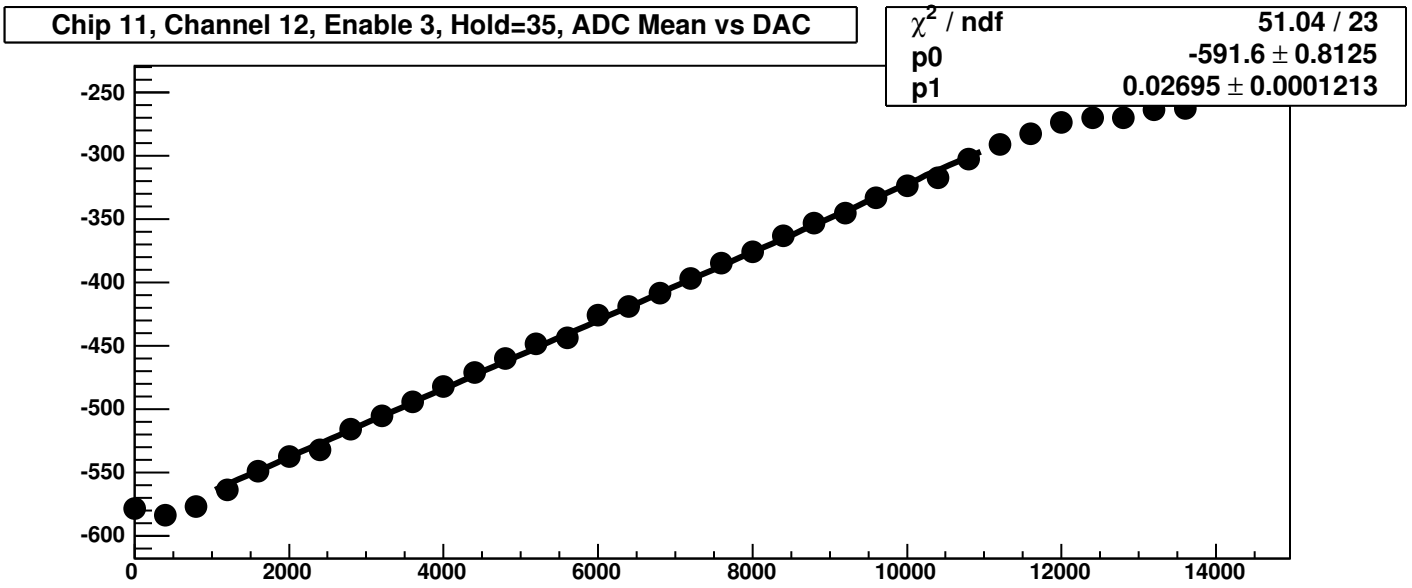

Chip 11, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

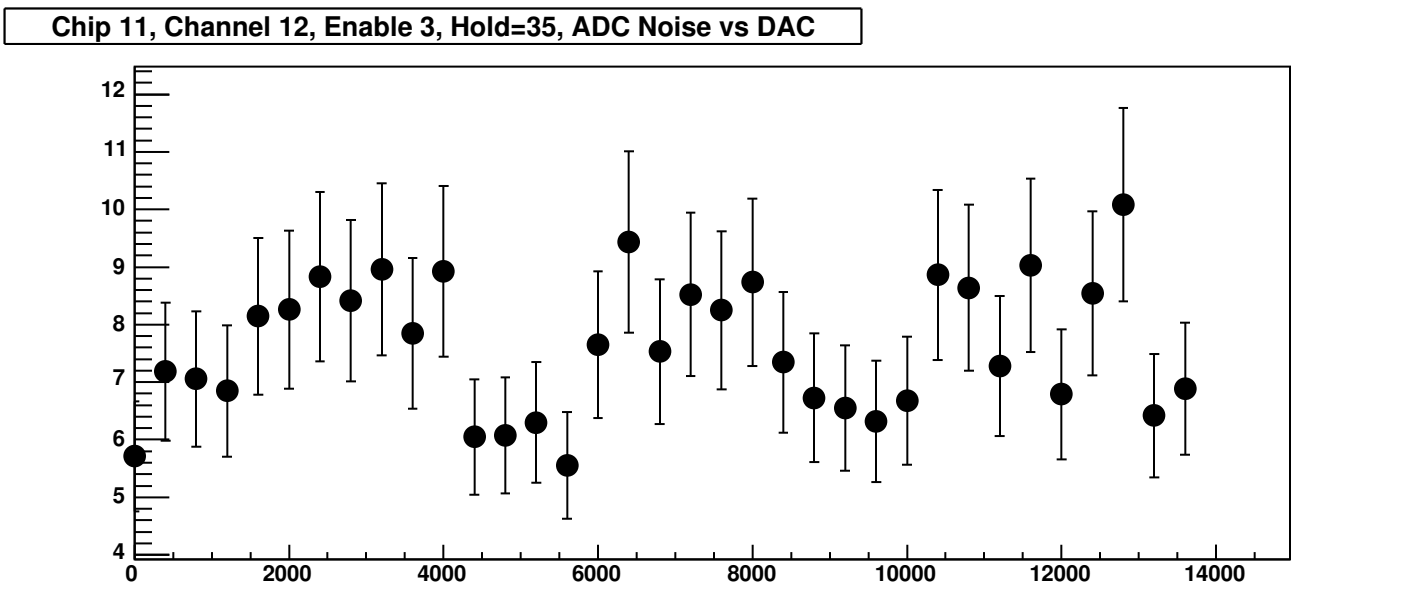

Chip 11, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

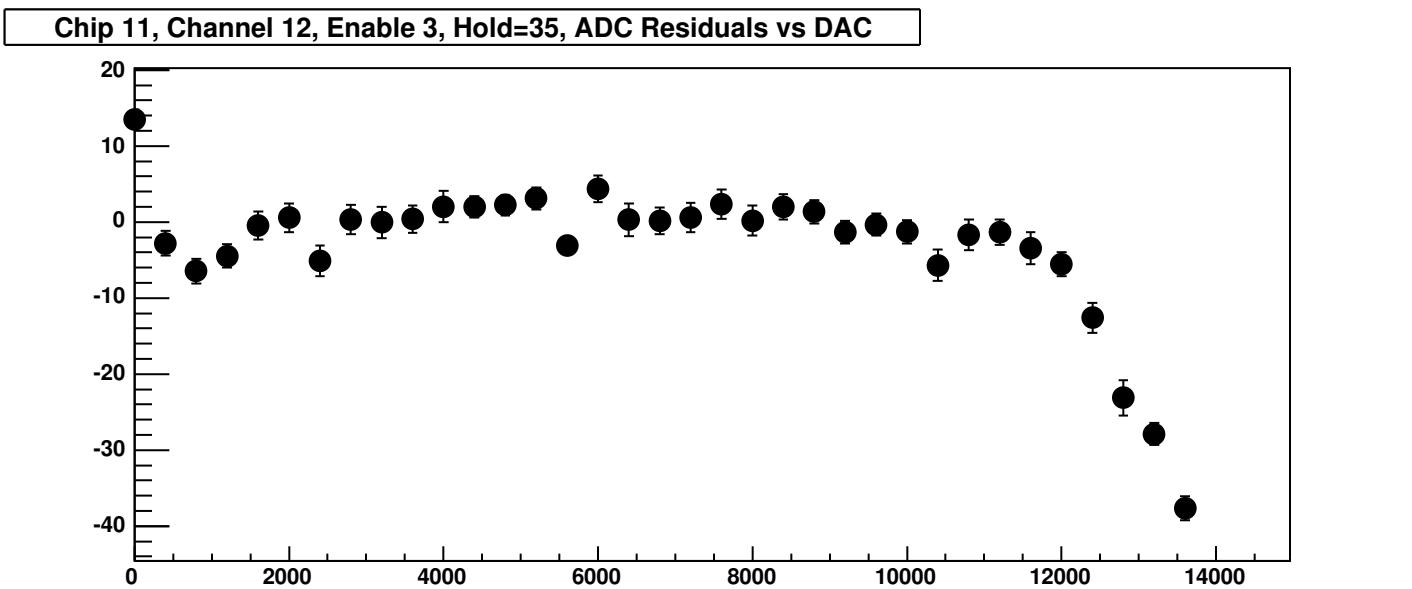

Chip 11, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 4004 / 23
p0 -503.9 ± 1.525
p1 -0.3076 ± 0.0004962
-1000
-2000
-3000
-4000
-5000
-6000
0
2000
4000
6000
8000
10000
12000
14000

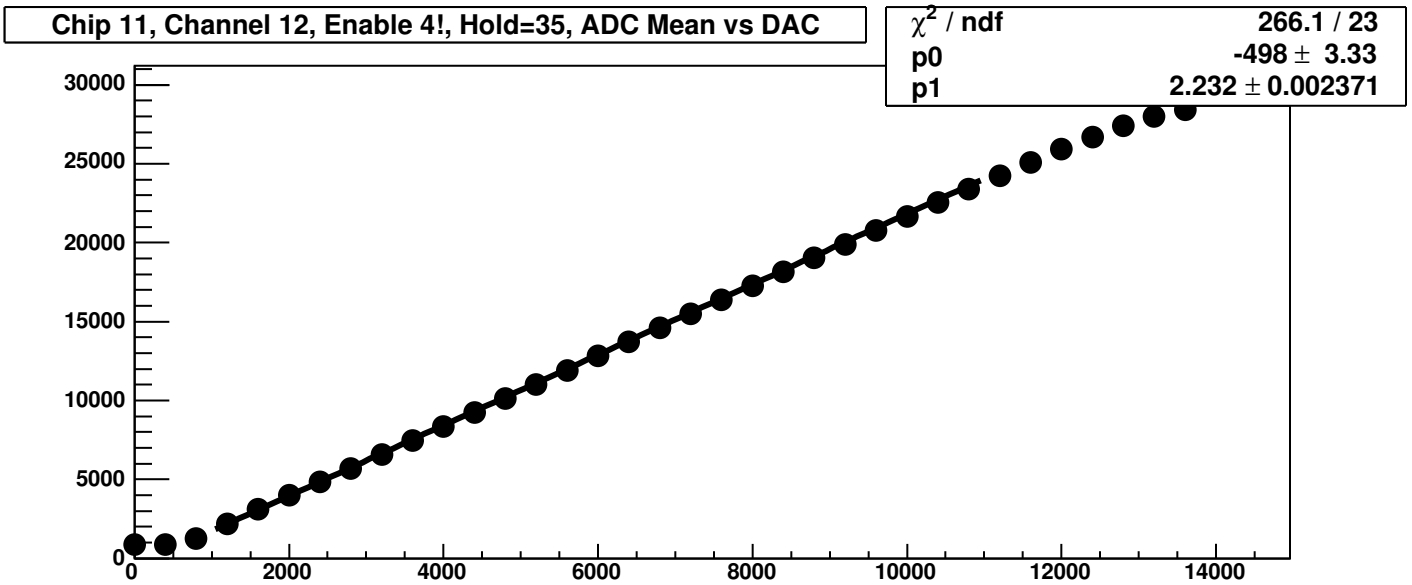

Chip 11, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

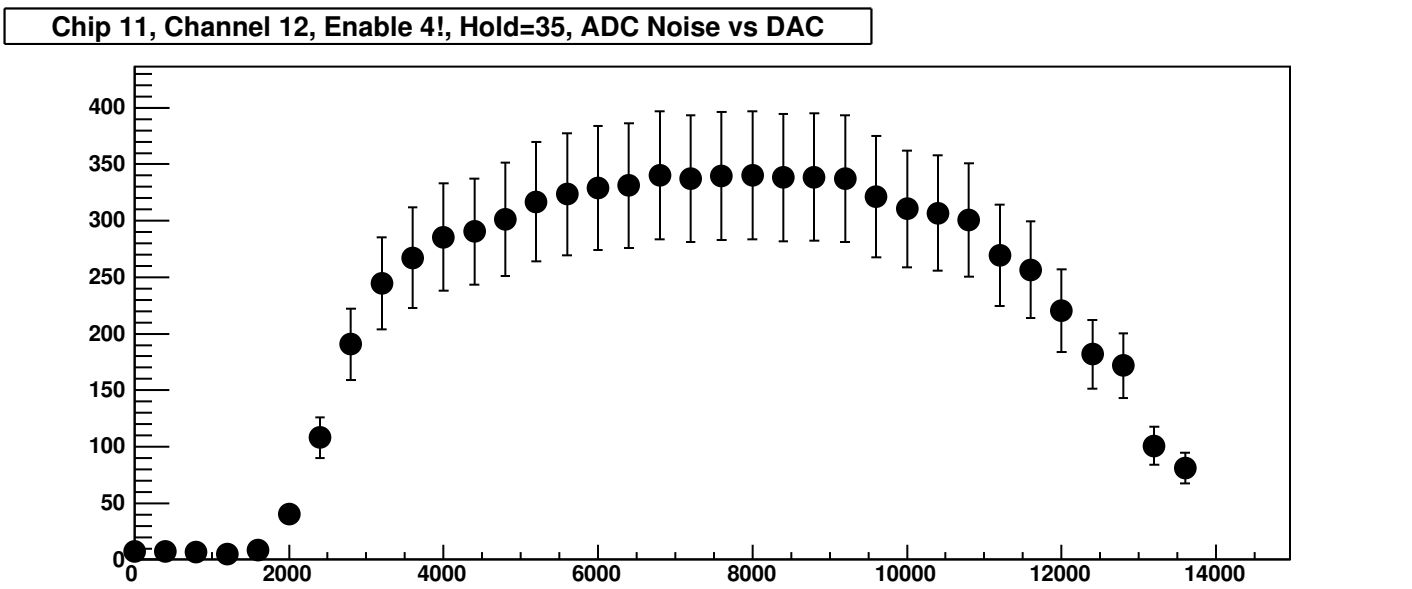

Chip 11, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

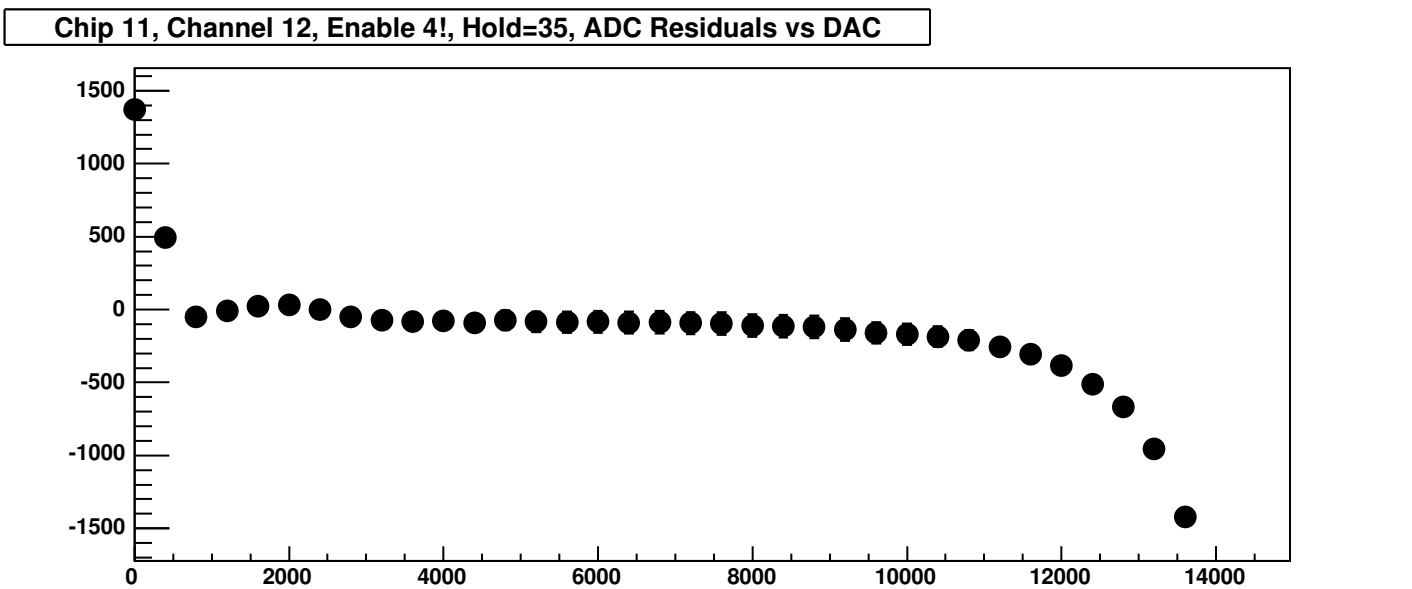

Chip 11, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 51.04 / 23
p0 -591.6 ± 0.8125
p1 0.02695 ± 0.0001213
-250
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

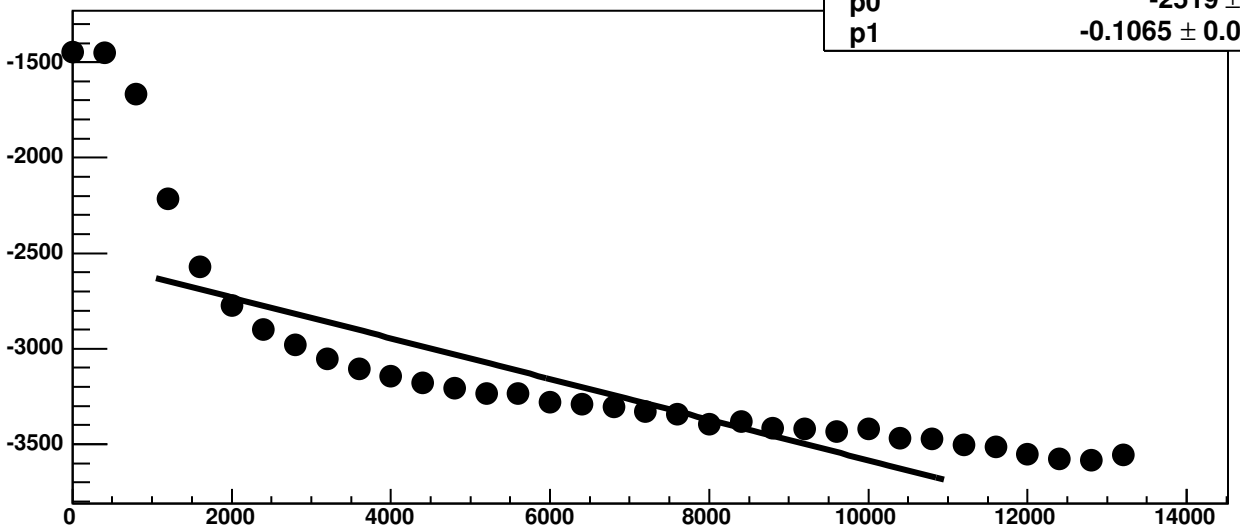

Chip 11, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 266.1 / 23
p0 -498 ± 3.33
p1 2.232 ± 0.002371
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

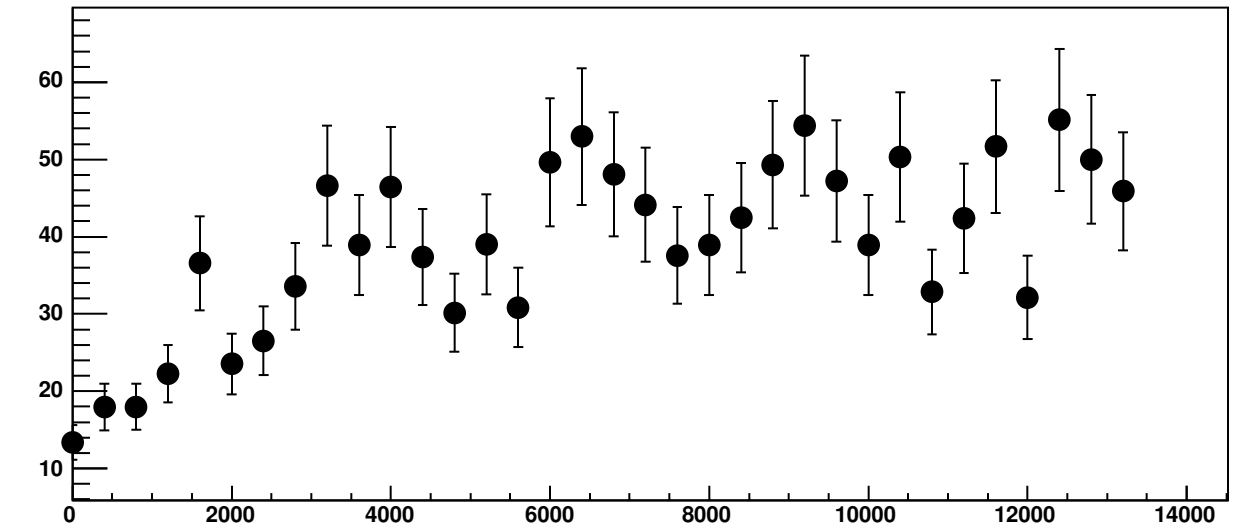

Chip 11, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

Chip 11, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC

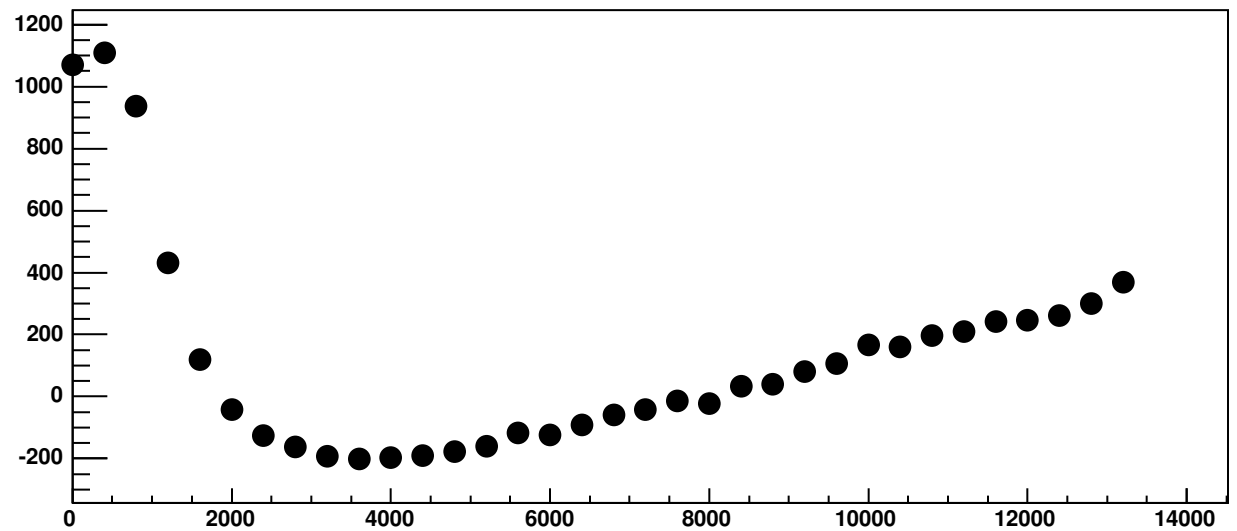
χ^2 / ndf	1.284e+04 / 23
p0	-2519 ± 3.239
p1	-0.1065 ± 0.000549

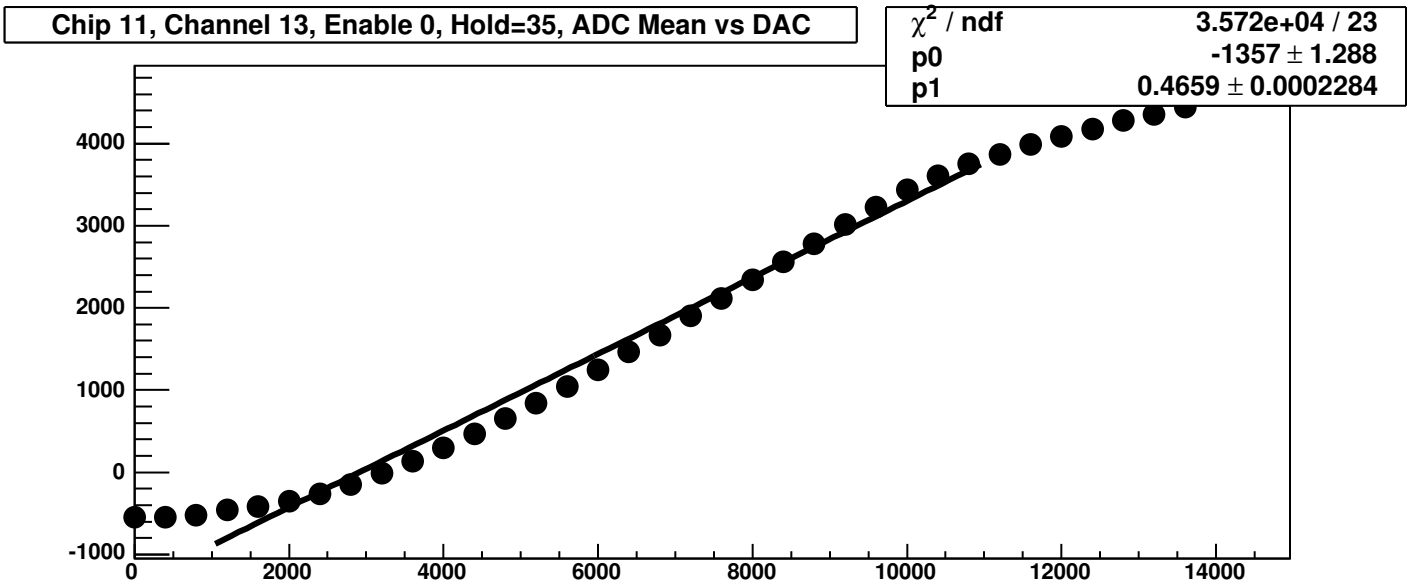


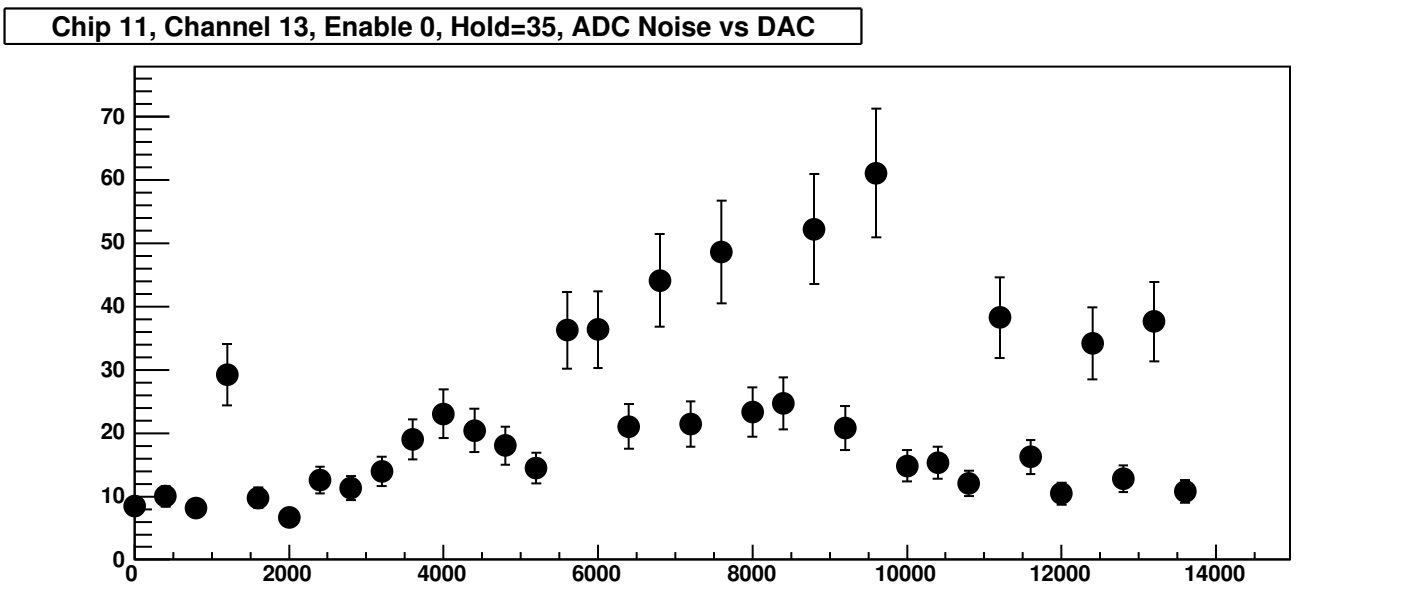
Chip 11, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC

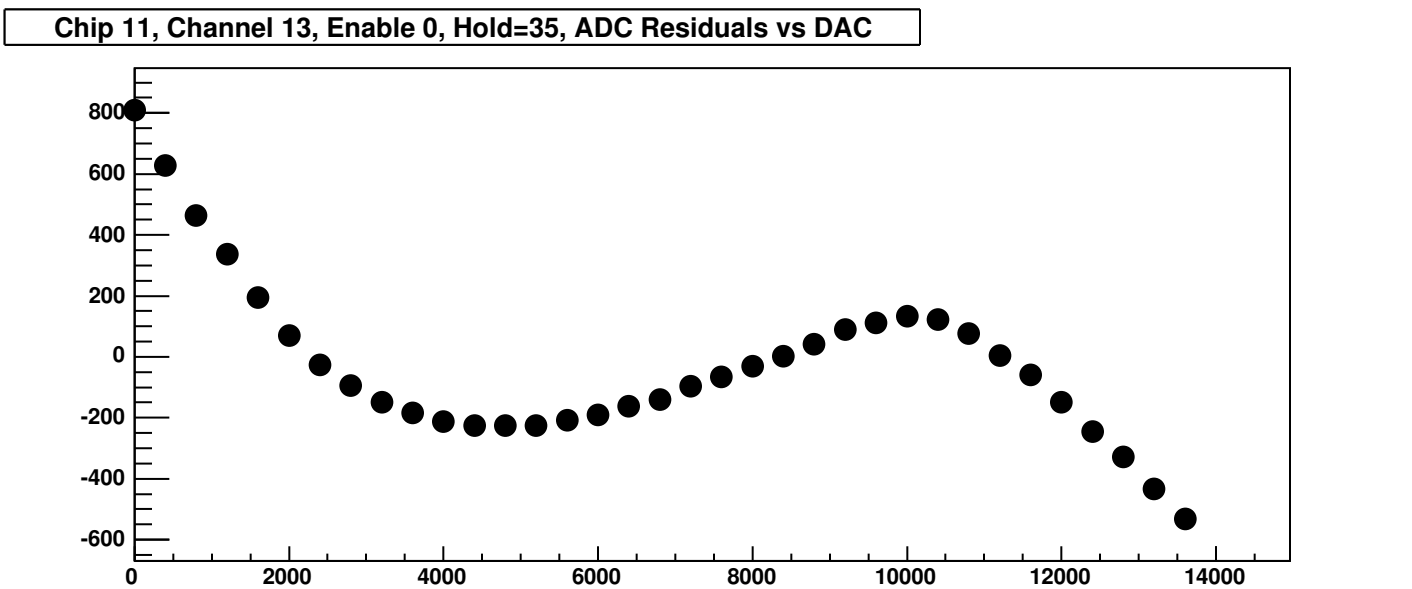


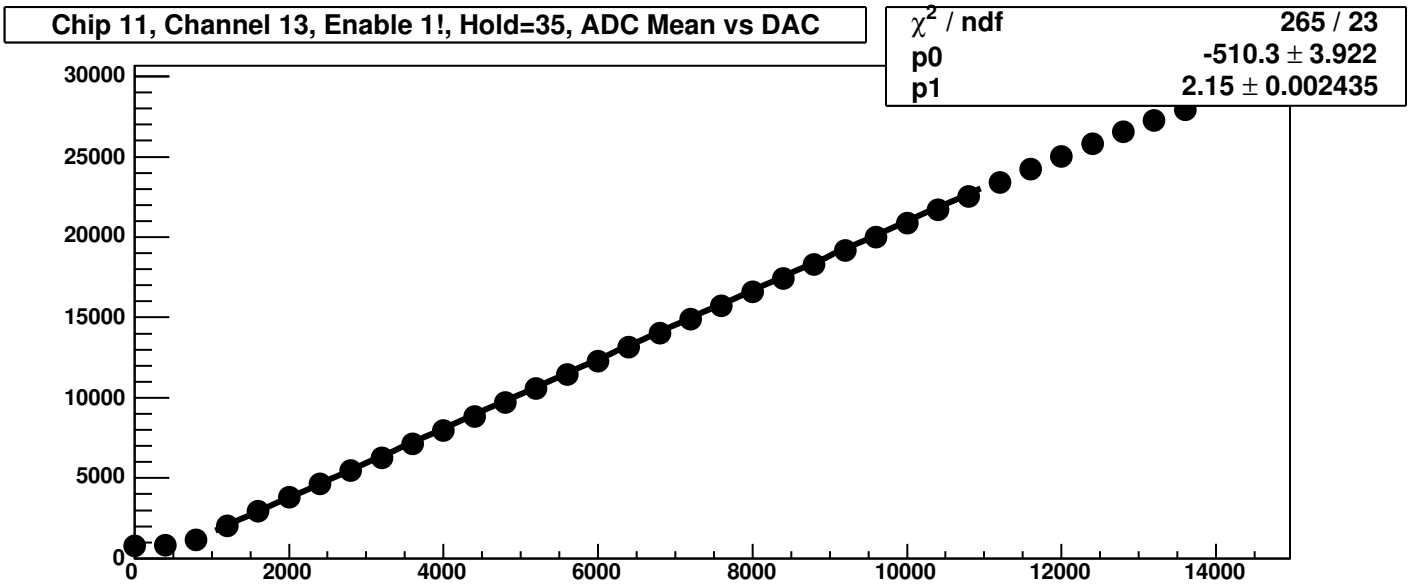
Chip 11, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC

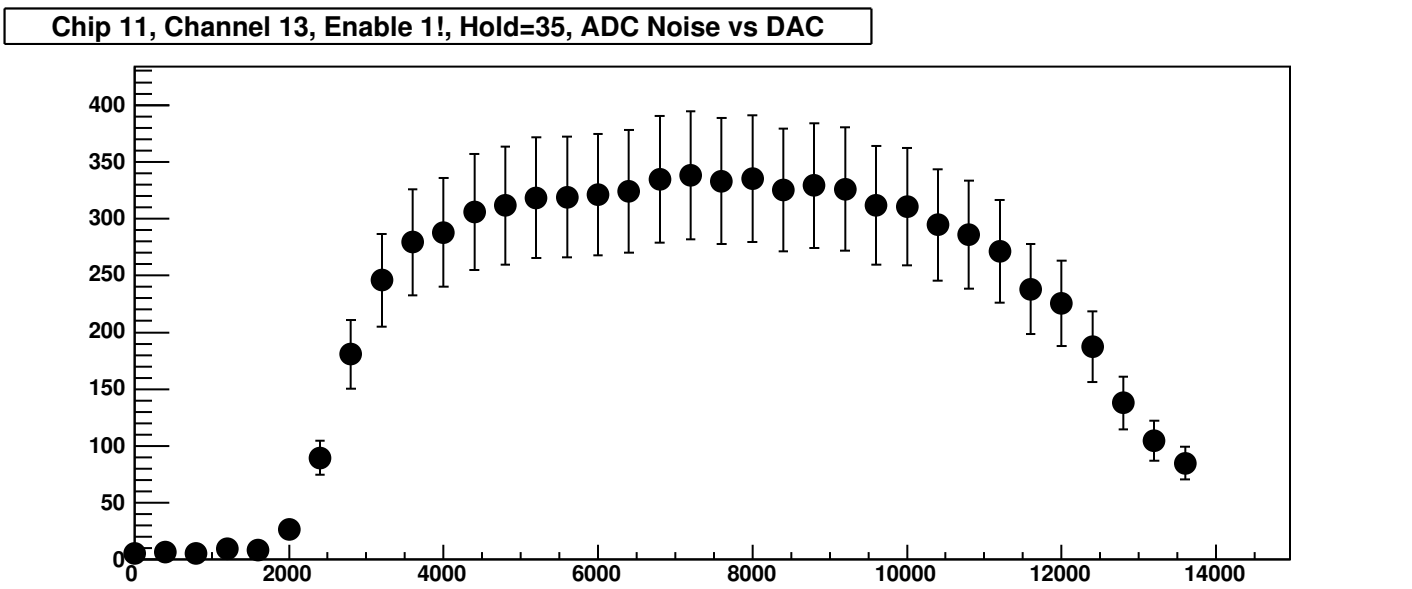


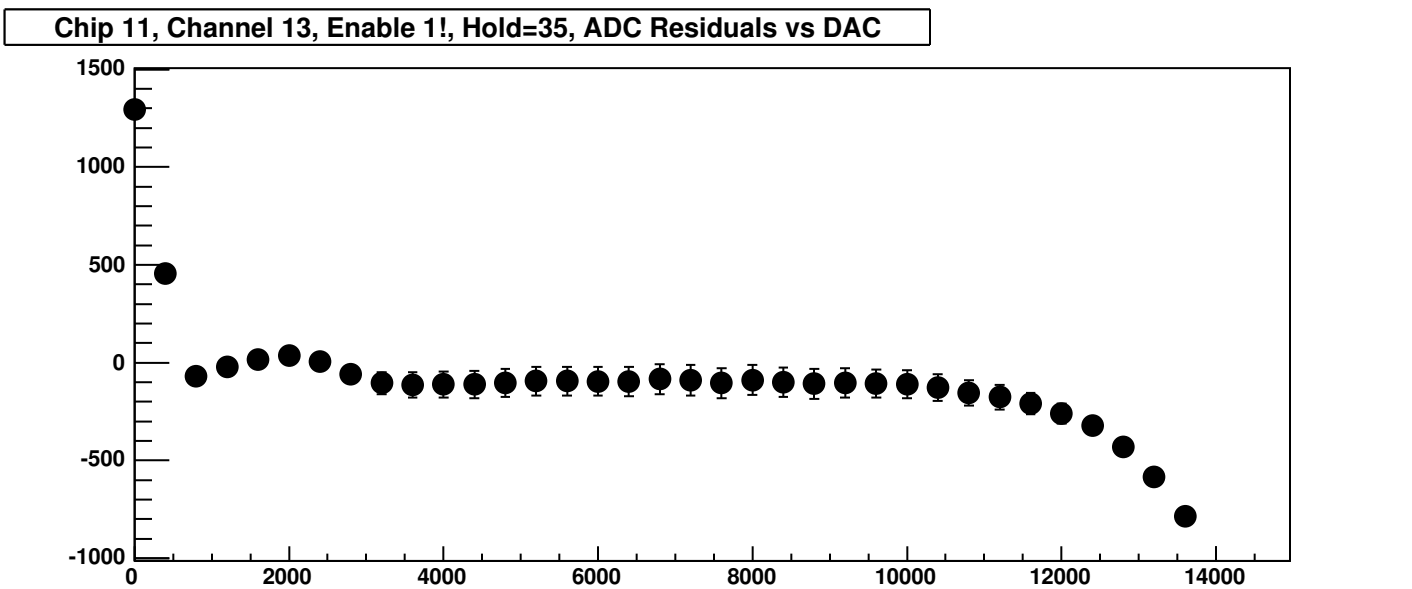

Chip 11, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 3.572e+04 / 23
p0 -1357 ± 1.288
p1 0.4659 ± 0.0002284
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

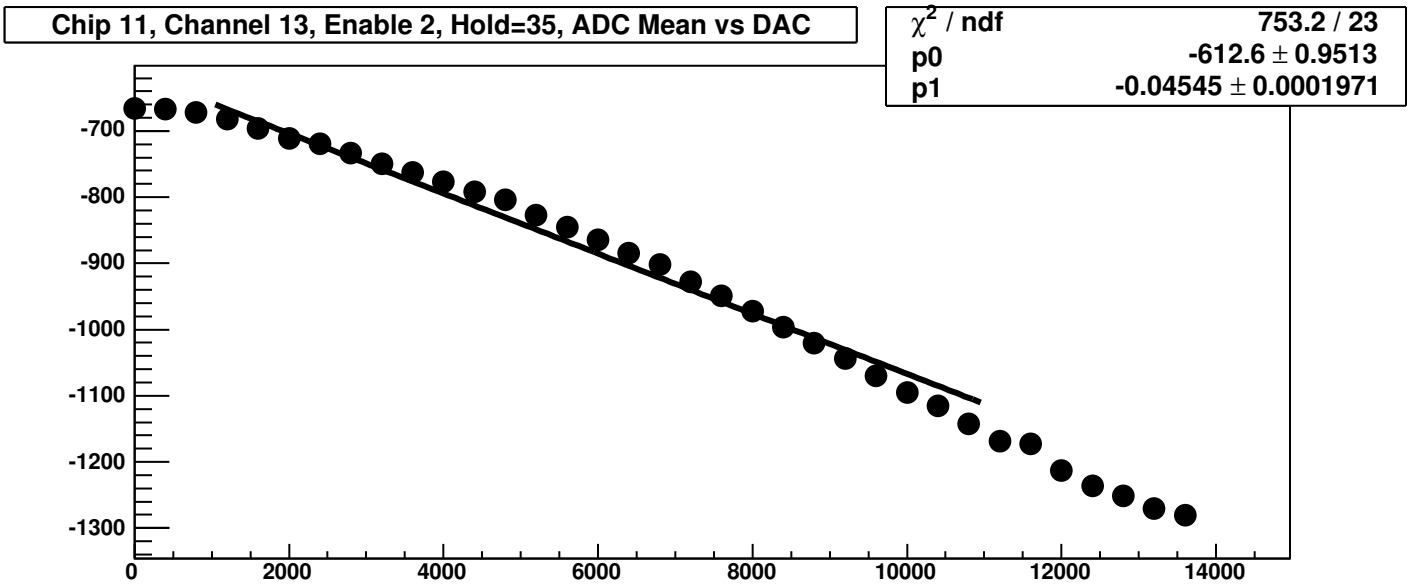

Chip 11, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

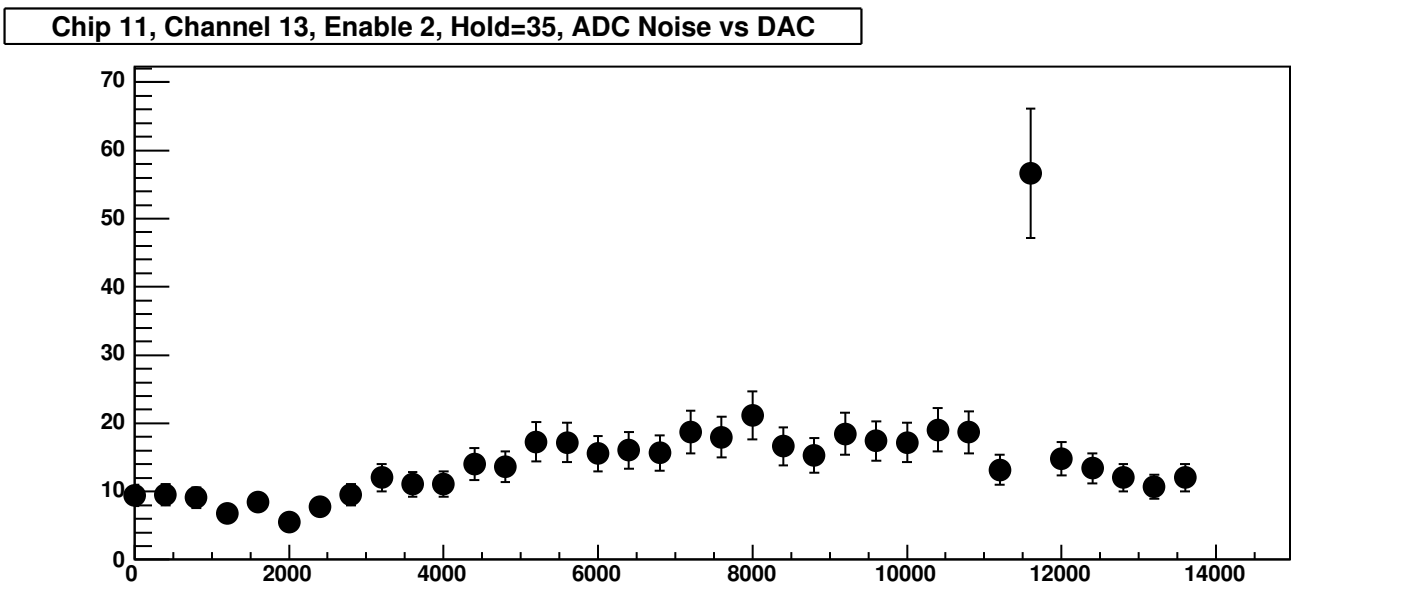

Chip 11, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
800
600
400
200
0
-200
-400
-600
0
2000
4000
6000
8000
10000
12000
14000

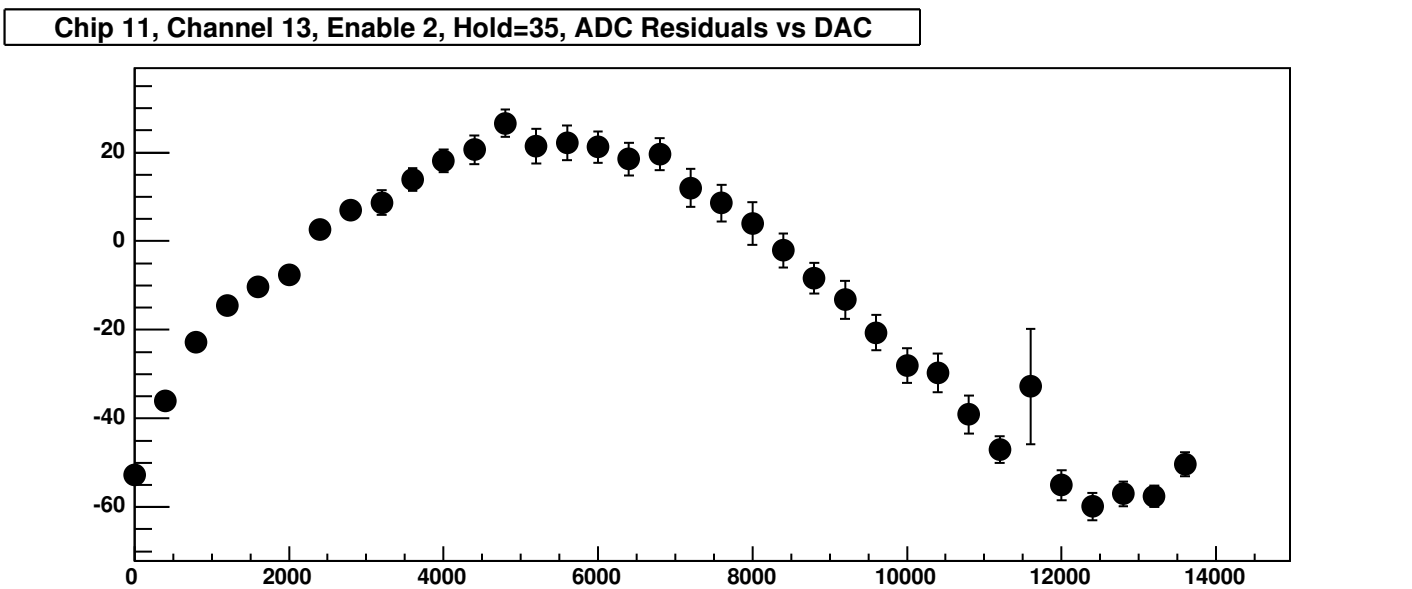

Chip 11, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 265 / 23
p0 -510.3 ± 3.922
p1 2.15 ± 0.002435
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

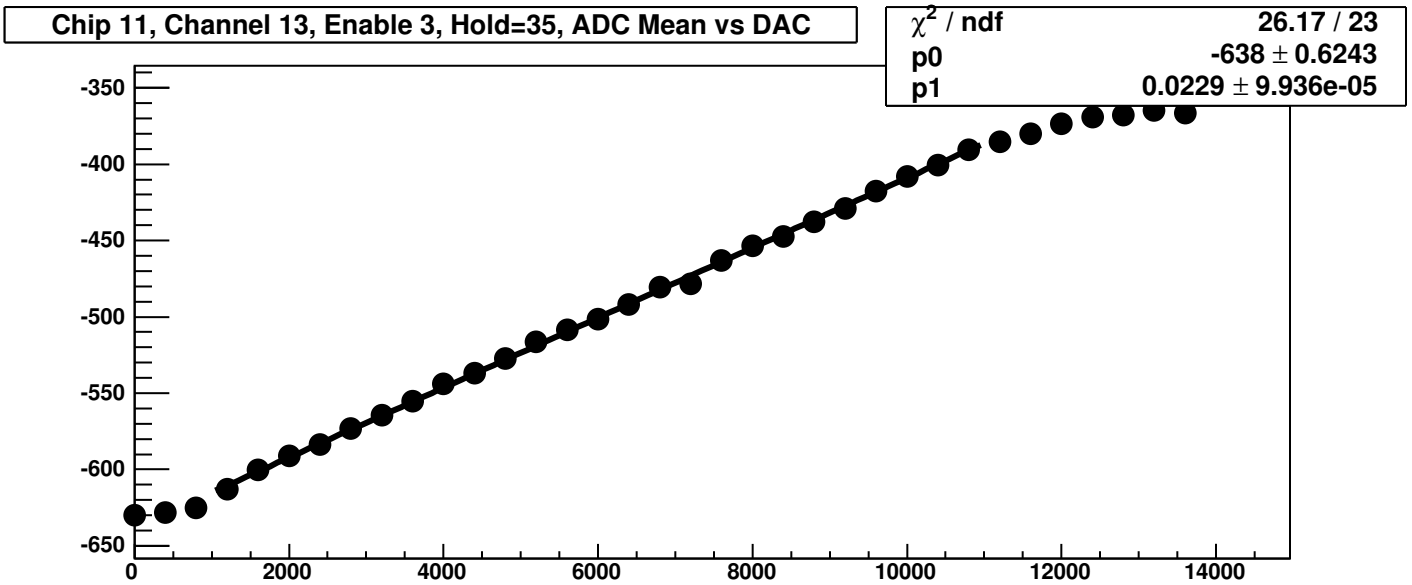

Chip 11, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

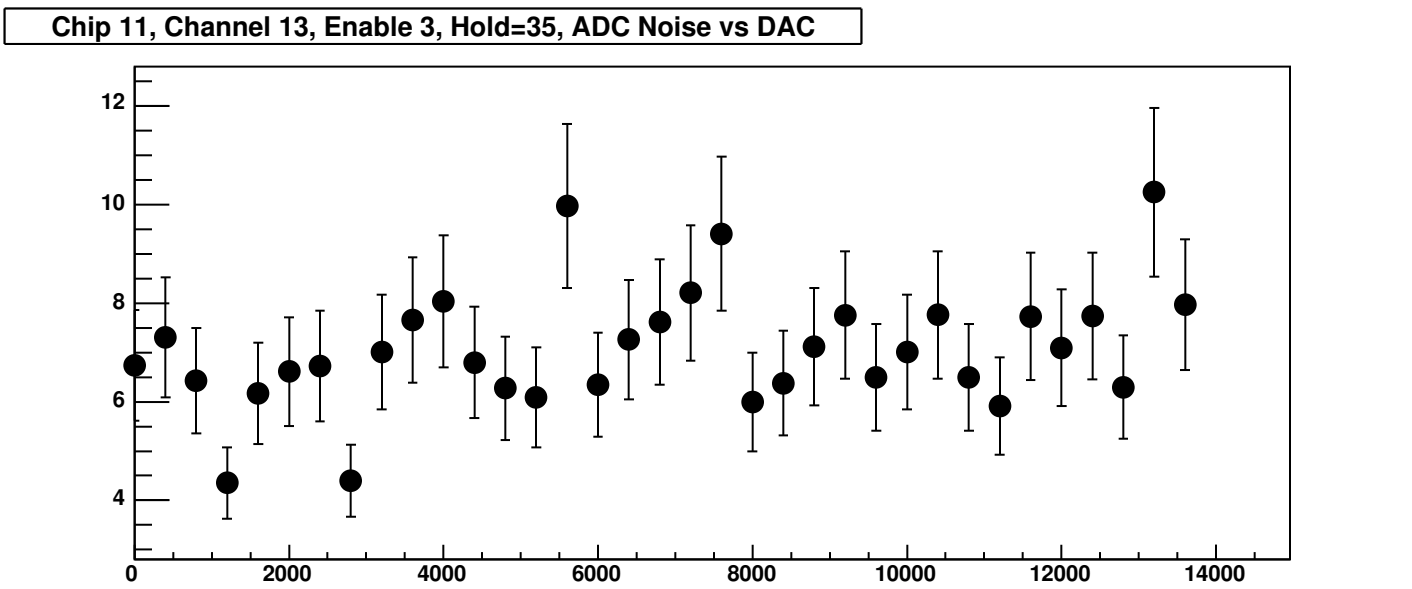

Chip 11, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000

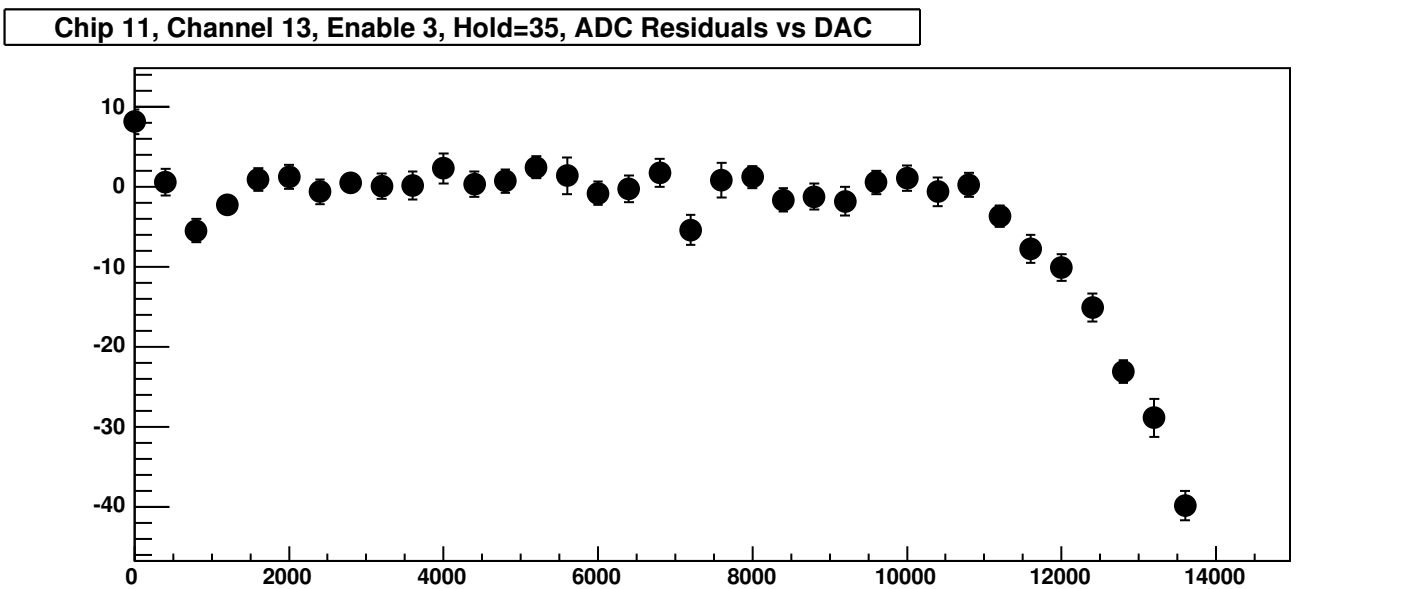

Chip 11, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 753.2 / 23
p0 -612.6 ± 0.9513
p1 -0.04545 ± 0.0001971
-700
-800
-900
-1000
-1100
-1200
-1300
0
2000
4000
6000
8000
10000
12000
14000

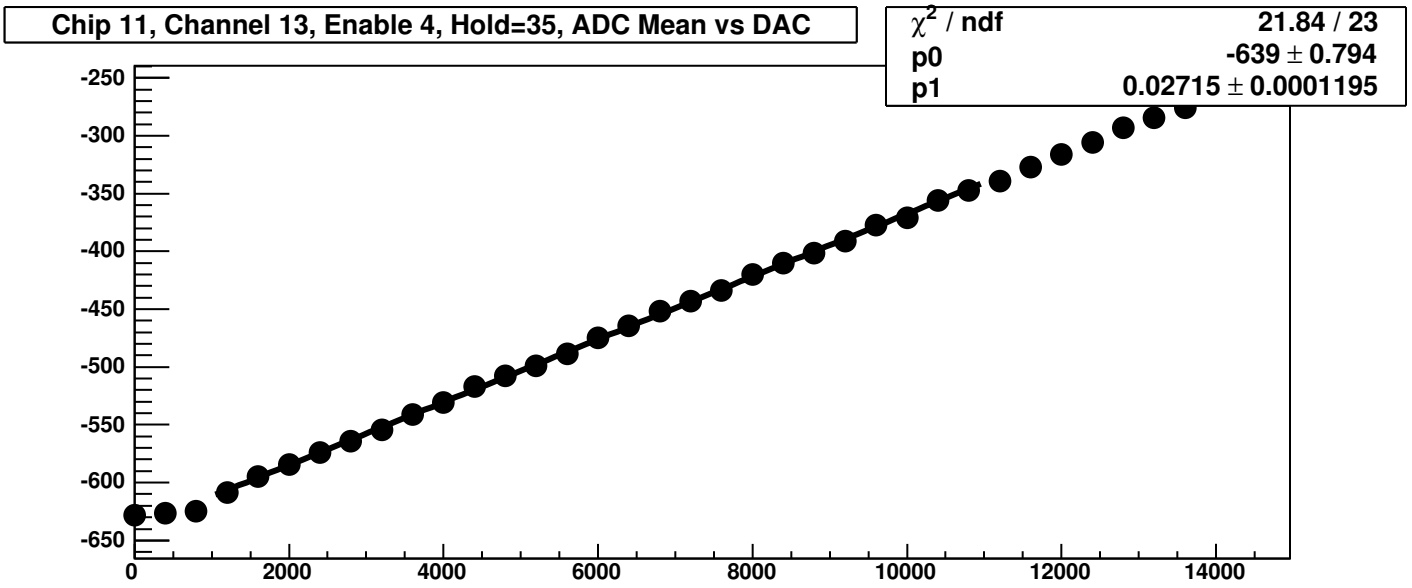

Chip 11, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

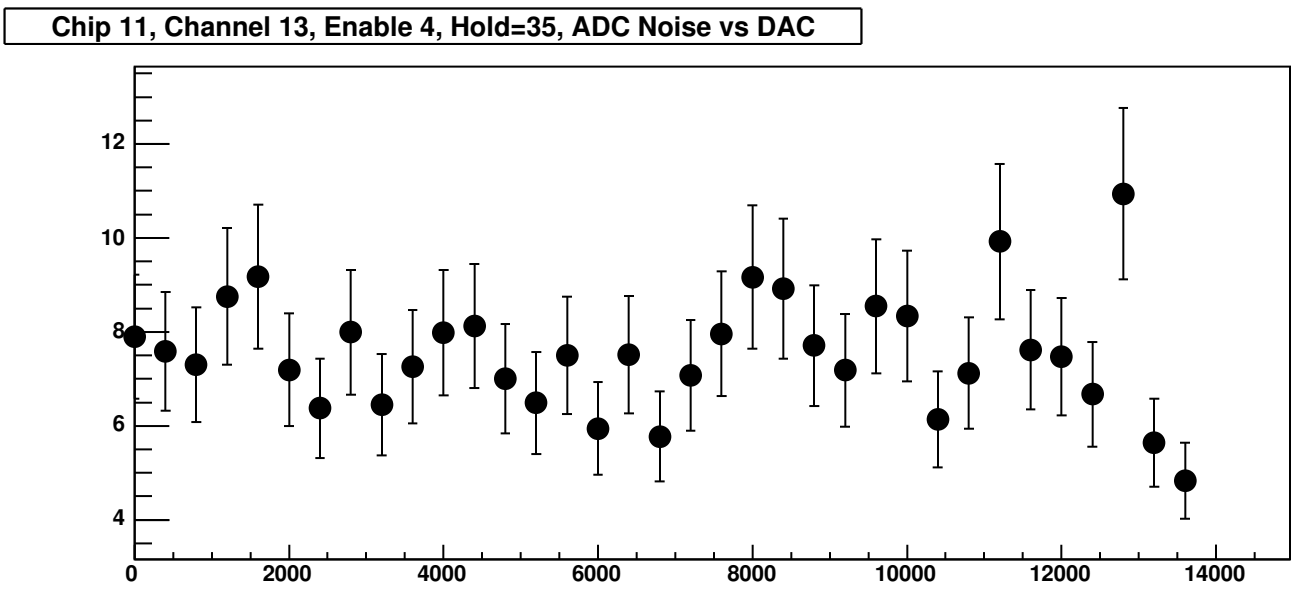

Chip 11, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
20
0
-20
-40
-60
0
2000
4000
6000
8000
10000
12000
14000

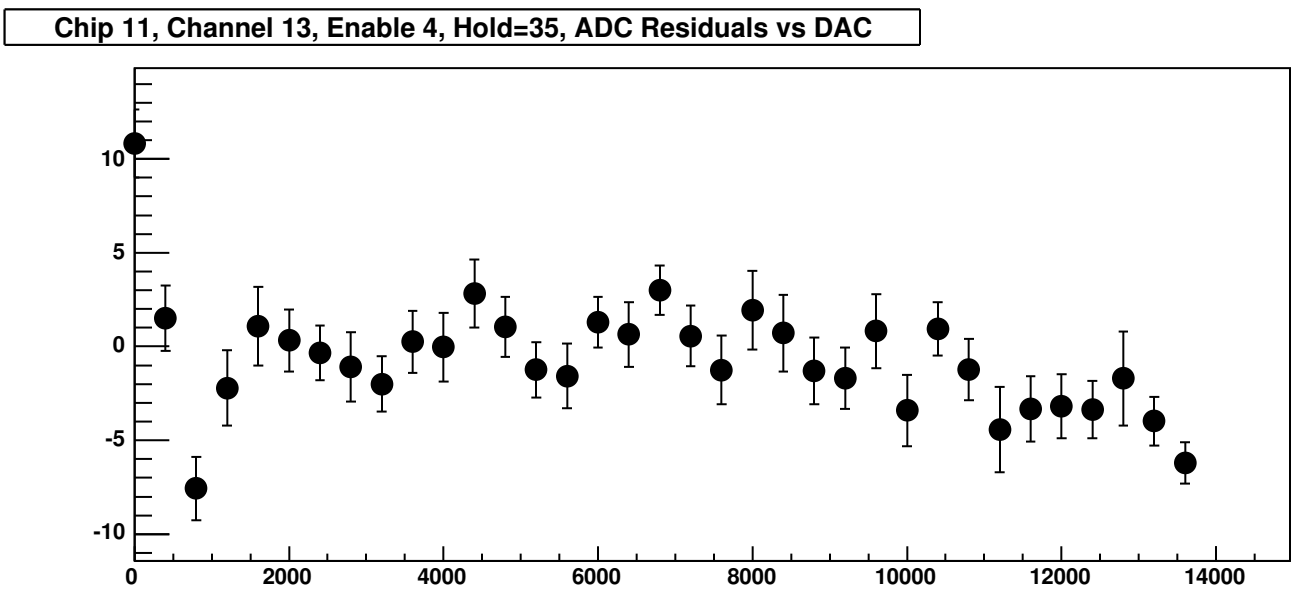

Chip 11, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.17 / 23
p0 -638 ± 0.6243
p1 0.0229 ± 9.936e-05
-350
-400
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

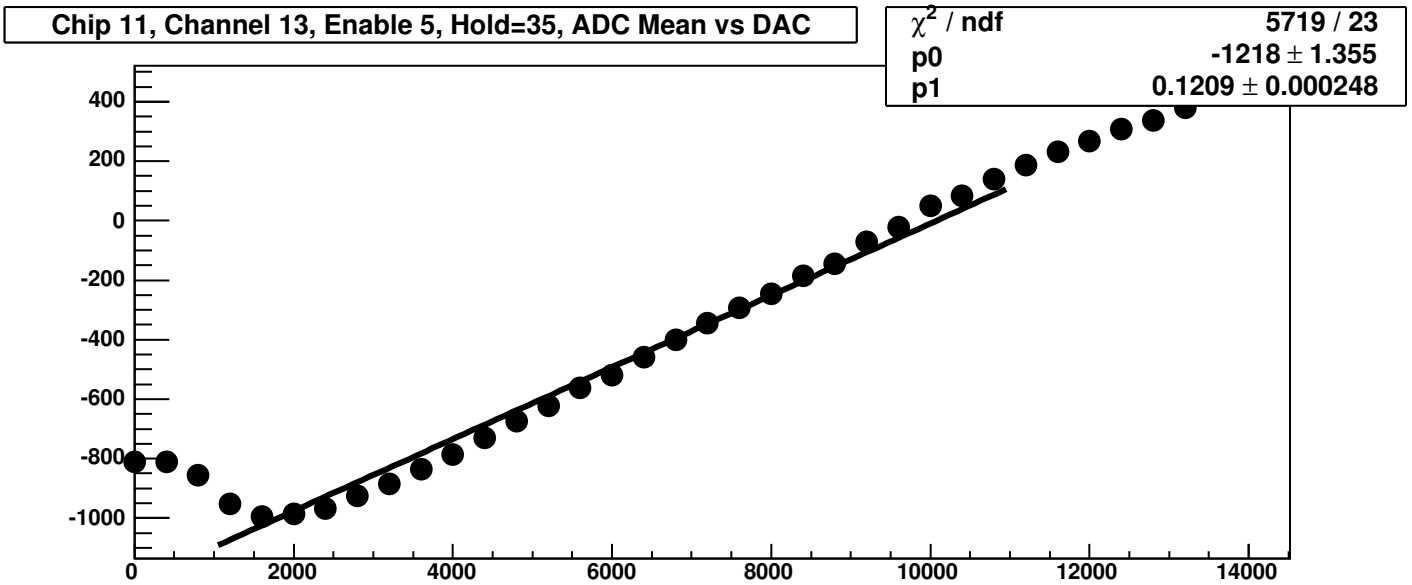

Chip 11, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

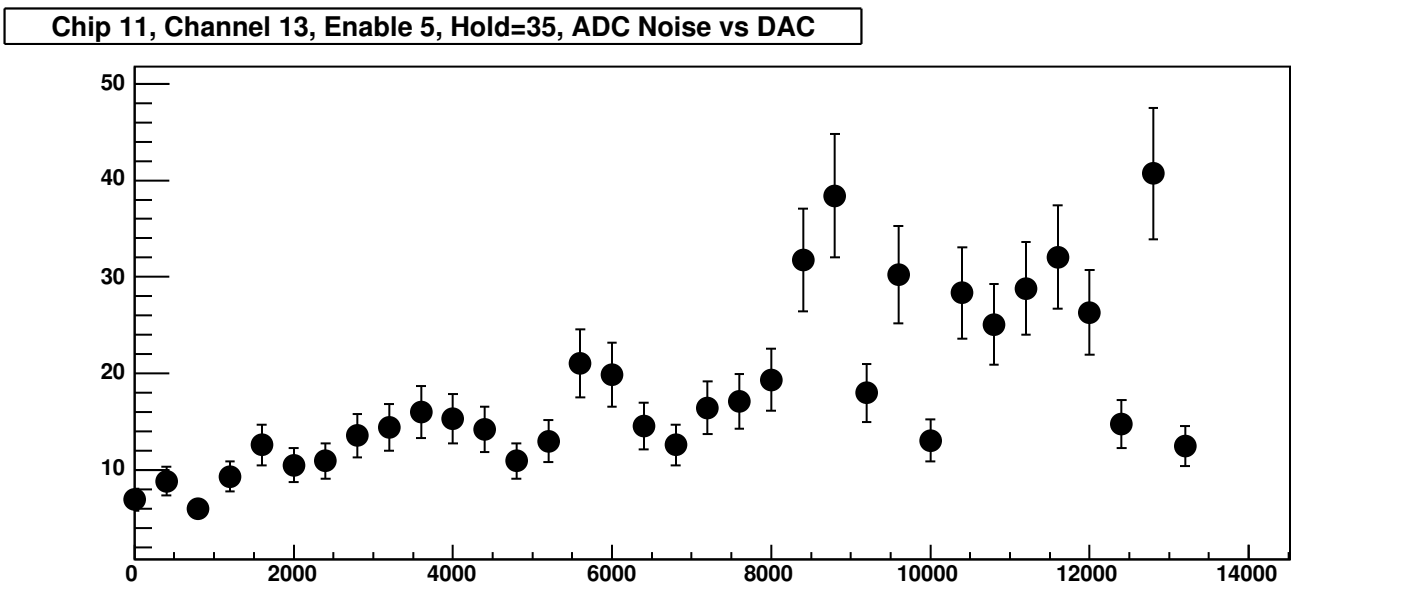

Chip 11, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
2000
4000
6000
8000
10000
12000
14000

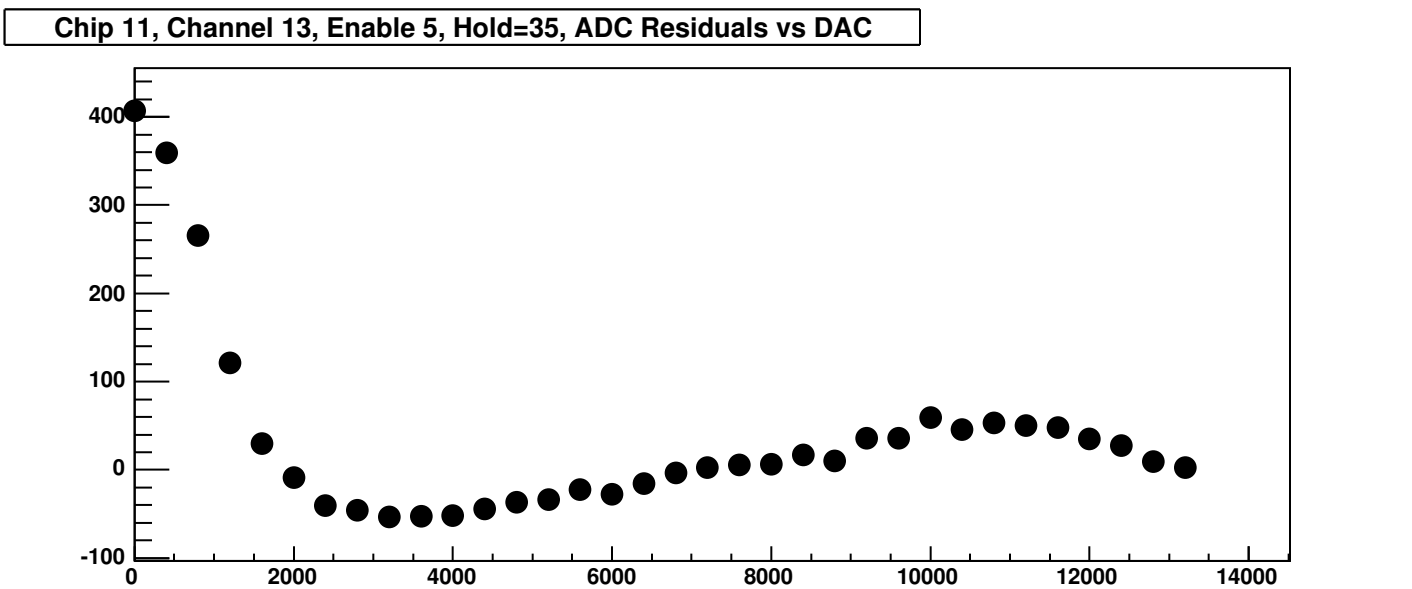

Chip 11, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 21.84 / 23
p0 -639 ± 0.794
p1 0.02715 ± 0.0001195
-250
-300
-350
-400
-450
-500
-550
-600
-650
0
2000
4000
6000
8000
10000
12000
14000

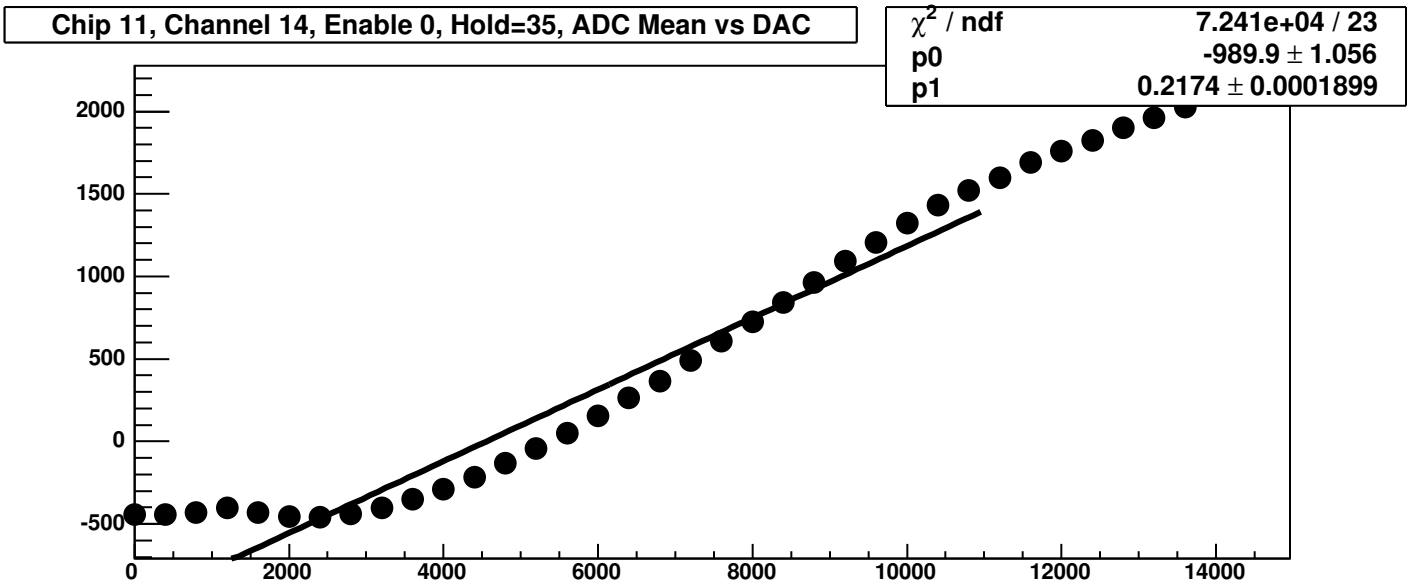

Chip 11, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

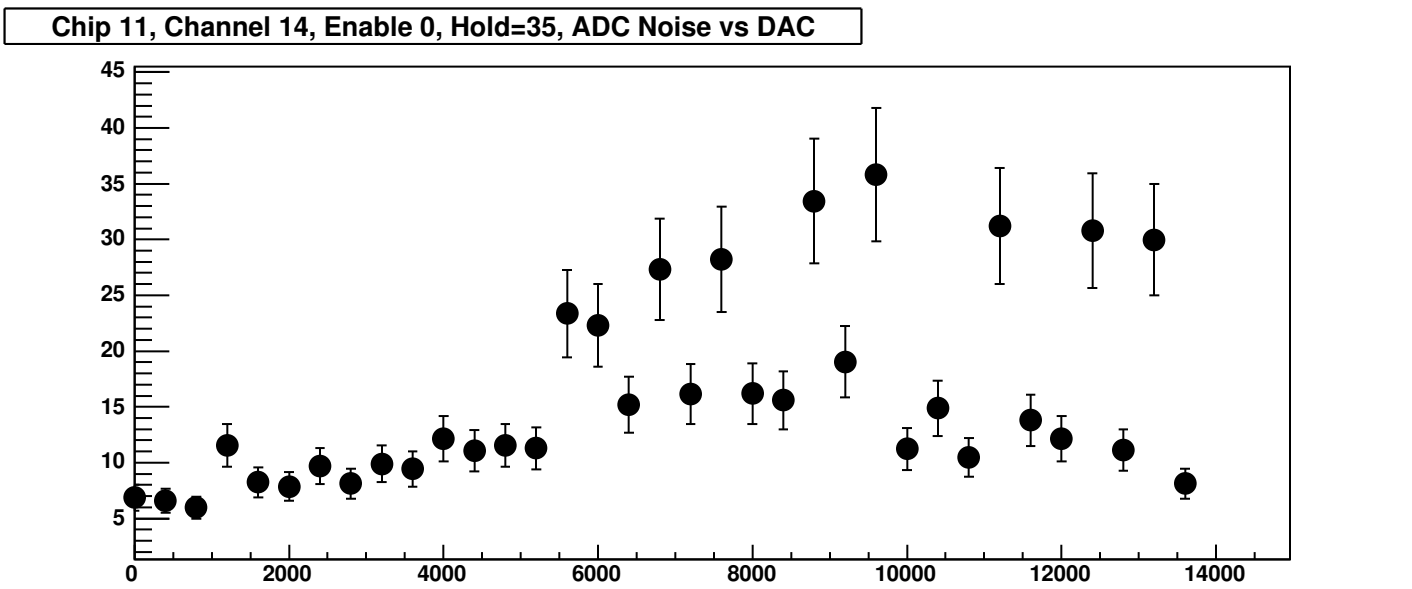

Chip 11, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

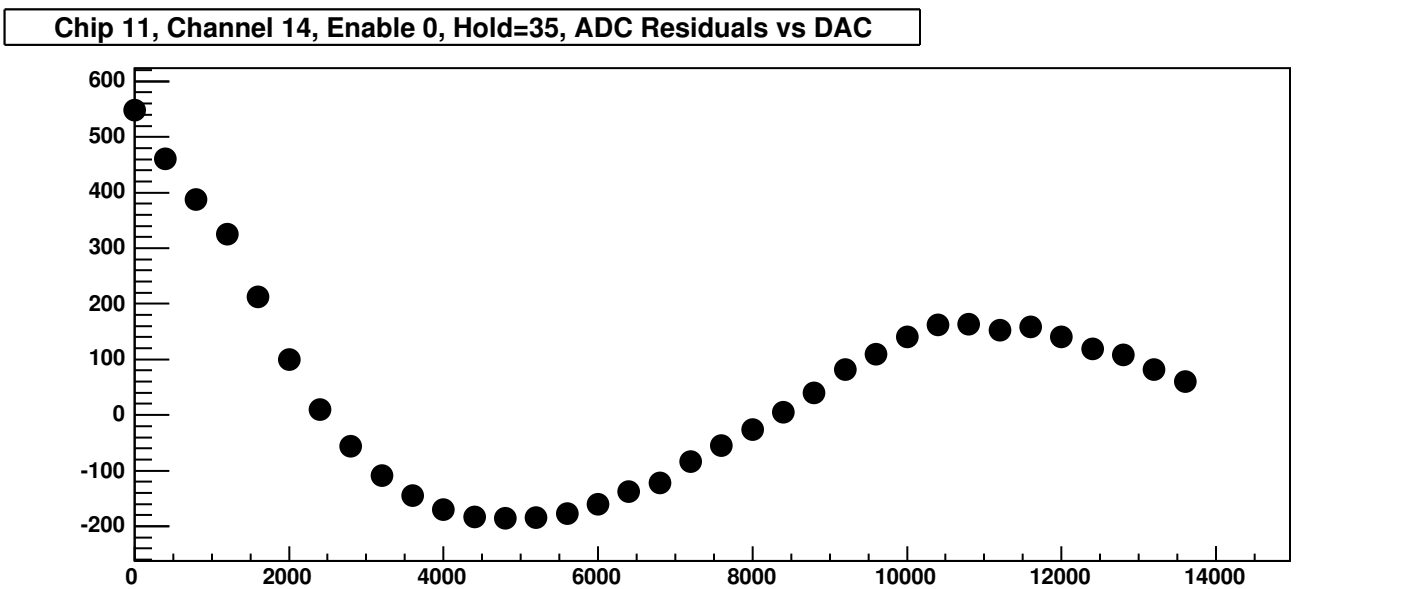

Chip 11, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5719 / 23
p0 -1218 ± 1.355
p1 0.1209 ± 0.000248
400
200
0
-200
-400
-600
-800
-1000
0
2000
4000
6000
8000
10000
12000
14000

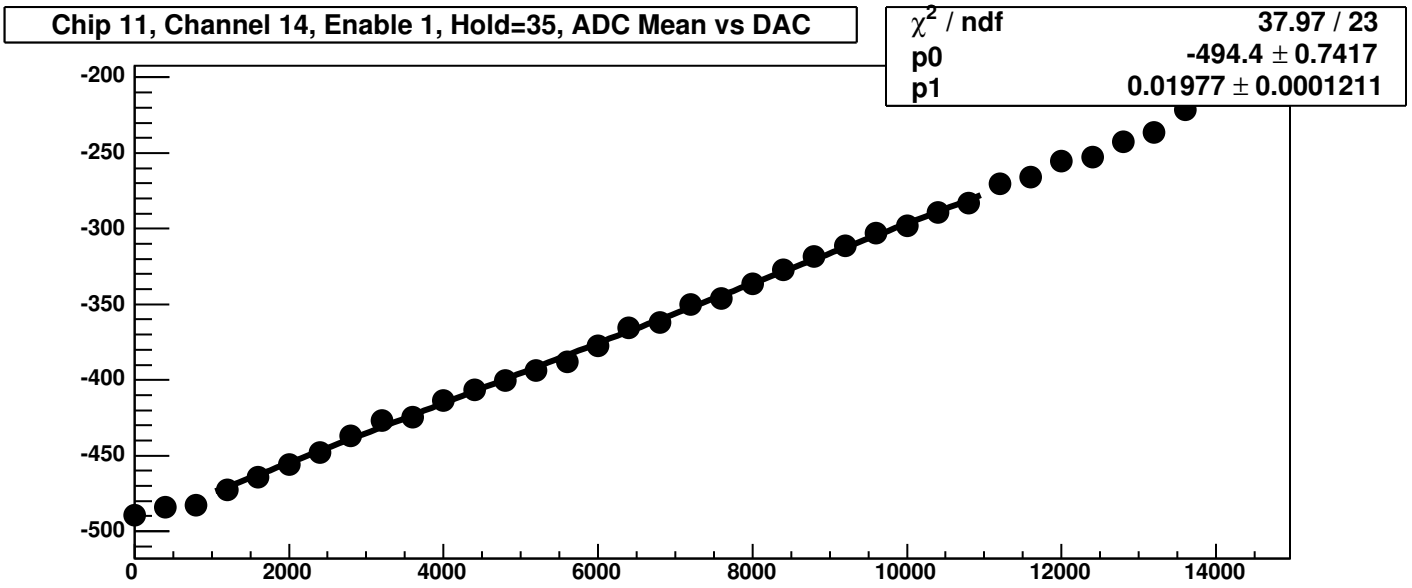

Chip 11, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

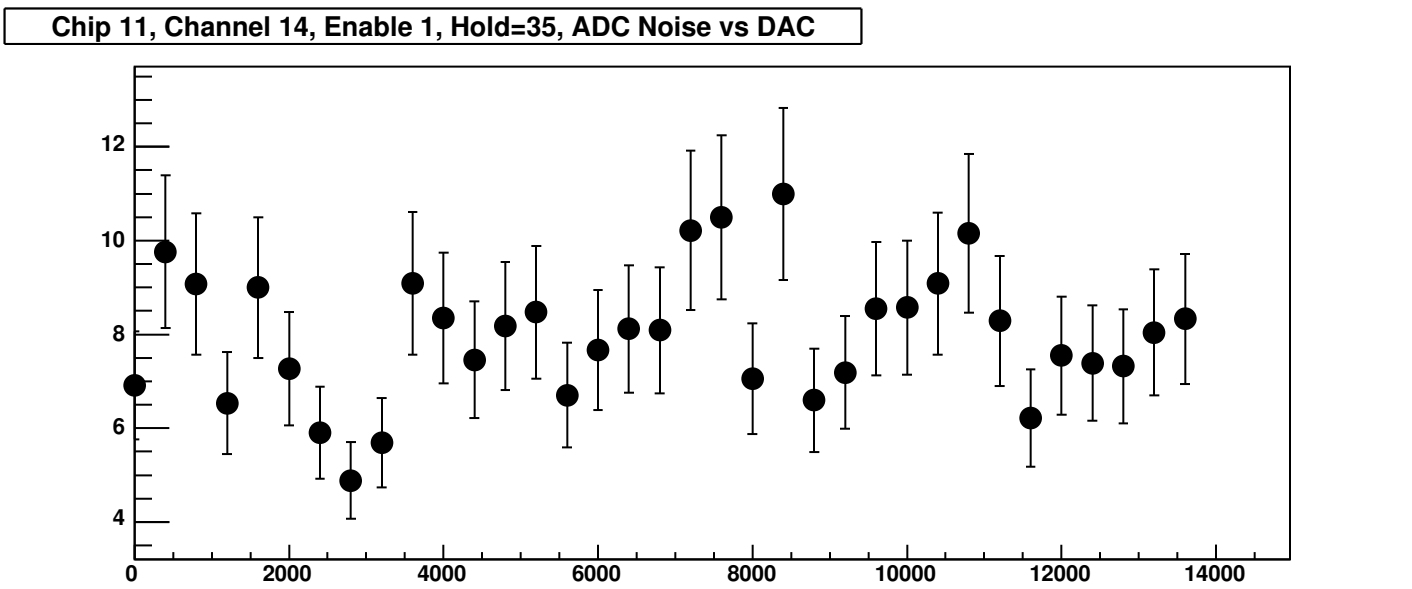

Chip 11, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
0
2000
4000
6000
8000
10000
12000
14000

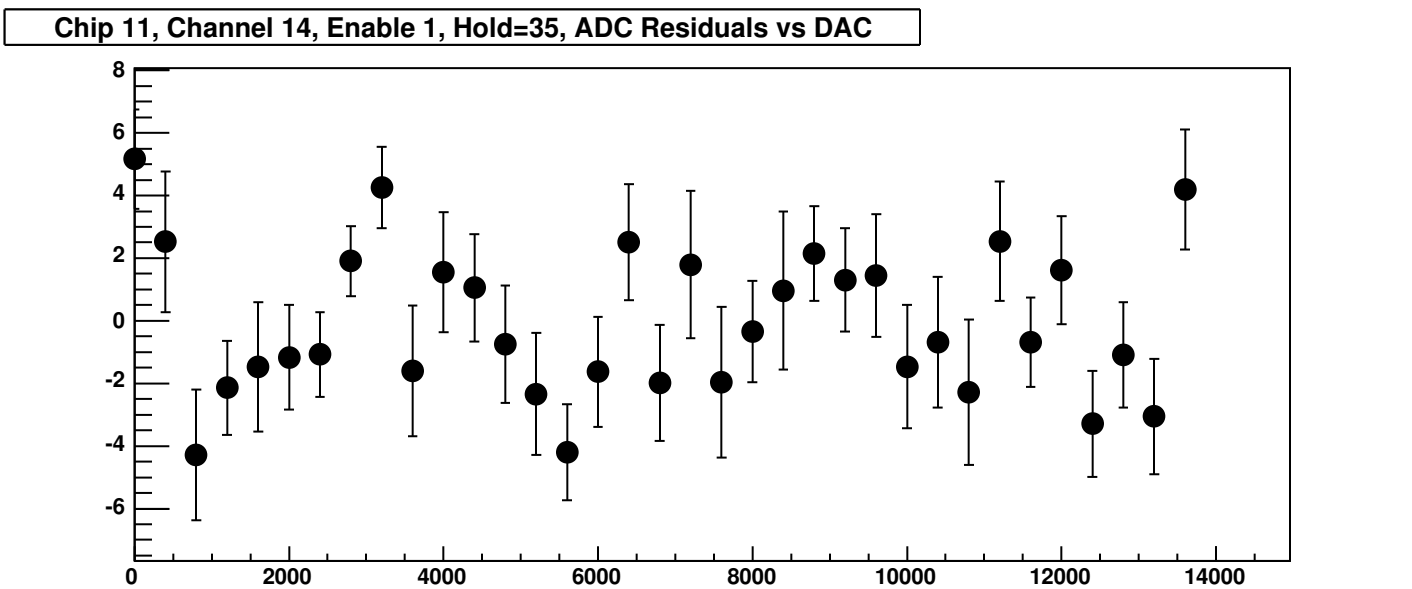

Chip 11, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 7.241e+04 / 23
p0 -989.9 ± 1.056
p1 0.2174 ± 0.0001899
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

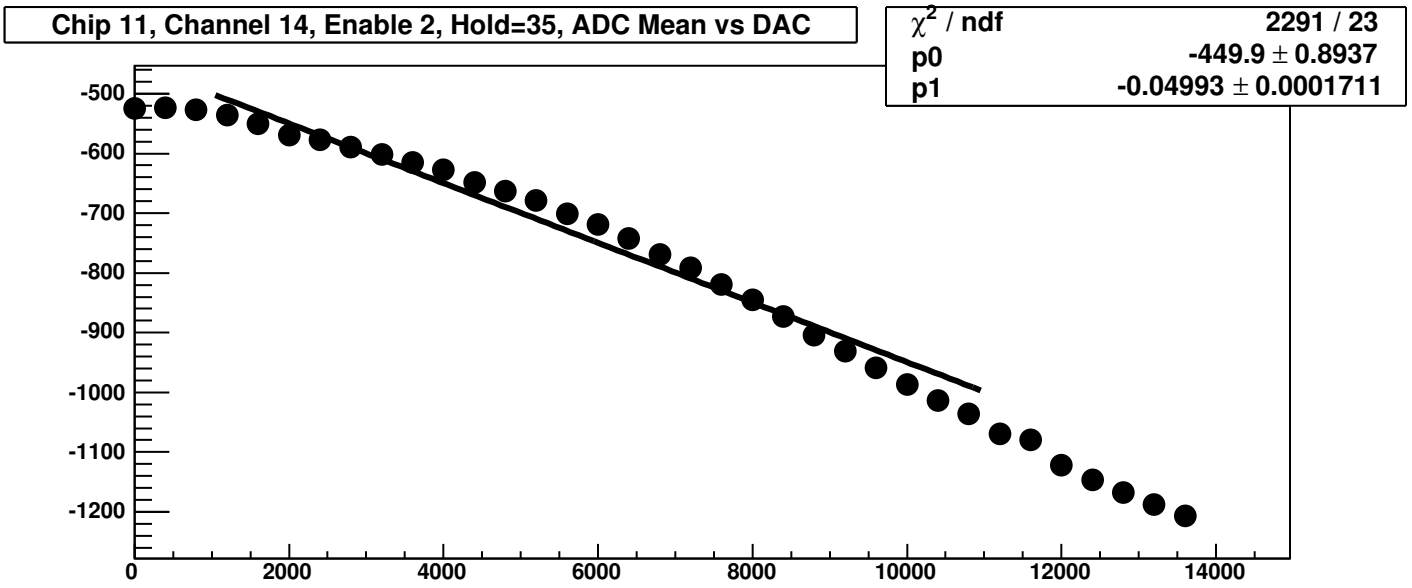

Chip 11, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

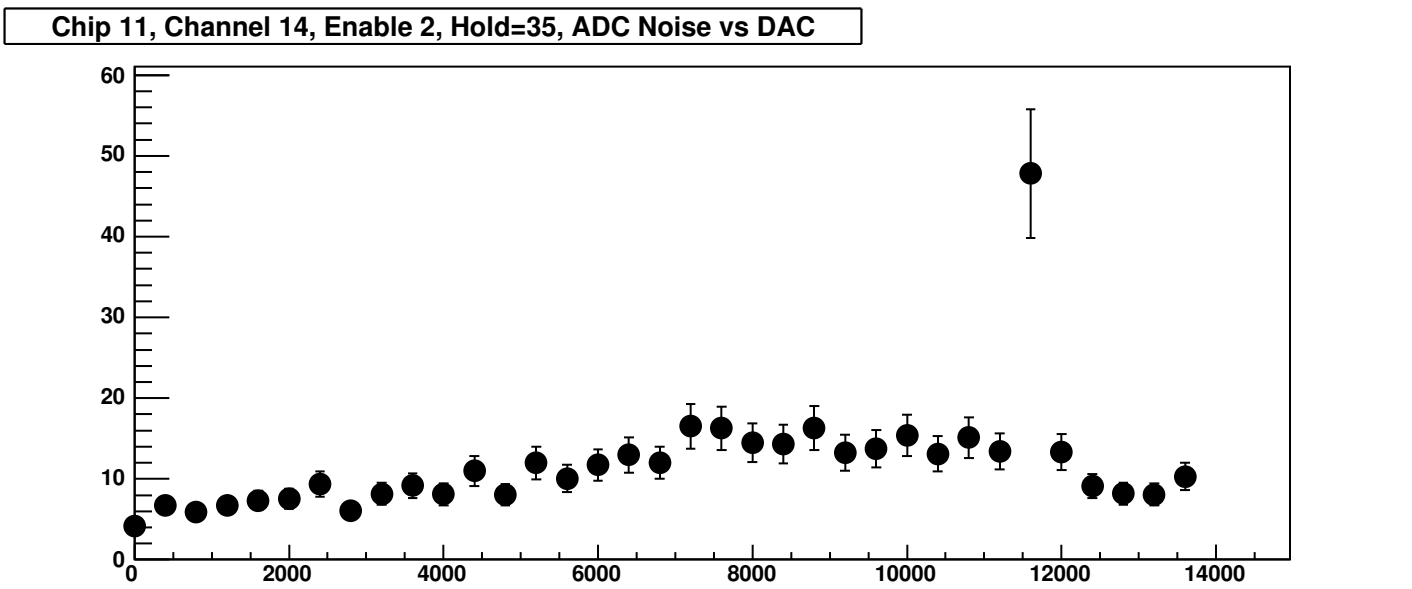

Chip 11, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

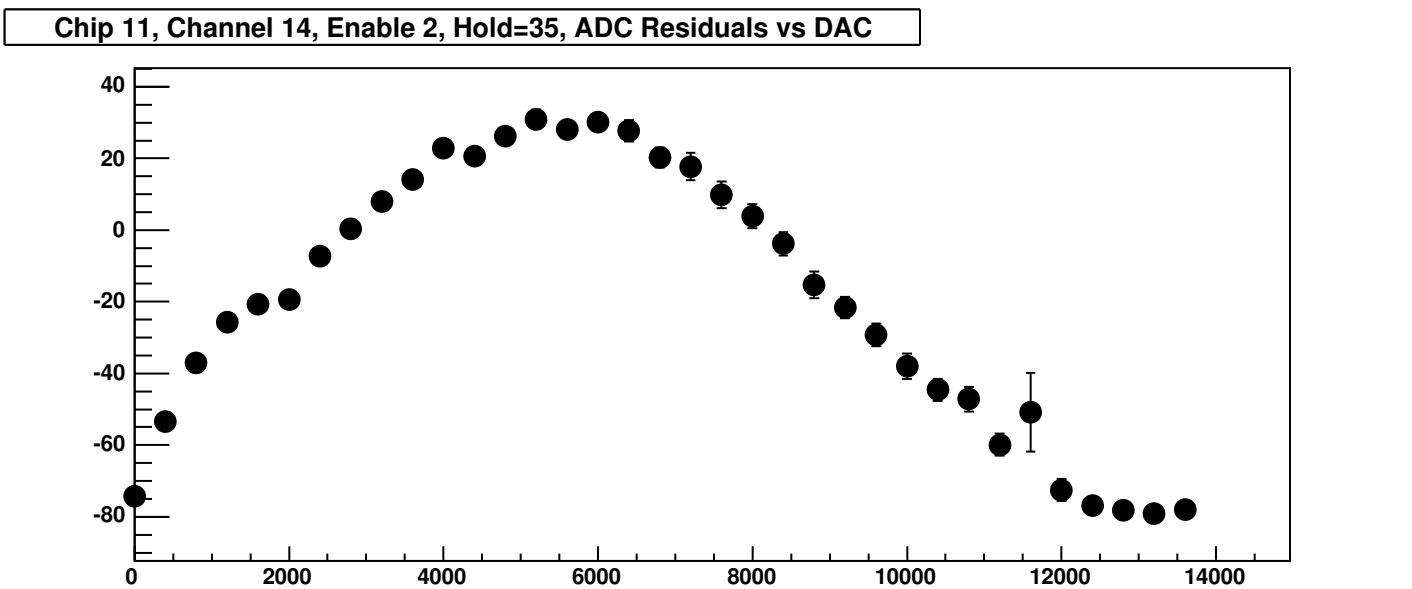

Chip 11, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 37.97 / 23
p0 -494.4 ± 0.7417
p1 0.01977 ± 0.0001211
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000

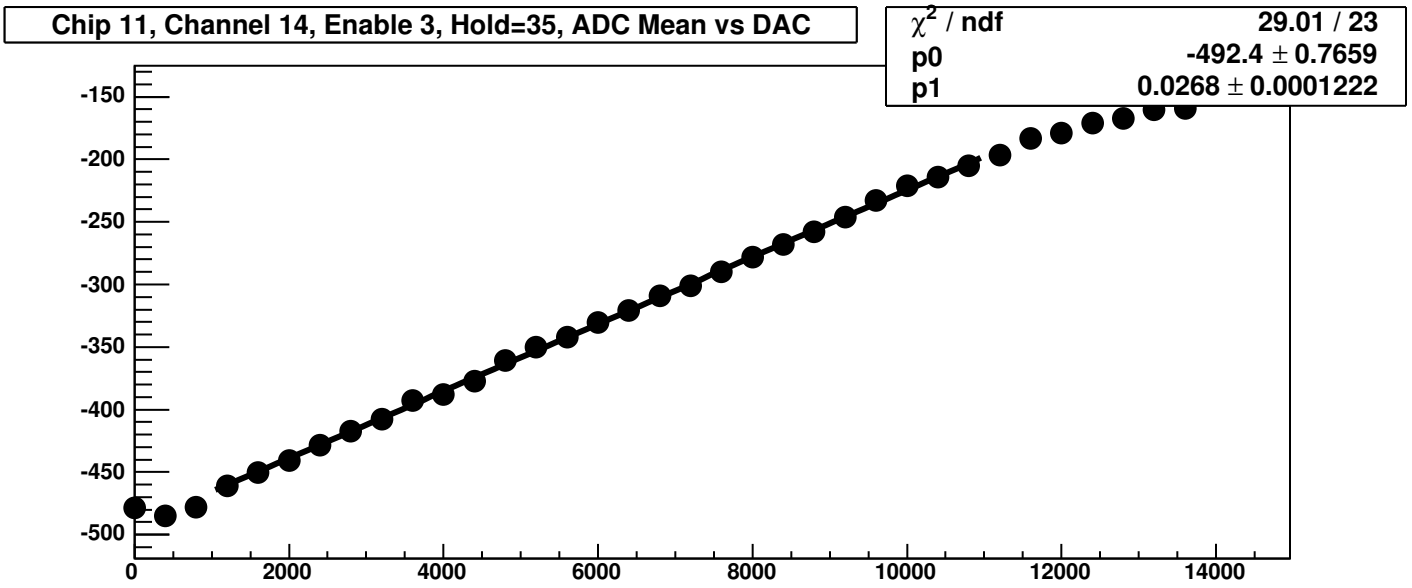

Chip 11, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

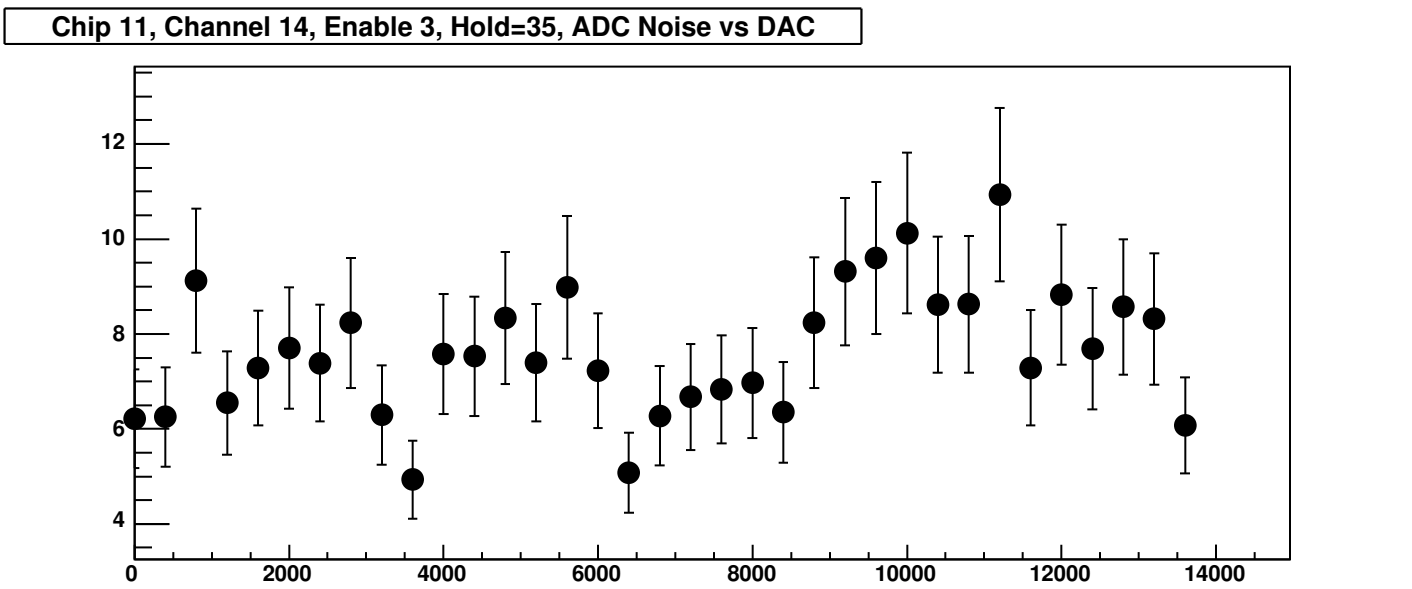

Chip 11, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
2000
4000
6000
8000
10000
12000
14000

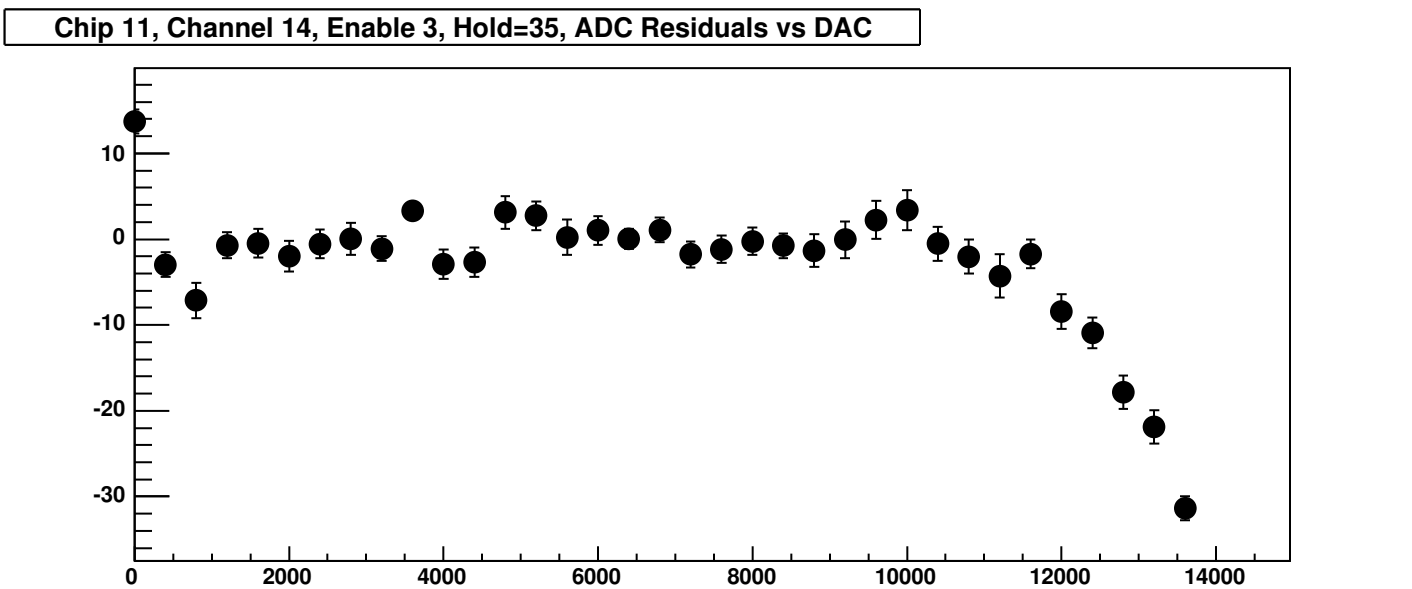

Chip 11, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 2291 / 23
p0 -449.9 ± 0.8937
p1 -0.04993 ± 0.0001711
-500
-600
-700
-800
-900
-1000
-1100
-1200
0
2000
4000
6000
8000
10000
12000
14000

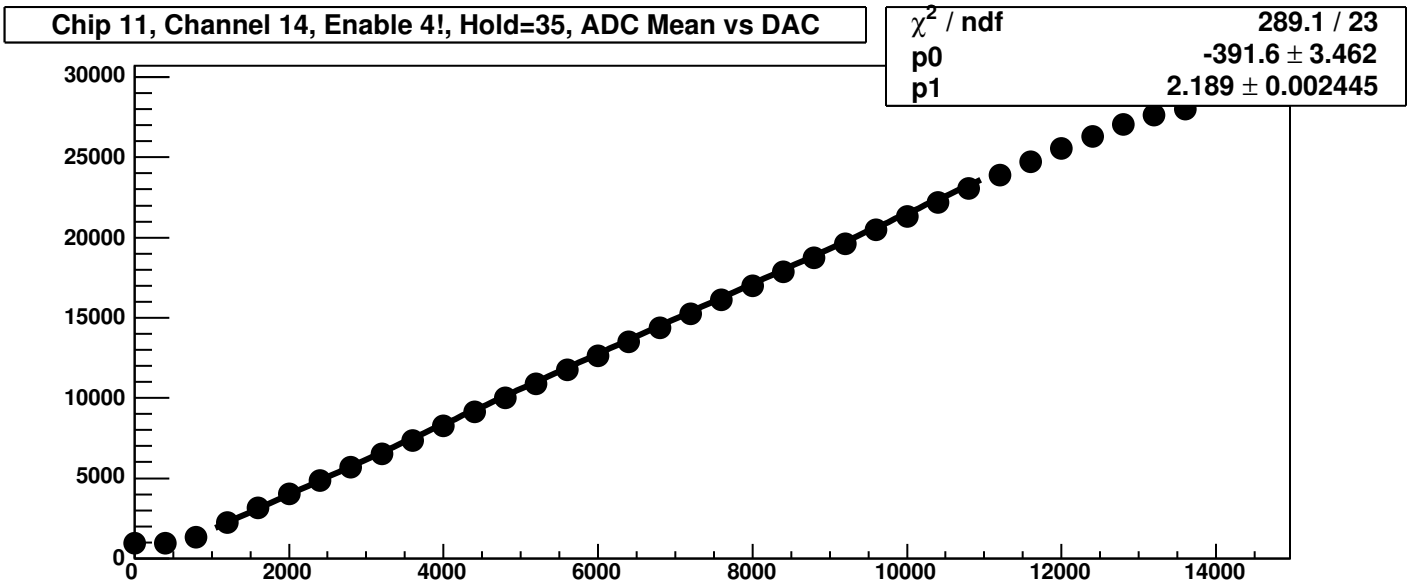

Chip 11, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

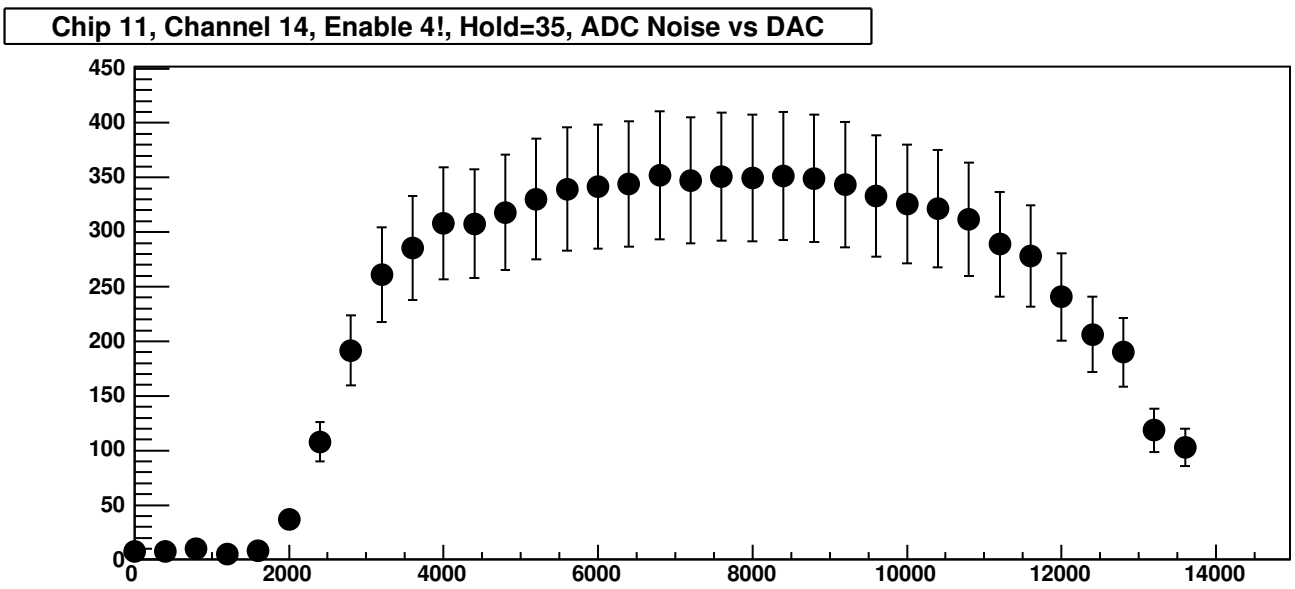

Chip 11, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
40
20
0
-20
-40
-60
-80
0
2000
4000
6000
8000
10000
12000
14000

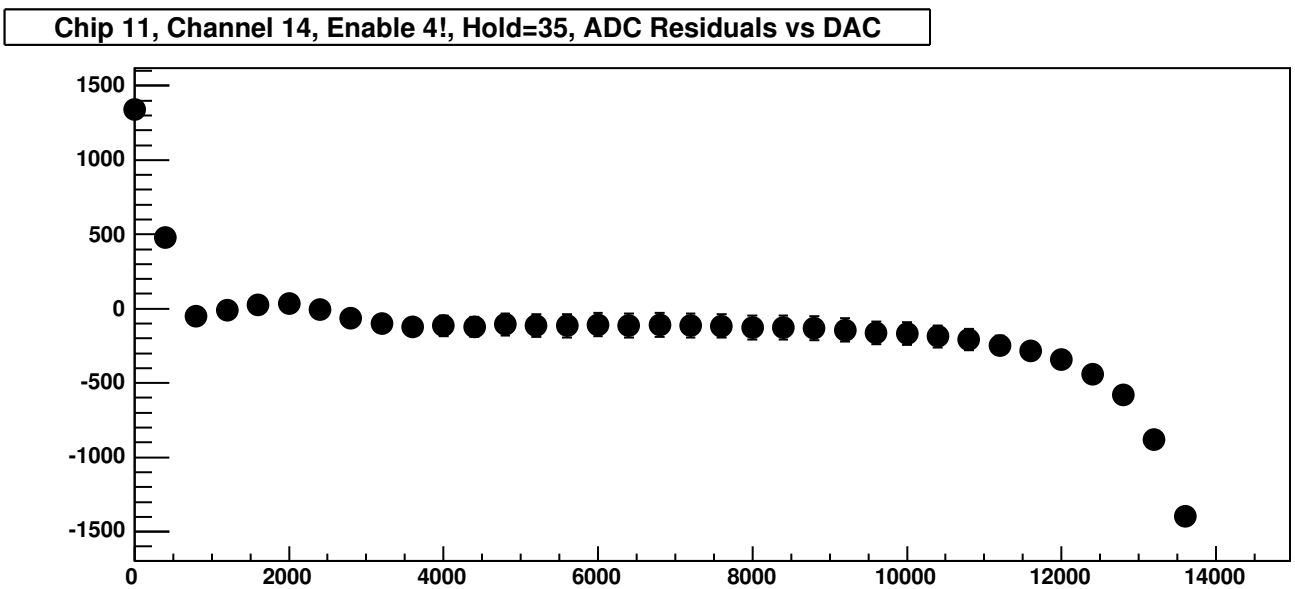

Chip 11, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 29.01 / 23
p0 -492.4 ± 0.7659
p1 0.0268 ± 0.0001222
-150
-200
-250
-300
-350
-400
-450
-500
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

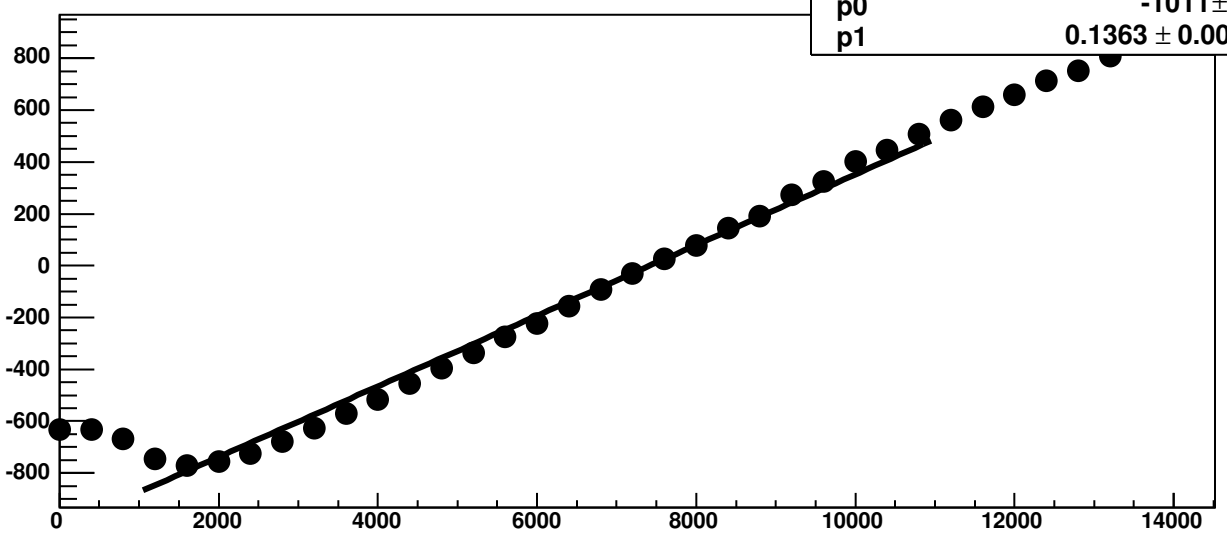

Chip 11, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 289.1 / 23
p0 -391.6 ± 3.462
p1 2.189 ± 0.002445
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
450
400
350
300
250
200
150
100
50
0
0
2000
4000
6000
8000
10000
12000
14000

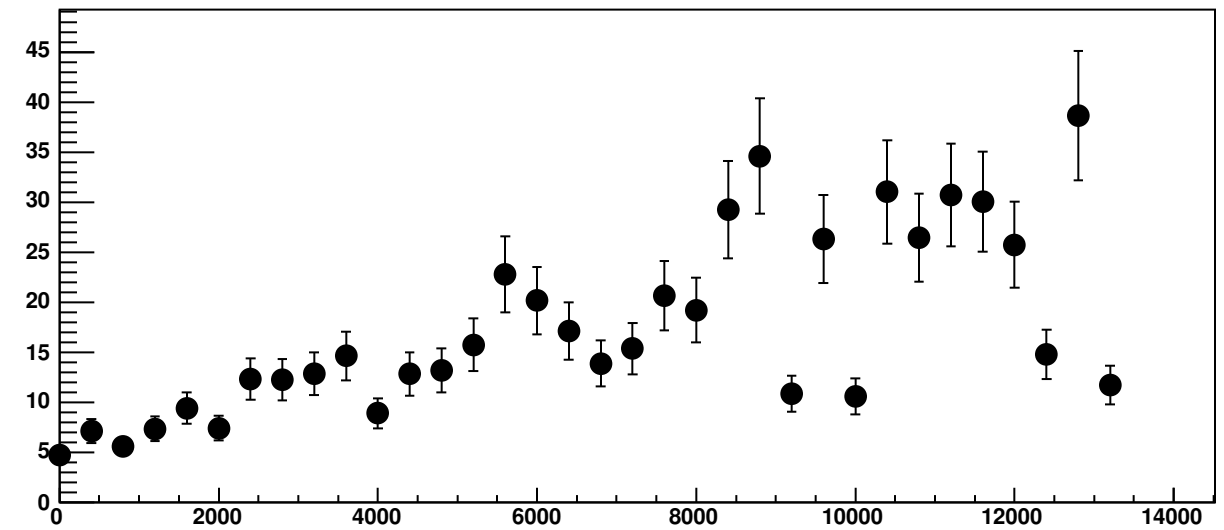

Chip 11, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

Chip 11, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

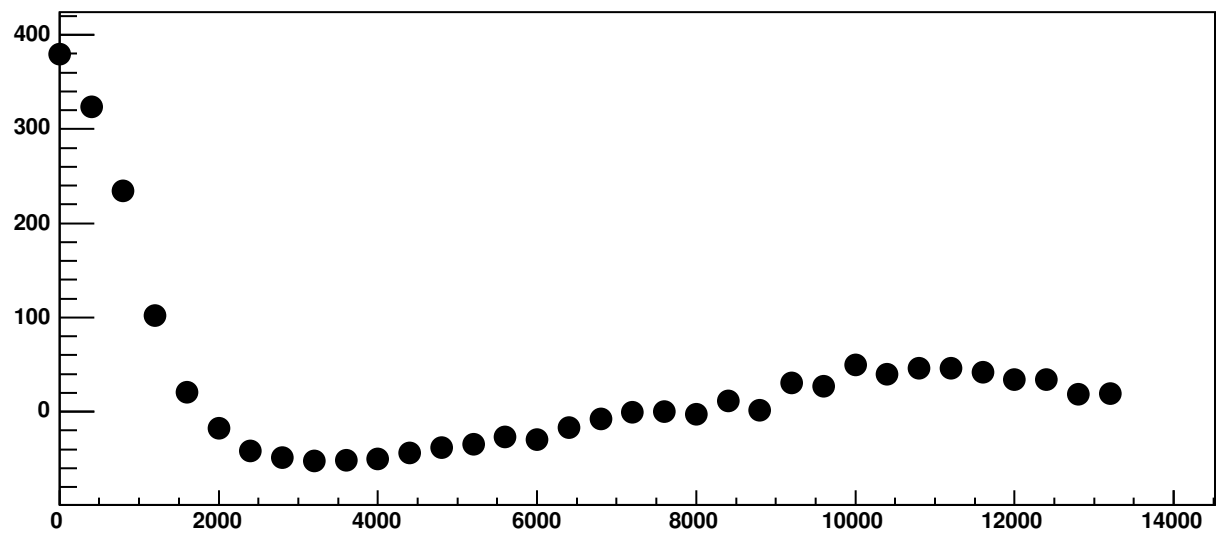
χ^2 / ndf	6751 / 23
p0	-1011 ± 1.102
p1	0.1363 ± 0.0002071

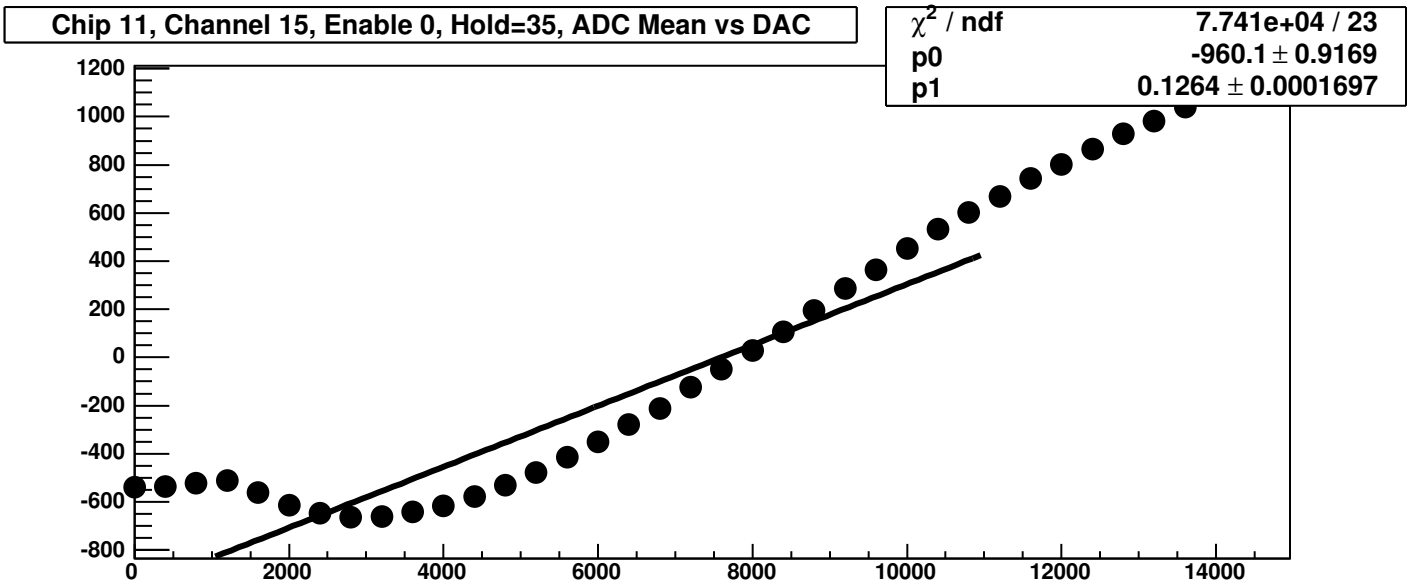


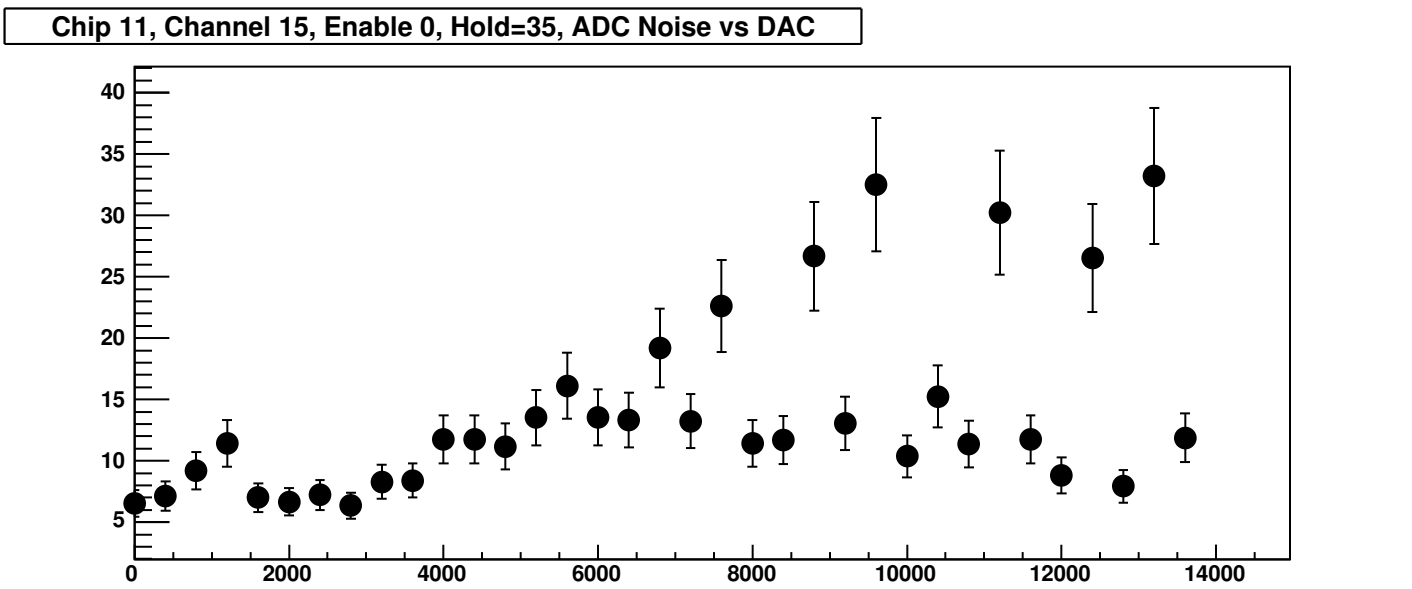
Chip 11, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC

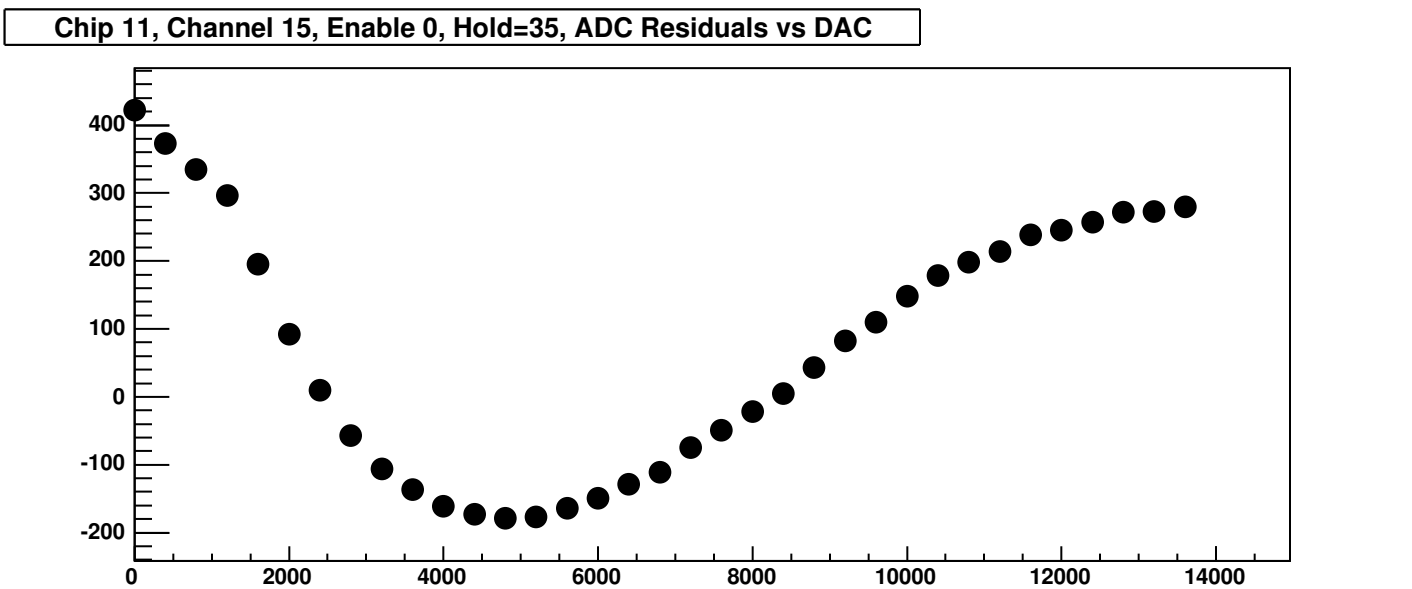


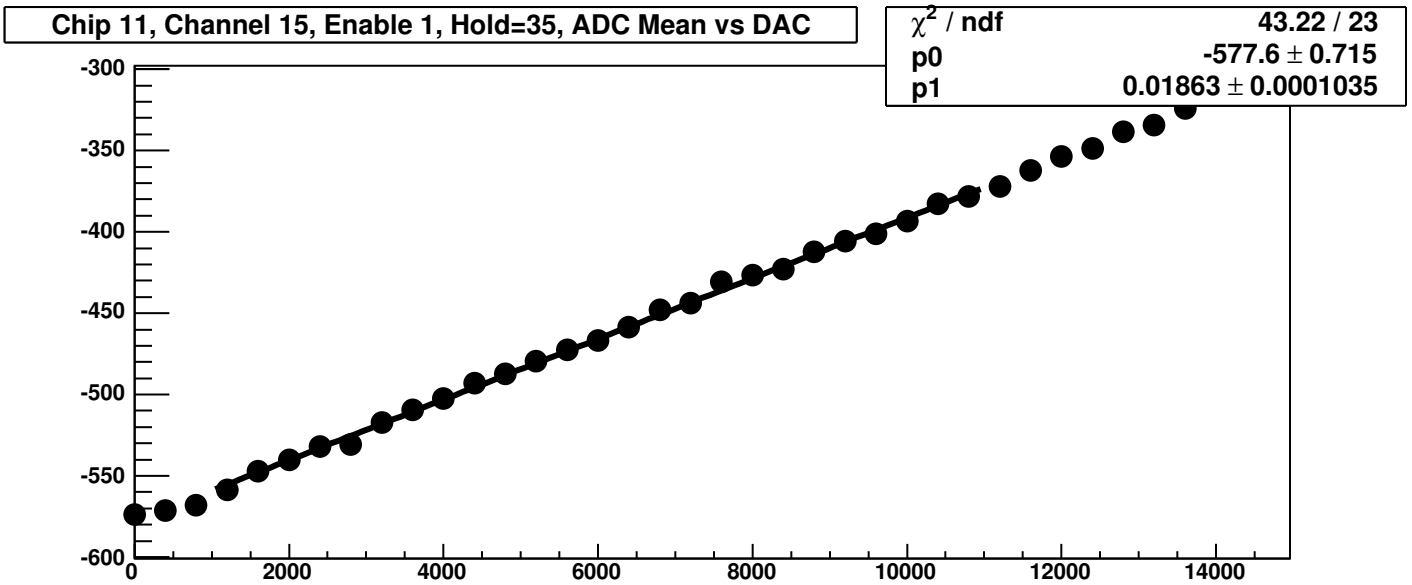
Chip 11, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

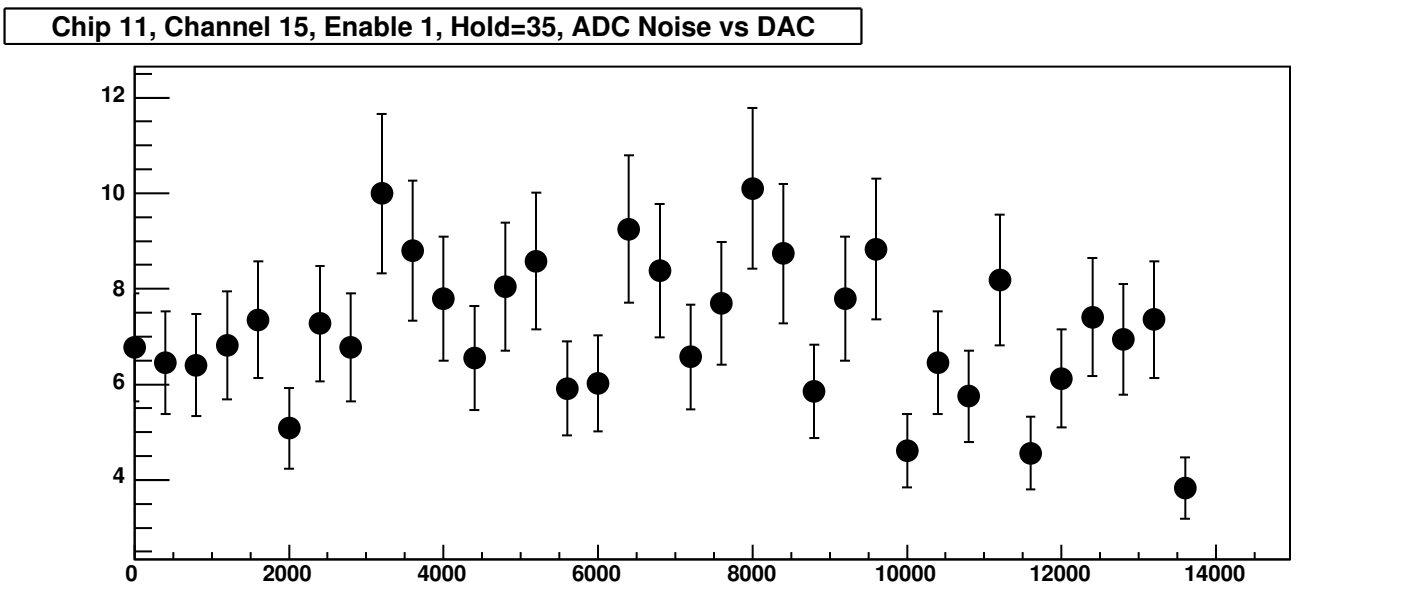


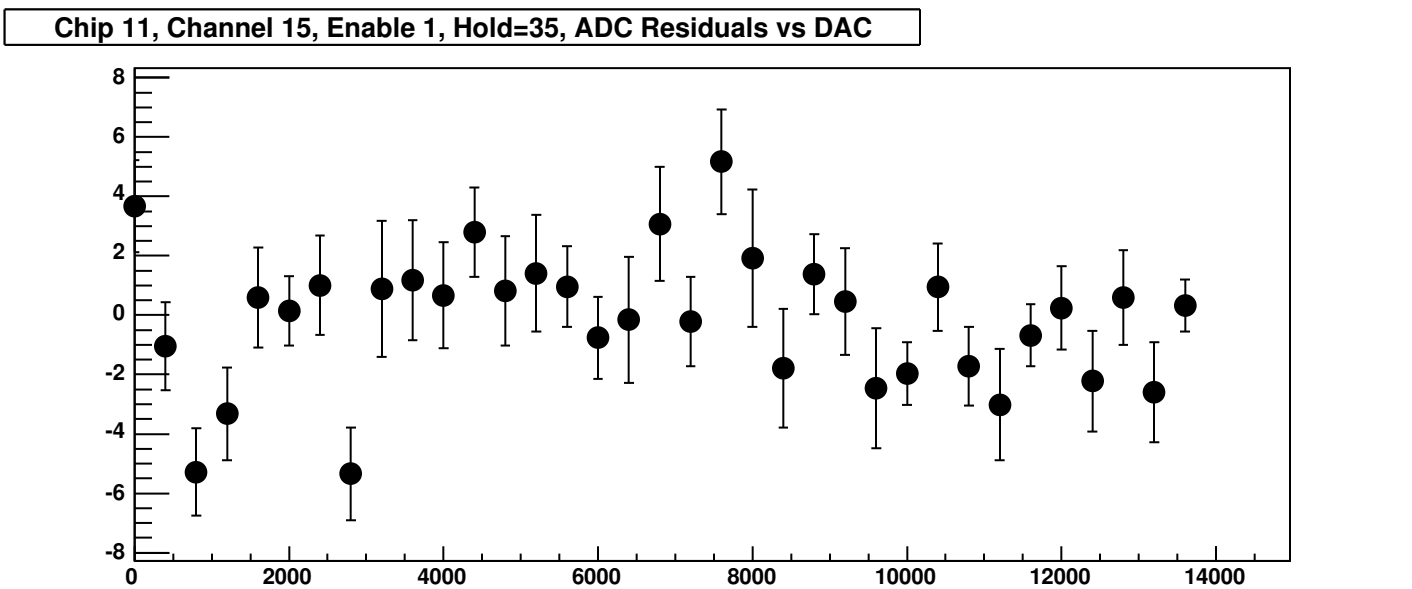

Chip 11, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 7.741e+04 / 23
p0 -960.1 ± 0.9169
p1 0.1264 ± 0.0001697
1200
1000
800
600
400
200
0
-200
-400
-600
-800
0
2000
4000
6000
8000
10000
12000
14000

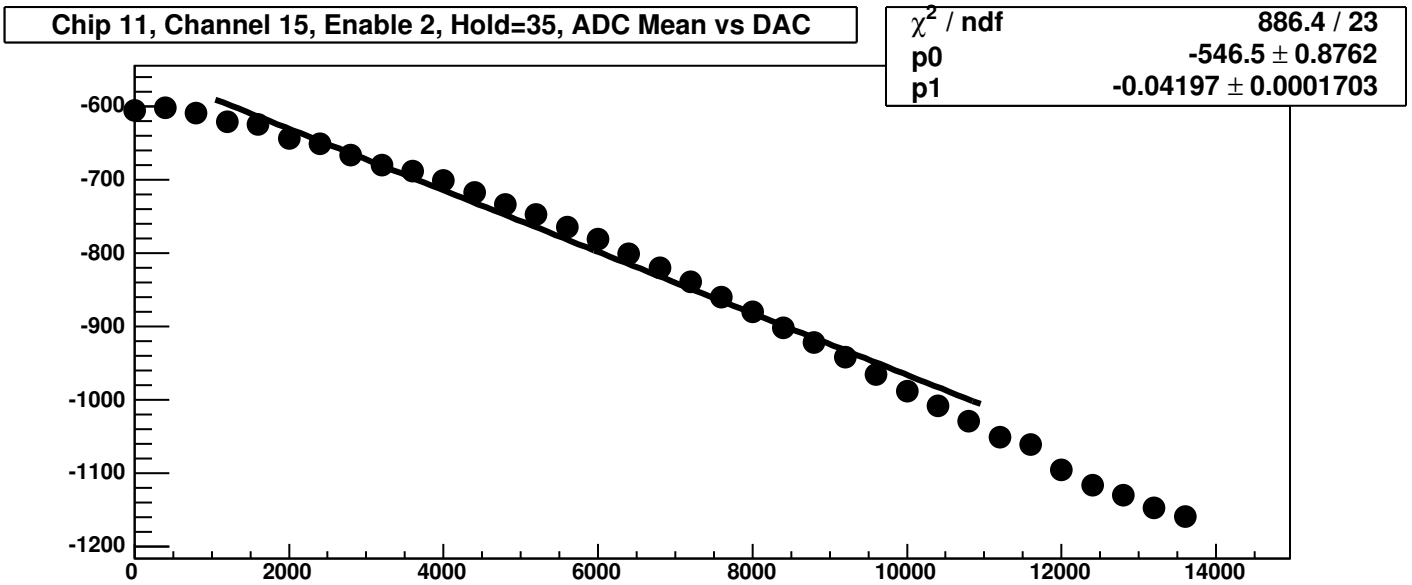

Chip 11, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

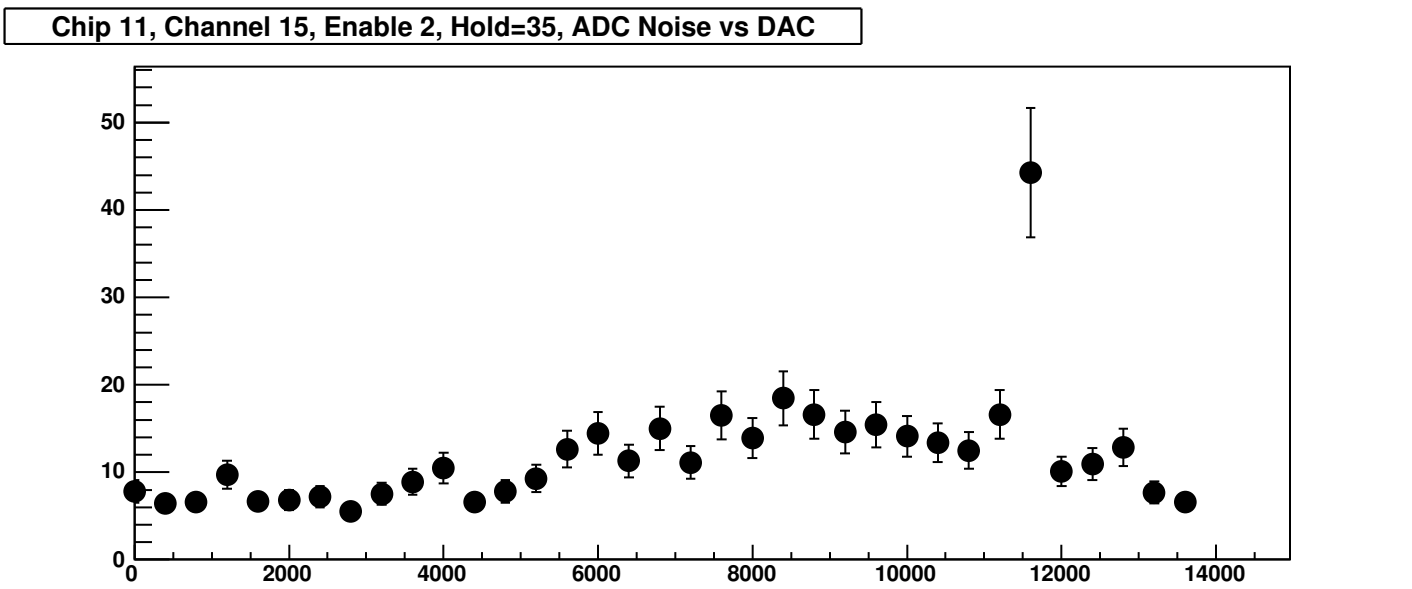

Chip 11, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

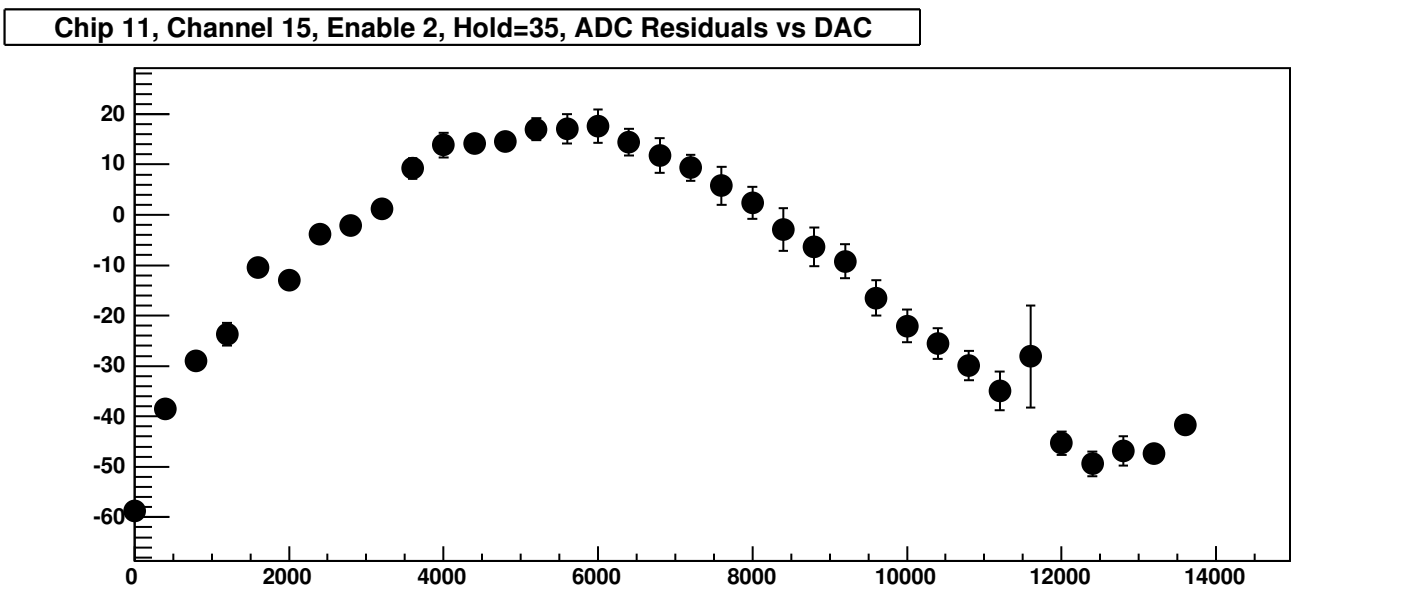

Chip 11, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 43.22 / 23
p0 -577.6 ± 0.715
p1 0.01863 ± 0.0001035
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

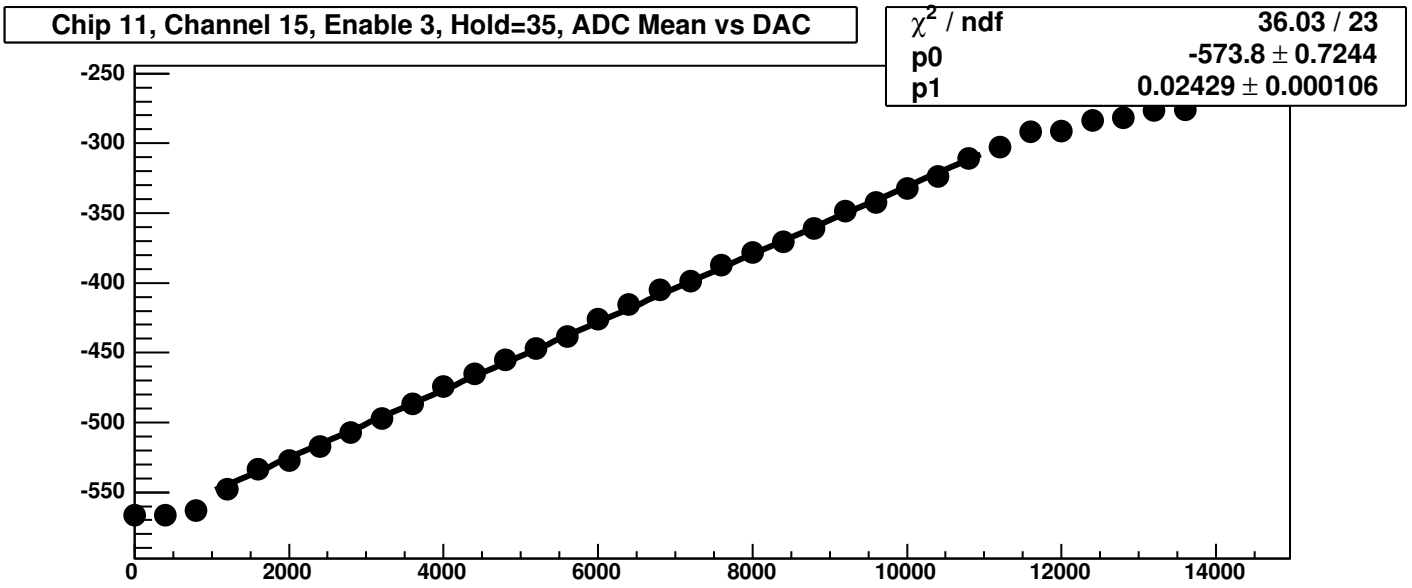

Chip 11, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

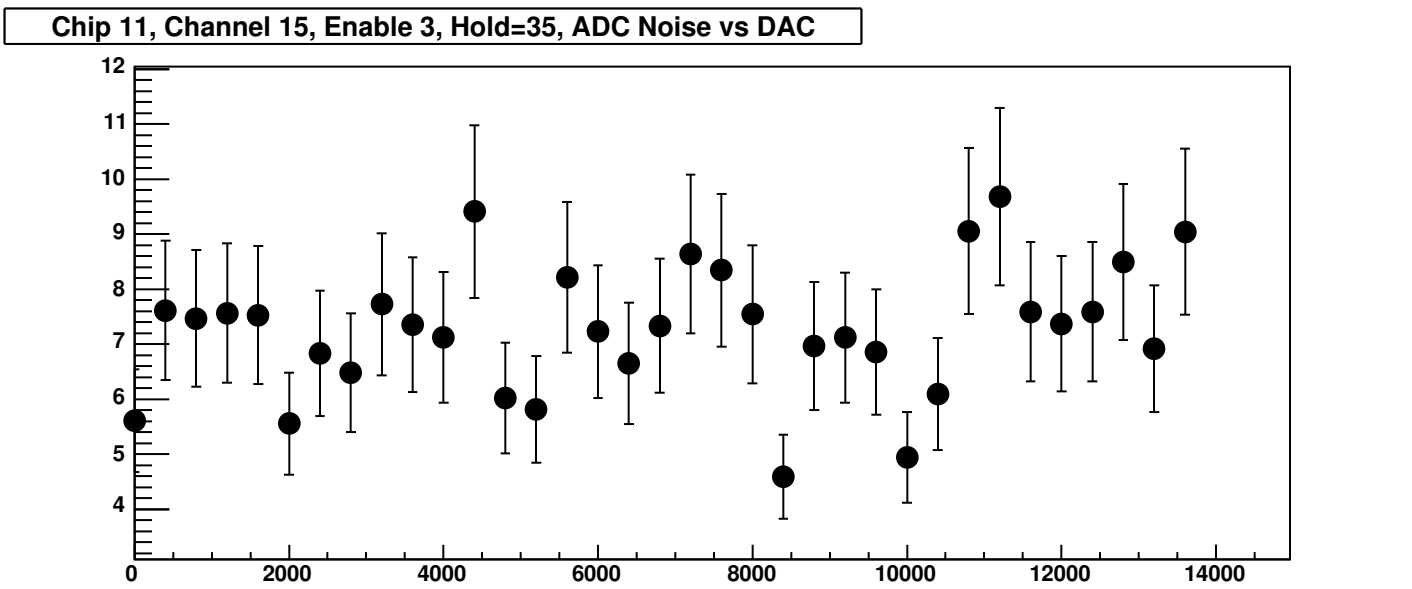

Chip 11, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

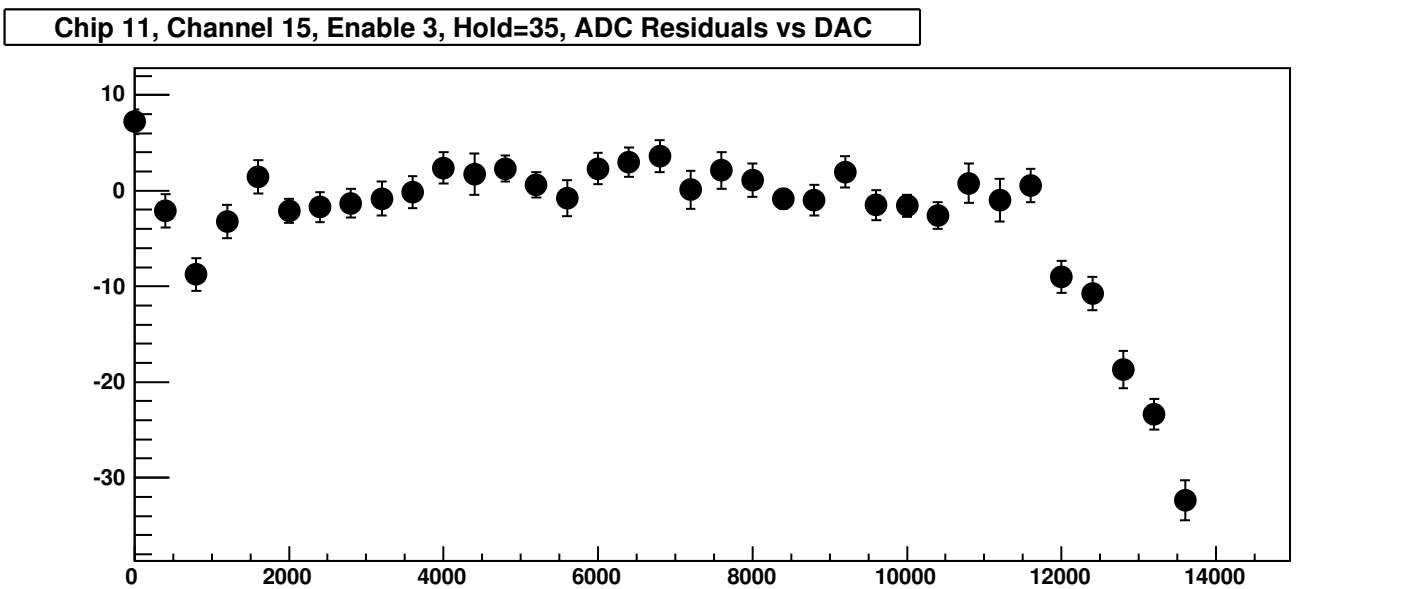

Chip 11, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 886.4 / 23
p0 -546.5 ± 0.8762
p1 -0.04197 ± 0.0001703
-600
-700
-800
-900
-1000
-1100
-1200
0
2000
4000
6000
8000
10000
12000
14000

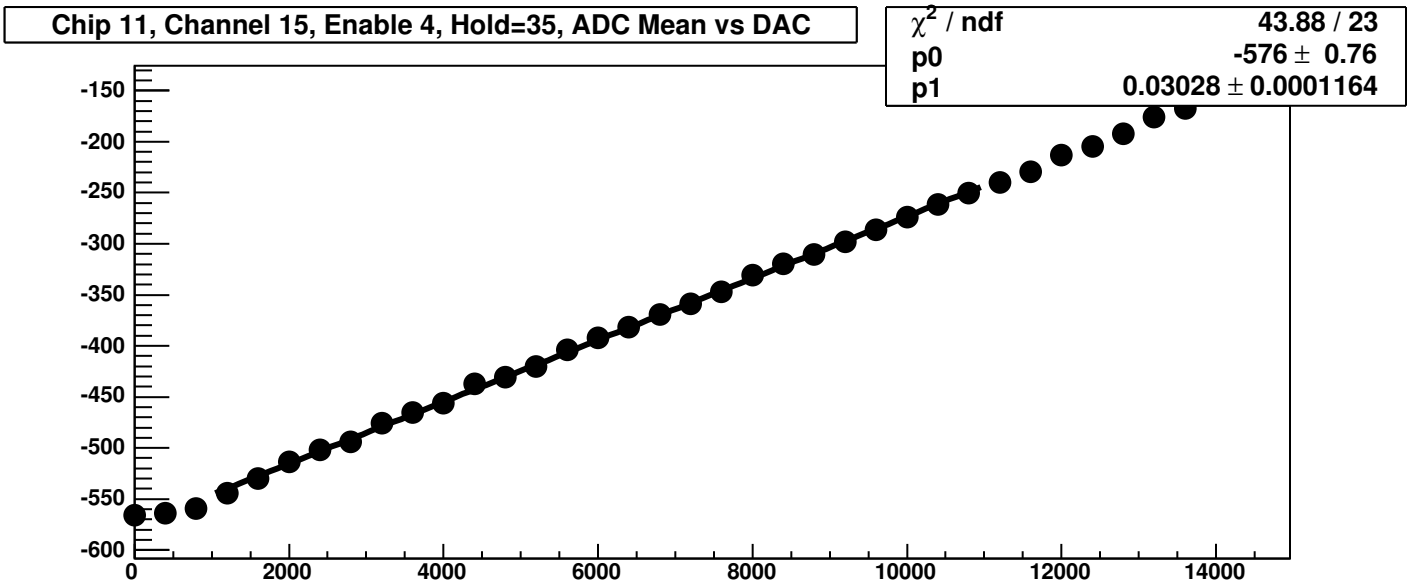

Chip 11, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

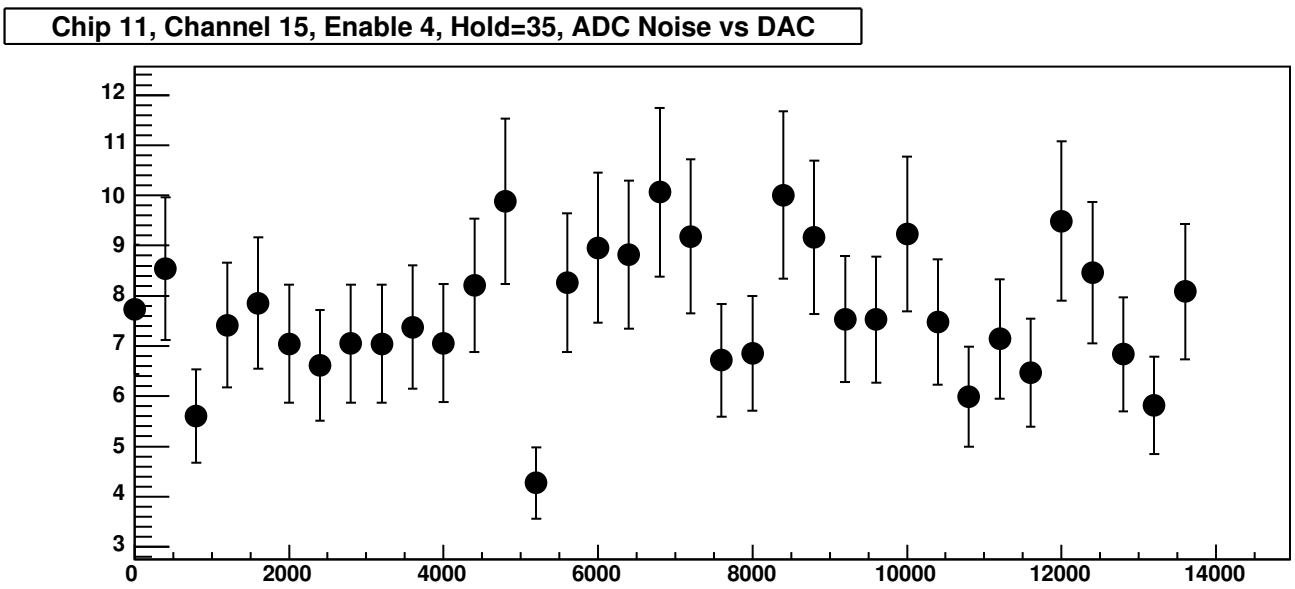

Chip 11, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
20
10
0
-10
-20
-30
-40
-50
-60
0
2000
4000
6000
8000
10000
12000
14000

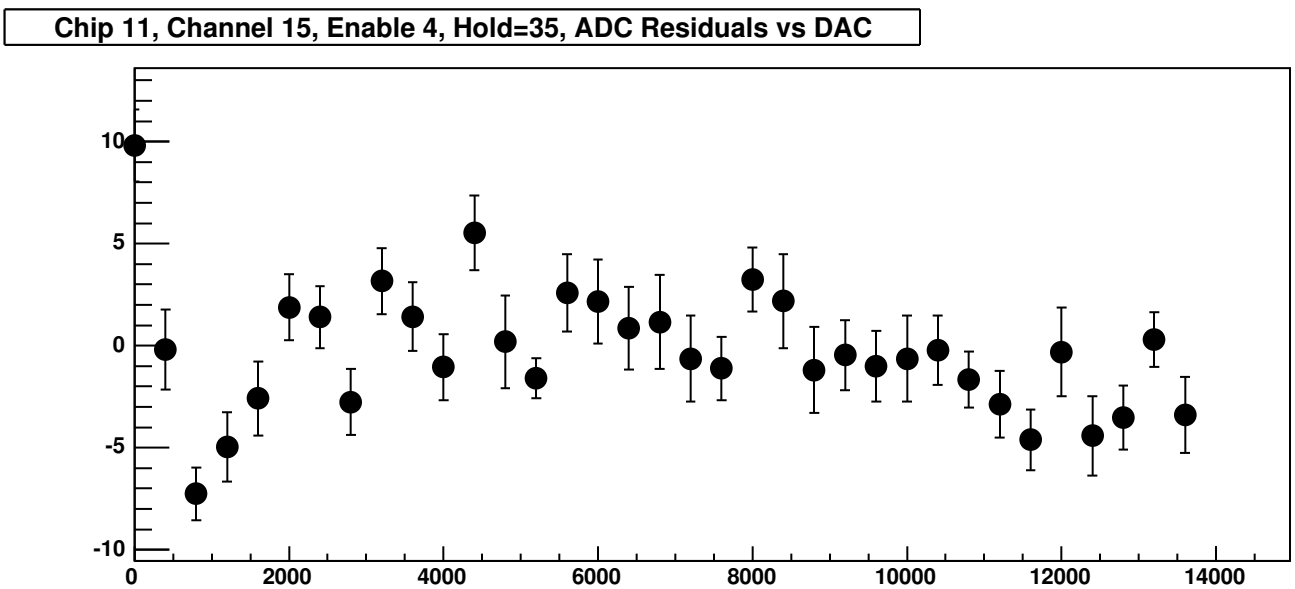

Chip 11, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.03 / 23
p0 -573.8 ± 0.7244
p1 0.02429 ± 0.000106
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

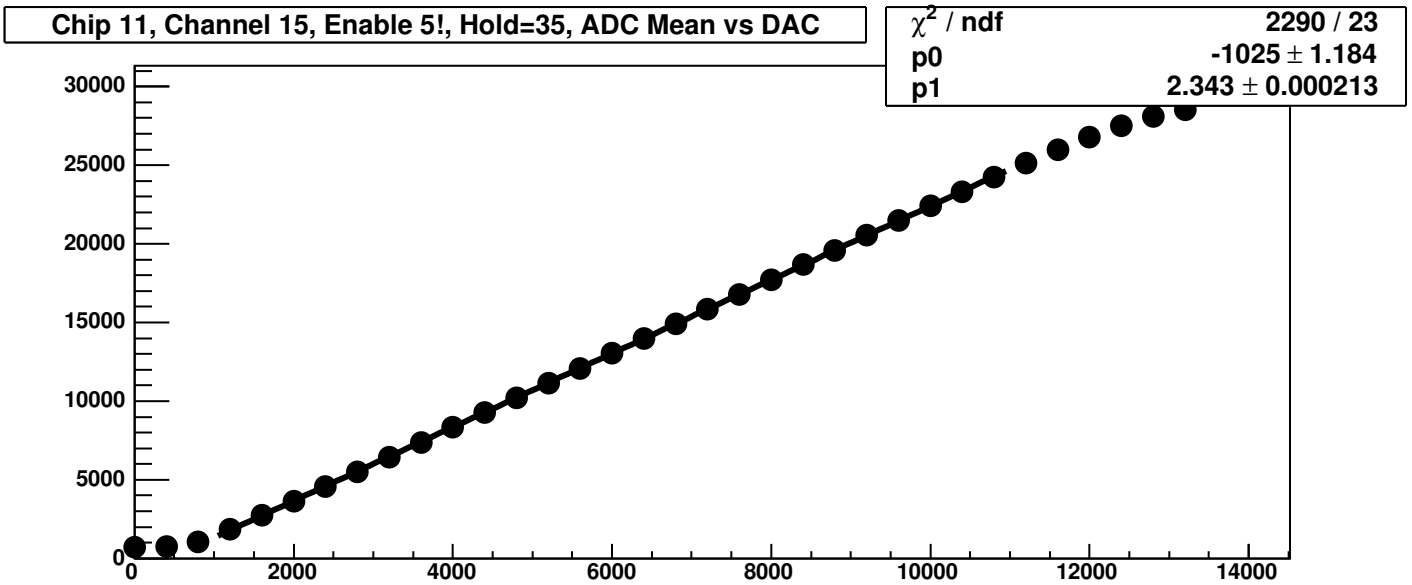

Chip 11, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000

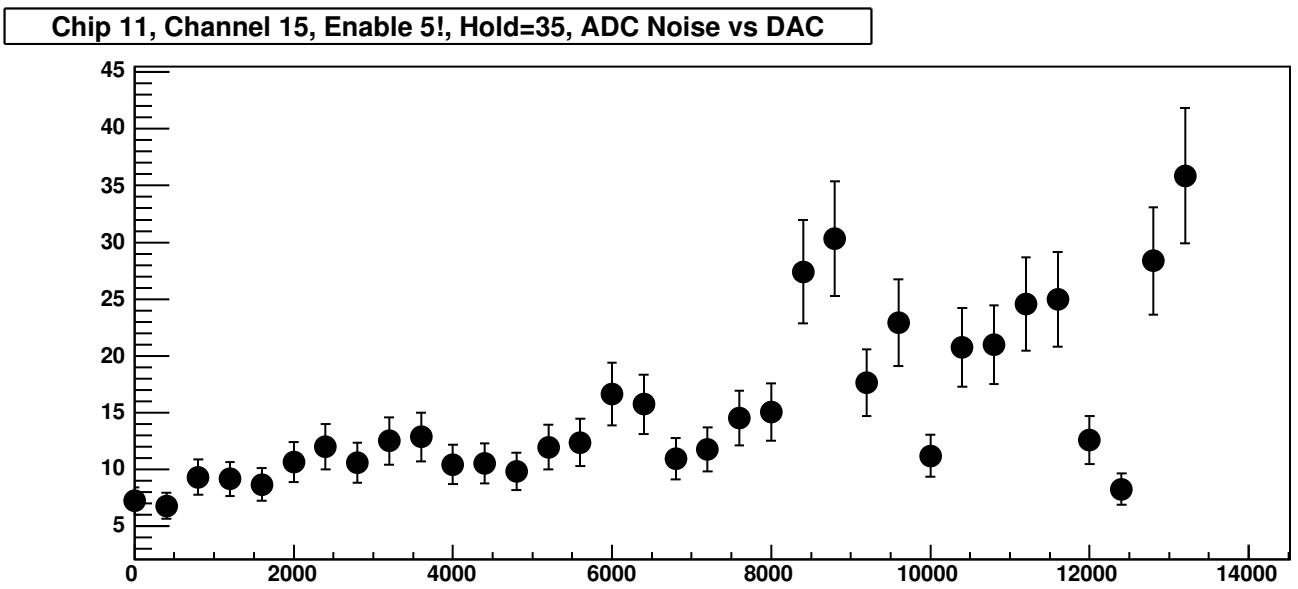

Chip 11, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000

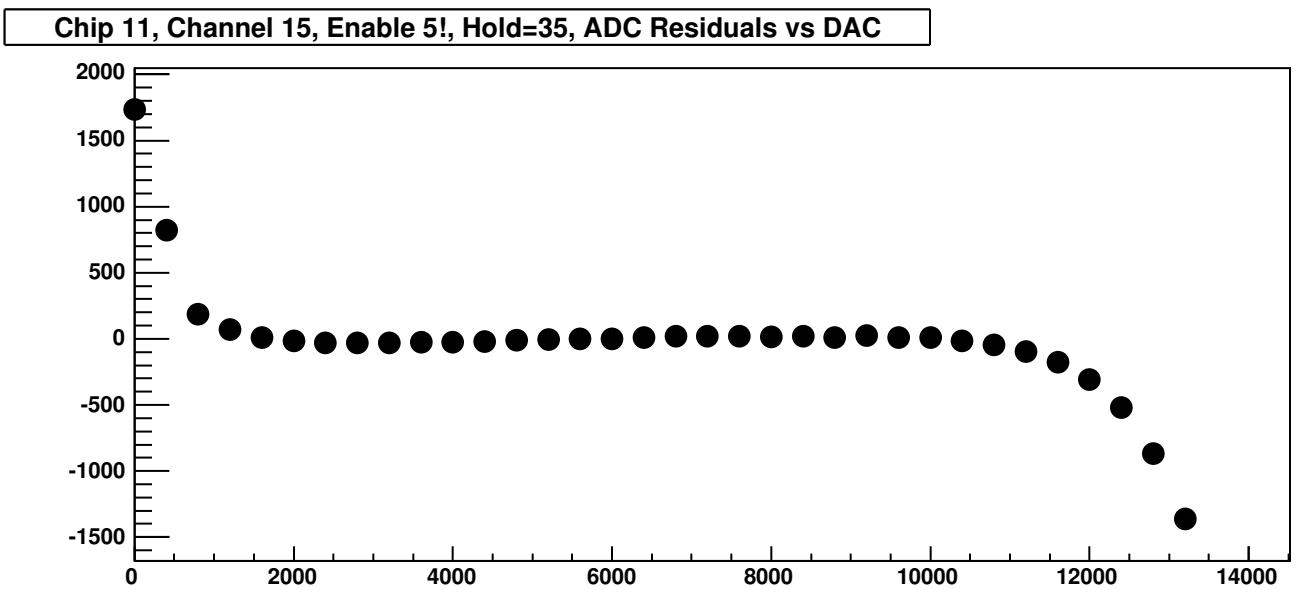

Chip 11, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 43.88 / 23
p0 -576 ± 0.76
p1 0.03028 ± 0.0001164
-150
-200
-250
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

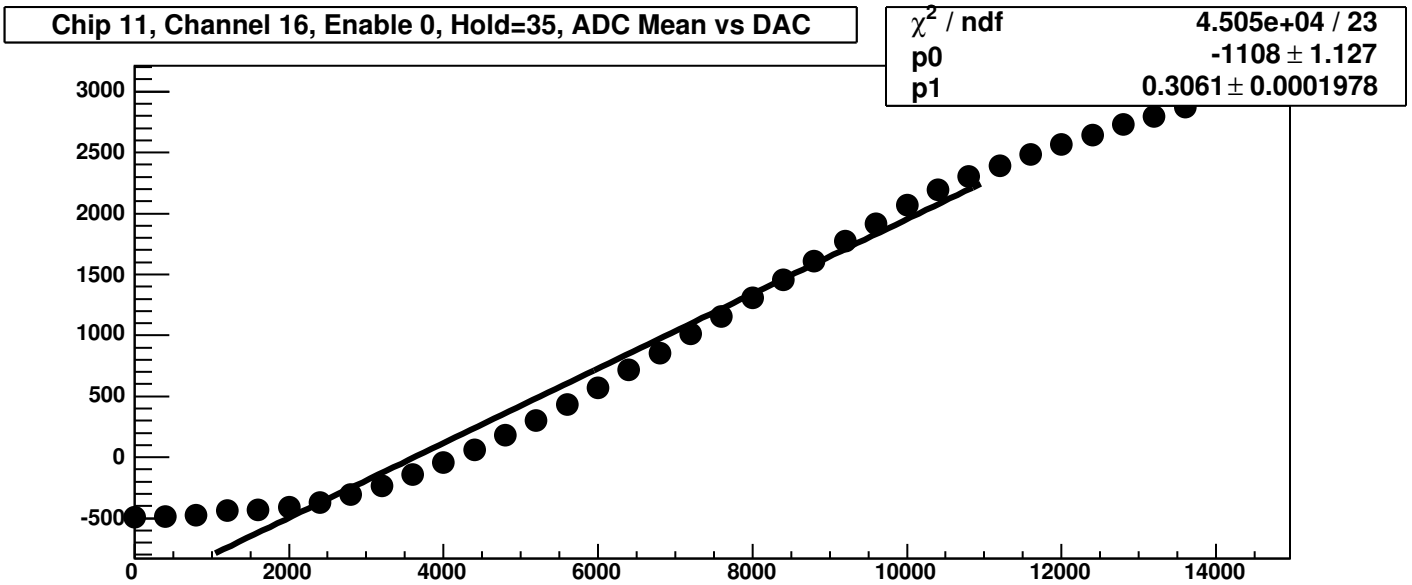

Chip 11, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
2000
4000
6000
8000
10000
12000
14000

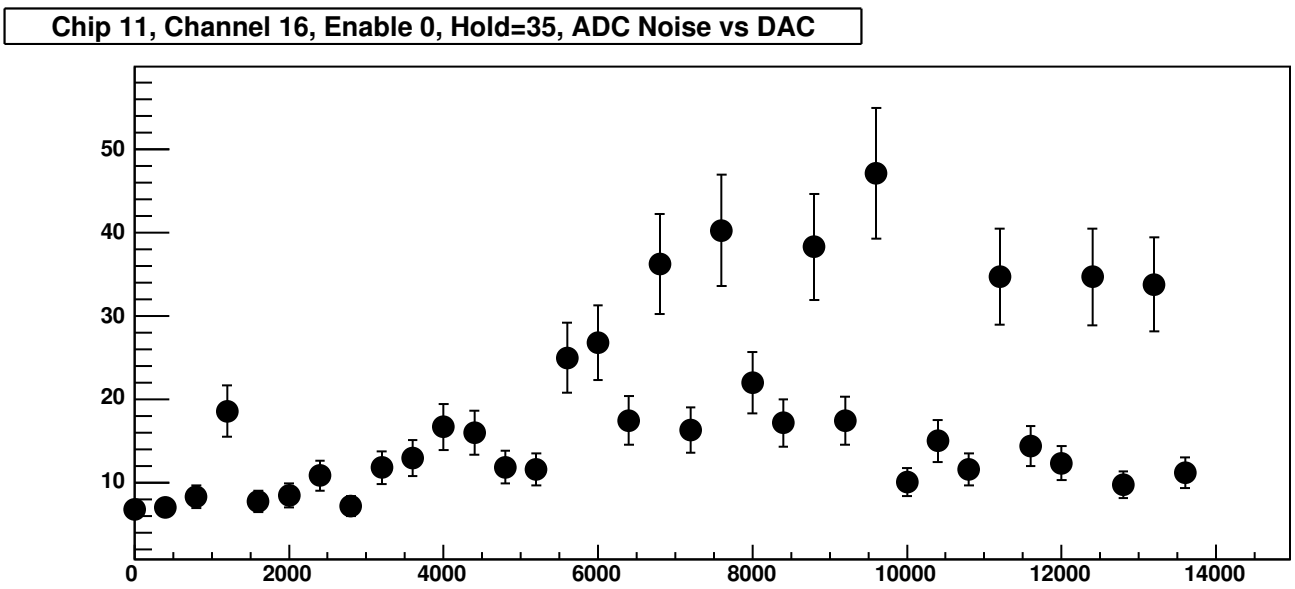

Chip 11, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000

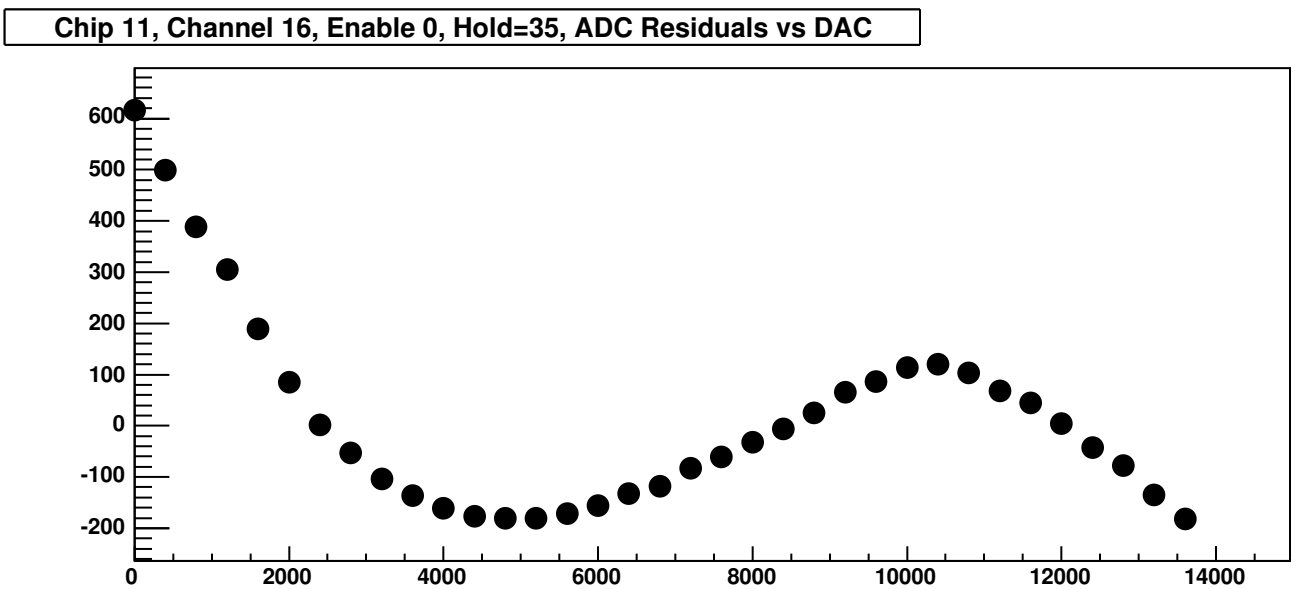

Chip 11, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 2290 / 23
p0 -1025 ± 1.184
p1 2.343 ± 0.000213
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000

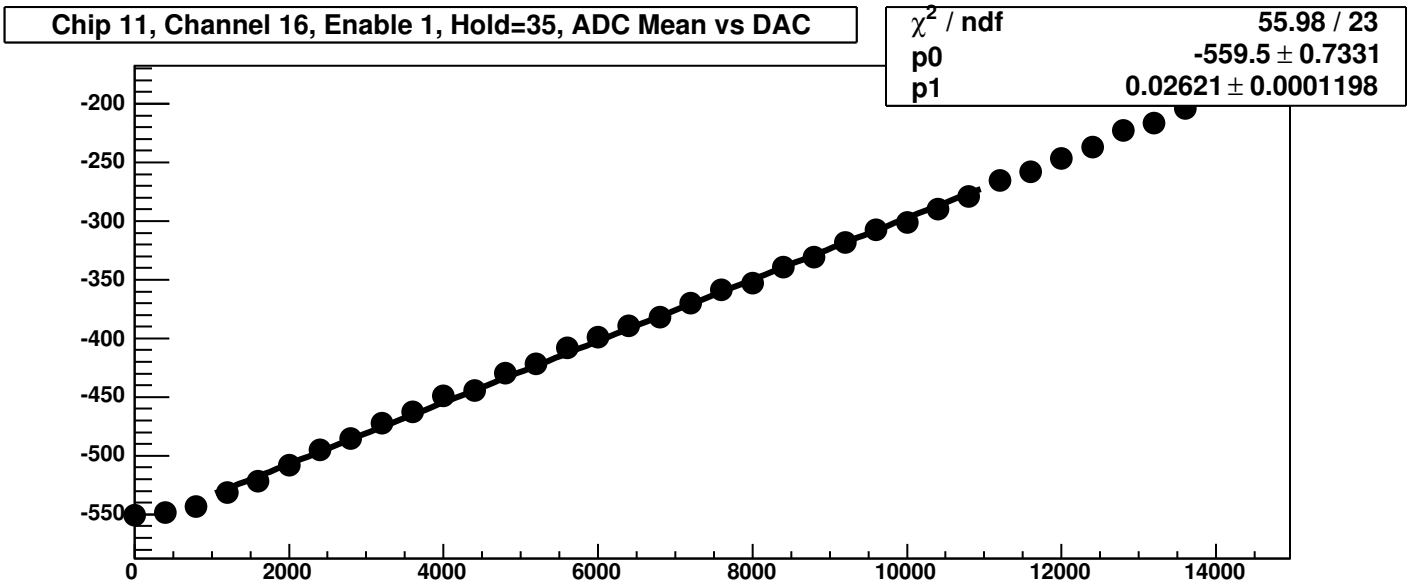

Chip 11, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

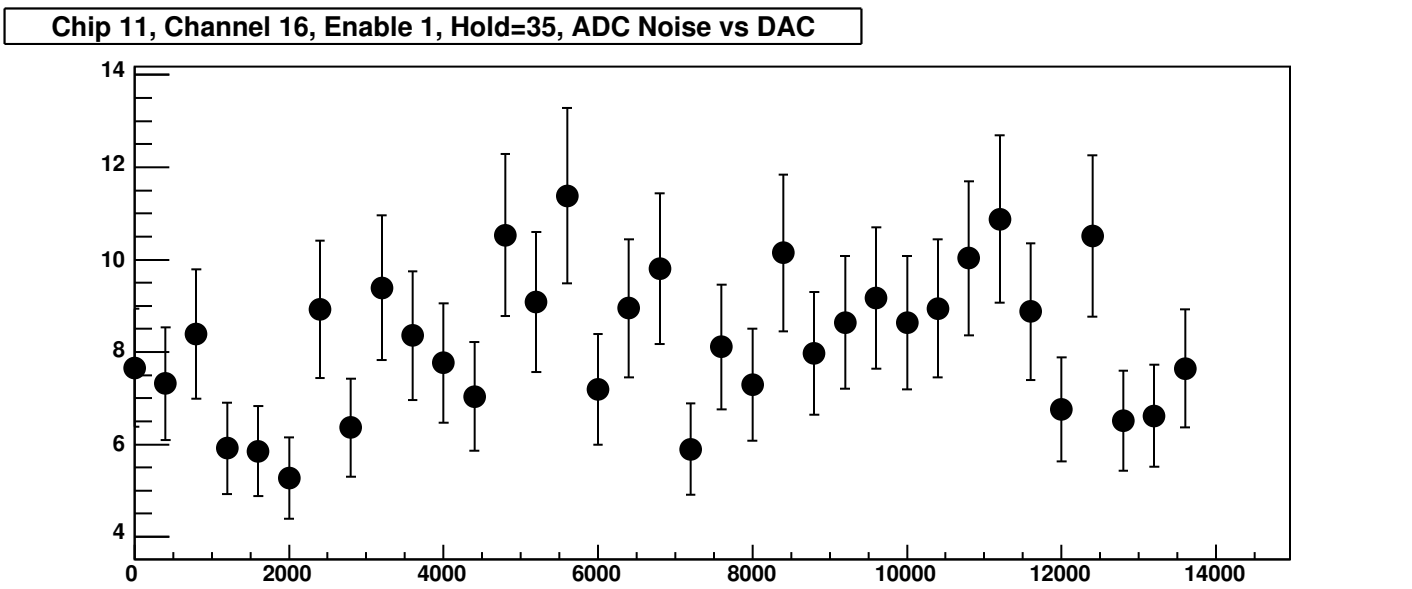

Chip 11, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
2000
1500
1000
500
0
-500
-1000
-1500
0
2000
4000
6000
8000
10000
12000
14000

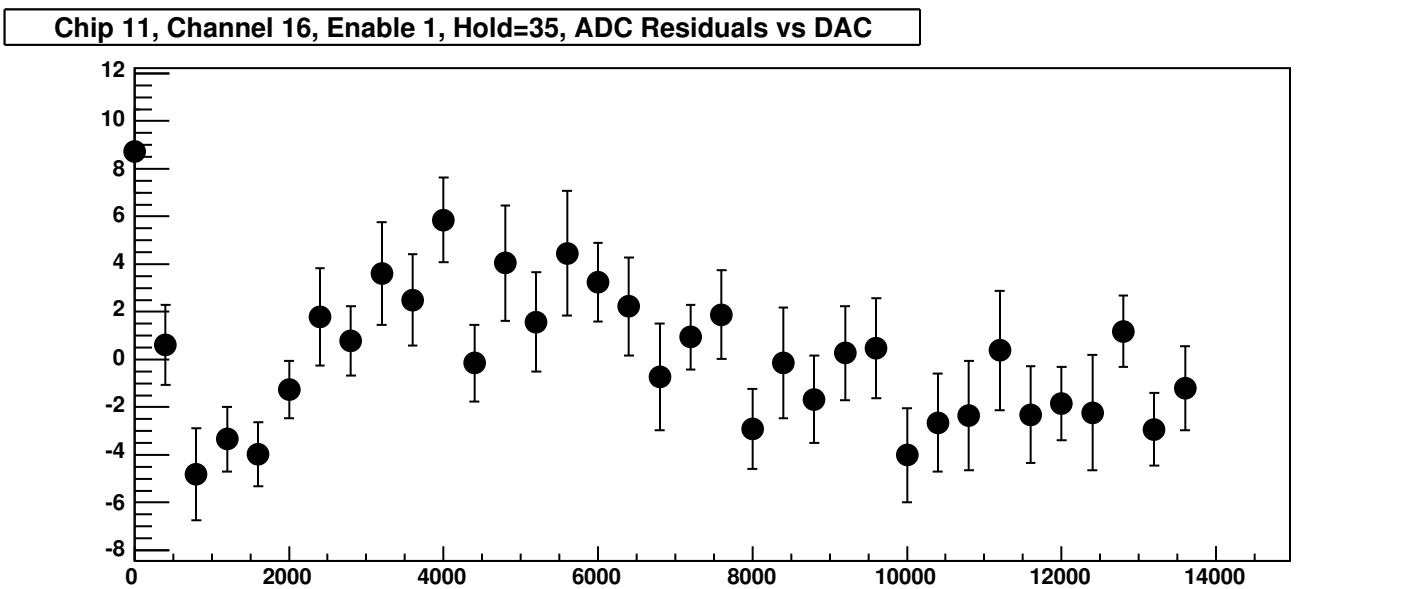

Chip 11, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 4.505e+04 / 23
p0 -1108 ± 1.127
p1 0.3061 ± 0.0001978
3000
2500
2000
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

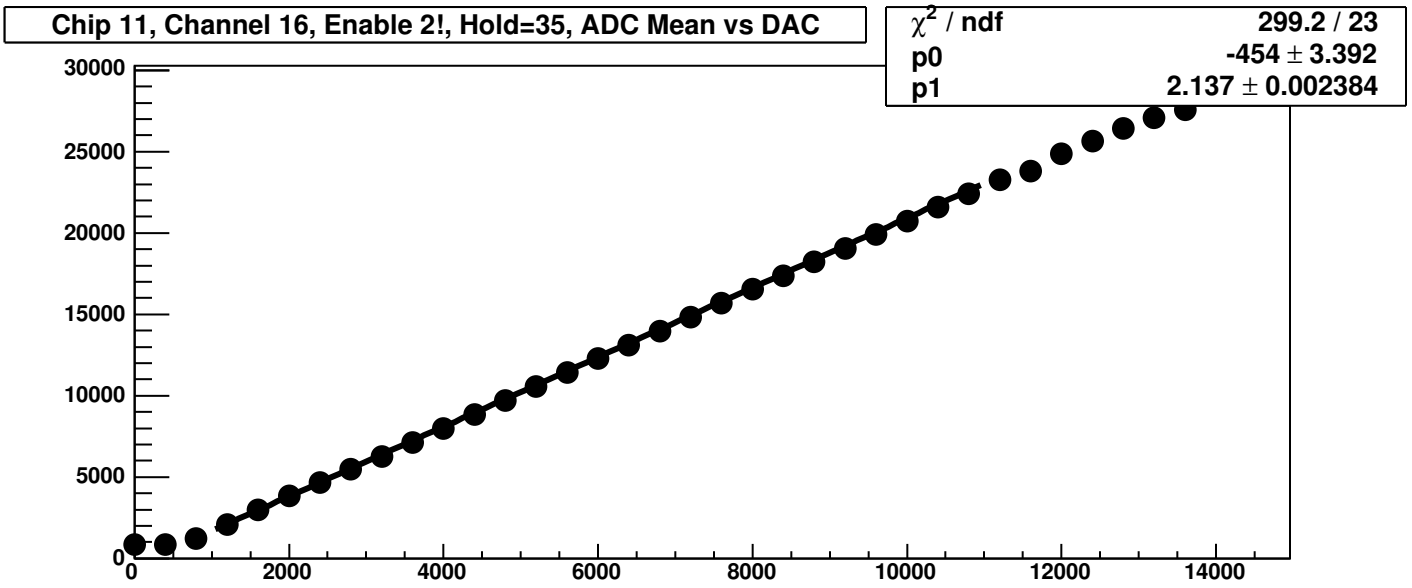

Chip 11, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
2000
4000
6000
8000
10000
12000
14000

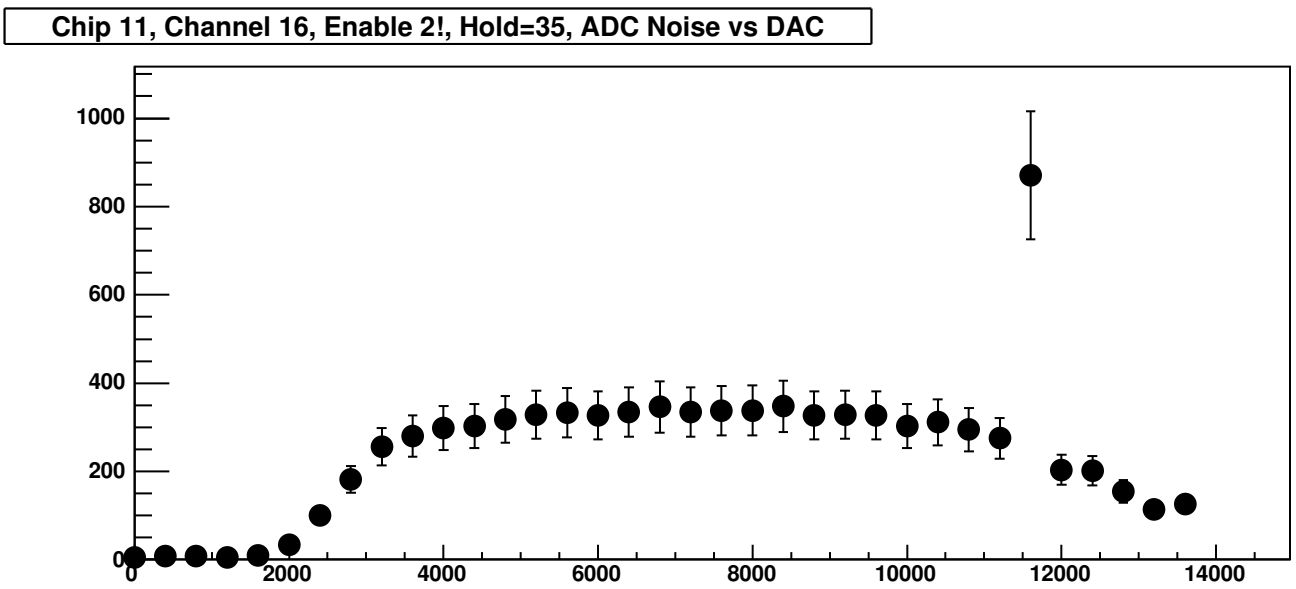

Chip 11, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
600
500
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

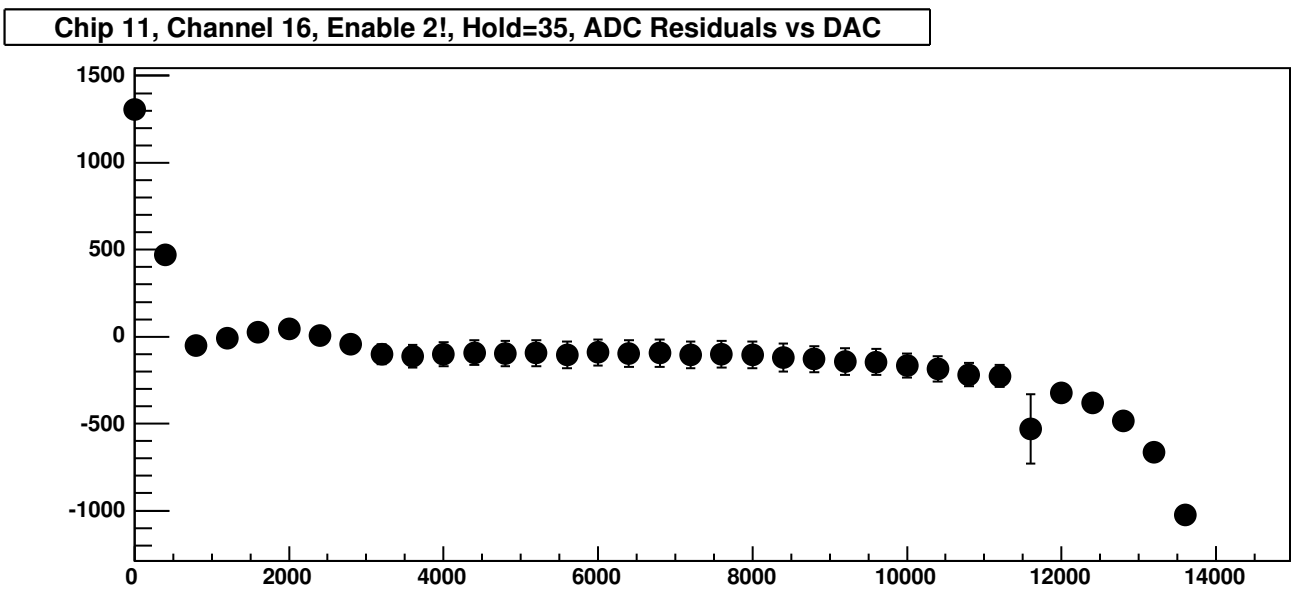

Chip 11, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 55.98 / 23
p0 -559.5 ± 0.7331
p1 0.02621 ± 0.0001198
-200
-250
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

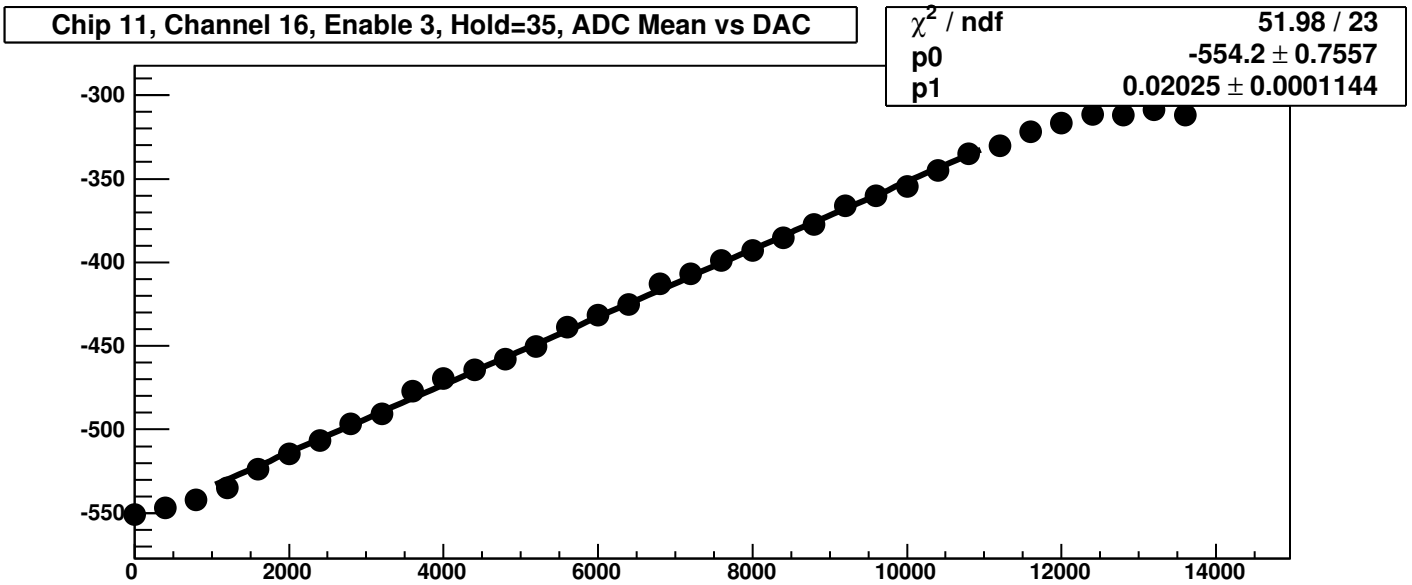

Chip 11, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

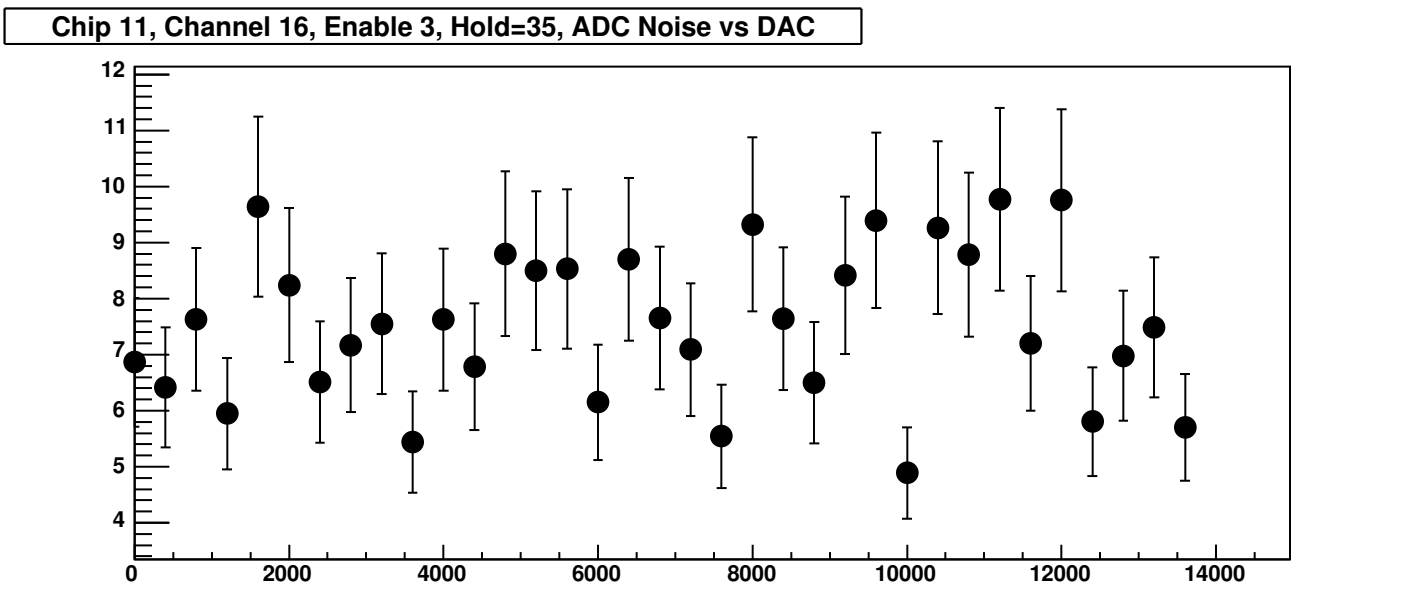

Chip 11, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

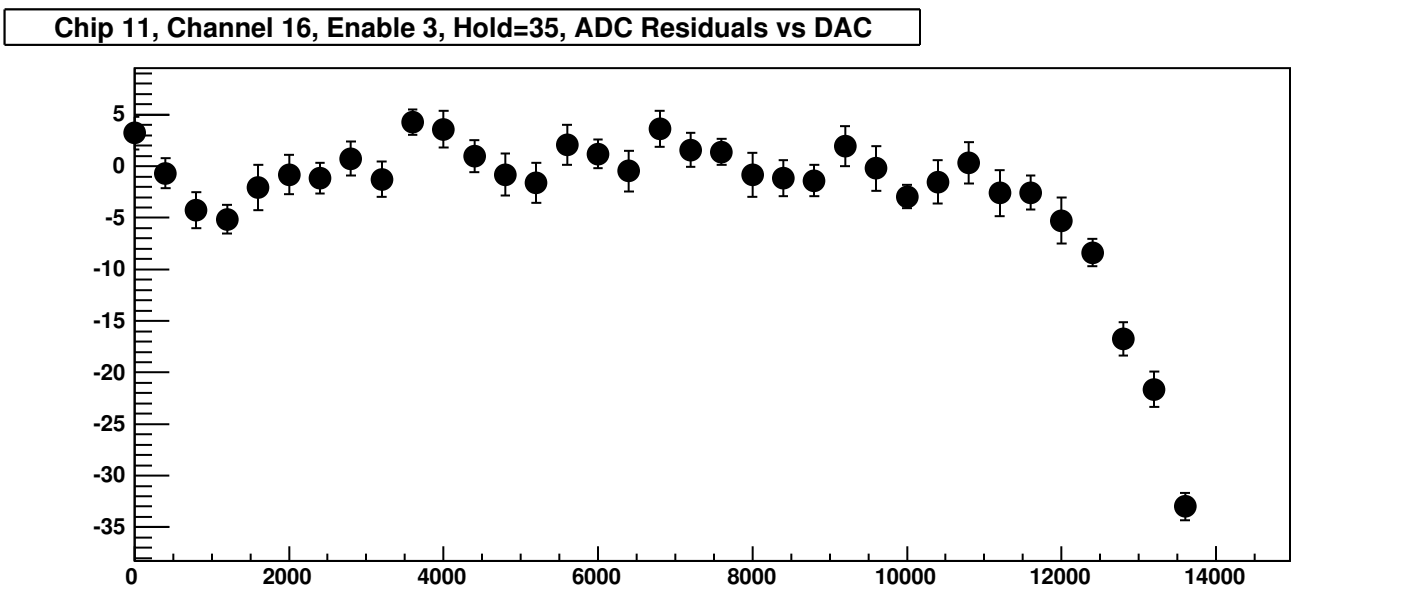

Chip 11, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 299.2 / 23
p0 -454 ± 3.392
p1 2.137 ± 0.002384
30000
25000
20000
15000
10000
5000
0
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
1000
800
600
400
200
0
0
2000
4000
6000
8000
10000
12000
14000

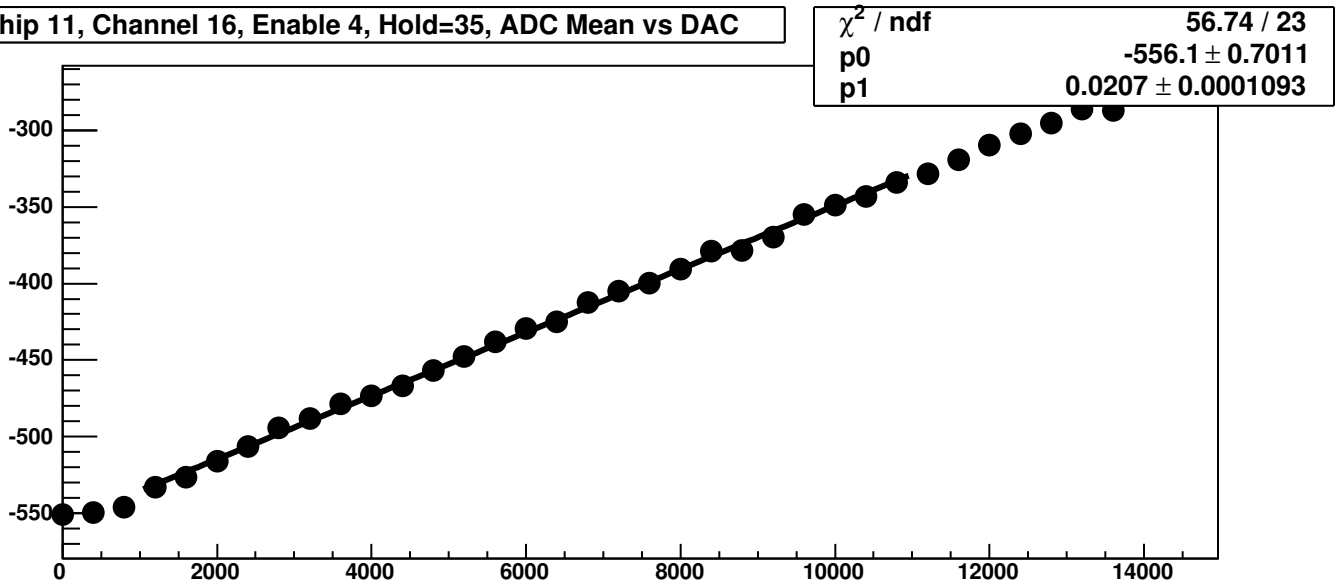

Chip 11, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
1500
1000
500
0
-500
-1000
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 51.98 / 23
p0 -554.2 ± 0.7557
p1 0.02025 ± 0.0001144
-300
-350
-400
-450
-500
-550
0
2000
4000
6000
8000
10000
12000
14000

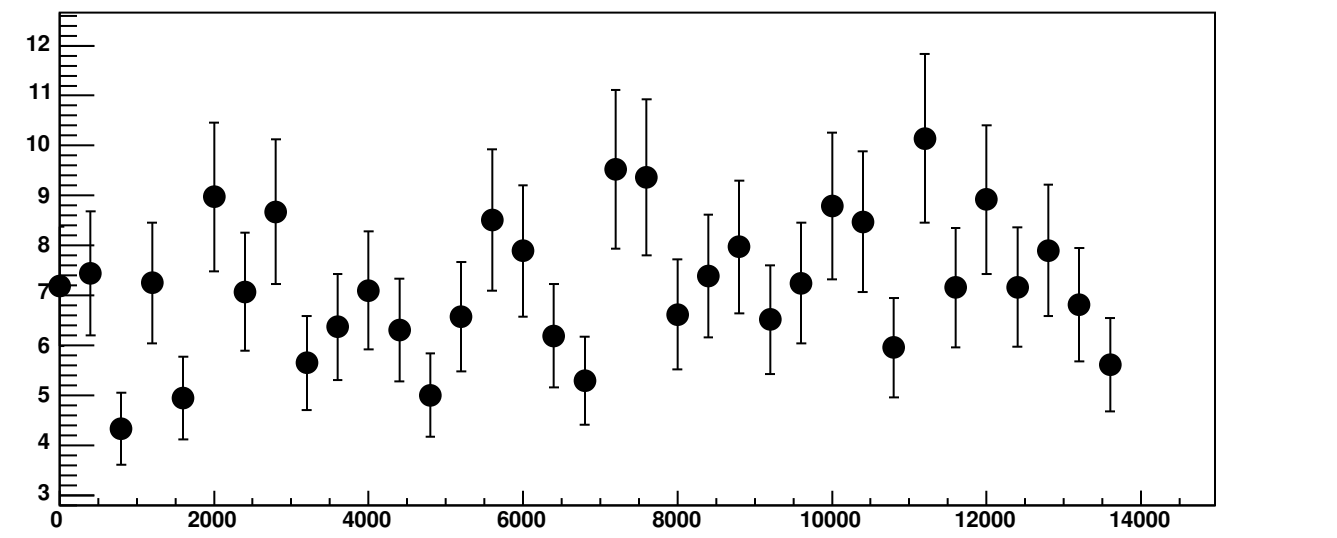

Chip 11, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
-35
0
2000
4000
6000
8000
10000
12000
14000

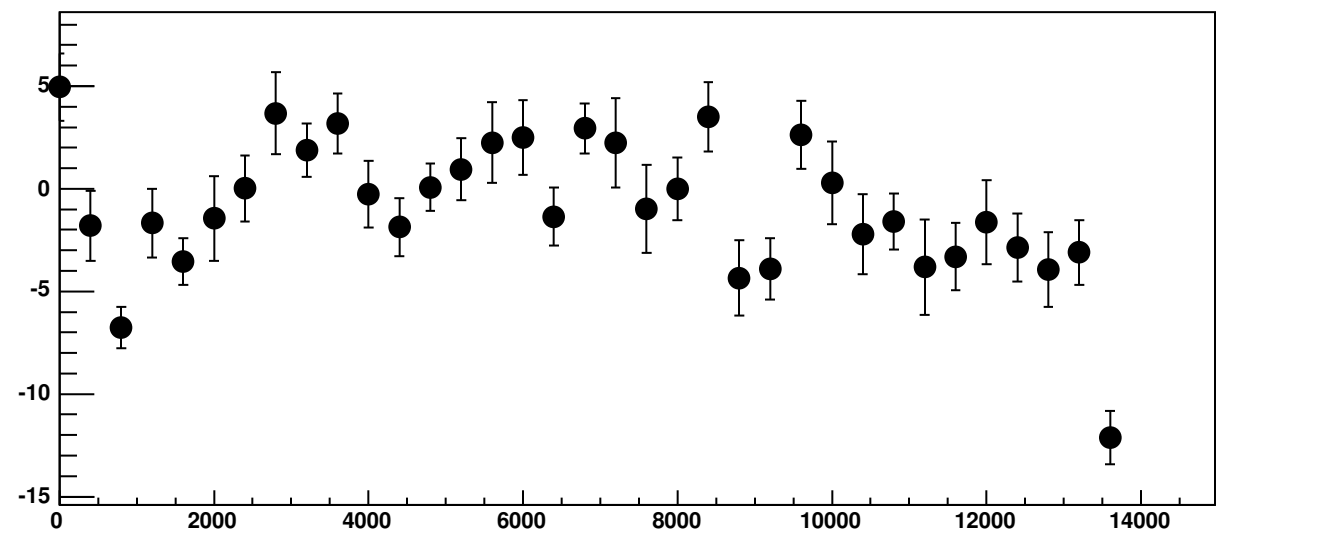
Chip 11, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC

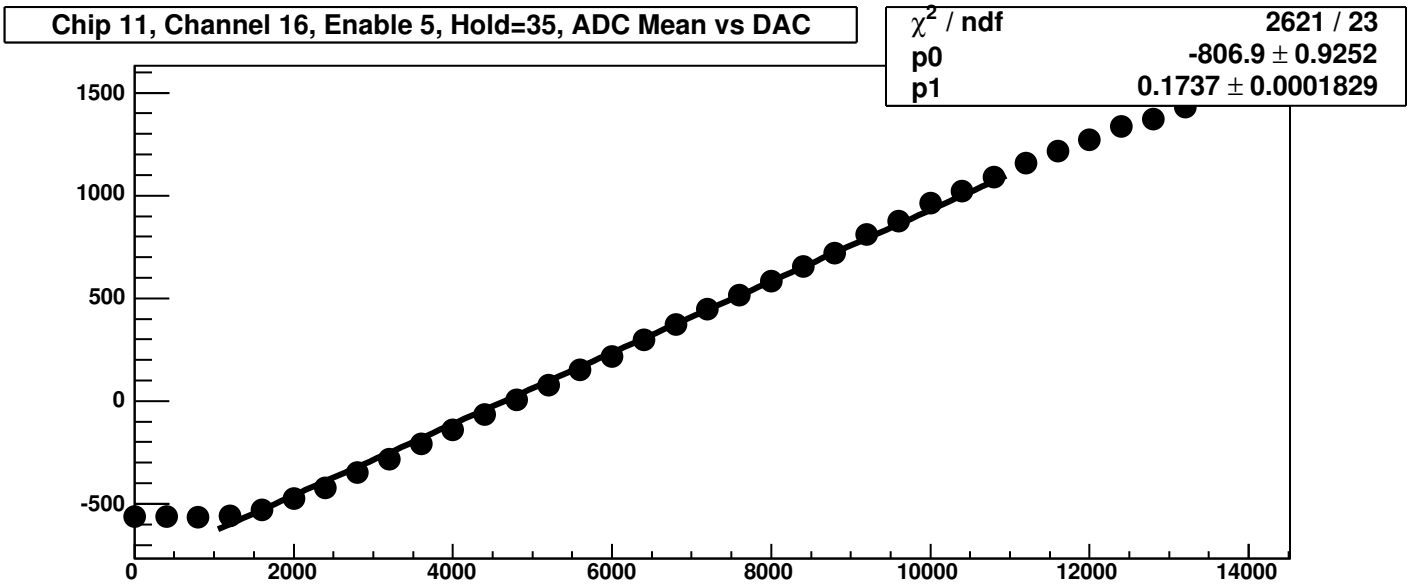


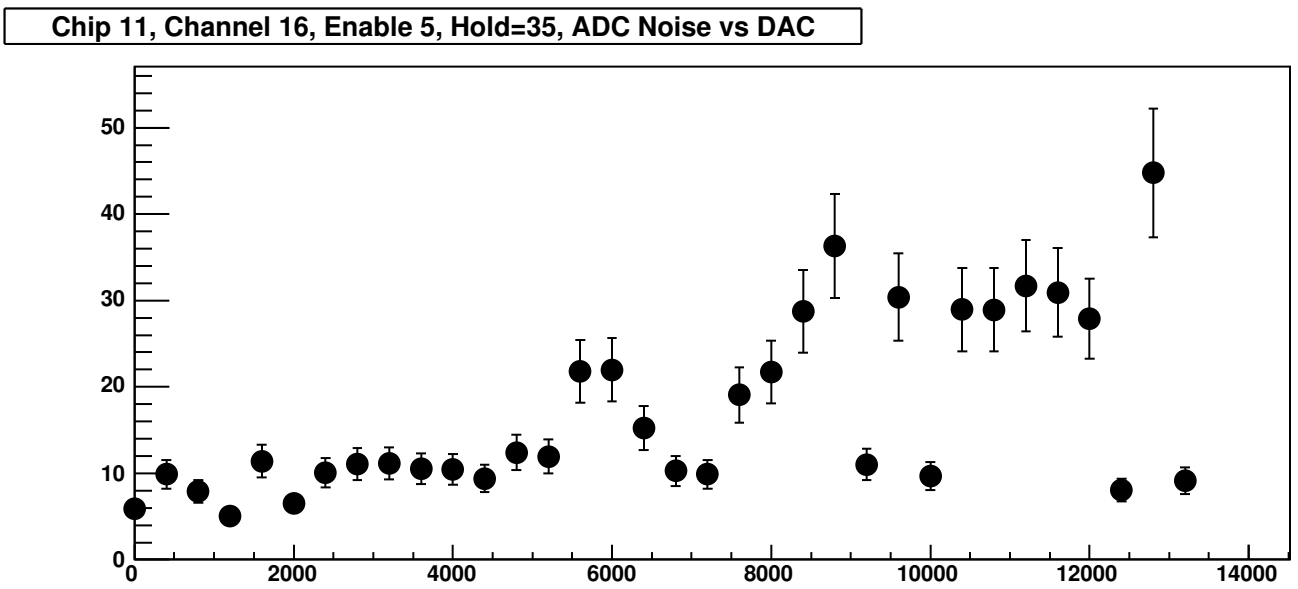
Chip 11, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC

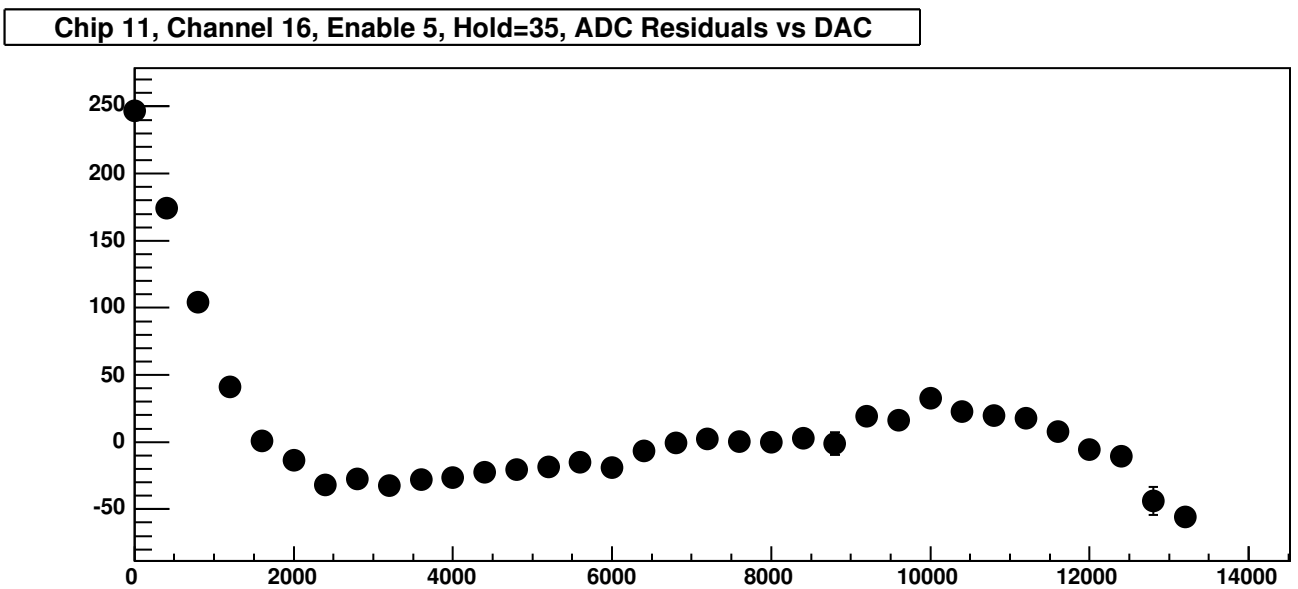


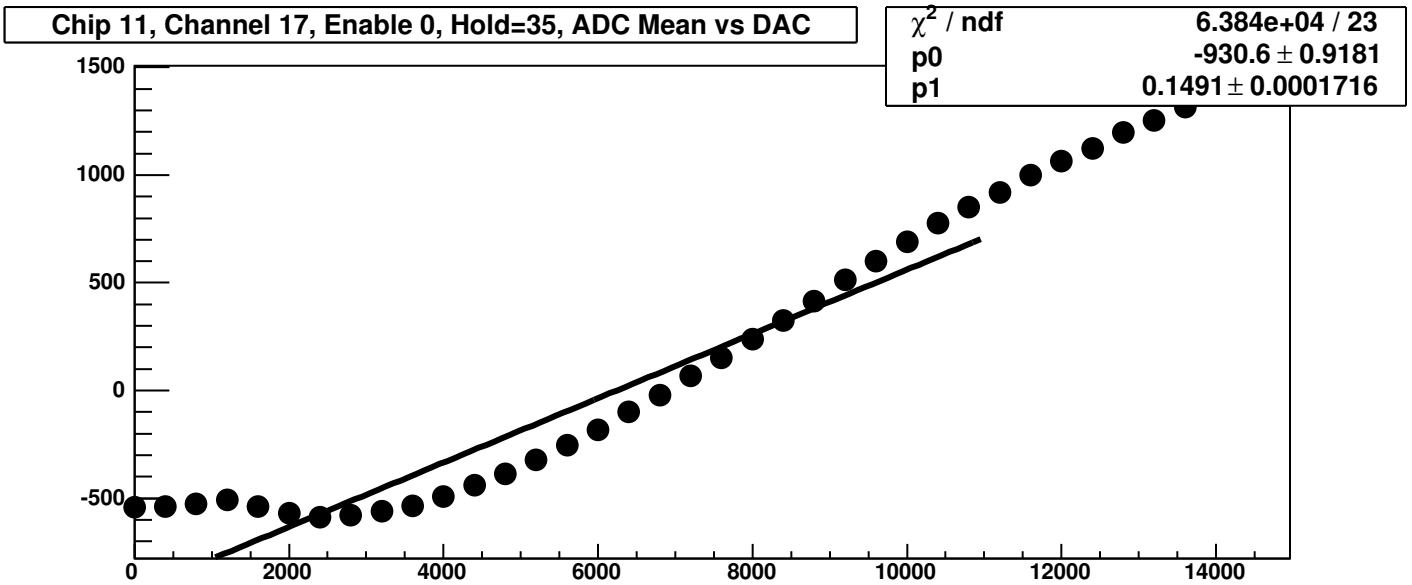
Chip 11, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC

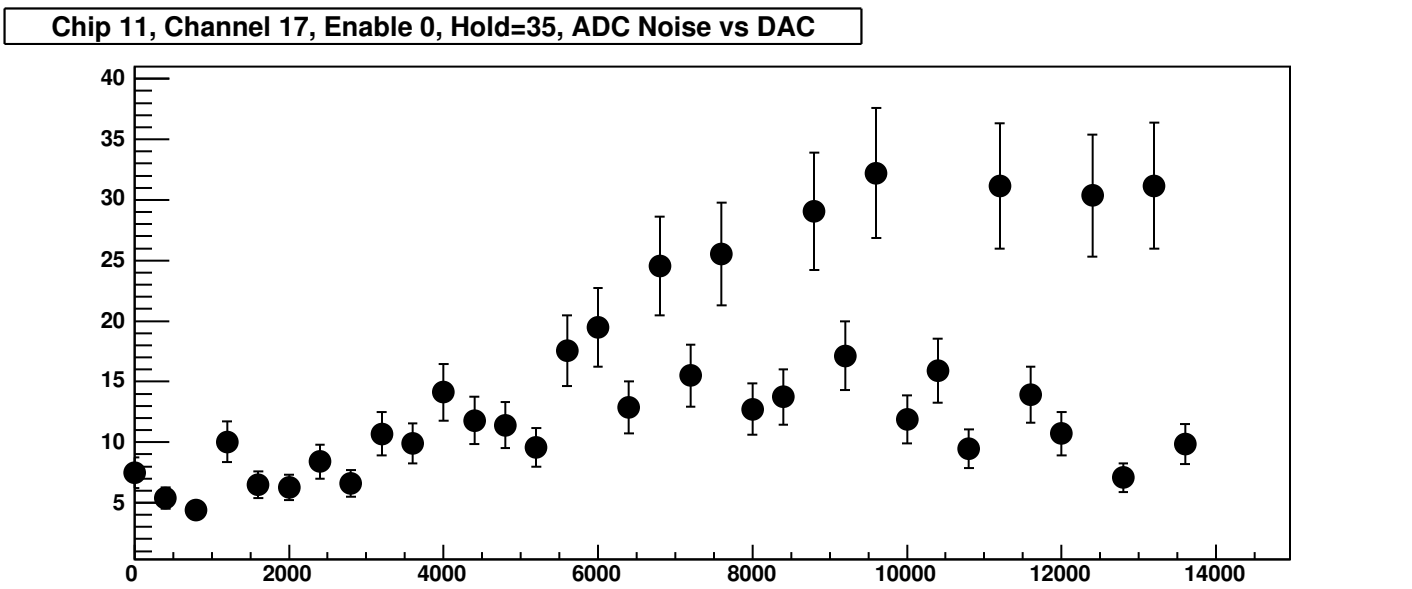


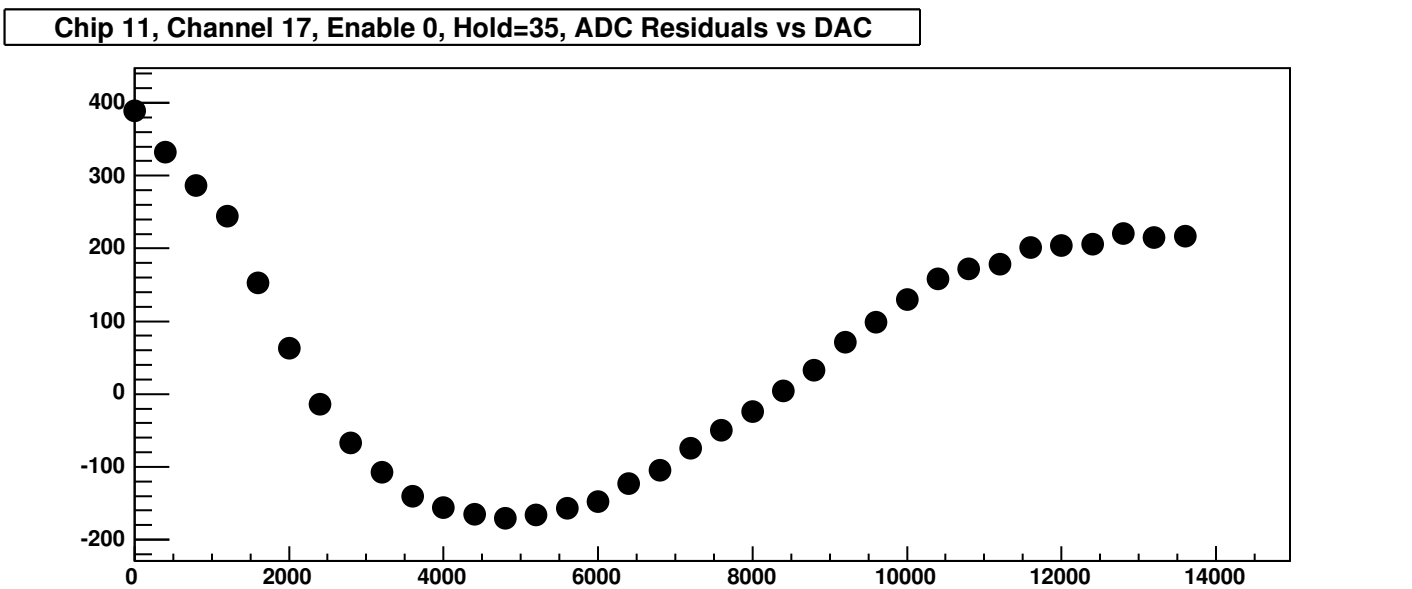

Chip 11, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 2621 / 23
p0 -806.9 ± 0.9252
p1 0.1737 ± 0.0001829
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

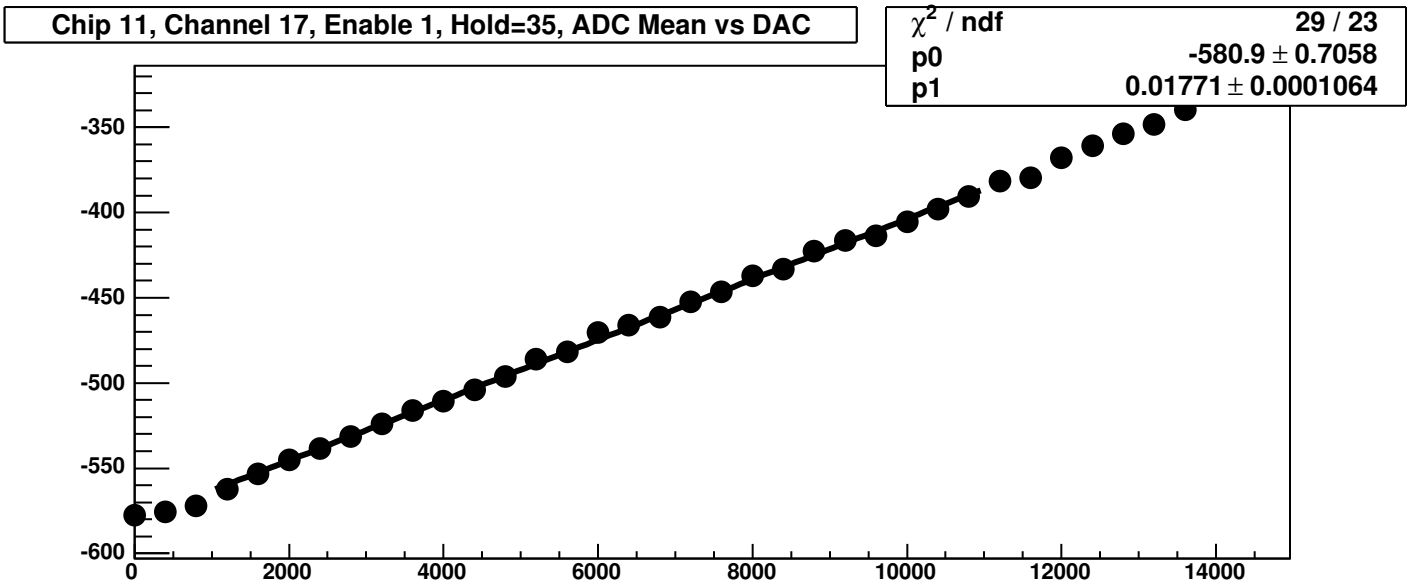

Chip 11, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
2000
4000
6000
8000
10000
12000
14000

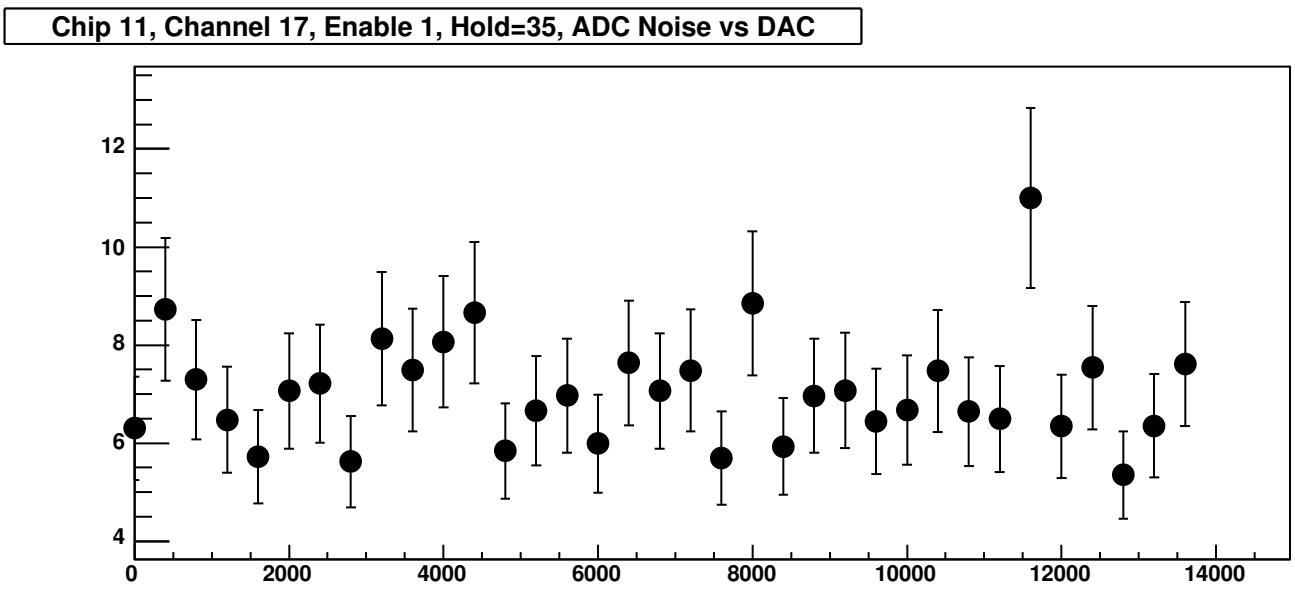

Chip 11, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC
250
200
150
100
50
0
-50
0
2000
4000
6000
8000
10000
12000
14000

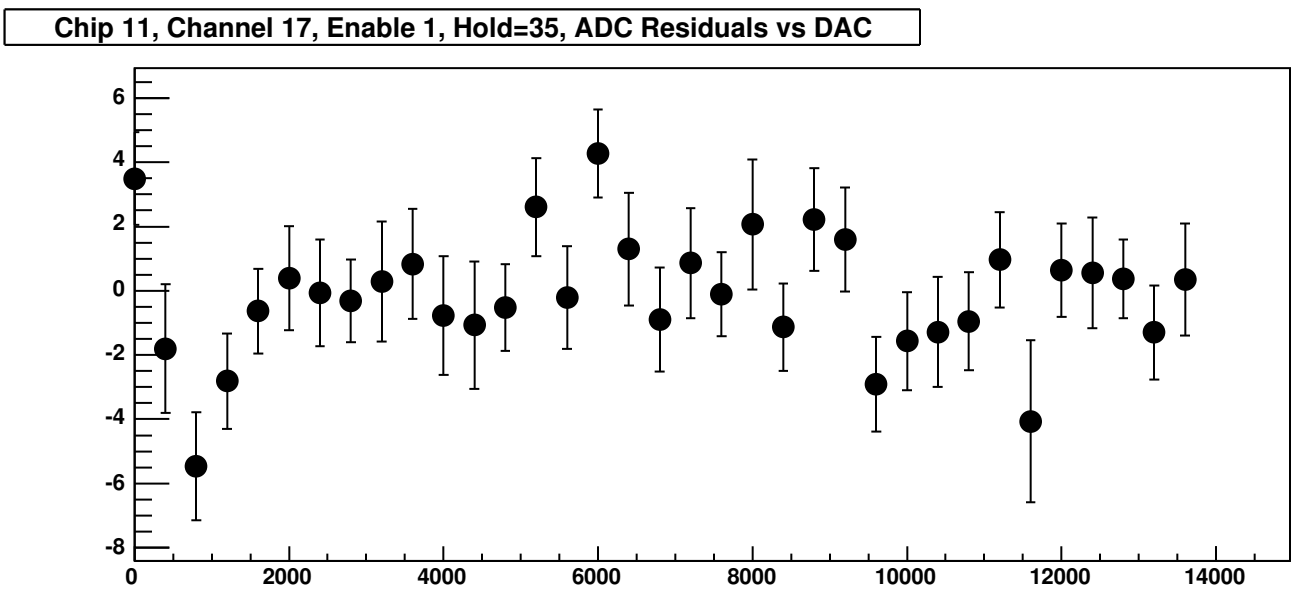

Chip 11, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 6.384e+04 / 23
p0 -930.6 ± 0.9181
p1 0.1491 ± 0.0001716
1500
1000
500
0
-500
0
2000
4000
6000
8000
10000
12000
14000

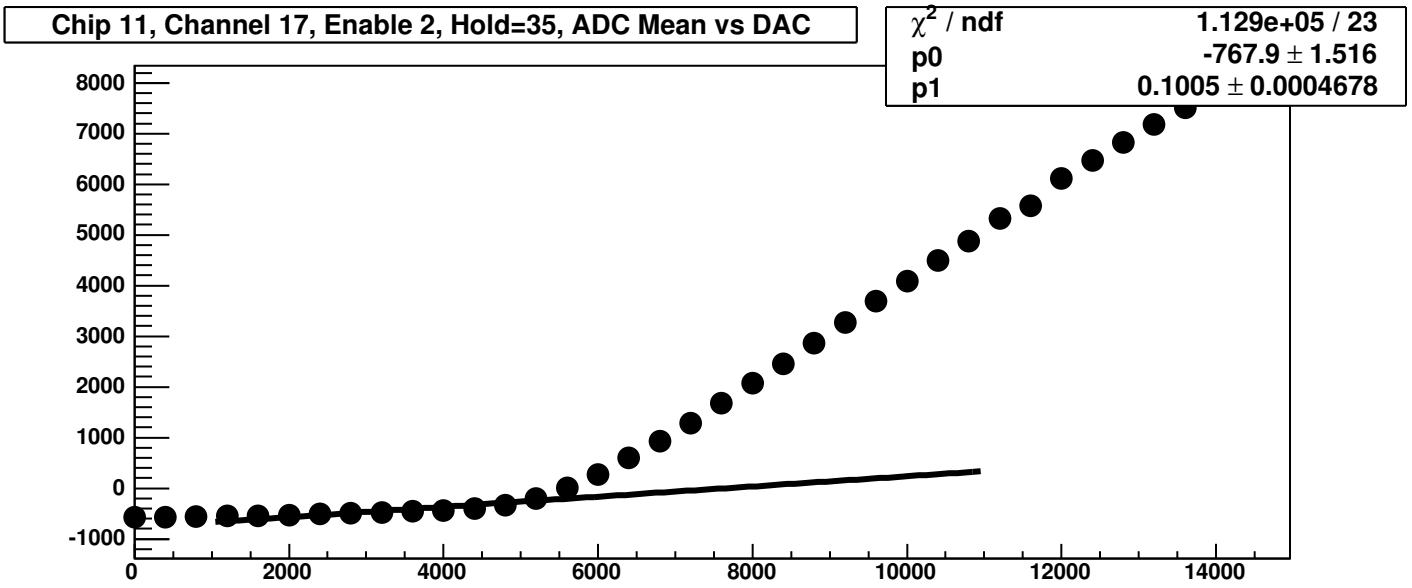

Chip 11, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
2000
4000
6000
8000
10000
12000
14000

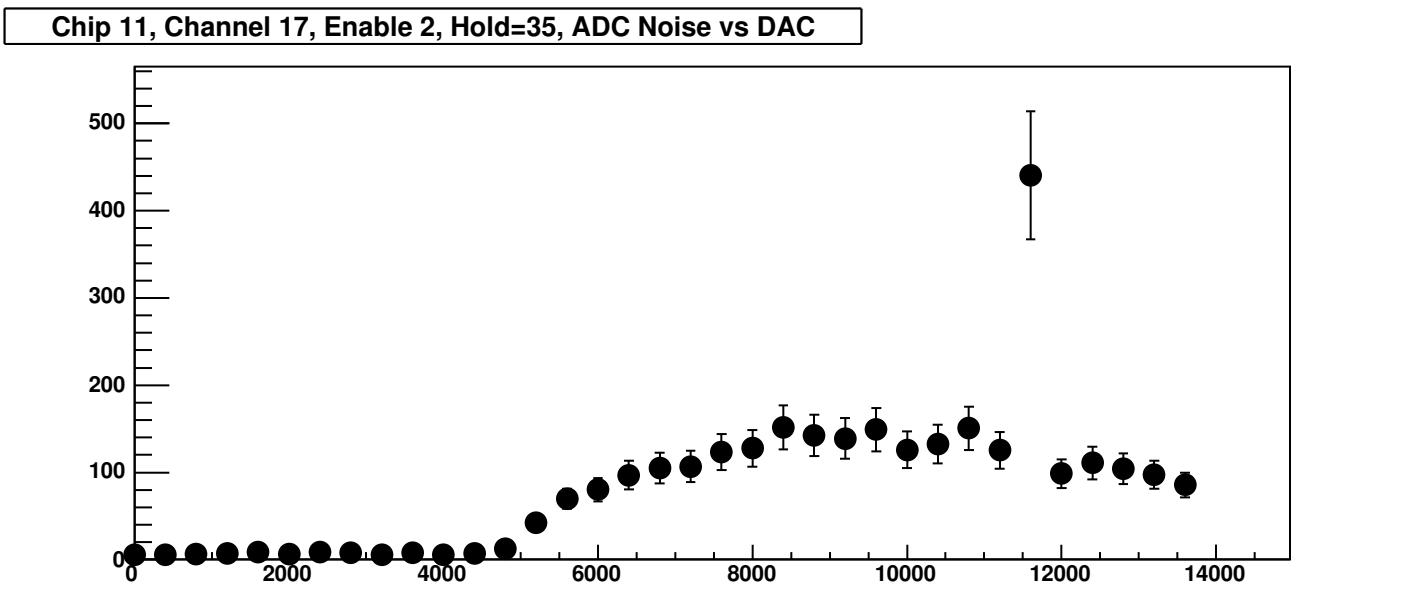

Chip 11, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
400
300
200
100
0
-100
-200
0
2000
4000
6000
8000
10000
12000
14000

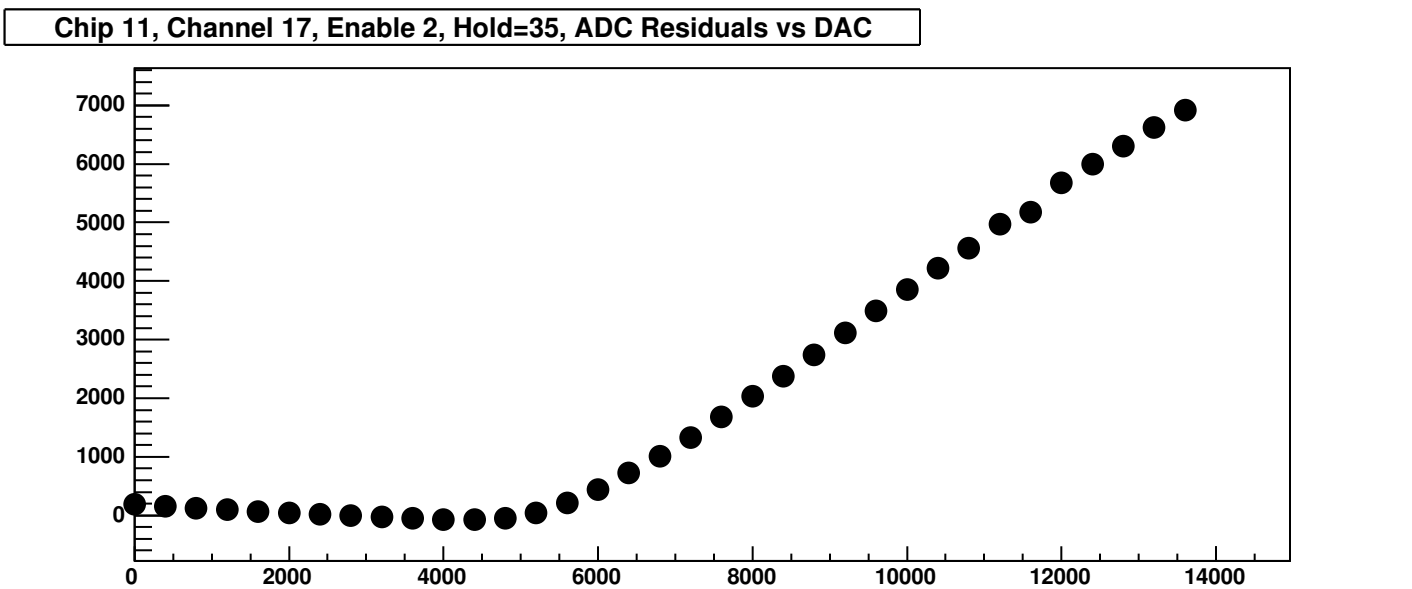

Chip 11, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 29 / 23
p0 -580.9 ± 0.7058
p1 0.01771 ± 0.0001064
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000

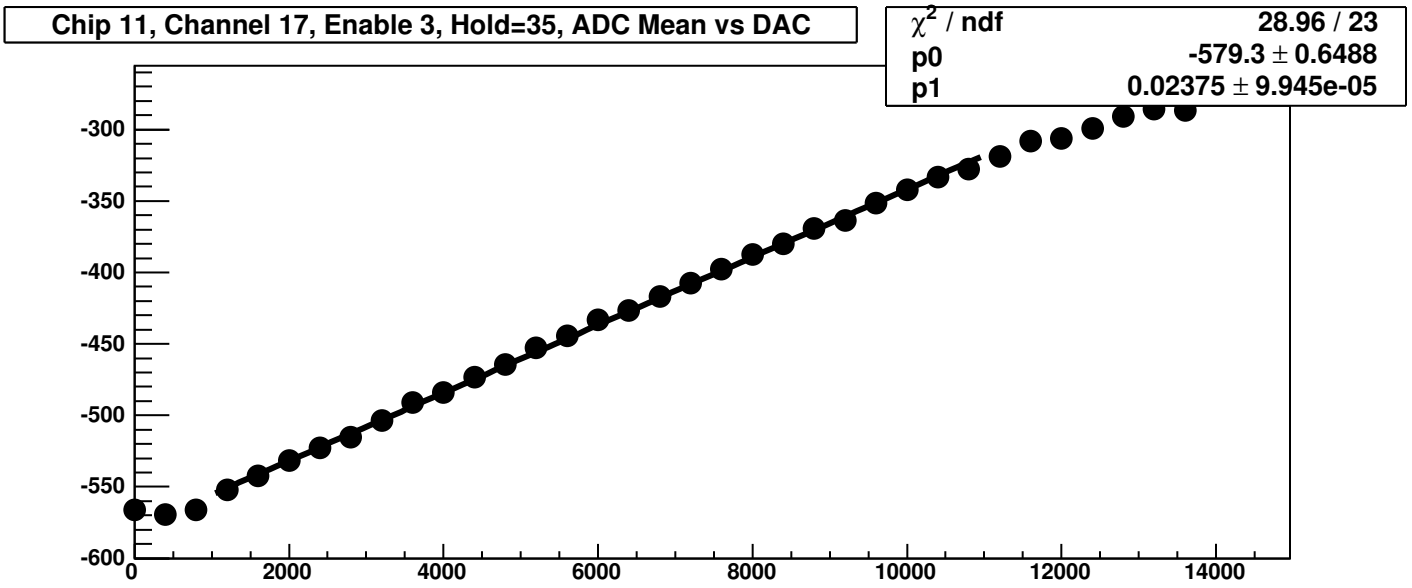

Chip 11, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000

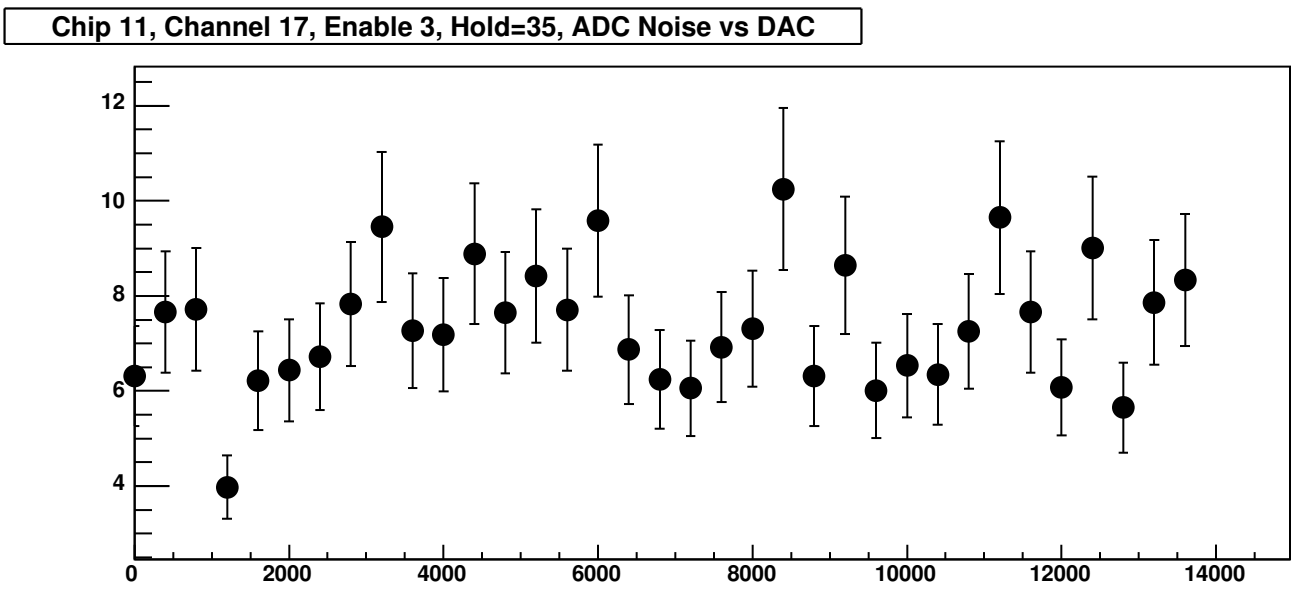

Chip 11, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
0
2000
4000
6000
8000
10000
12000
14000

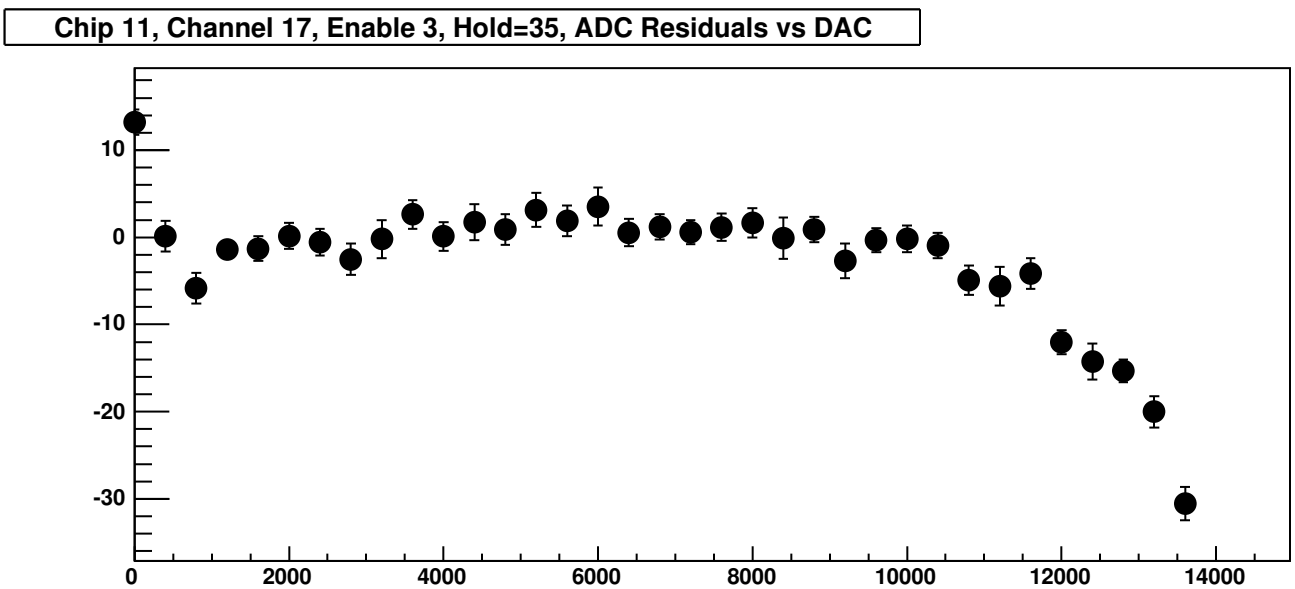

Chip 11, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1.129e+05 / 23
p0 -767.9 ± 1.516
p1 0.1005 ± 0.0004678
8000
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
2000
4000
6000
8000
10000
12000
14000

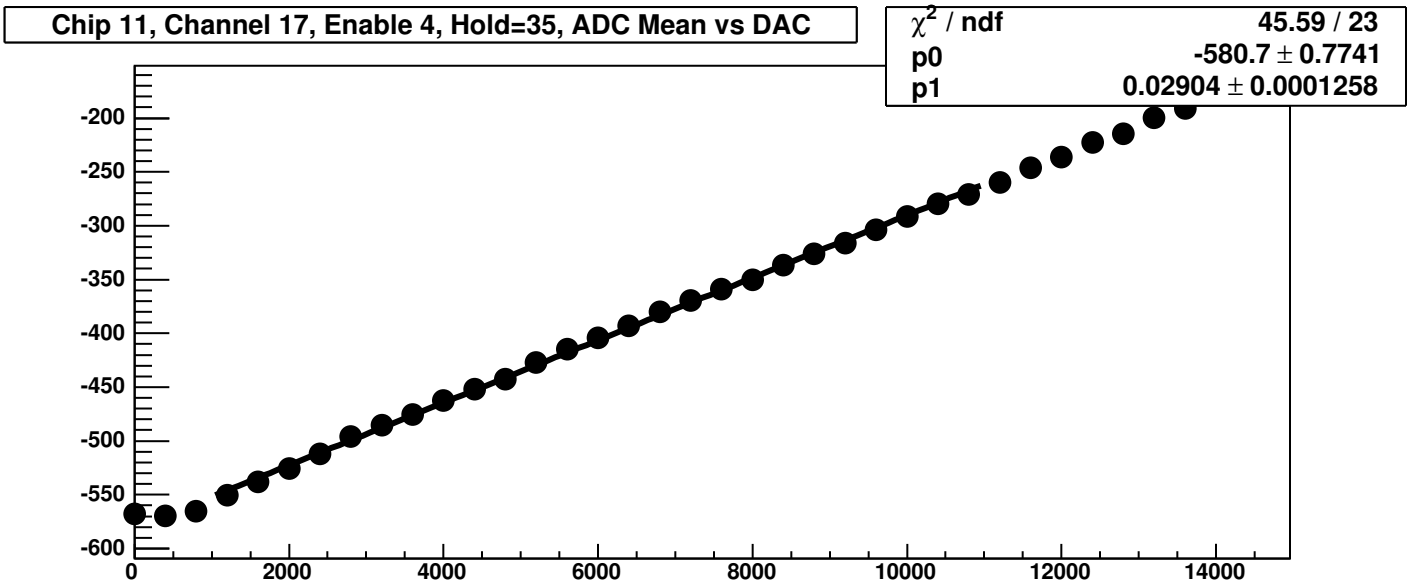

Chip 11, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
500
400
300
200
100
0
0
2000
4000
6000
8000
10000
12000
14000

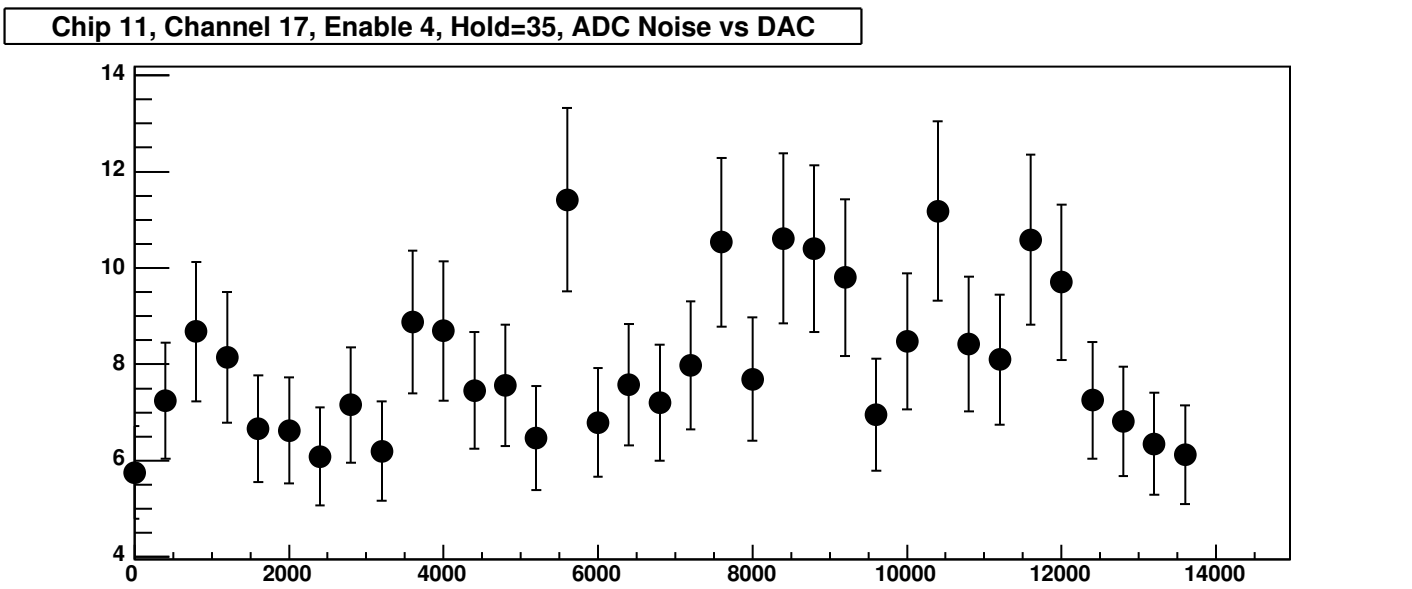

Chip 11, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
7000
6000
5000
4000
3000
2000
1000
0
0
2000
4000
6000
8000
10000
12000
14000

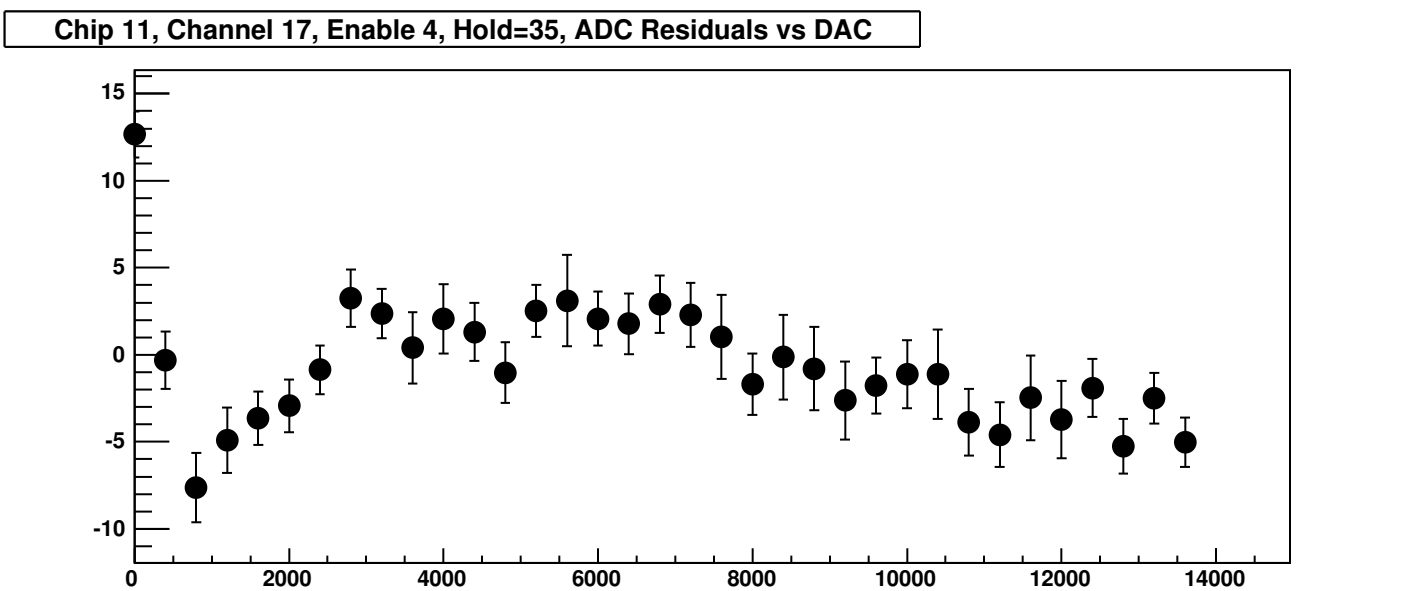

Chip 11, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.96 / 23
p0 -579.3 ± 0.6488
p1 0.02375 ± 9.945e-05
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 45.59 / 23
p0 -580.7 ± 0.7741
p1 0.02904 ± 0.0001258
-200
-250
-300
-350
-400
-450
-500
-550
-600
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
2000
4000
6000
8000
10000
12000
14000


Chip 11, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
2000
4000
6000
8000
10000
12000
14000