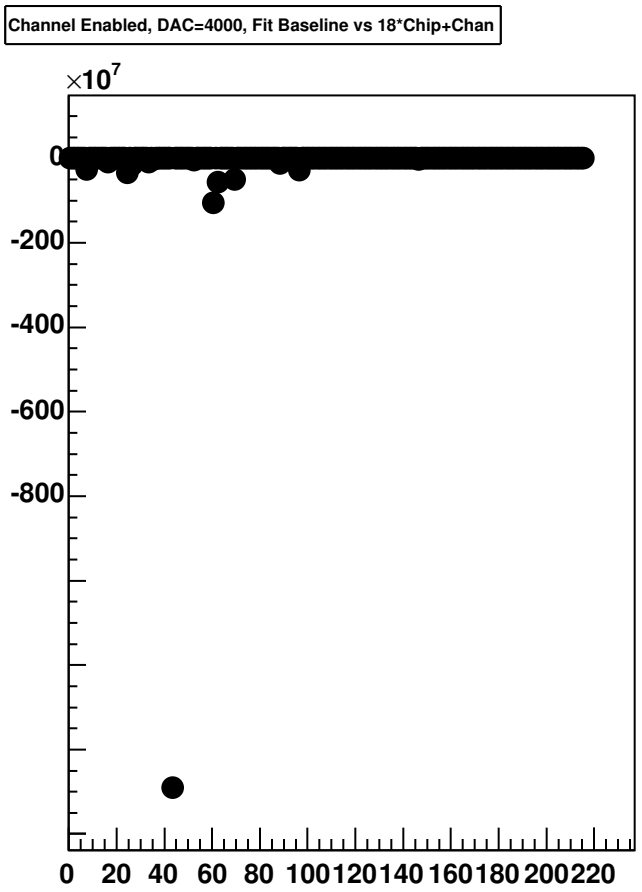
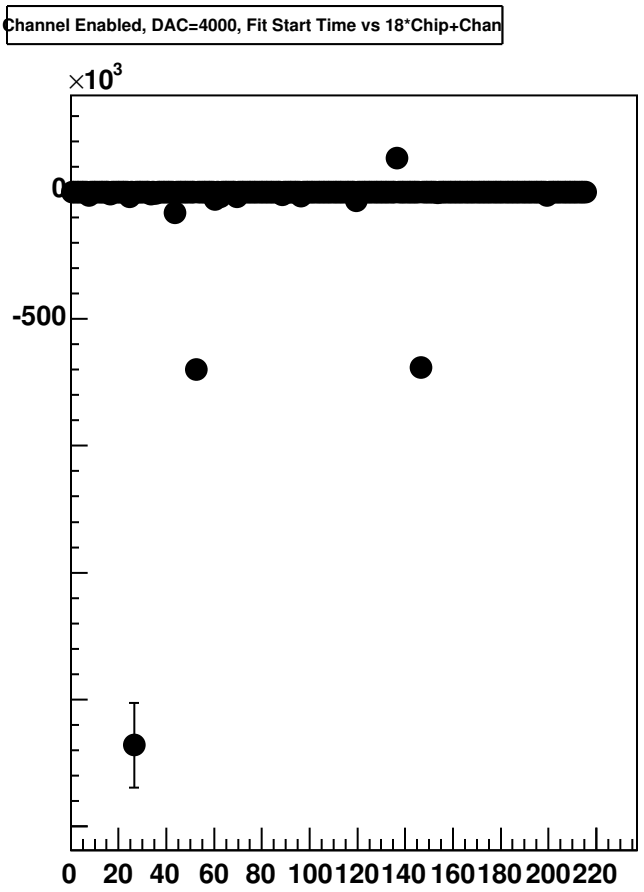
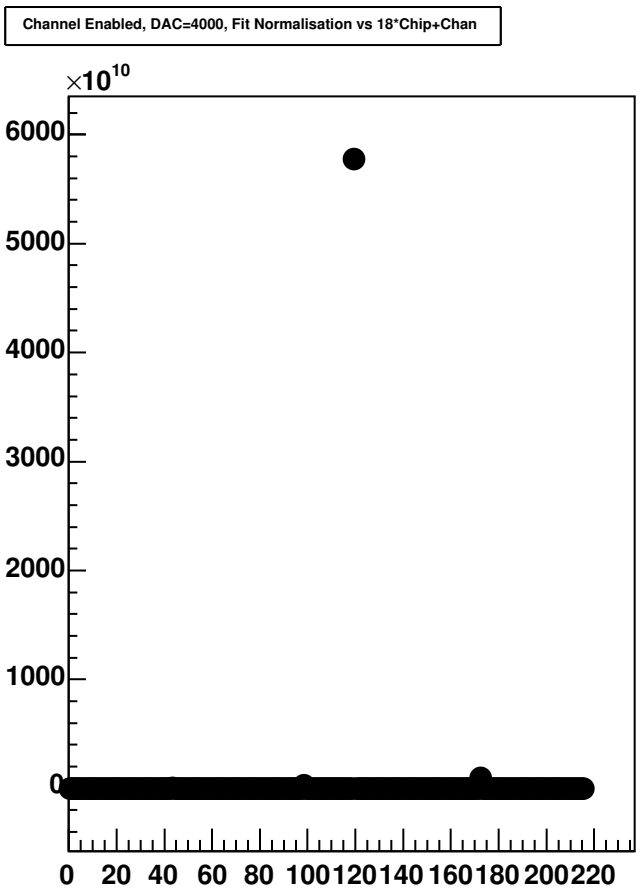
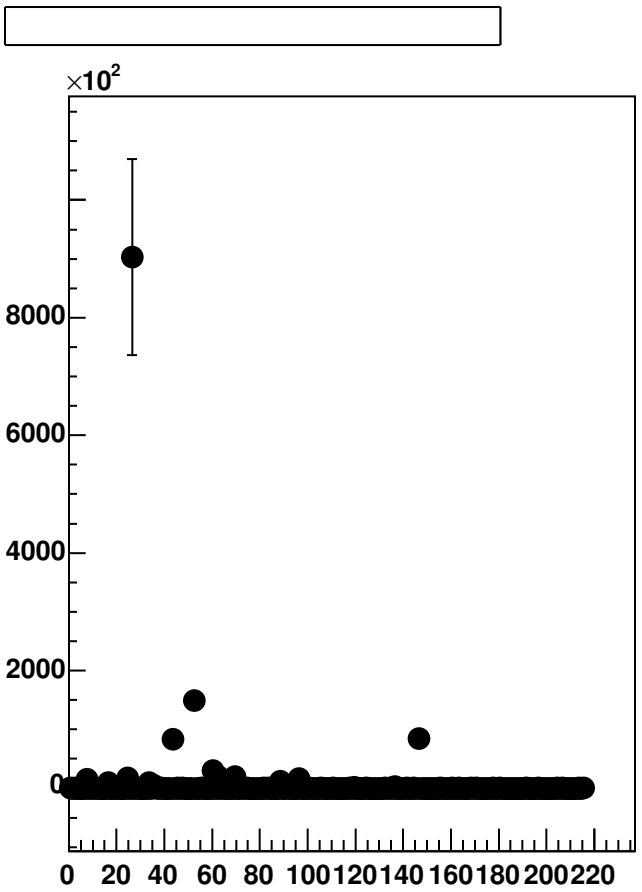
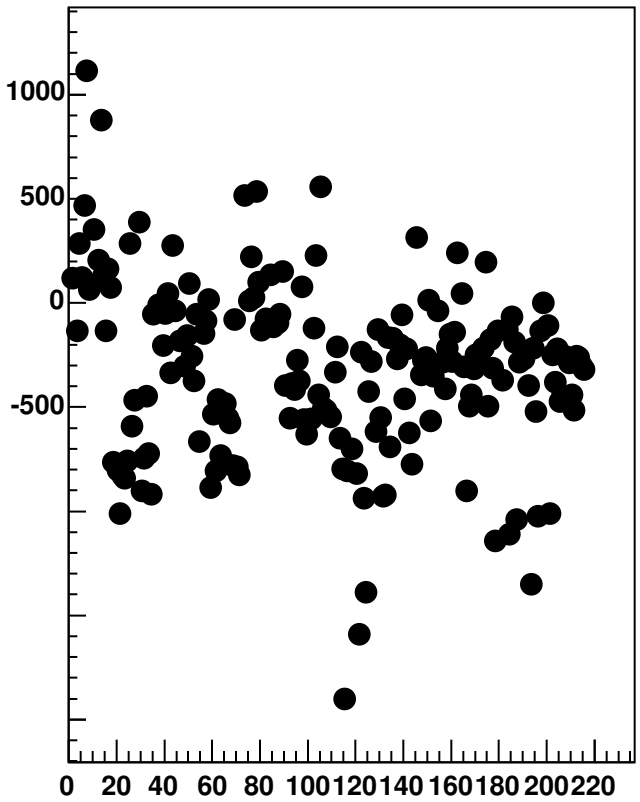

Channel Enabled, DAC=4000, Fit Baseline vs 18*Chip+Chan
×10^7
0
-200
-400
-600
-800
0 20 40 60 80 100 120 140 160 180 200 220

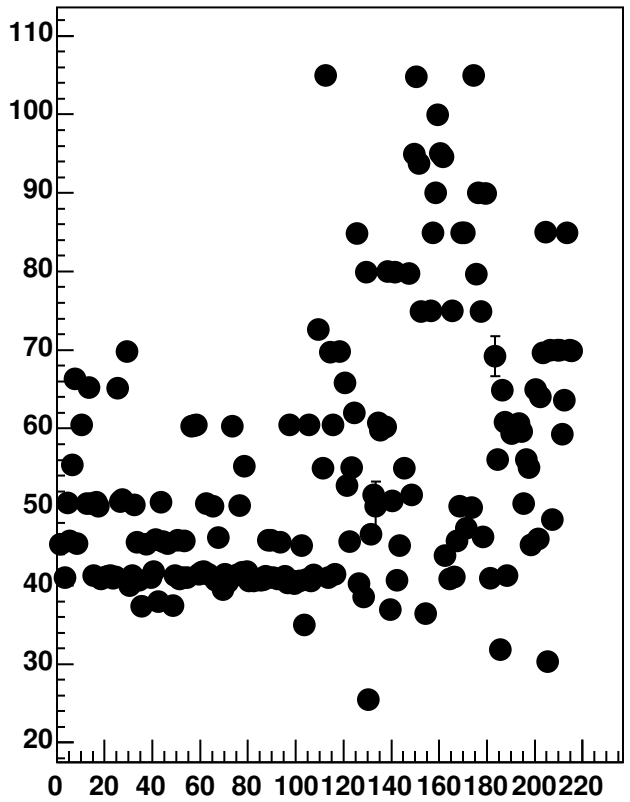

Channel Enabled, DAC=4000, Fit Start Time vs 18*Chip+Chan
×10^3
0
-500
0 20 40 60 80 100 120 140 160 180 200 220


Channel Enabled, DAC=4000, Fit Normalisation vs 18*Chip+Chan
×10^10
6000
5000
4000
3000
2000
1000
0
0 20 40 60 80 100 120 140 160 180 200 220

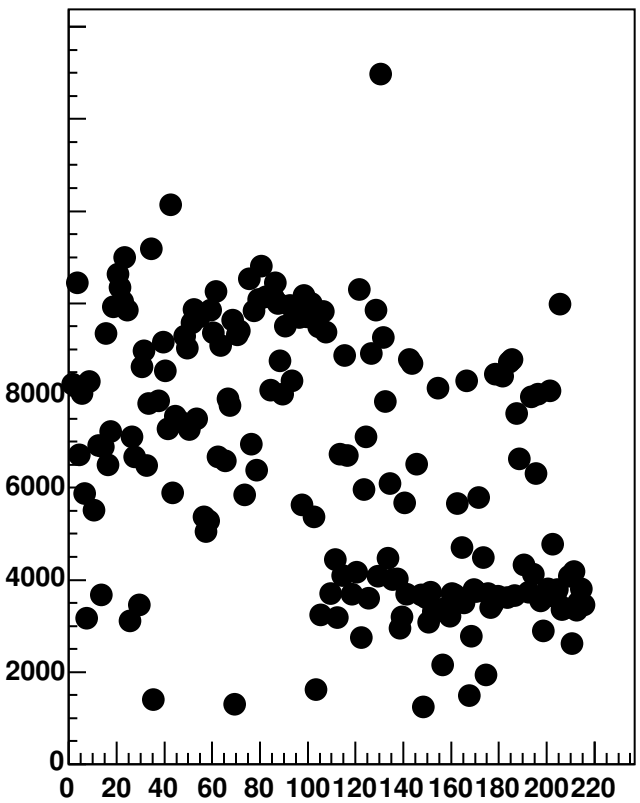

×10^2
8000
6000
4000
2000
0
0 20 40 60 80 100 120 140 160 180 200 220

Disabled 0, DAC=4000, Fit Baseline vs 18*Chip+Chan

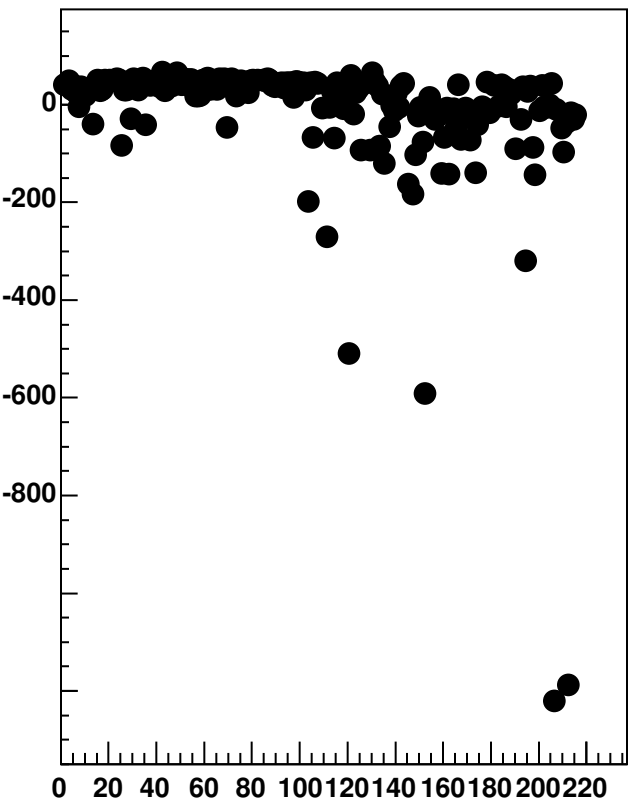




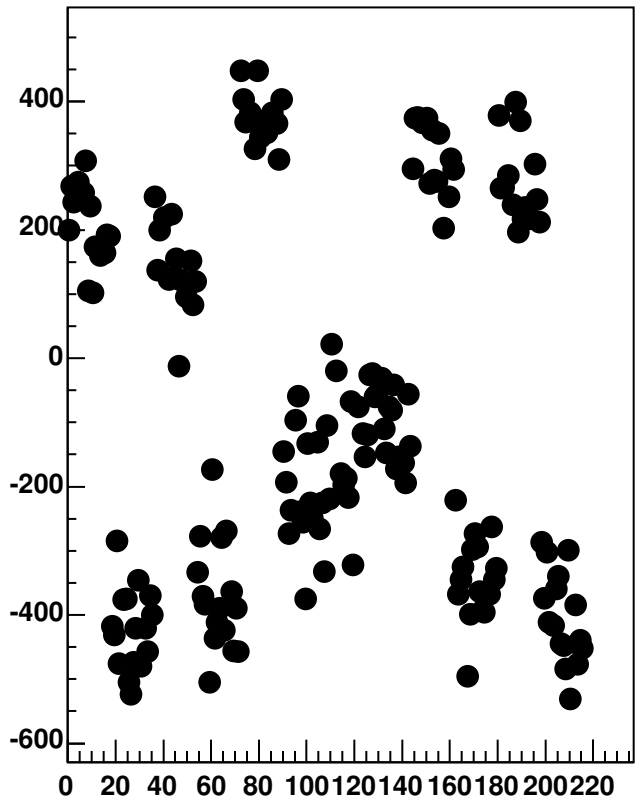
Disabled 0, DAC=4000, Fit Normalisation vs 18*Chip+Chan

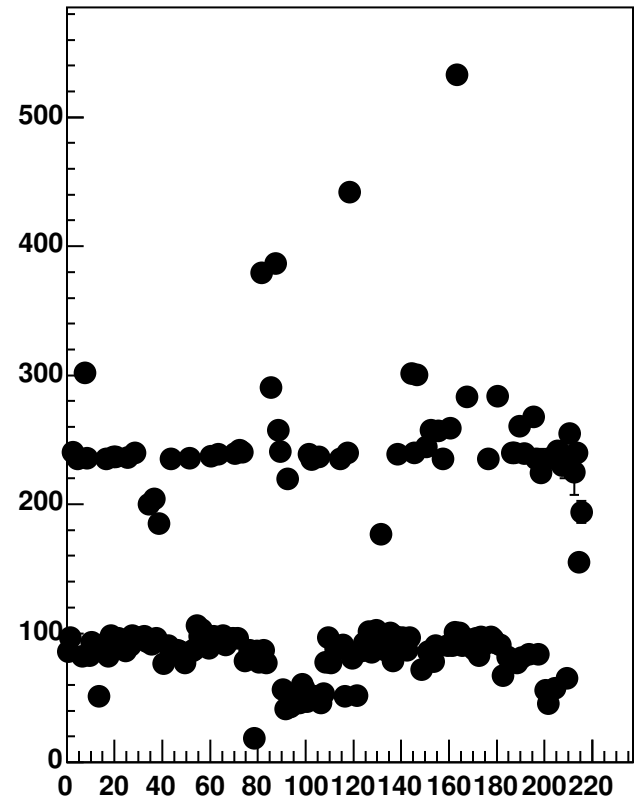


Disabled 0, DAC=4000, Fit Shaping Time vs 18*Chip+Chan

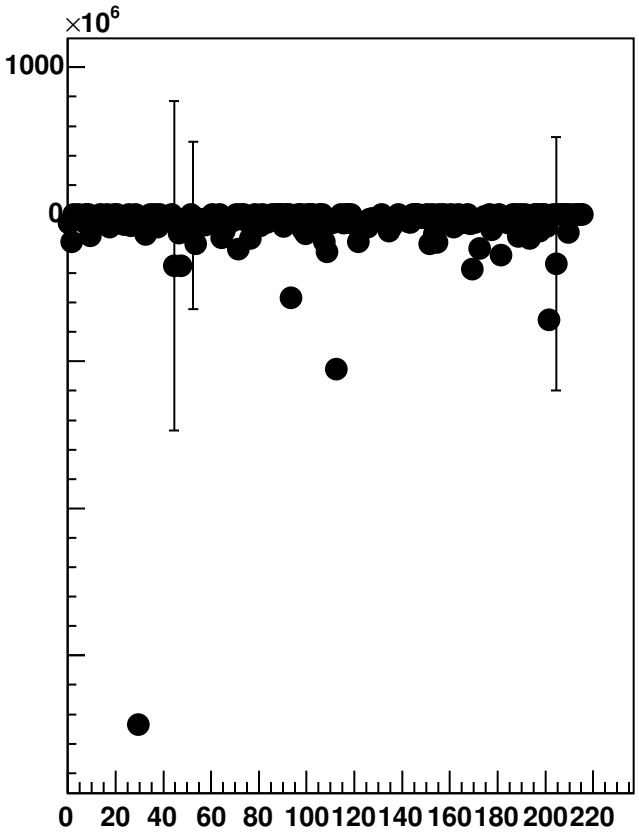


Disabled 1, DAC=4000, Fit Baseline vs 18*Chip+Chan

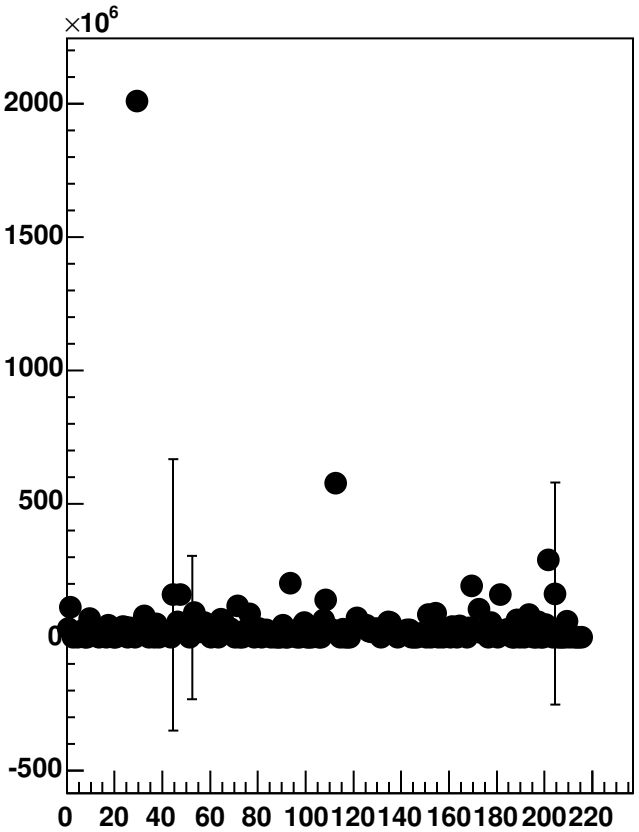




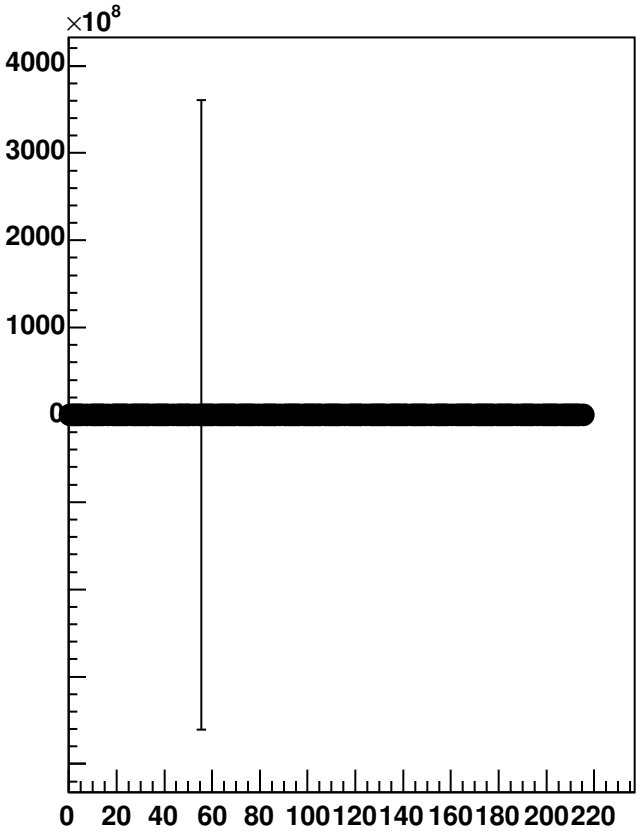
Disabled 1, DAC=4000, Fit Normalisation vs 18*Chip+Chan

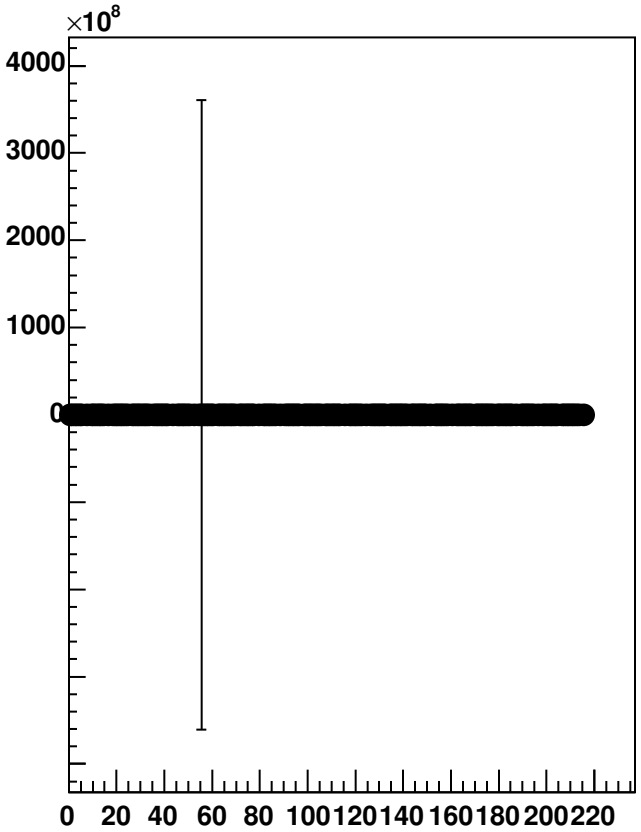


Disabled 1, DAC=4000, Fit Shaping Time vs 18*Chip+Chan

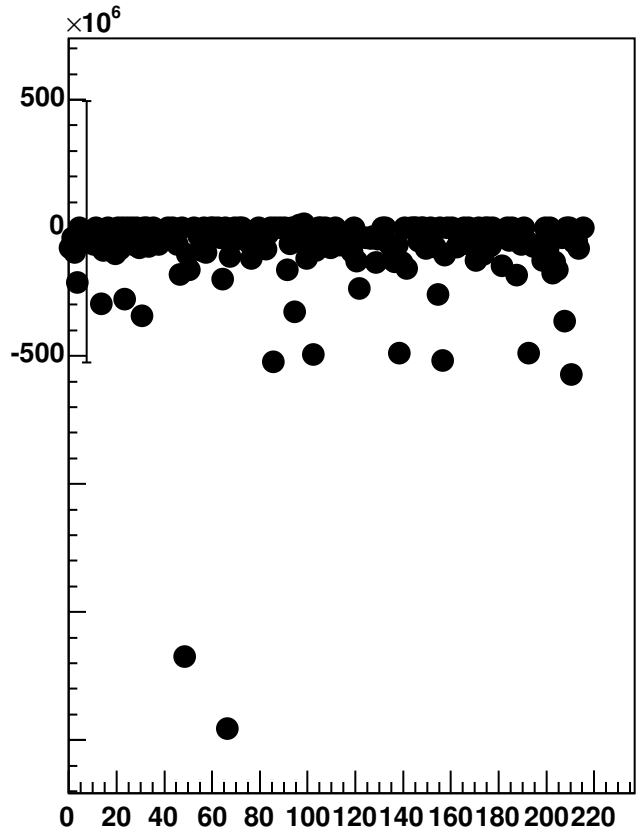


Disabled 2, DAC=4000, Fit Baseline vs 18*Chip+Chan

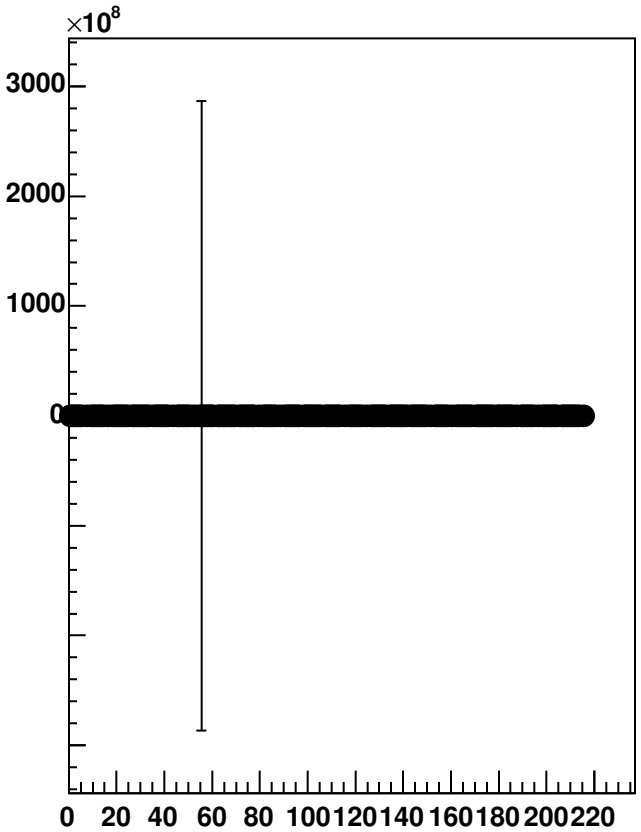




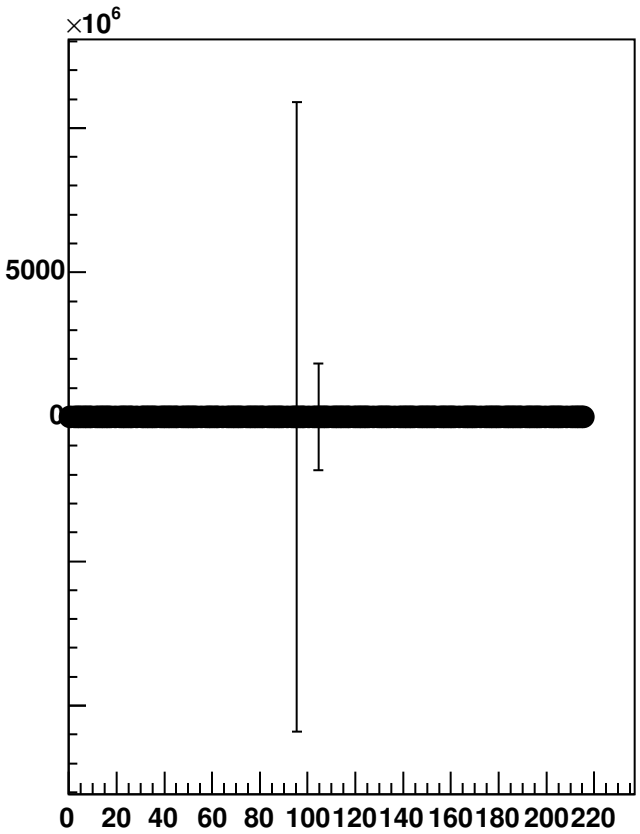
Disabled 2, DAC=4000, Fit Normalisation vs 18*Chip+Chan

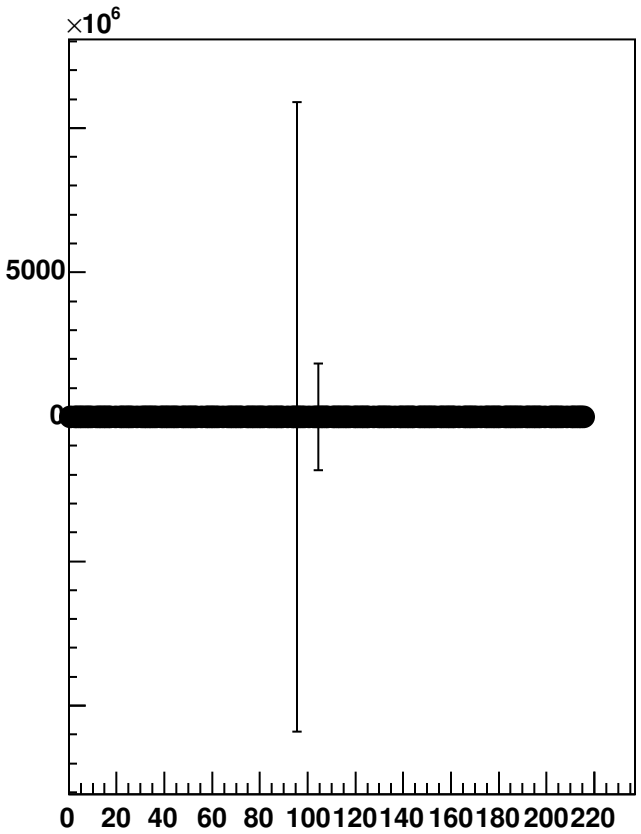


Disabled 2, DAC=4000, Fit Shaping Time vs 18*Chip+Chan



Disabled 3, DAC=4000, Fit Baseline vs 18*Chip+Chan





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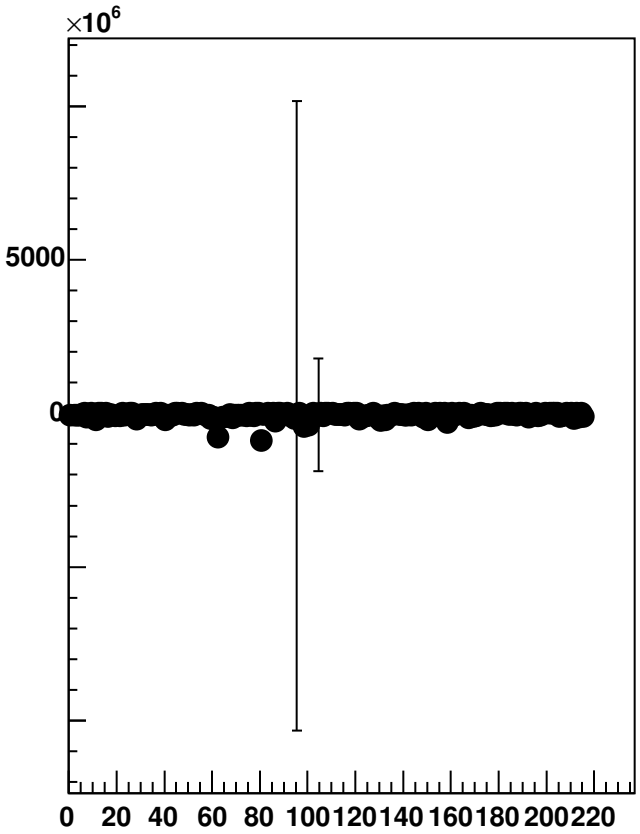
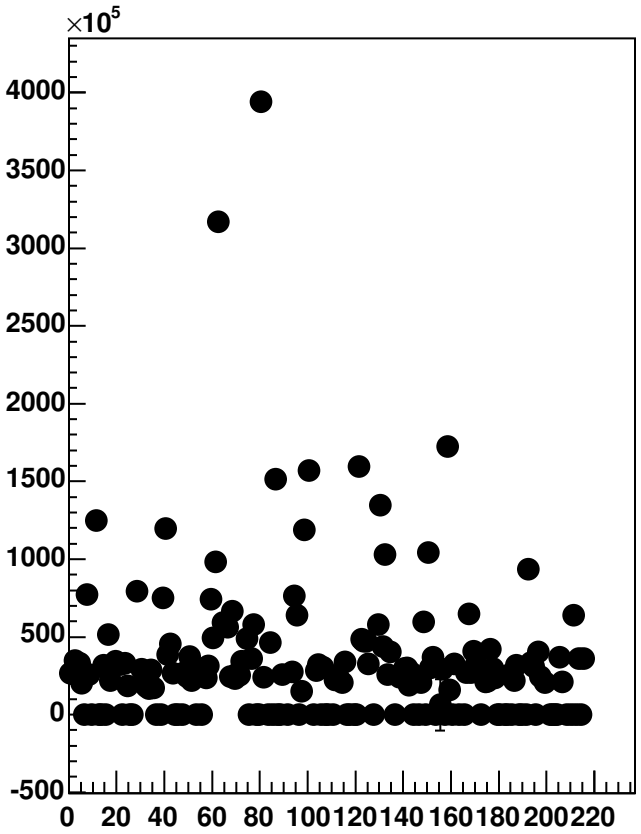
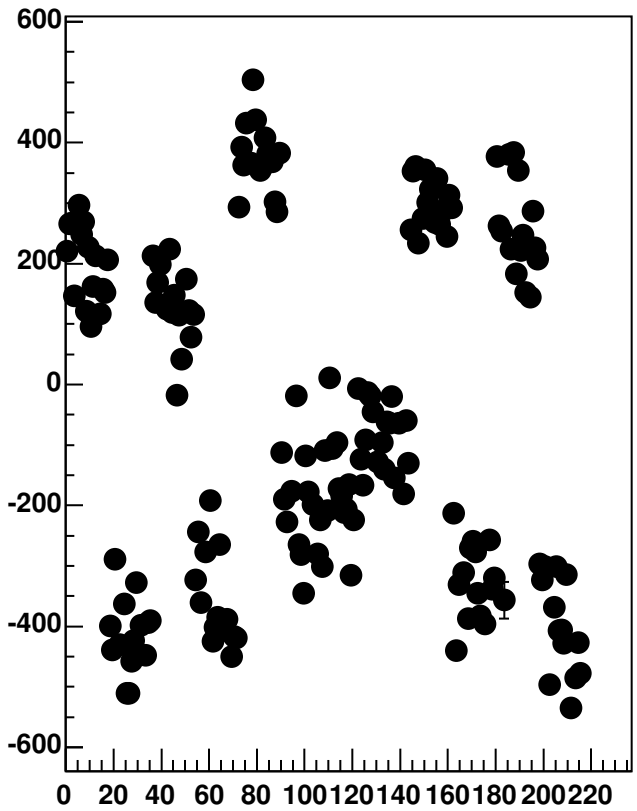
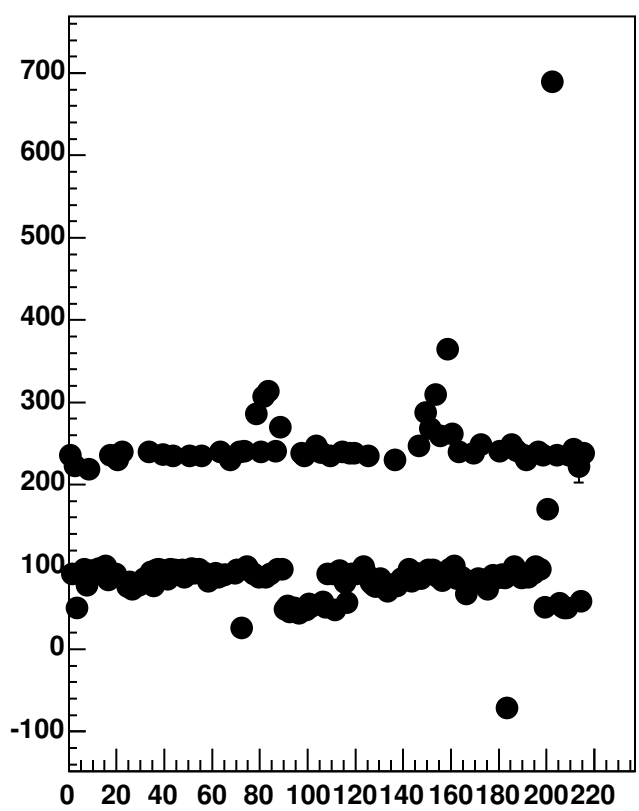


Disabled 3, DAC=4000, Fit Shaping Time vs 18*Chip+Chan

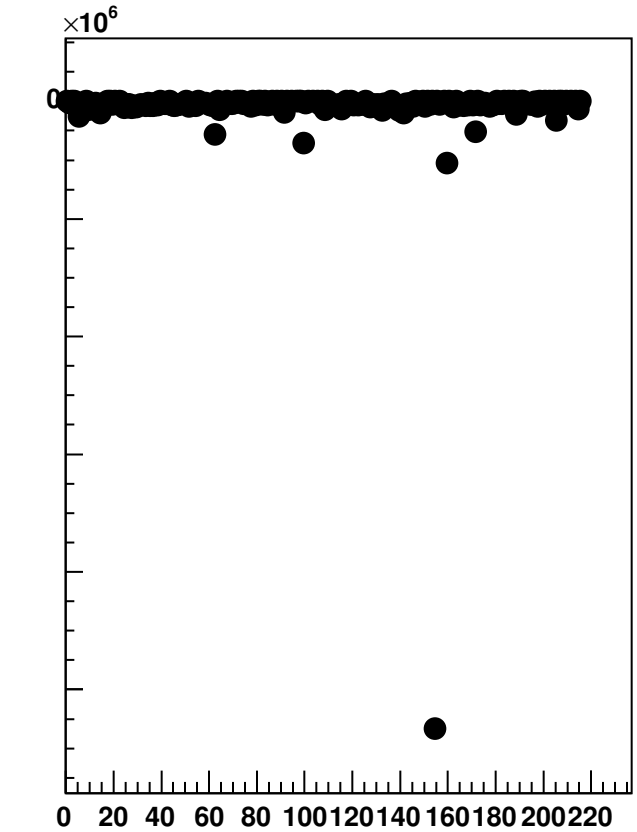


Disabled 4, DAC=4000, Fit Baseline vs 18*Chip+Chan

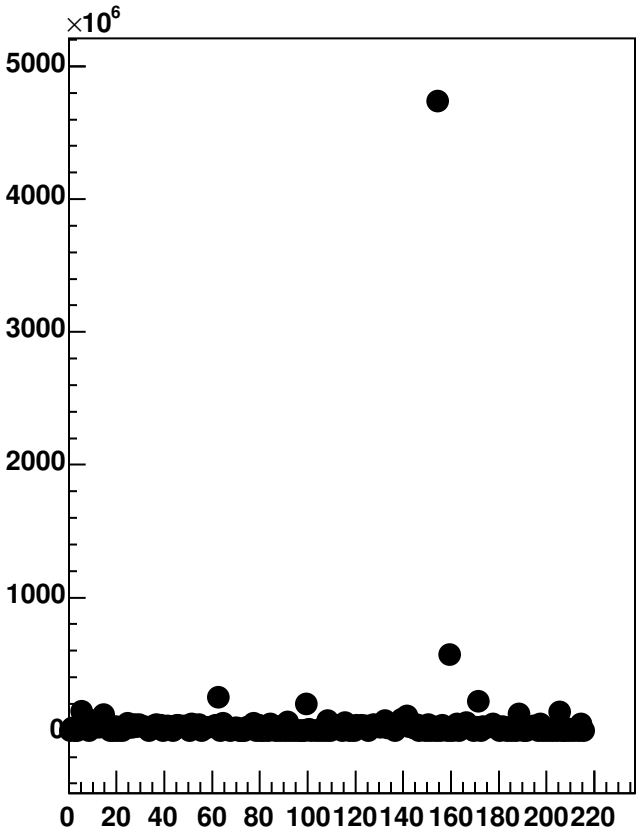




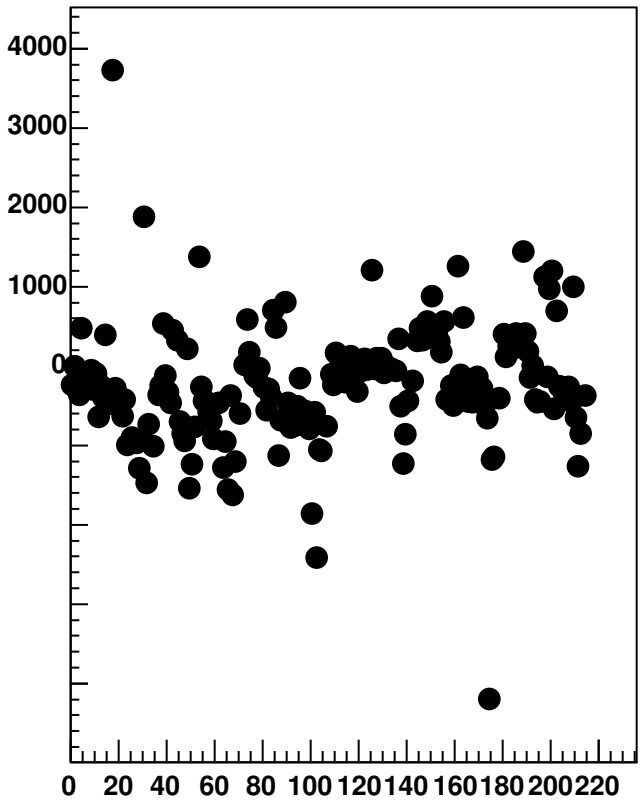
Disabled 4, DAC=4000, Fit Normalisation vs 18*Chip+Chan

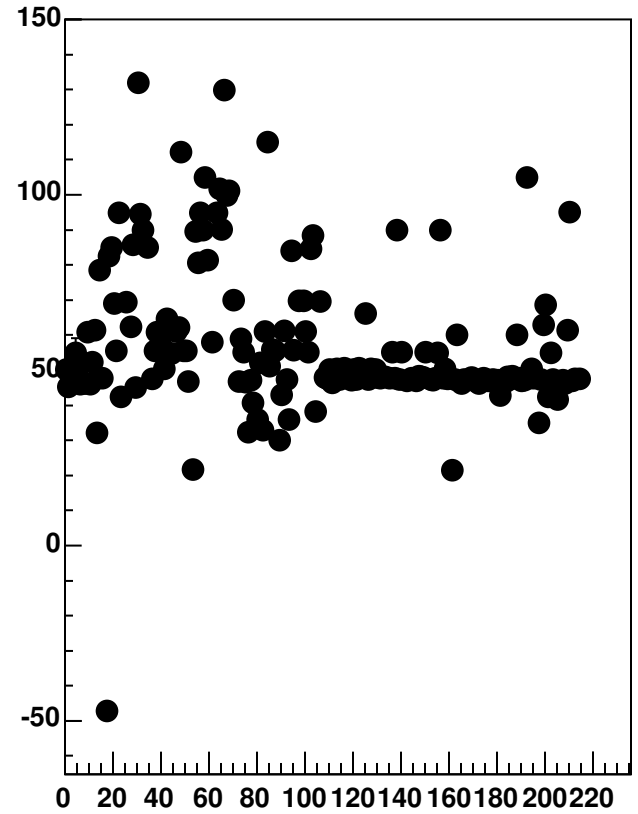


Disabled 4, DAC=4000, Fit Shaping Time vs 18*Chip+Chan

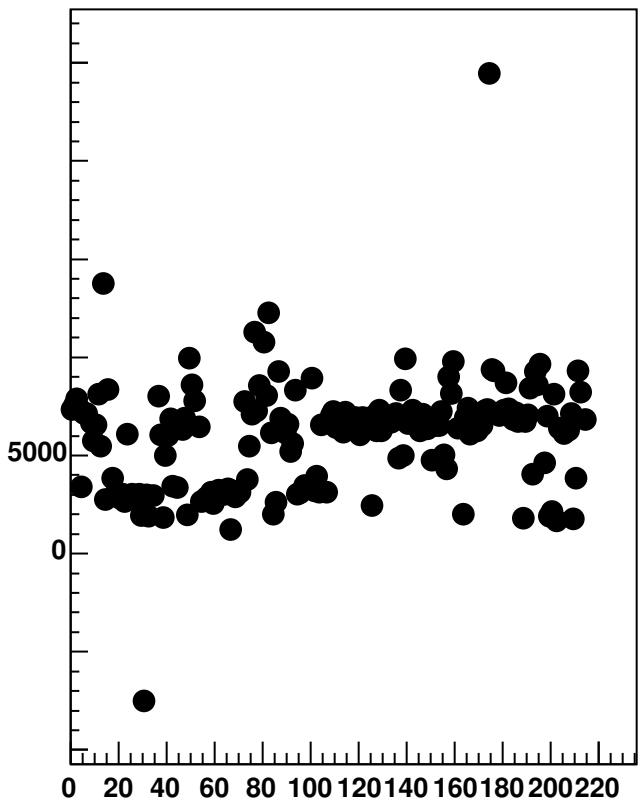


Disabled 5, DAC=4000, Fit Baseline vs 18*Chip+Chan

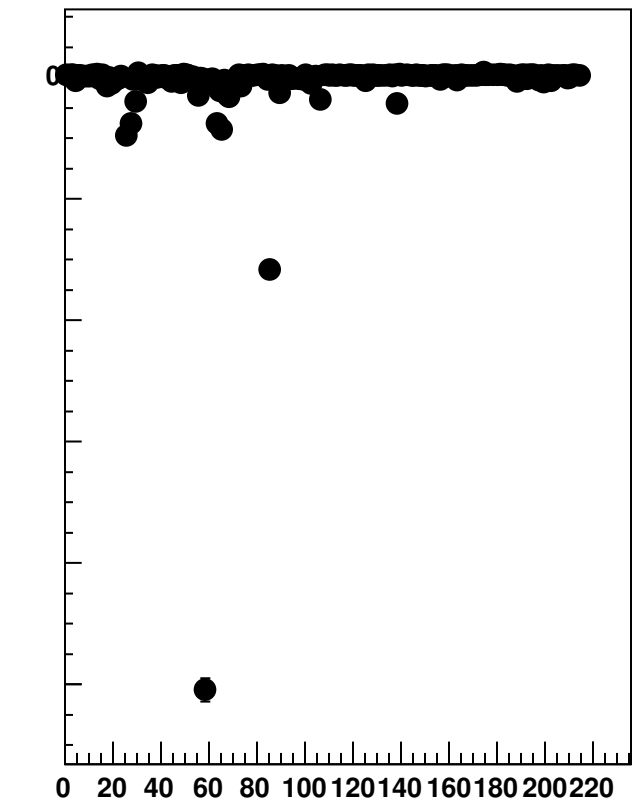




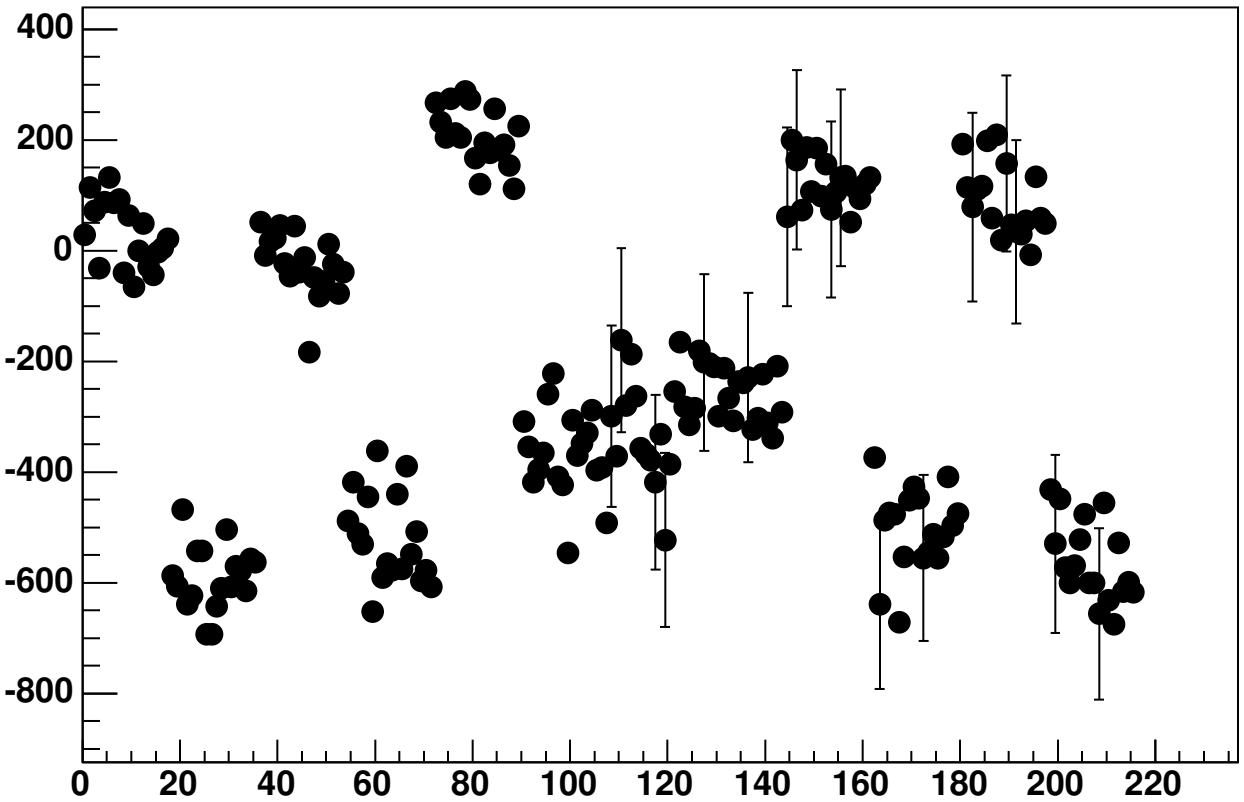
Disabled 5, DAC=4000, Fit Normalisation vs 18*Chip+Chan



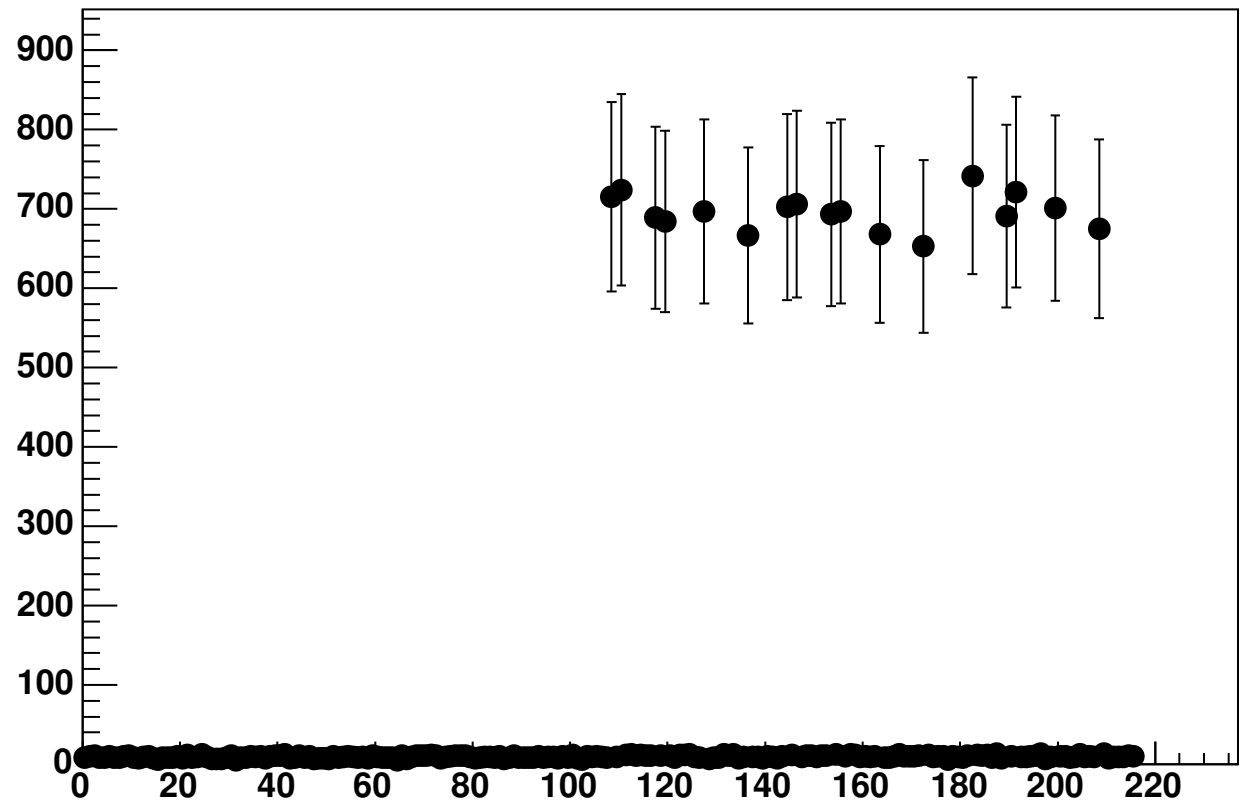
Disabled 5, DAC=4000, Fit Shaping Time vs 18*Chip+Chan



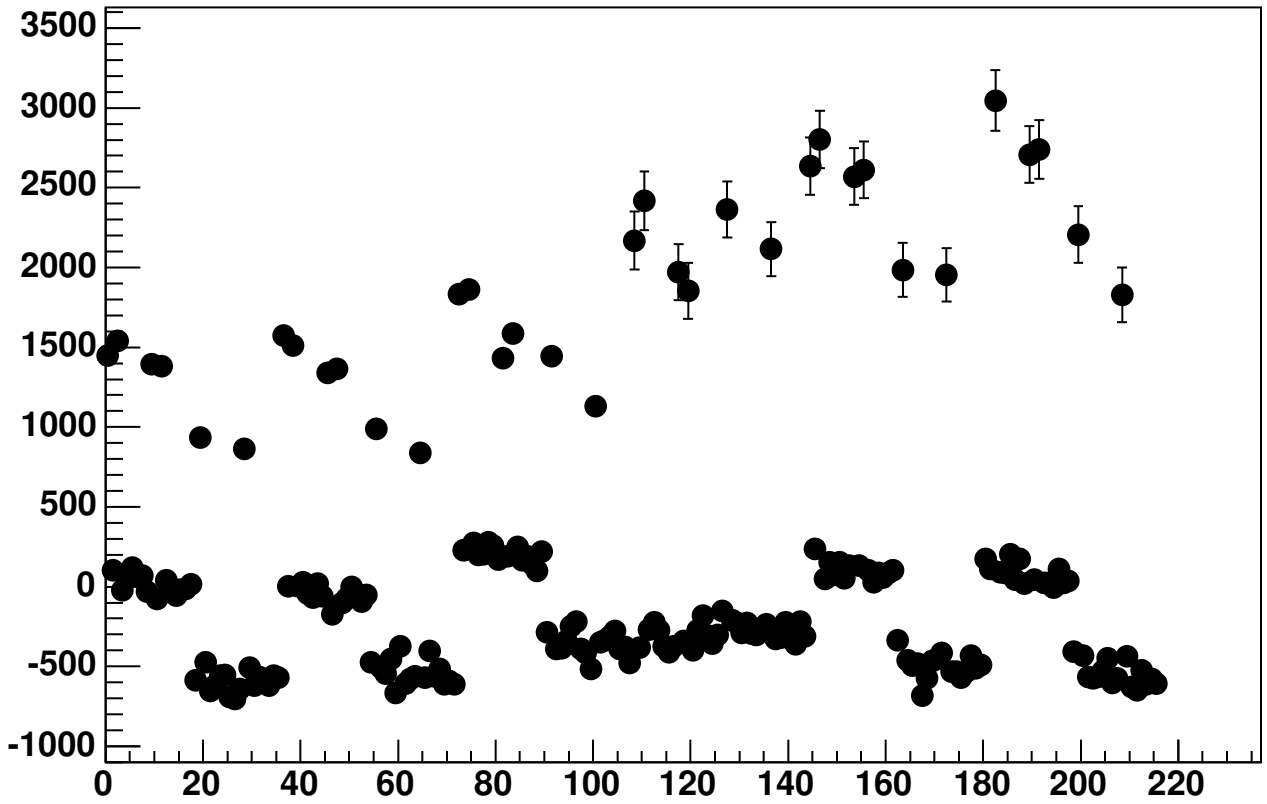
Enable 0, DAC=4000, Hold=0, ADC Mean vs 18*Chip+Chan



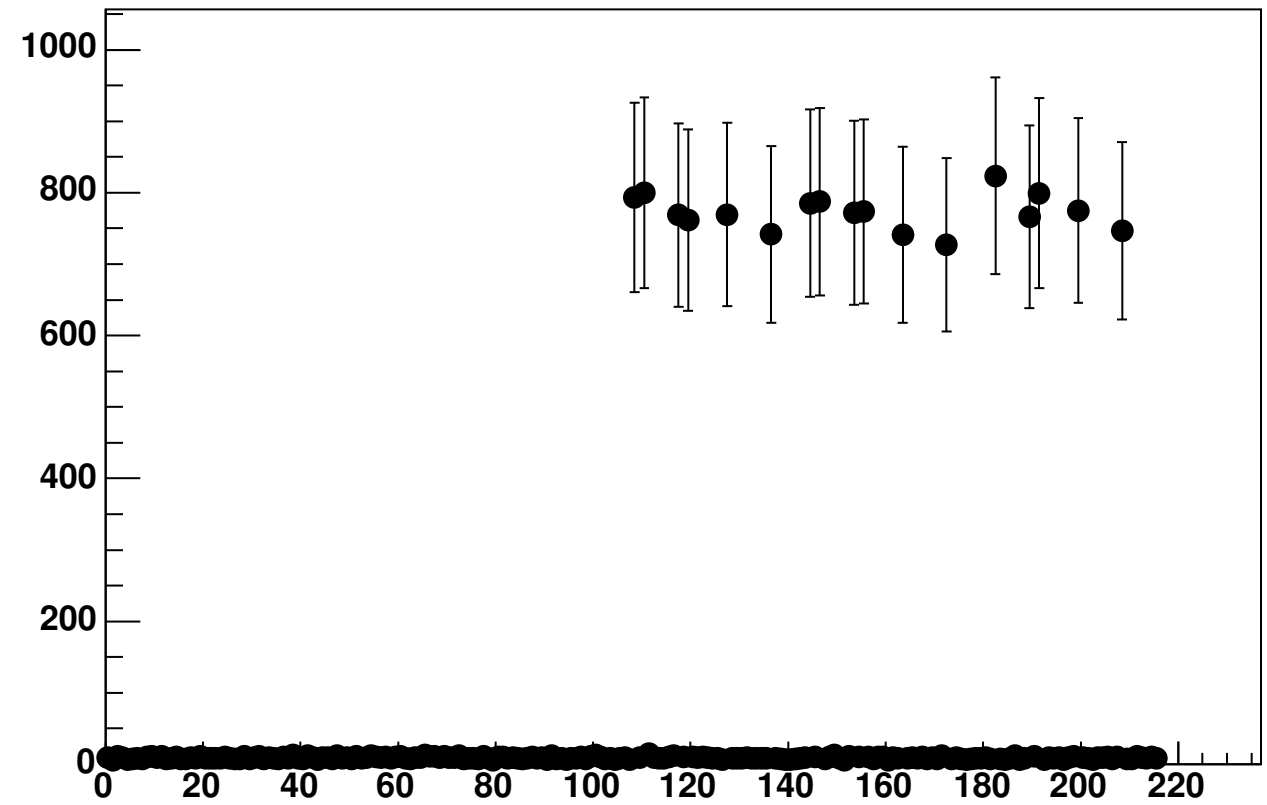
Enable 0, DAC=4000, Hold=0, ADC Noise vs 18*Chip+Chan



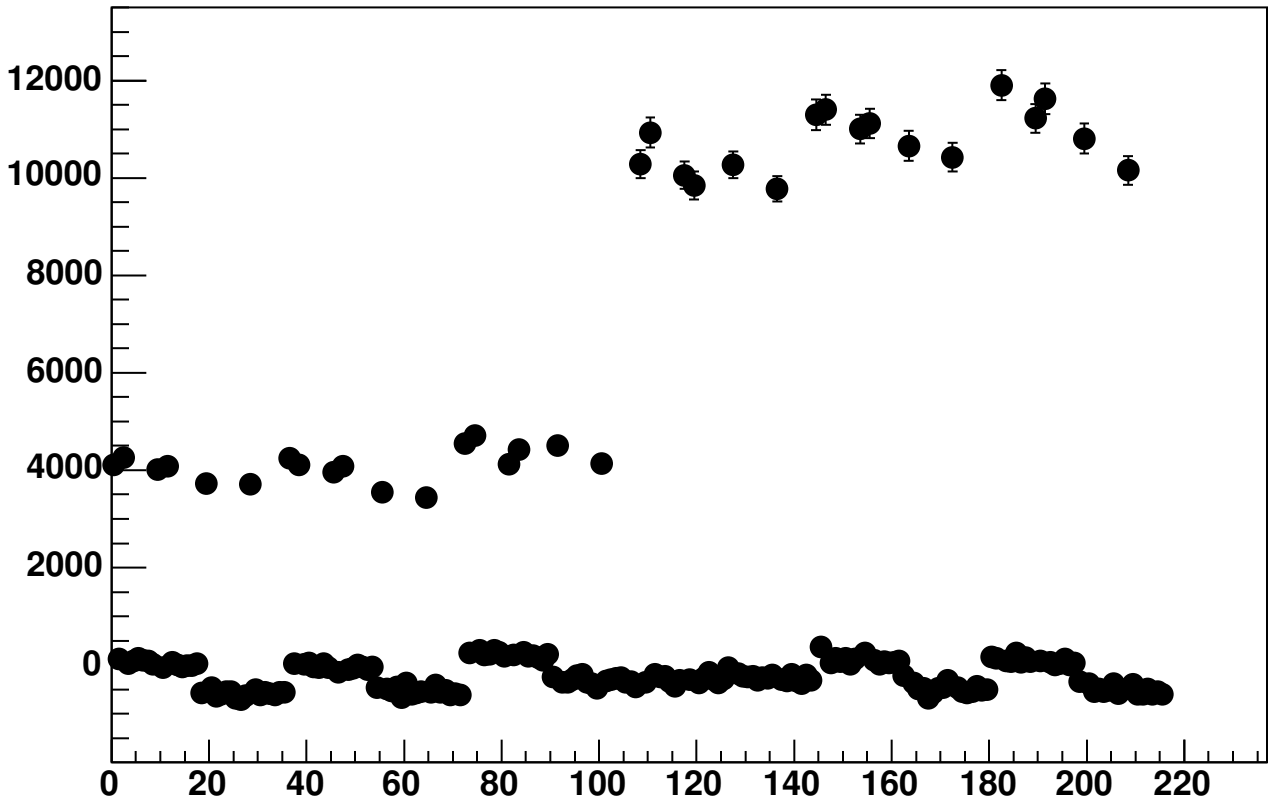
Enable 0, DAC=4000, Hold=5, ADC Mean vs 18*Chip+Chan



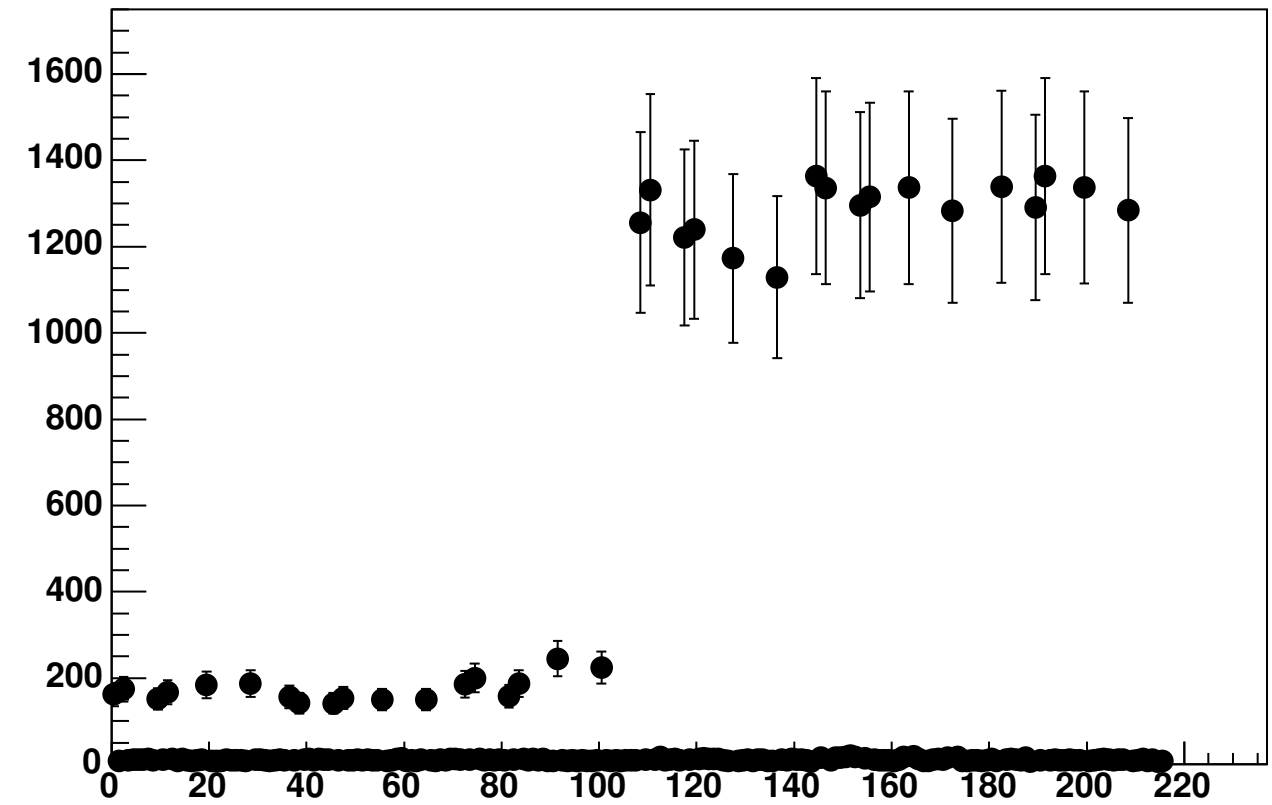
Enable 0, DAC=4000, Hold=5, ADC Noise vs 18*Chip+Chan



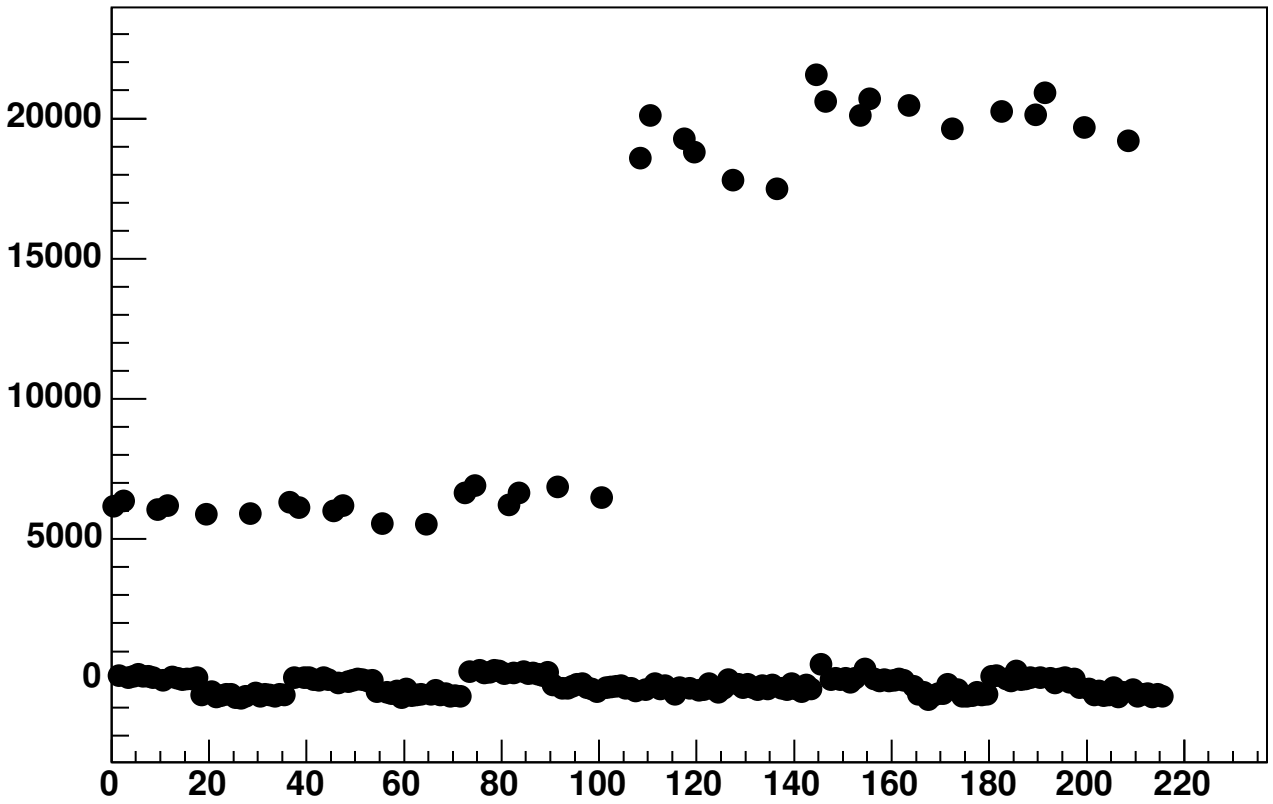
Enable 0, DAC=4000, Hold=10, ADC Mean vs 18*Chip+Chan



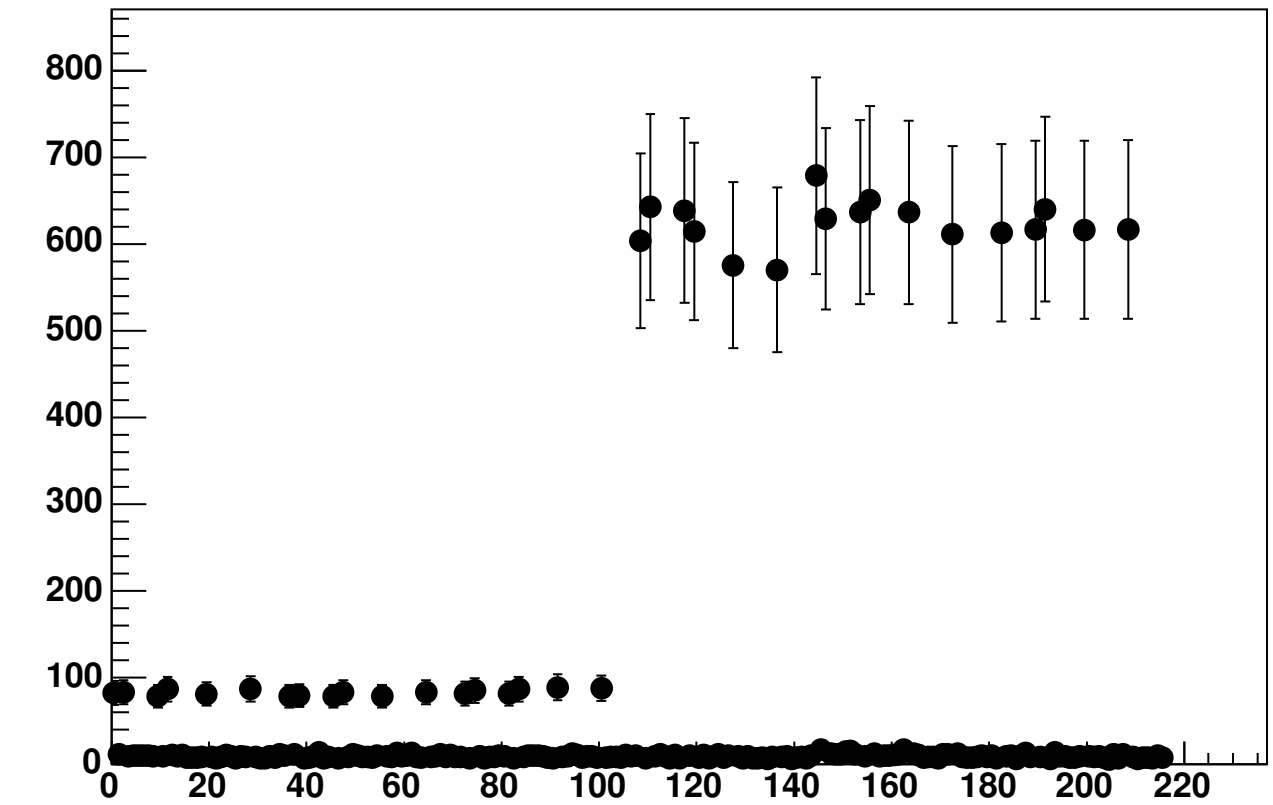
Enable 0, DAC=4000, Hold=10, ADC Noise vs 18*Chip+Chan



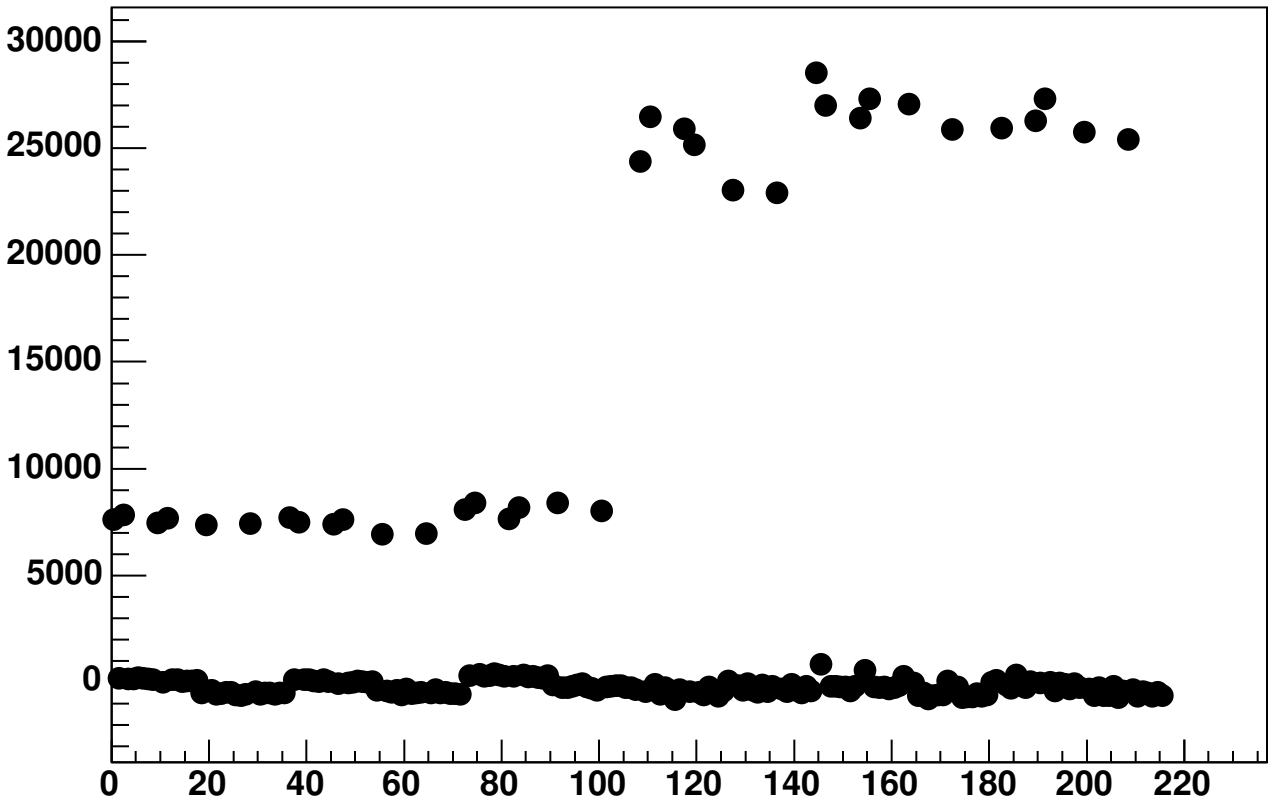
Enable 0, DAC=4000, Hold=15, ADC Mean vs 18*Chip+Chan



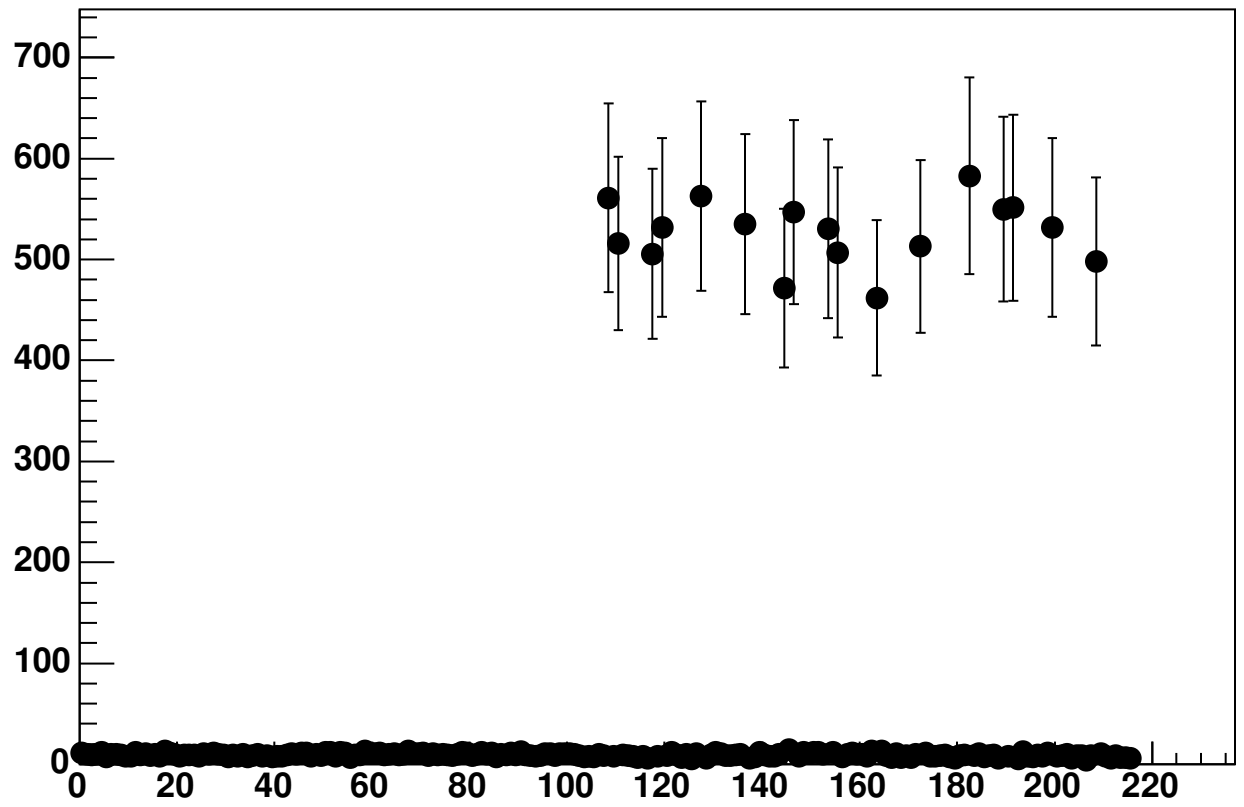
Enable 0, DAC=4000, Hold=15, ADC Noise vs 18*Chip+Chan



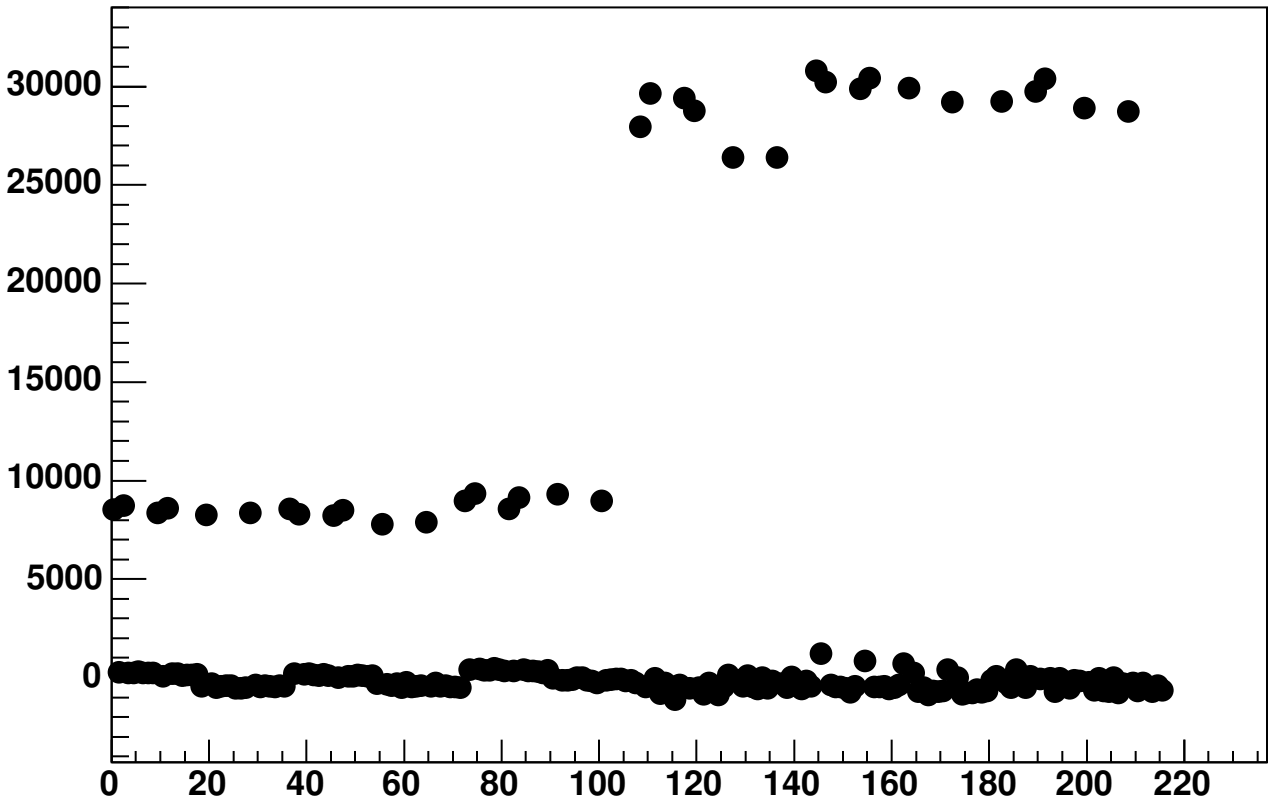
Enable 0, DAC=4000, Hold=20, ADC Mean vs 18*Chip+Chan



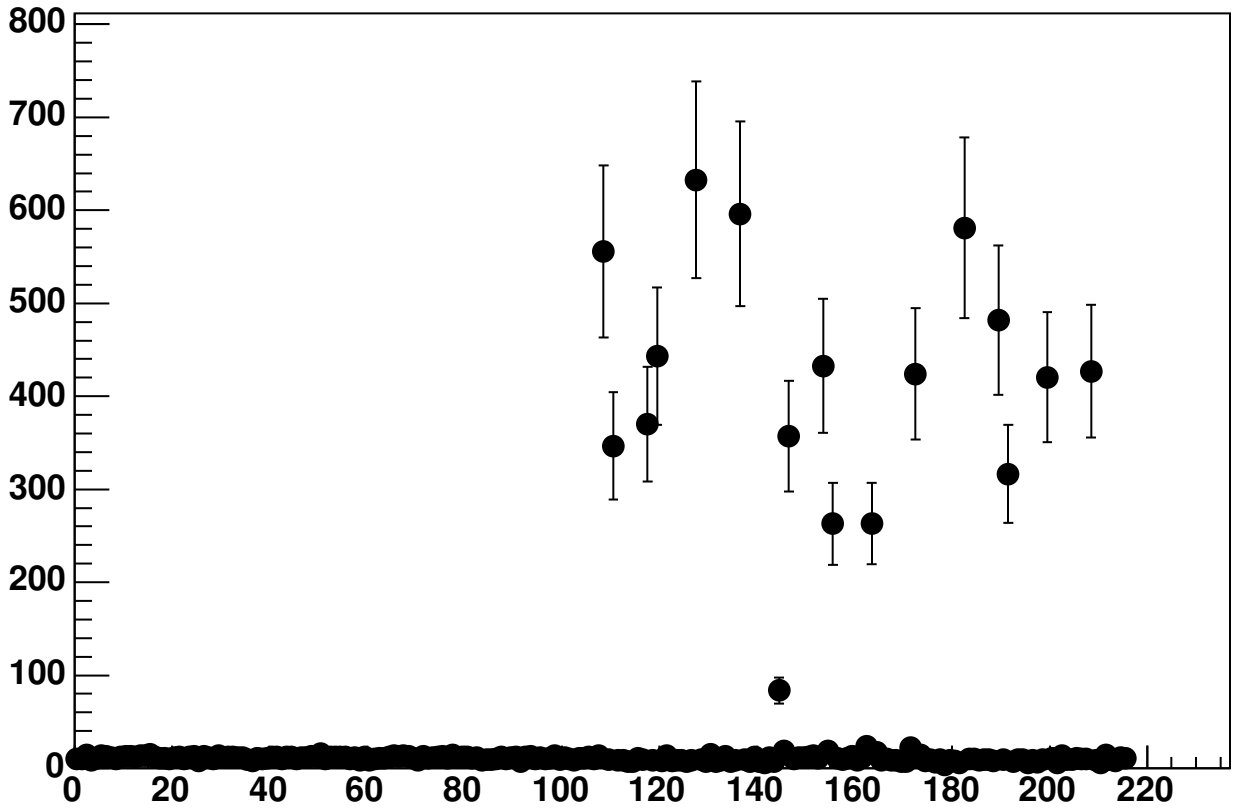
Enable 0, DAC=4000, Hold=20, ADC Noise vs 18*Chip+Chan



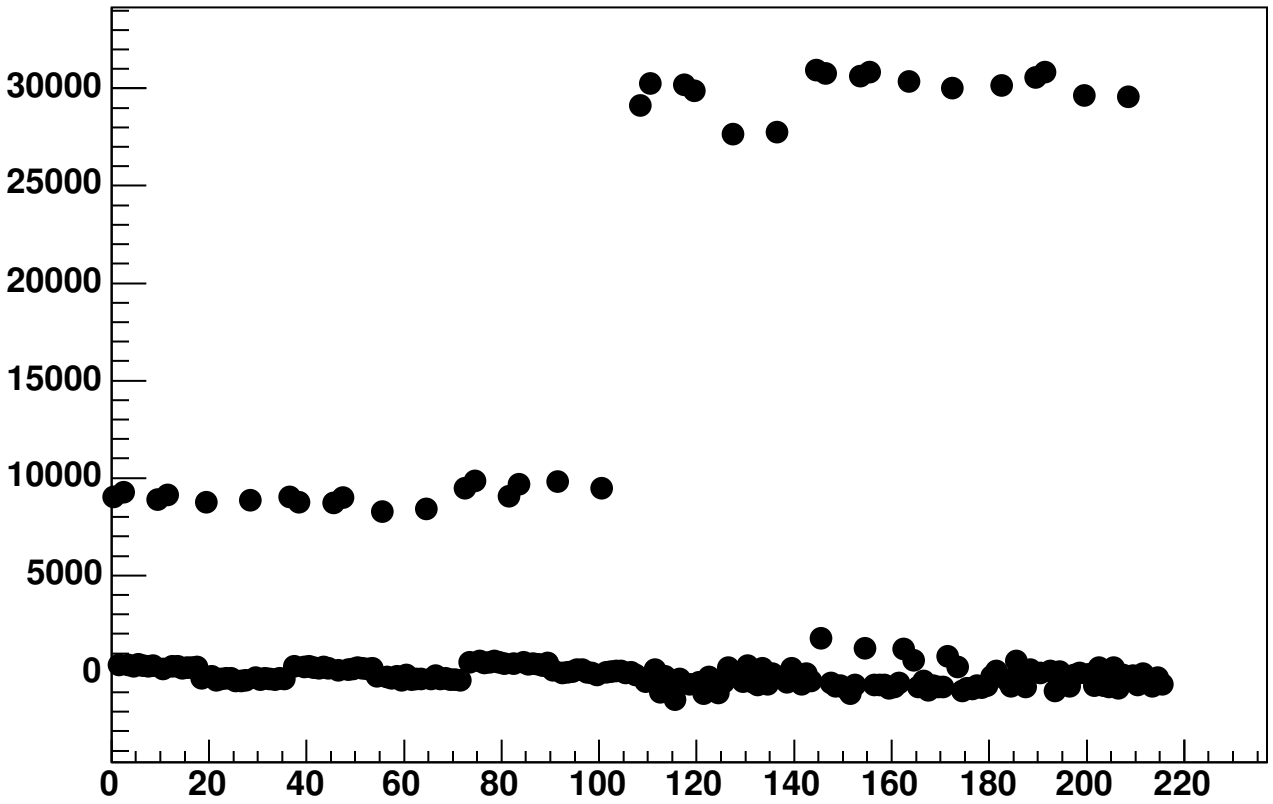
Enable 0, DAC=4000, Hold=25, ADC Mean vs 18*Chip+Chan



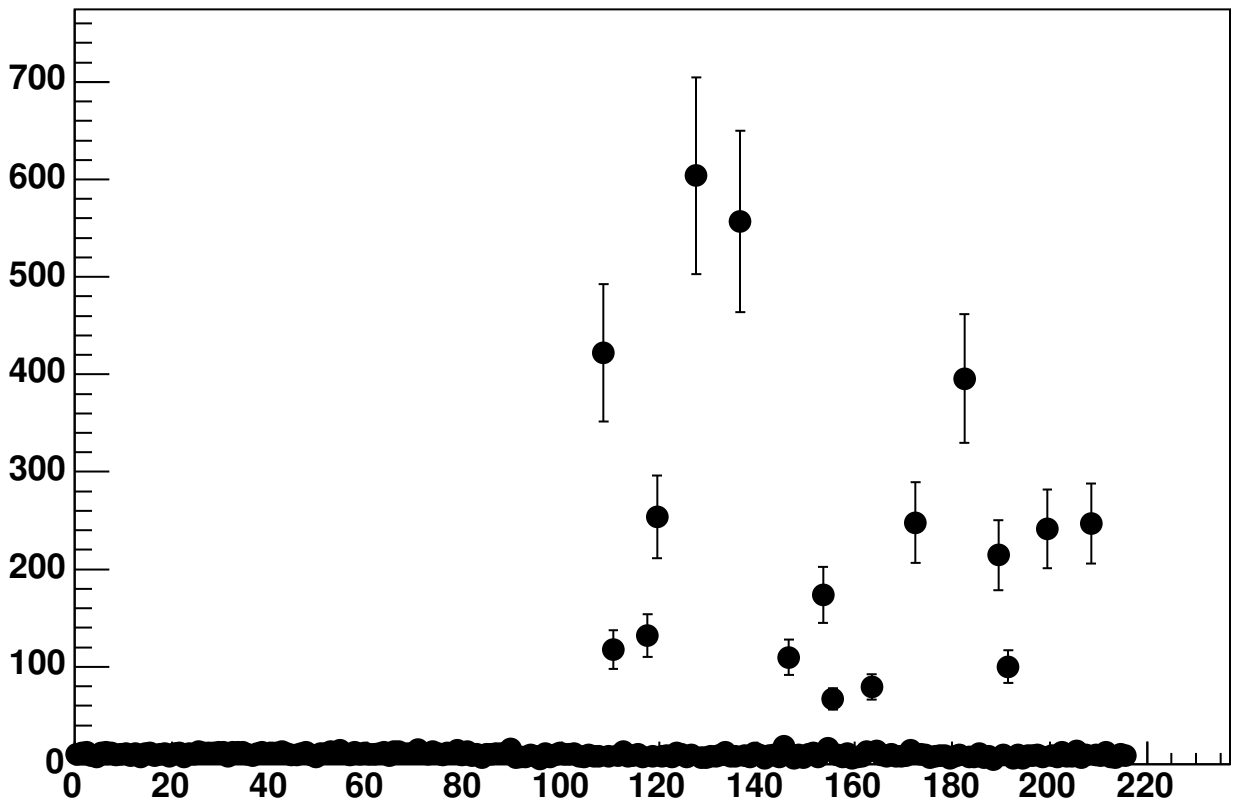
Enable 0, DAC=4000, Hold=25, ADC Noise vs 18*Chip+Chan



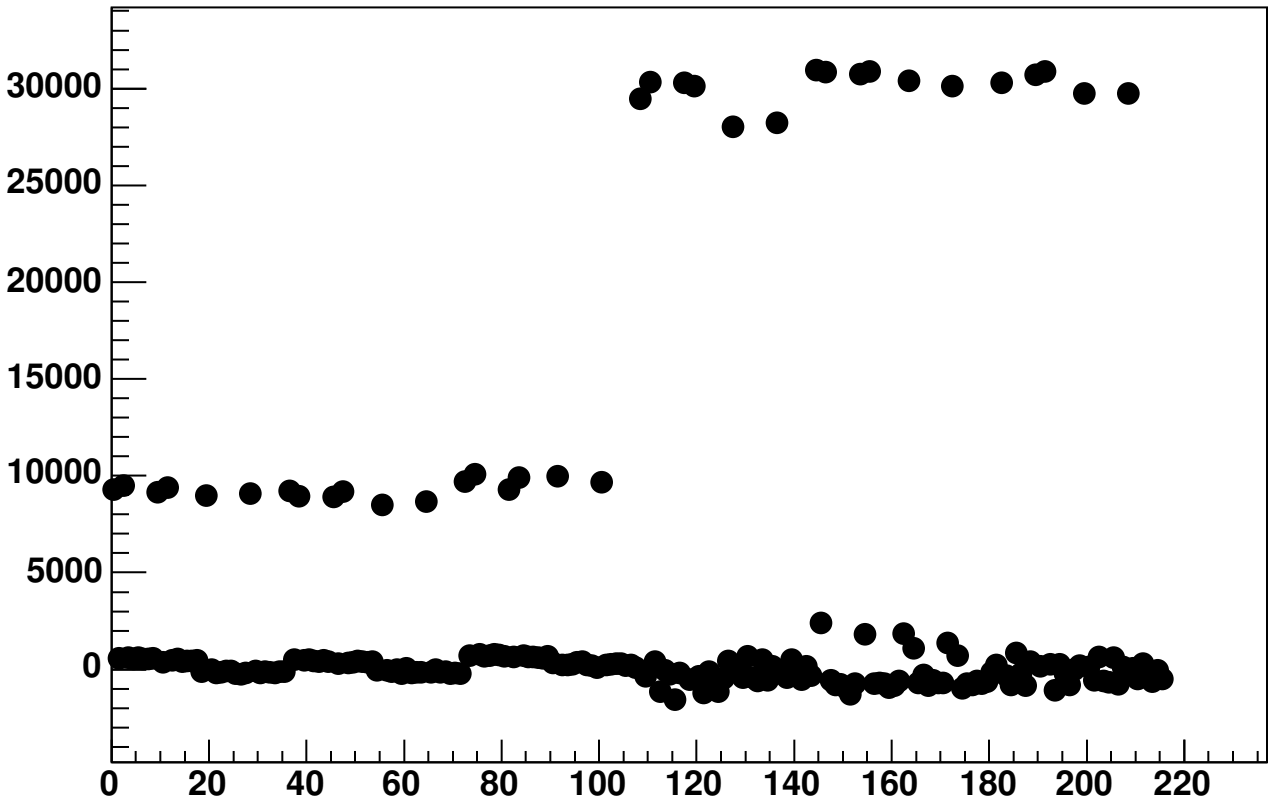
Enable 0, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



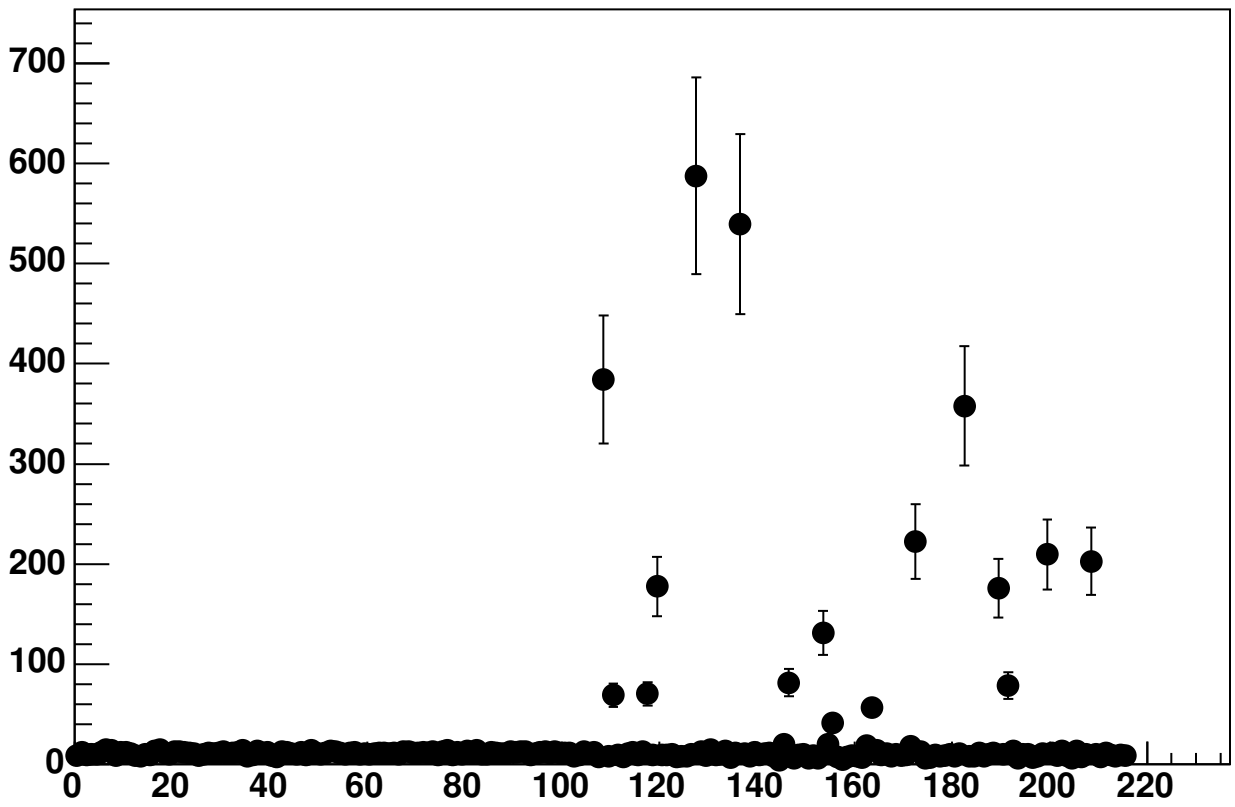
Enable 0, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



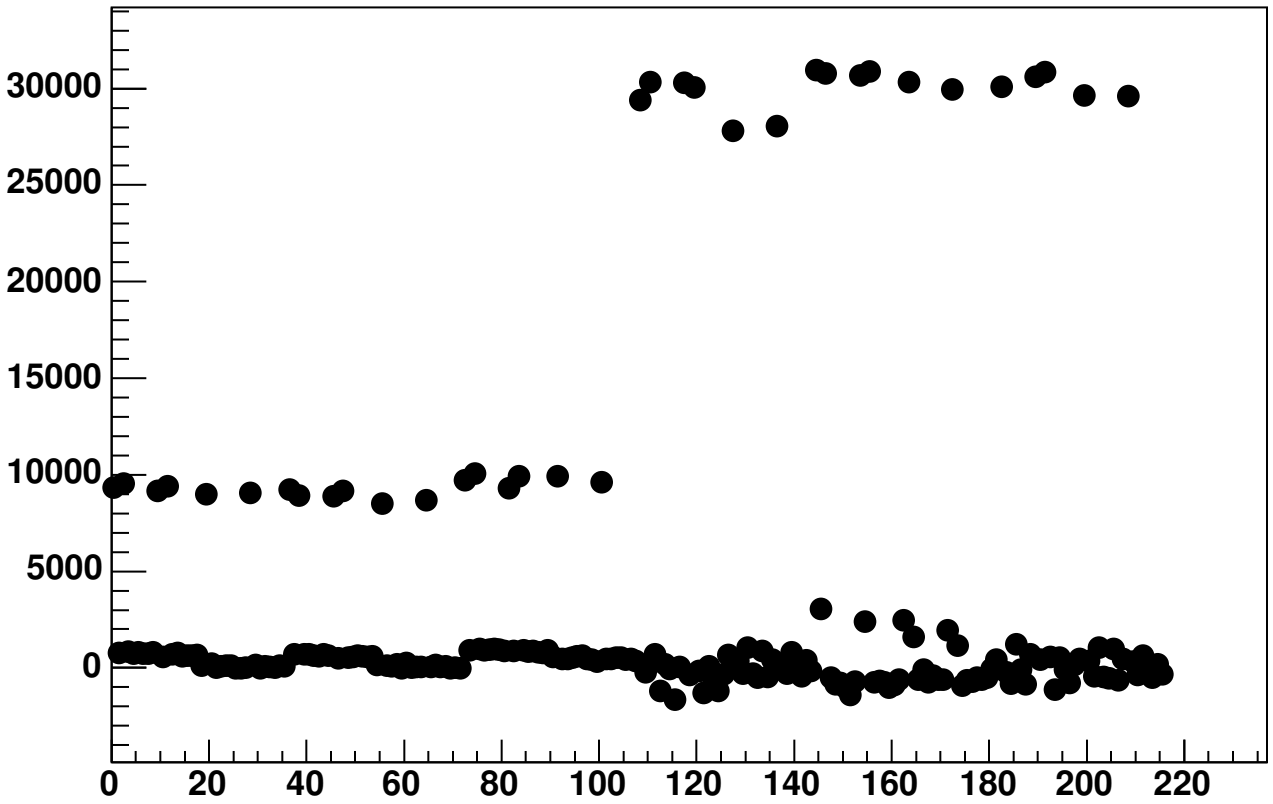
Enable 0, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



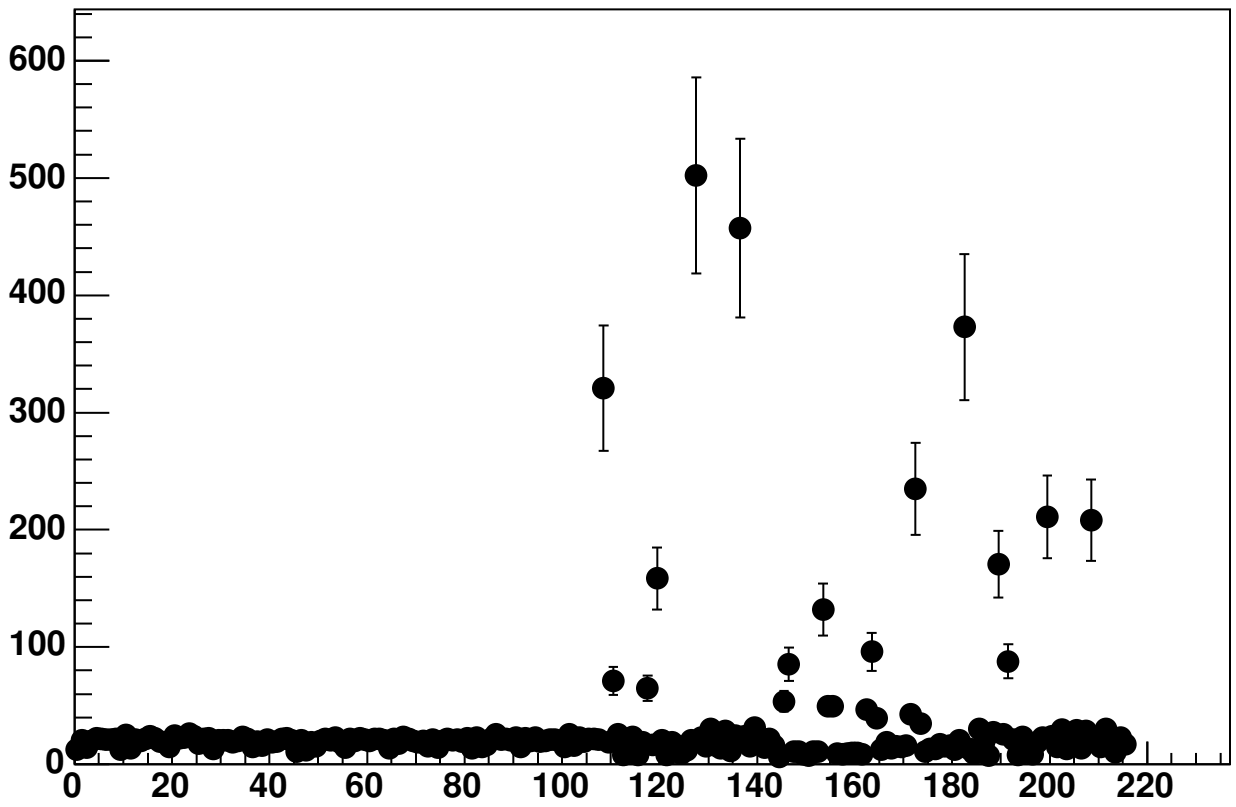
Enable 0, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



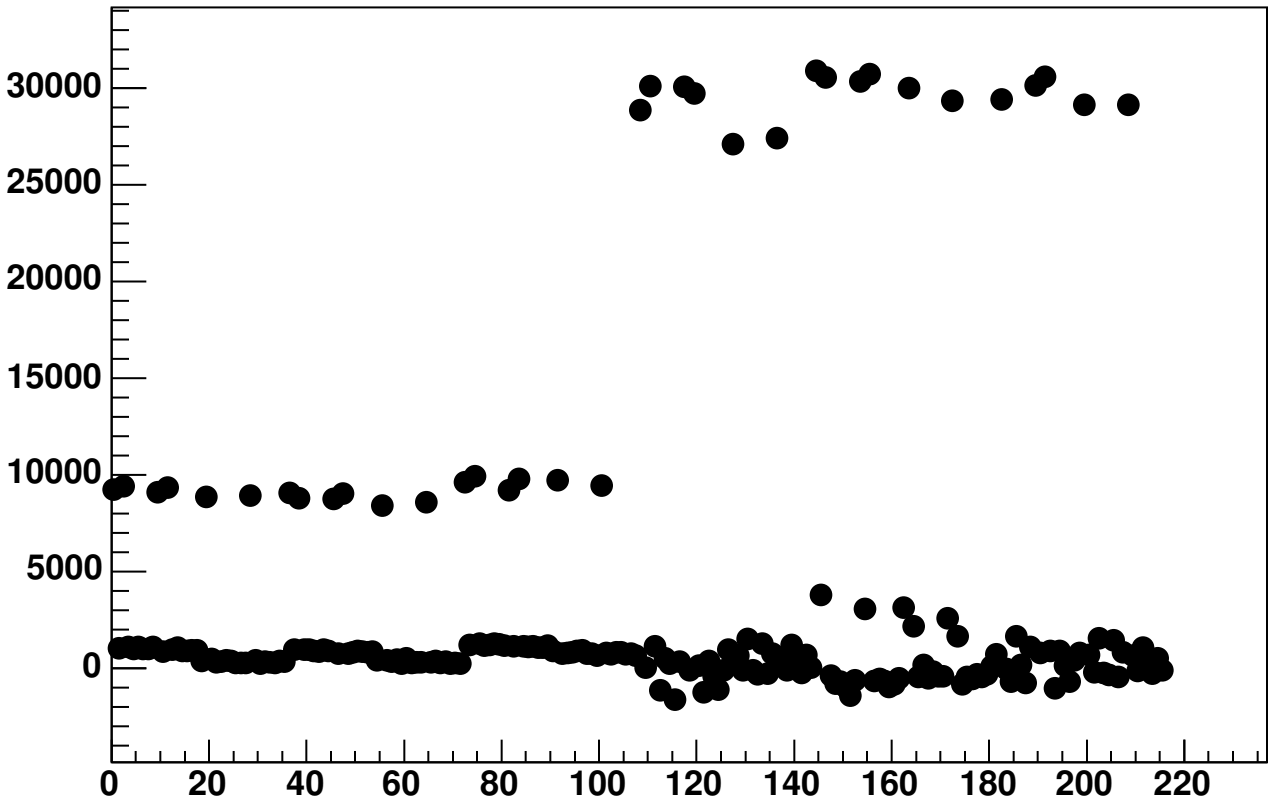
Enable 0, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



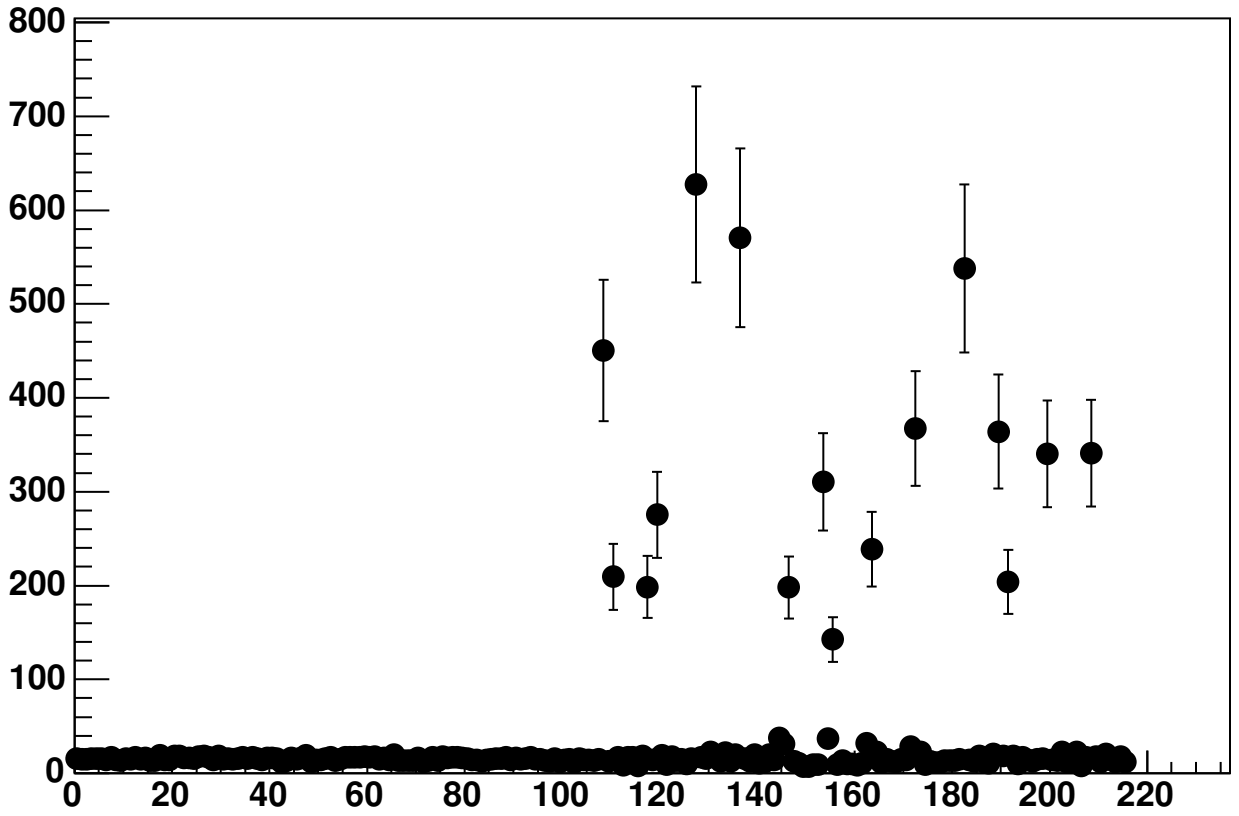
Enable 0, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



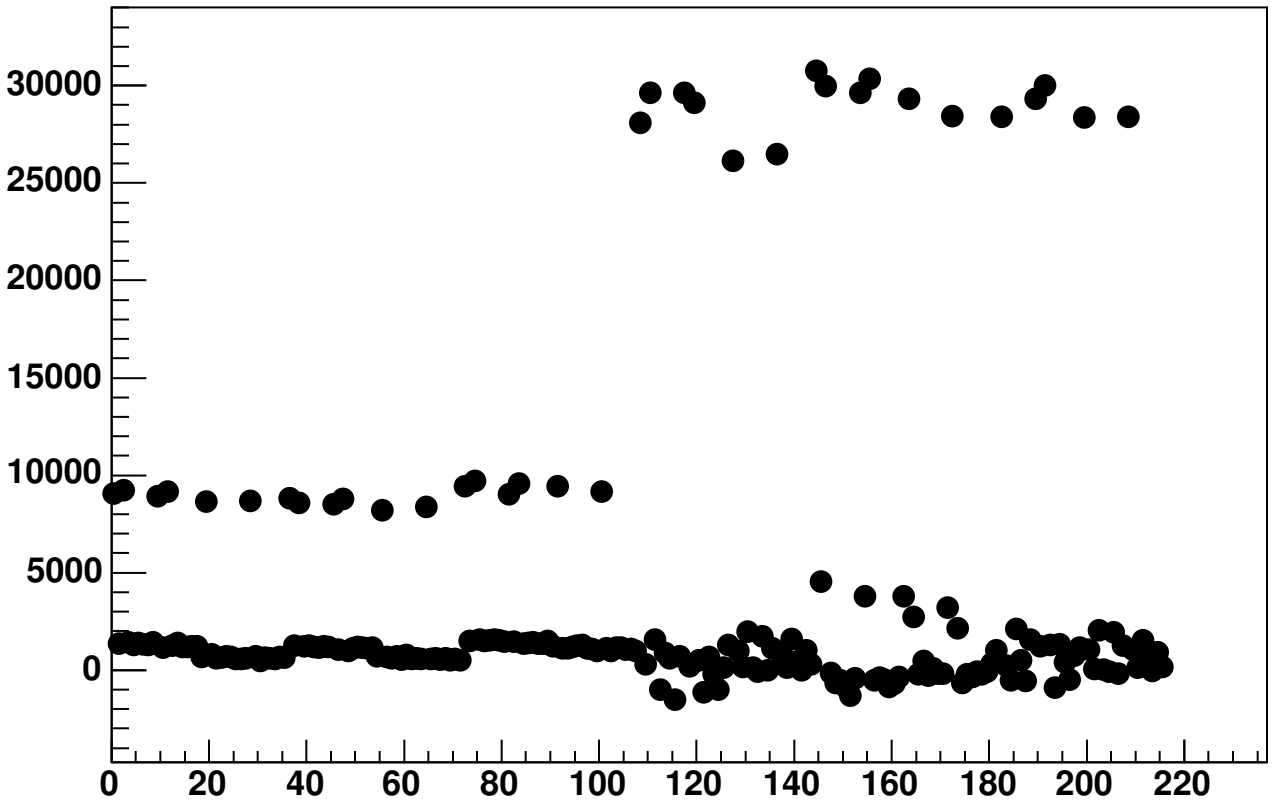
Enable 0, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



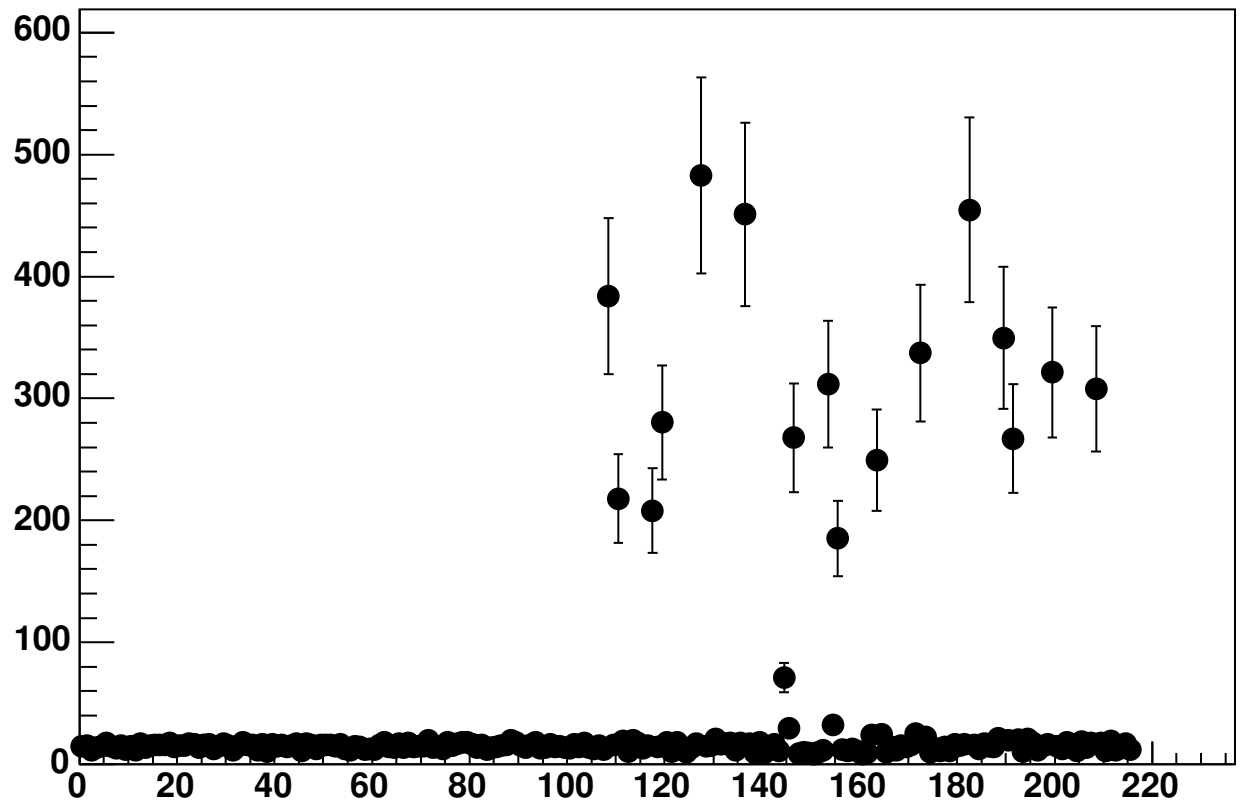
Enable 0, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



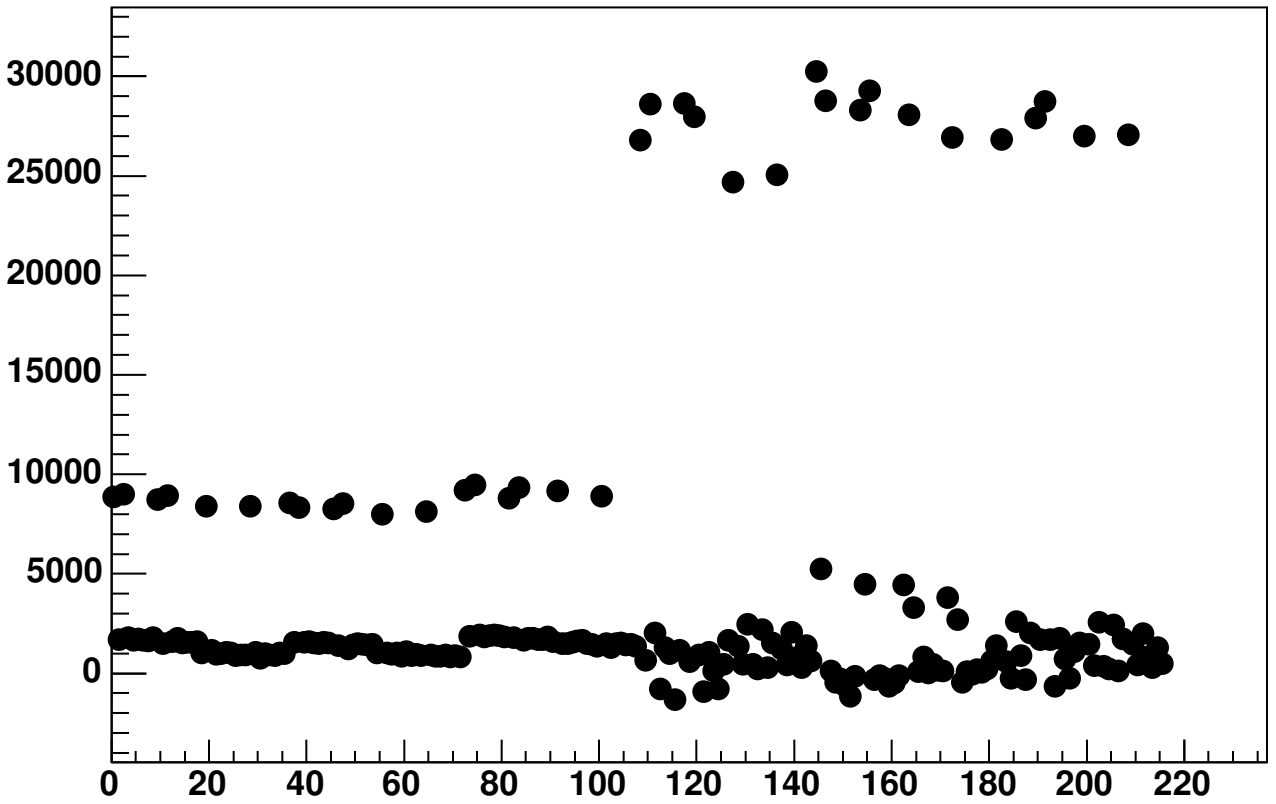
Enable 0, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



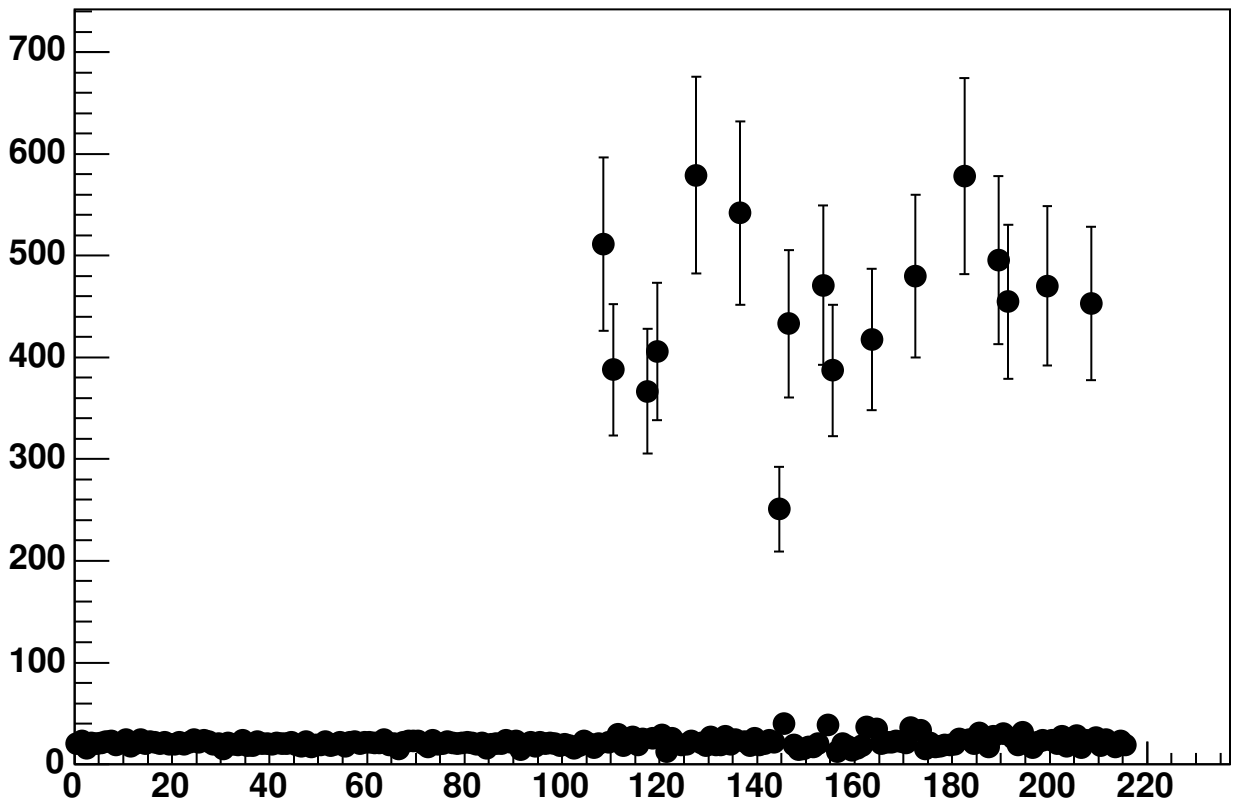
Enable 0, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



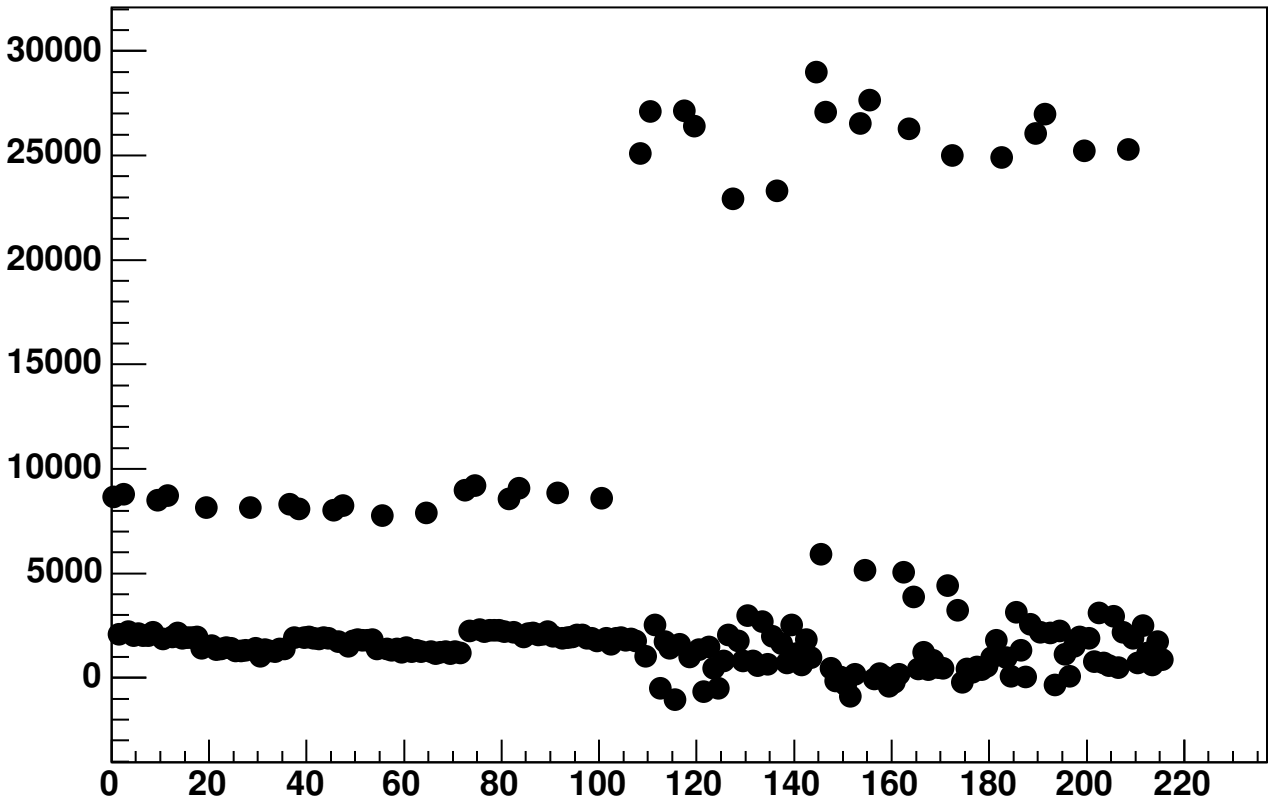
Enable 0, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



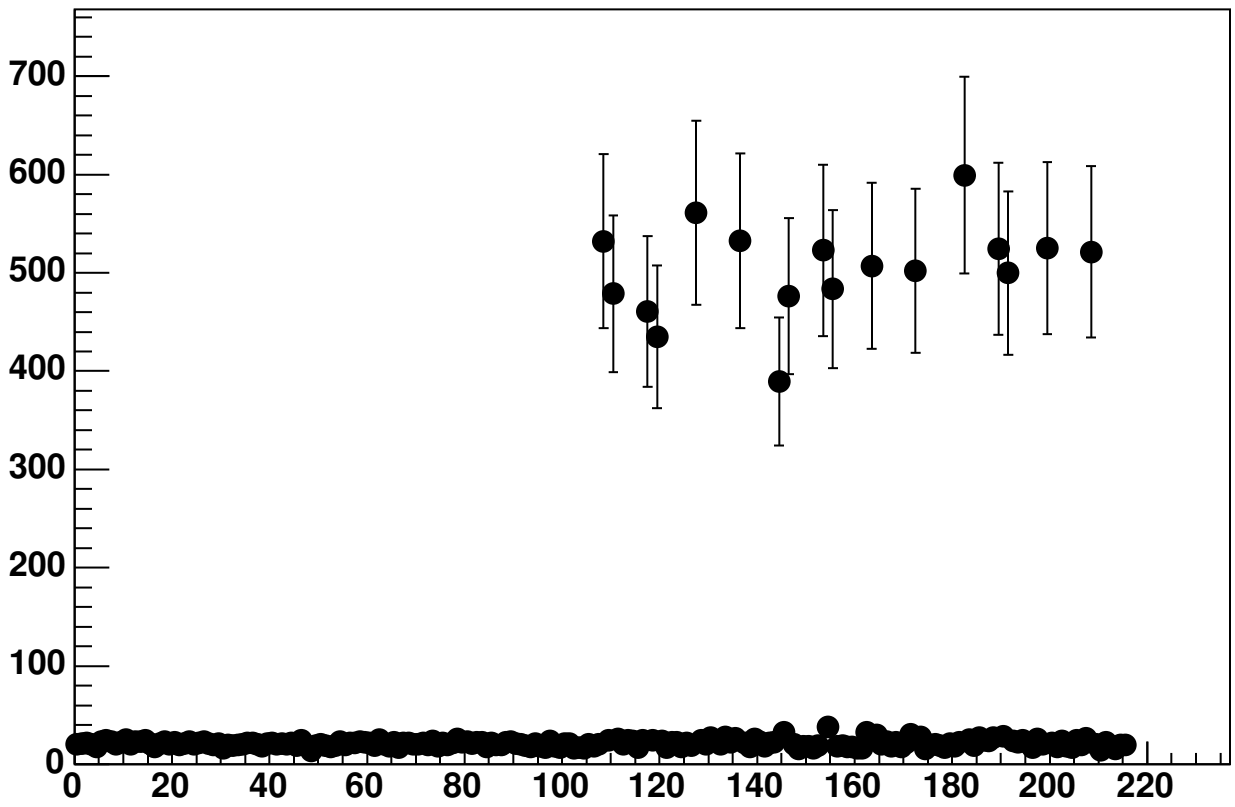
Enable 0, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



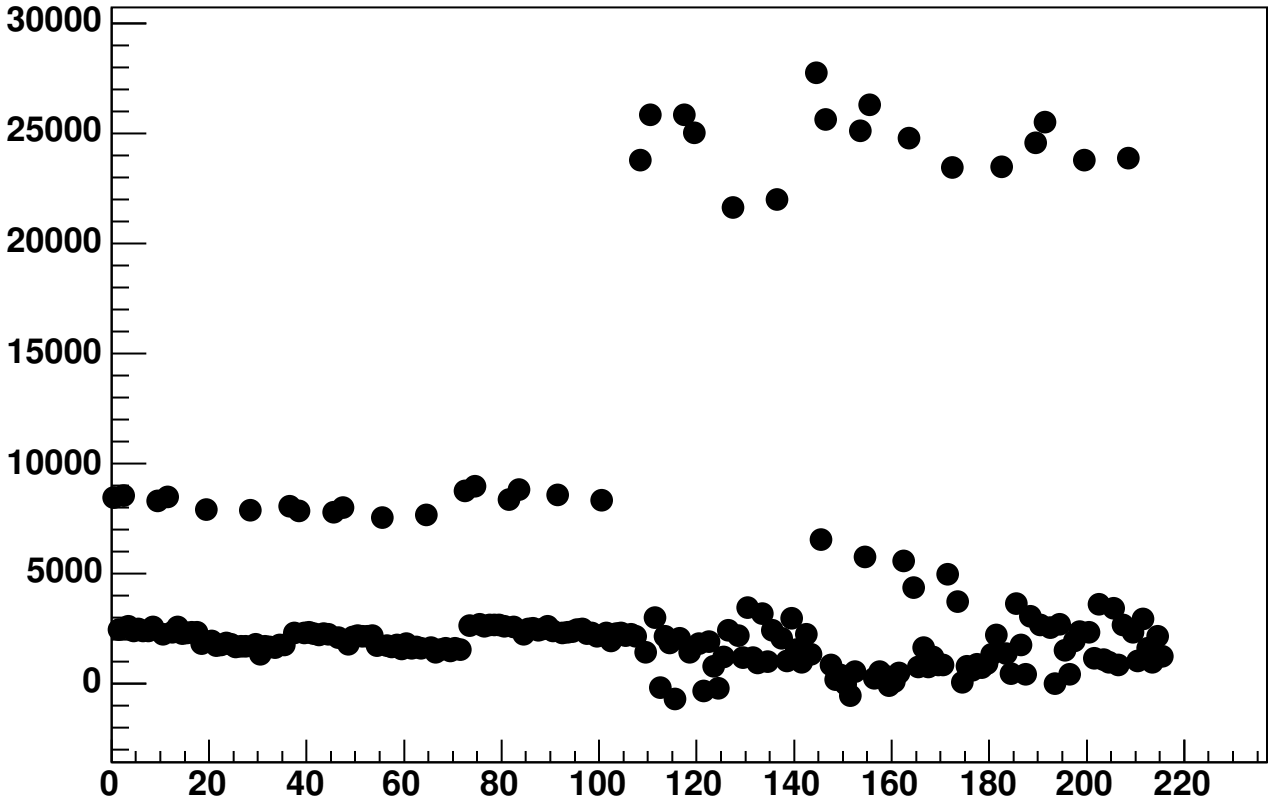
Enable 0, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



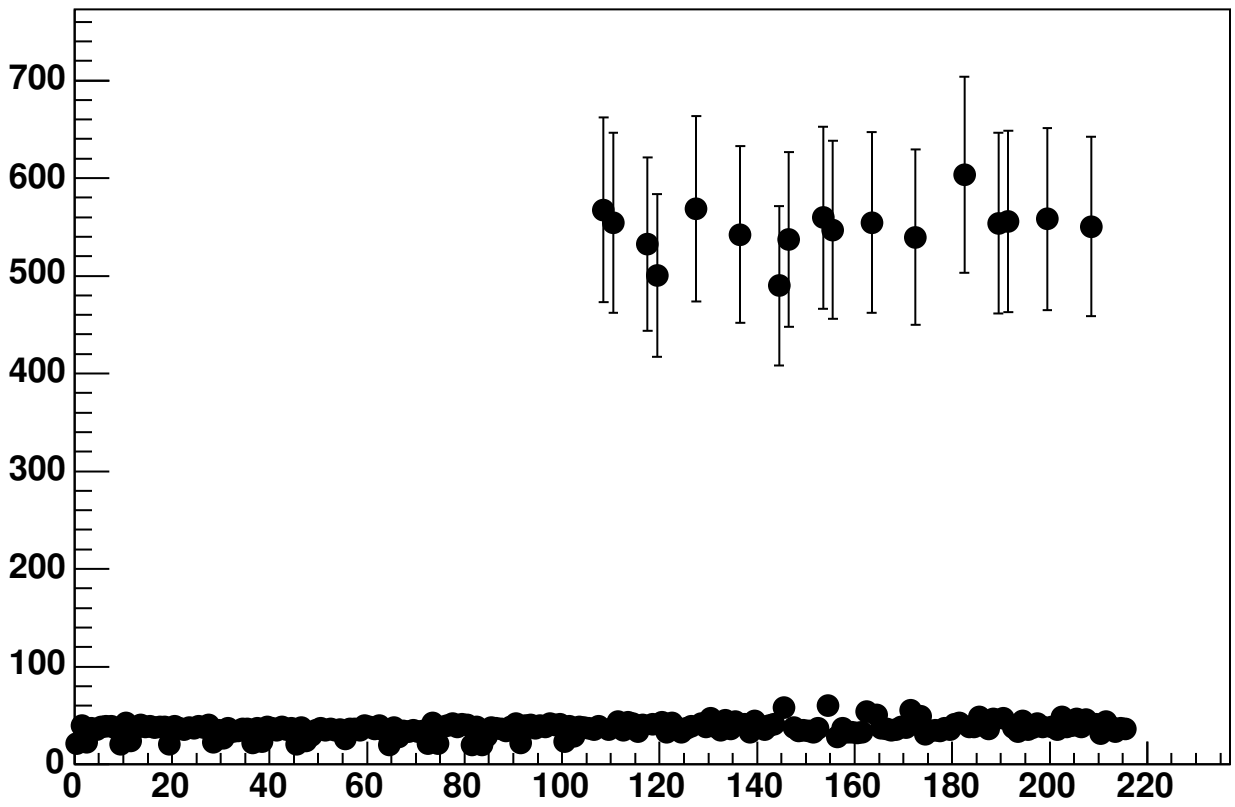
Enable 0, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



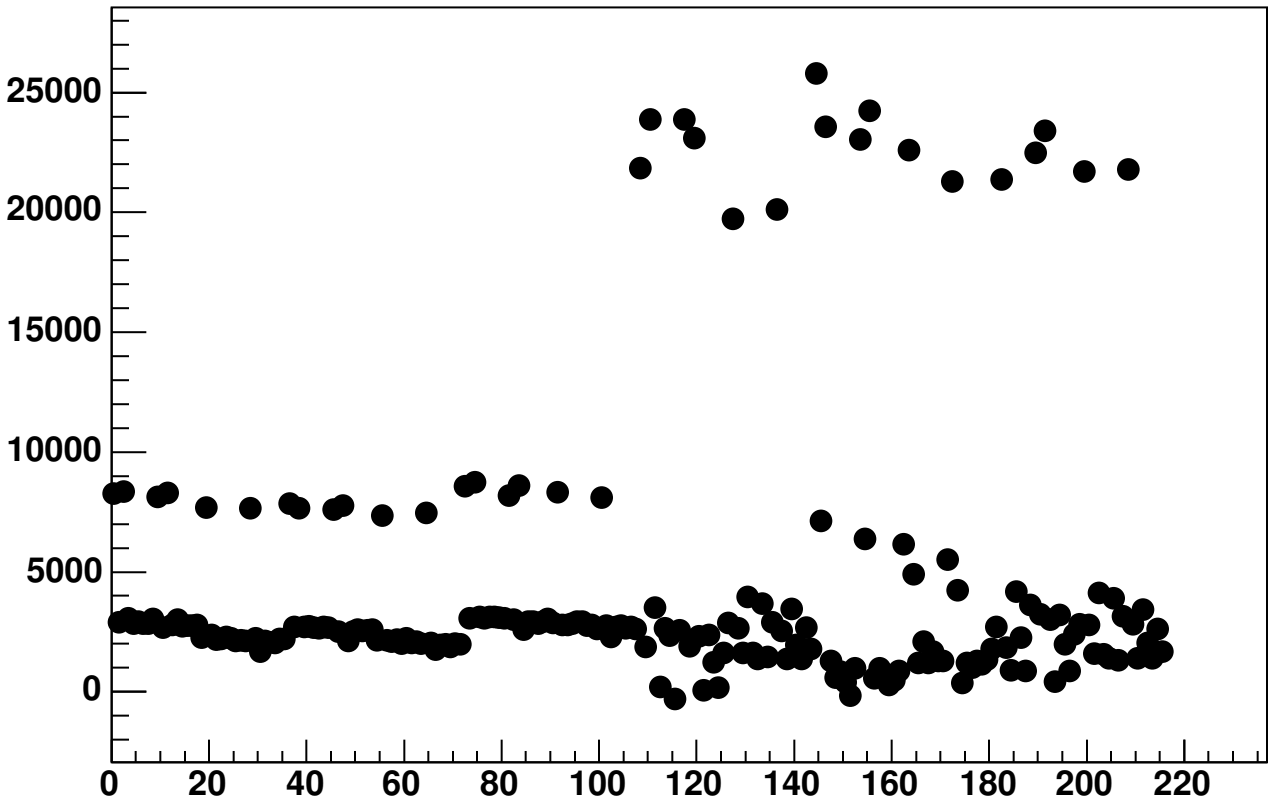
Enable 0, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



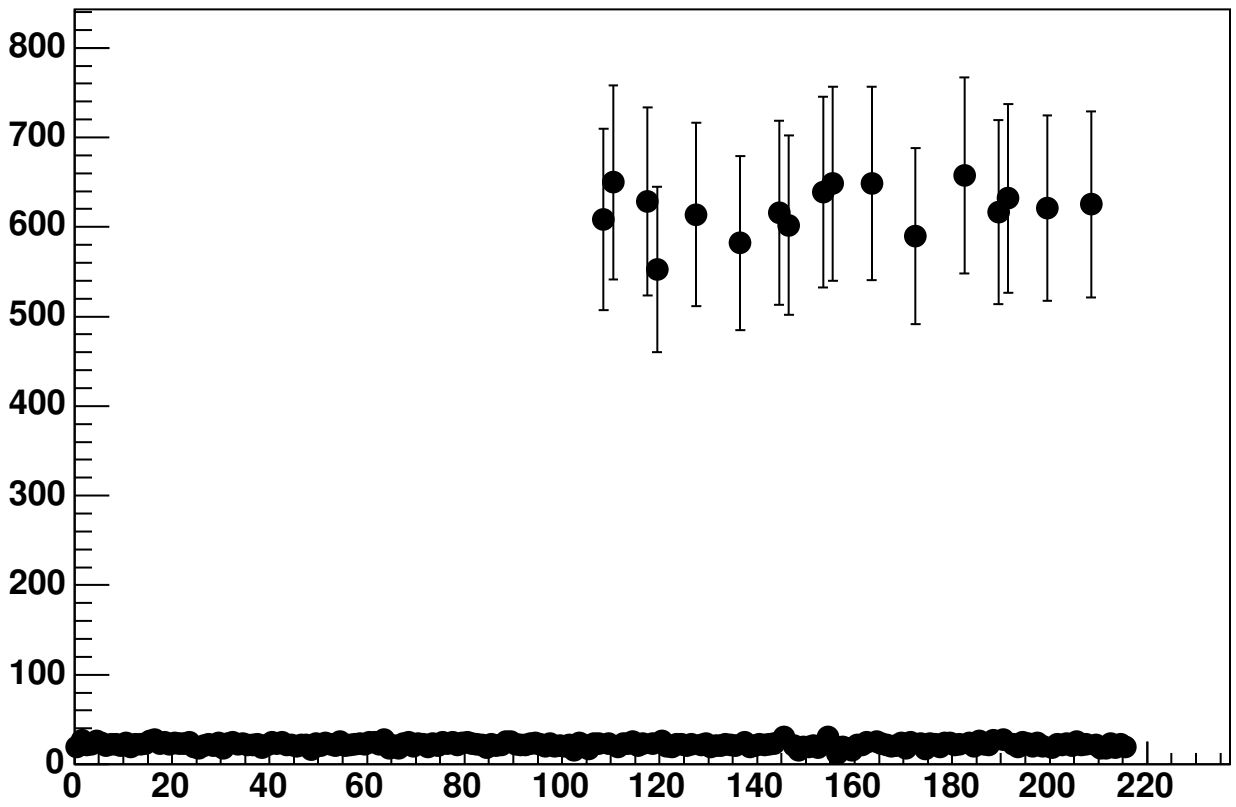
Enable 0, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



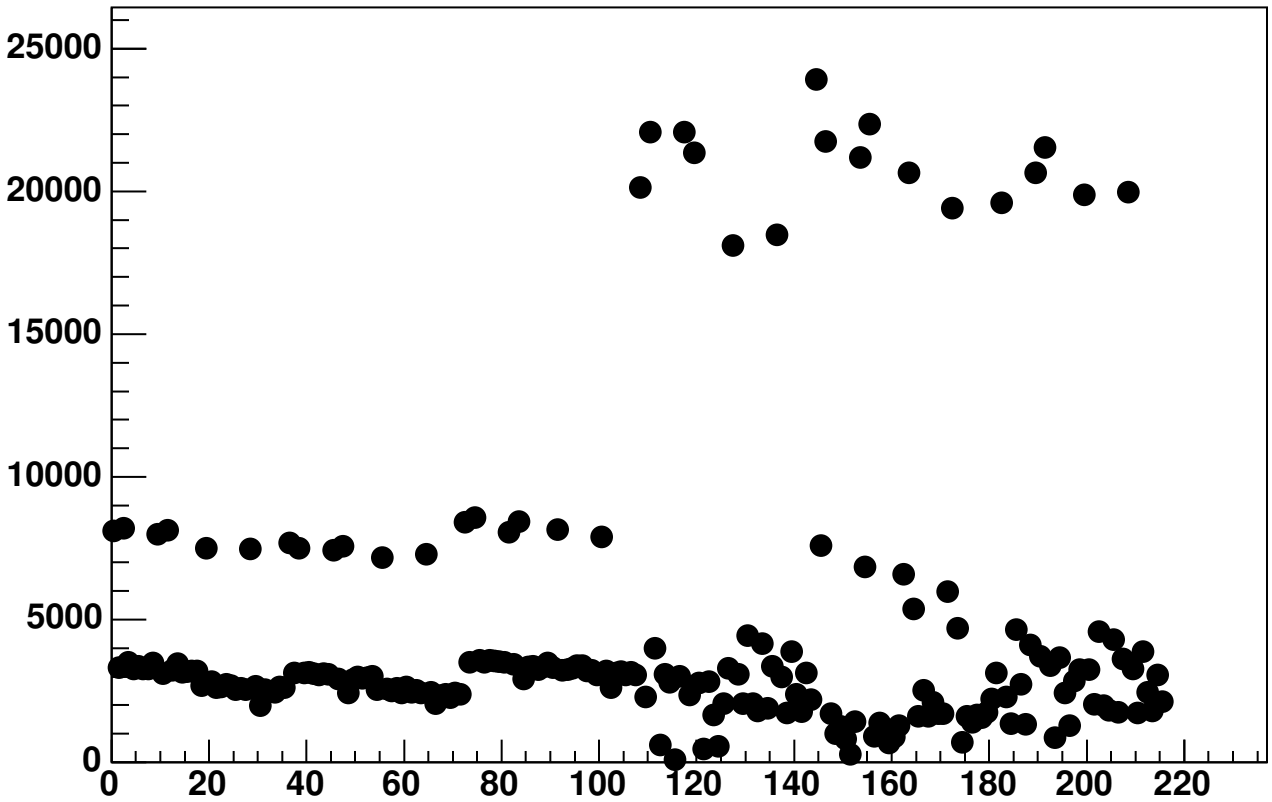
Enable 0, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



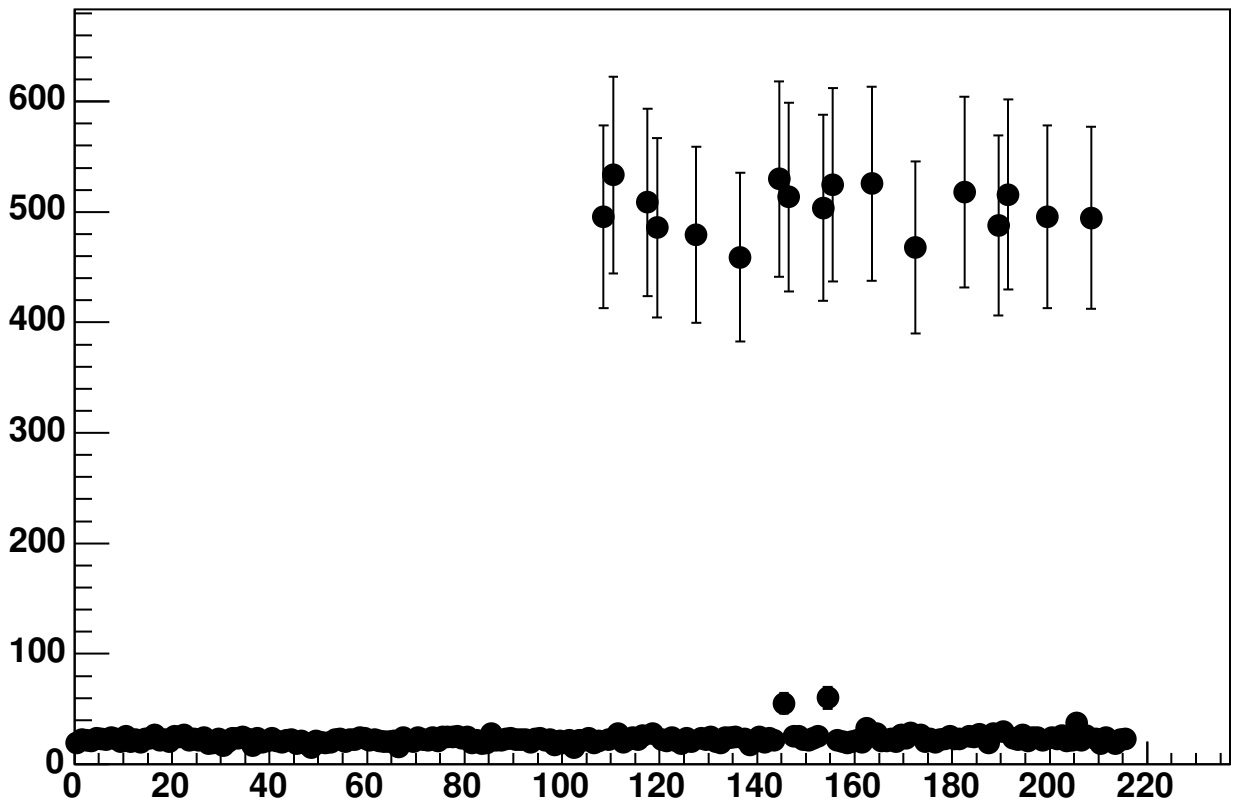
Enable 0, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



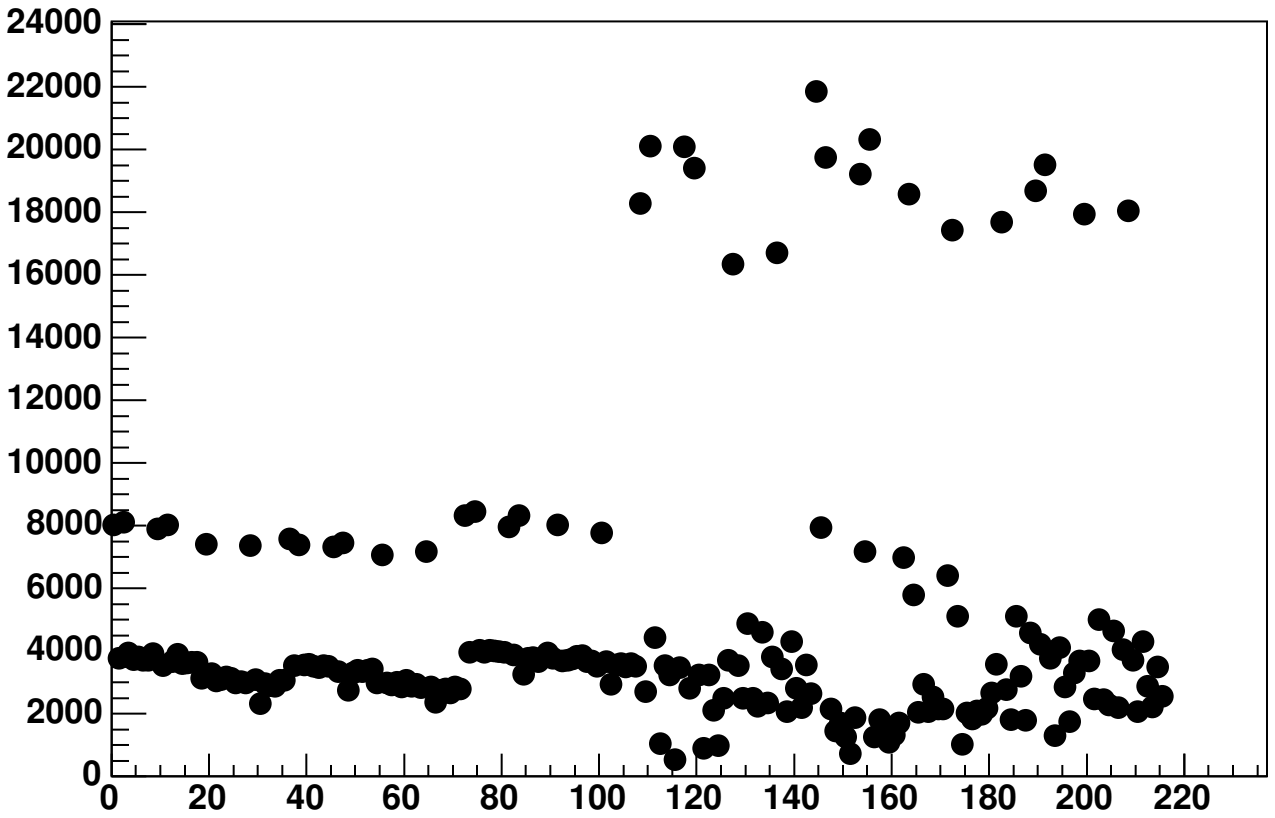
Enable 0, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



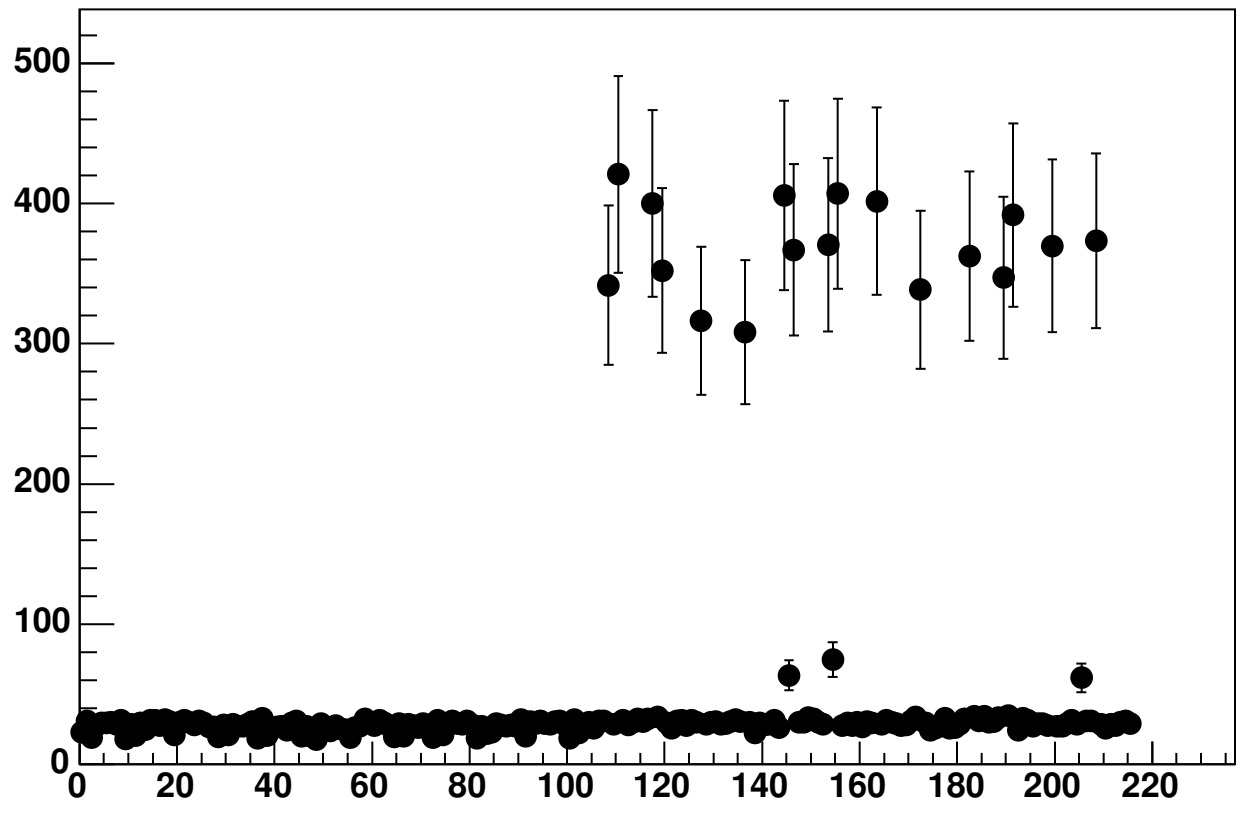
Enable 0, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



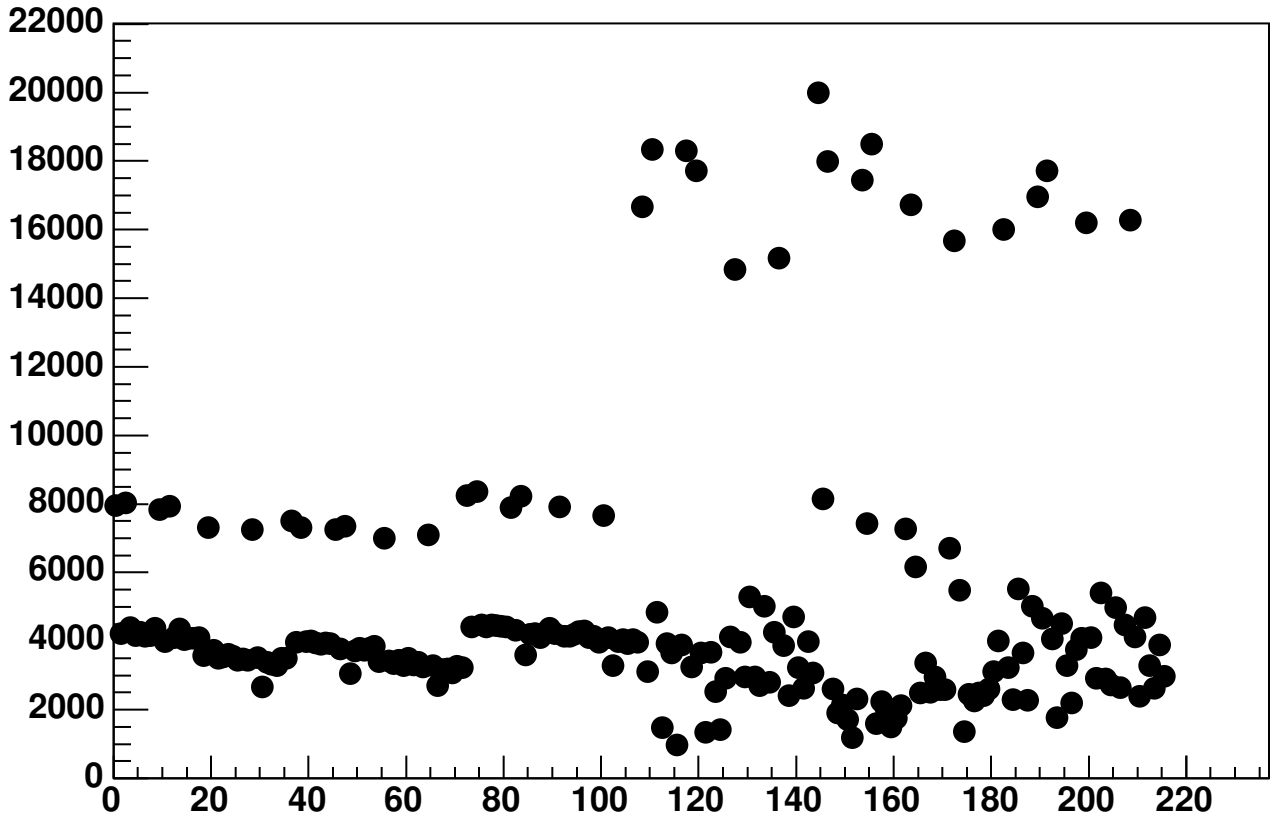
Enable 0, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



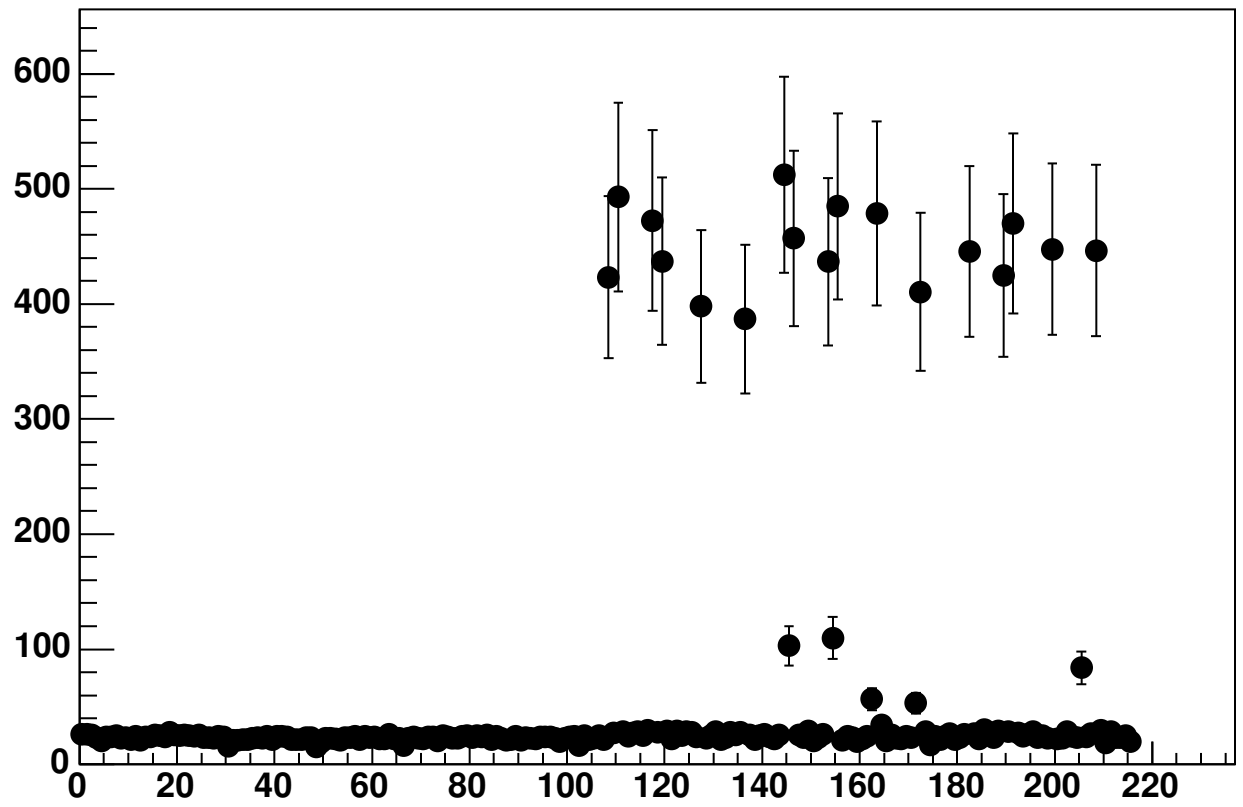
Enable 0, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



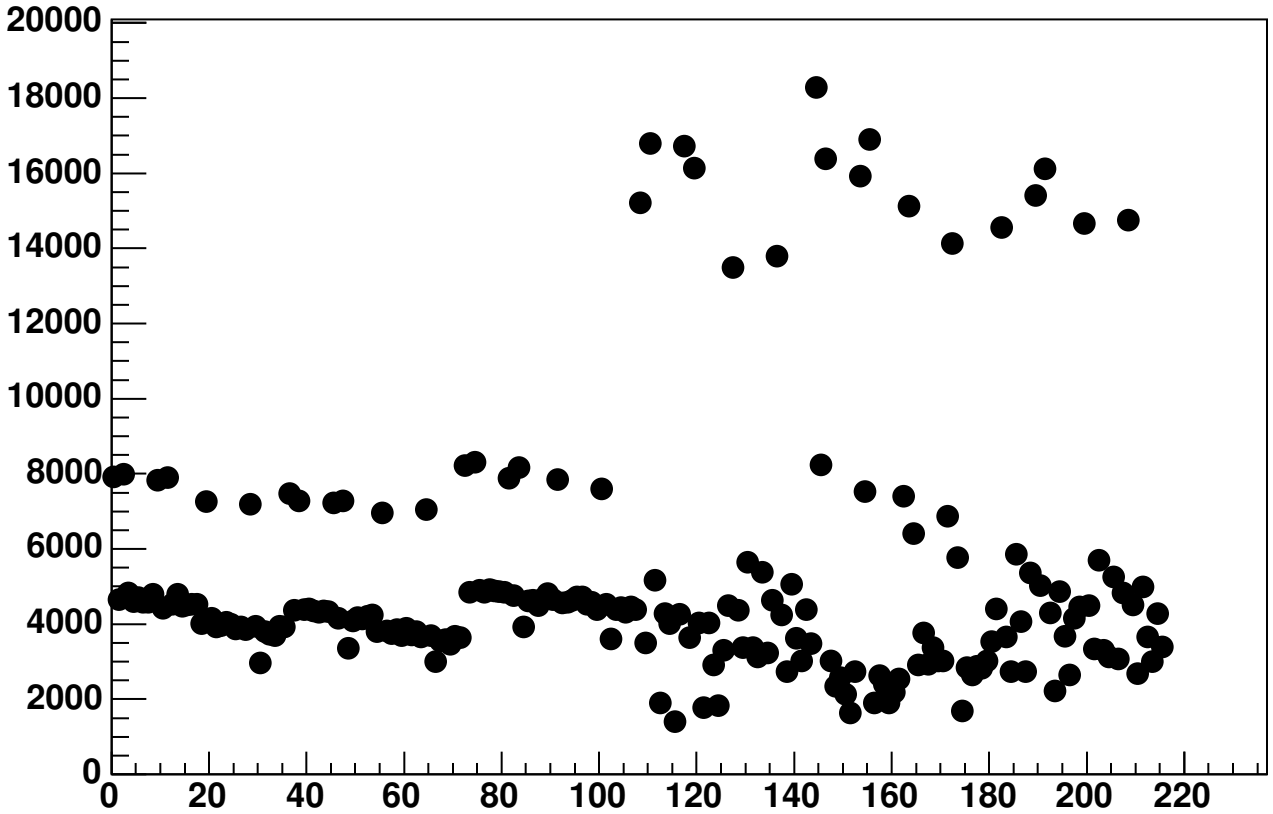
Enable 0, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



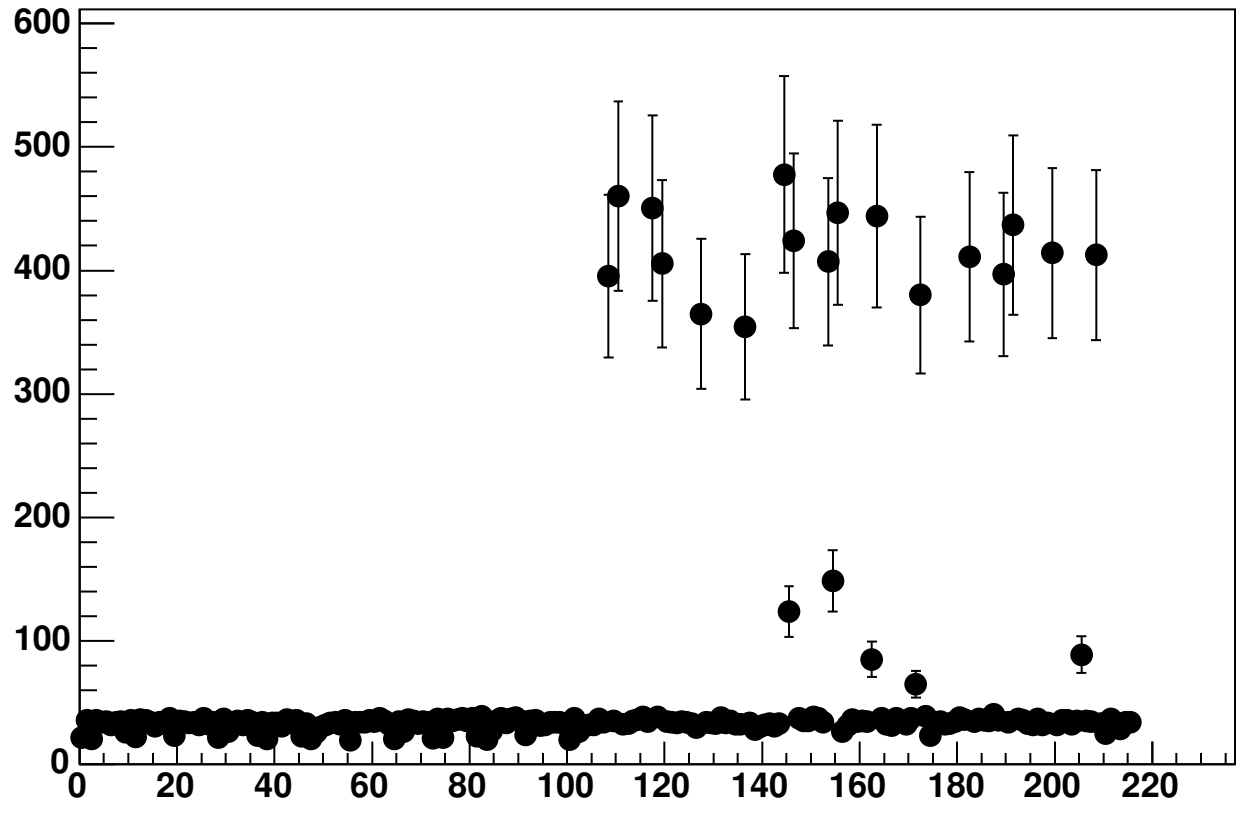
Enable 0, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



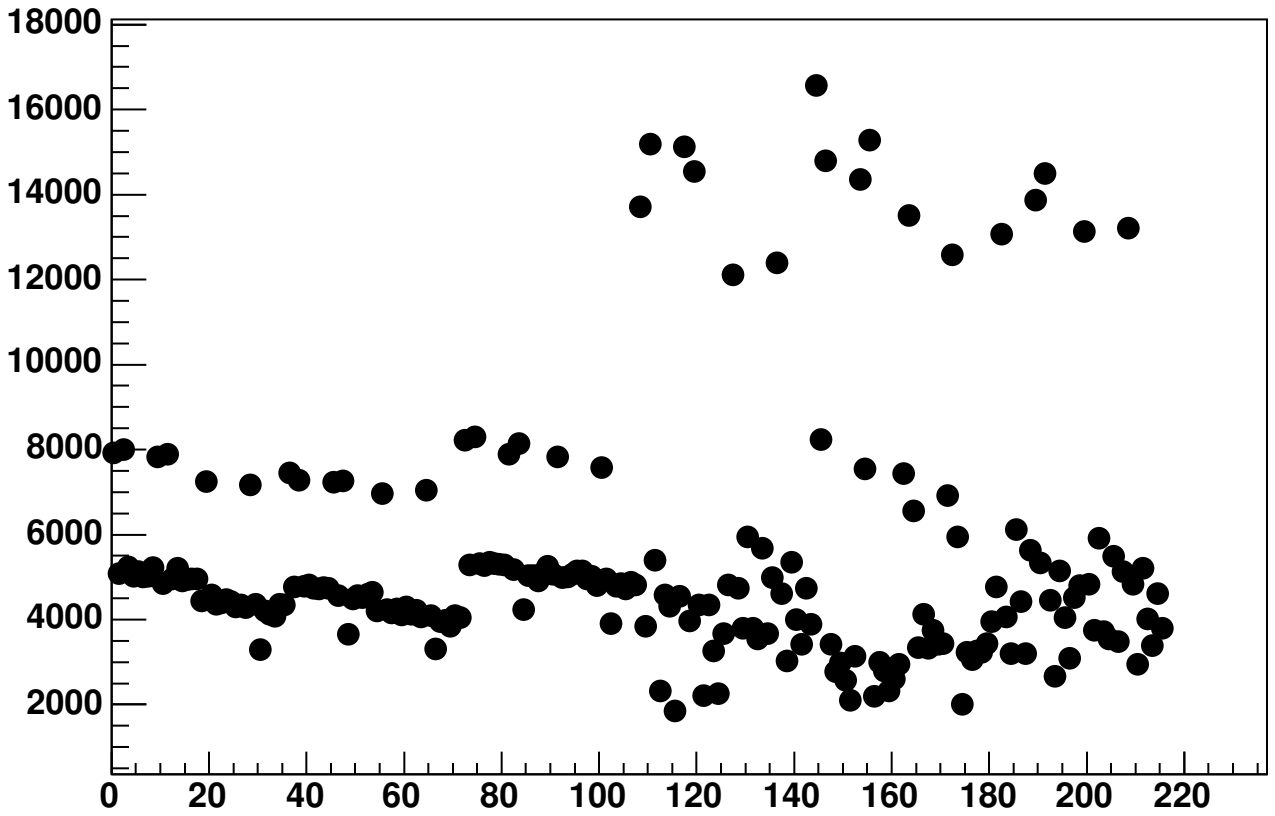
Enable 0, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



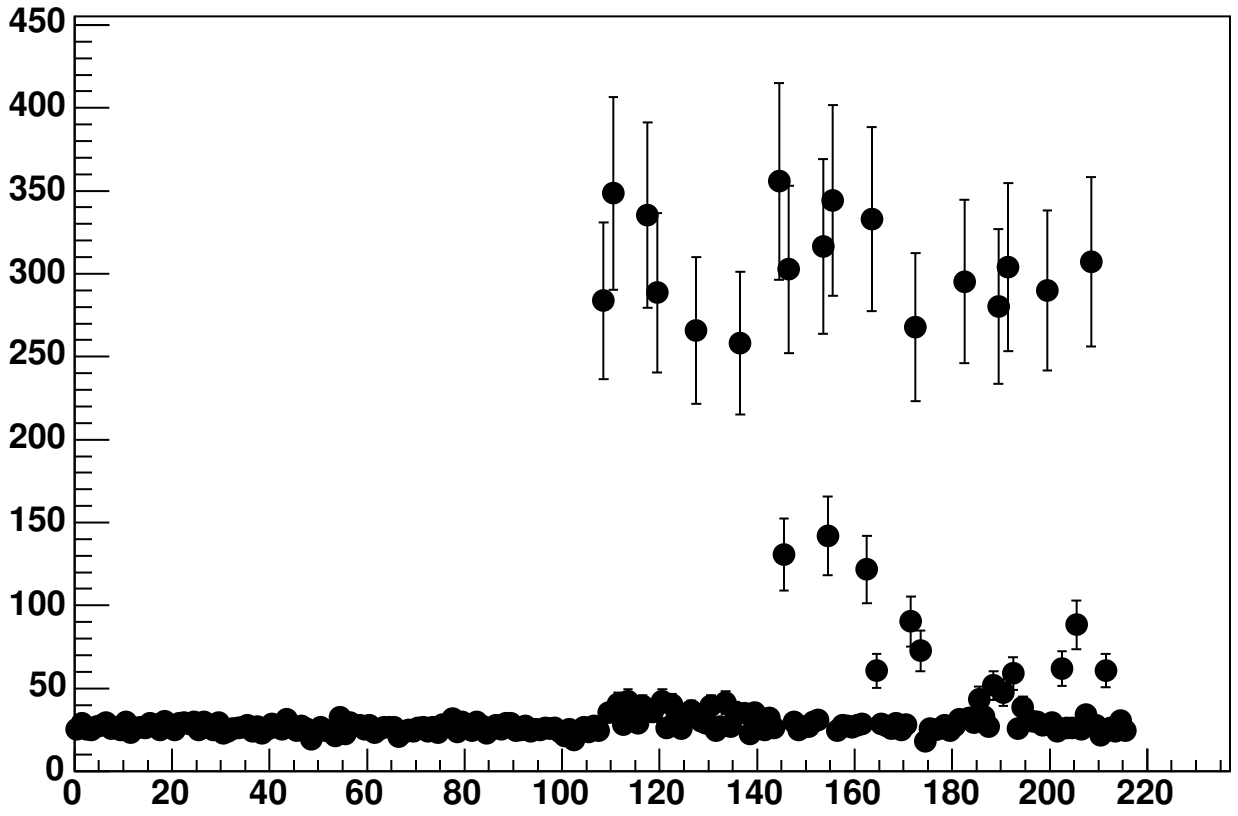
Enable 0, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



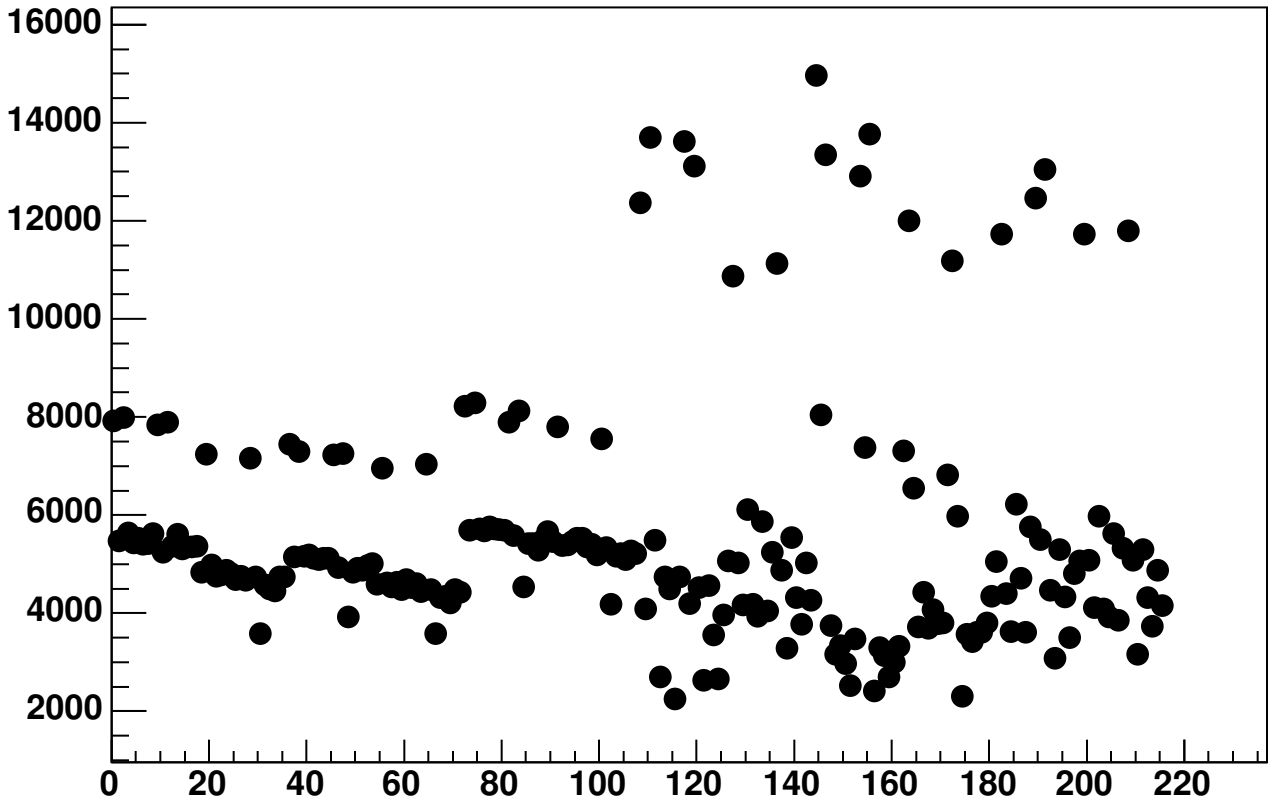
Enable 0, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



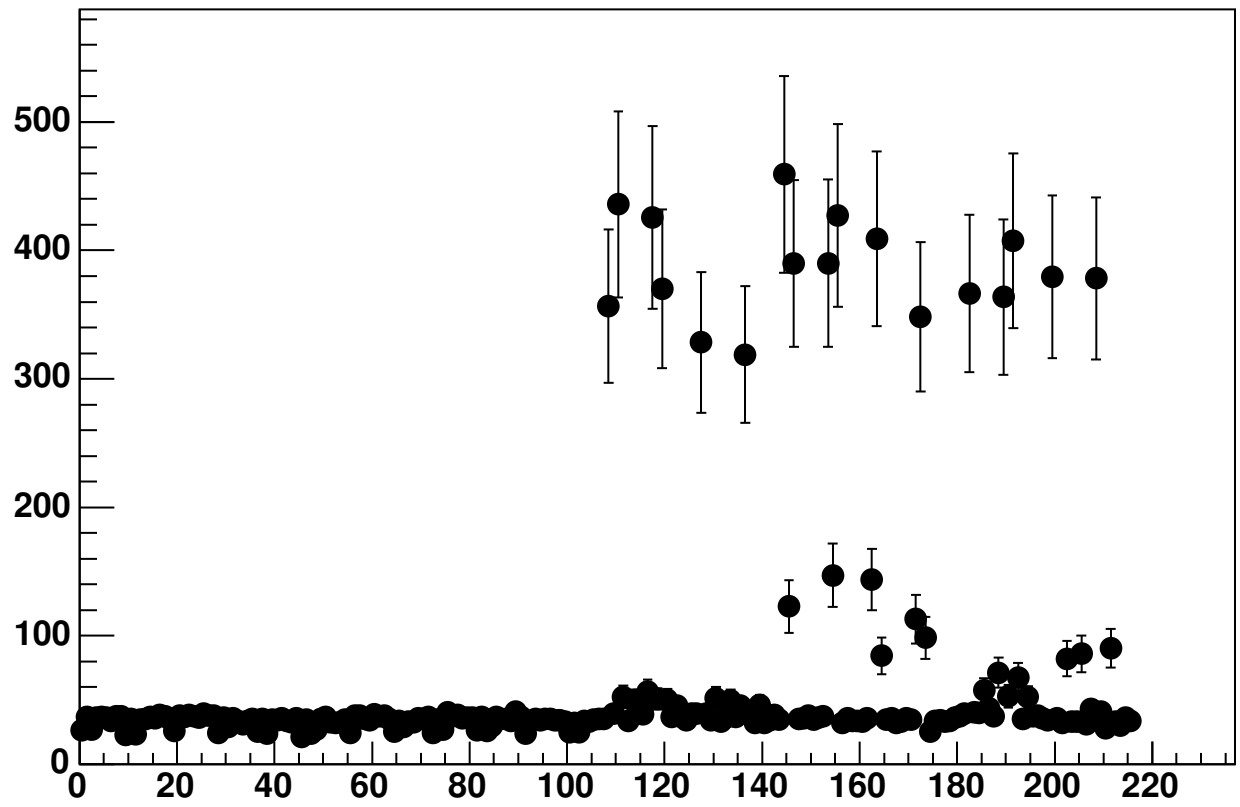
Enable 0, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



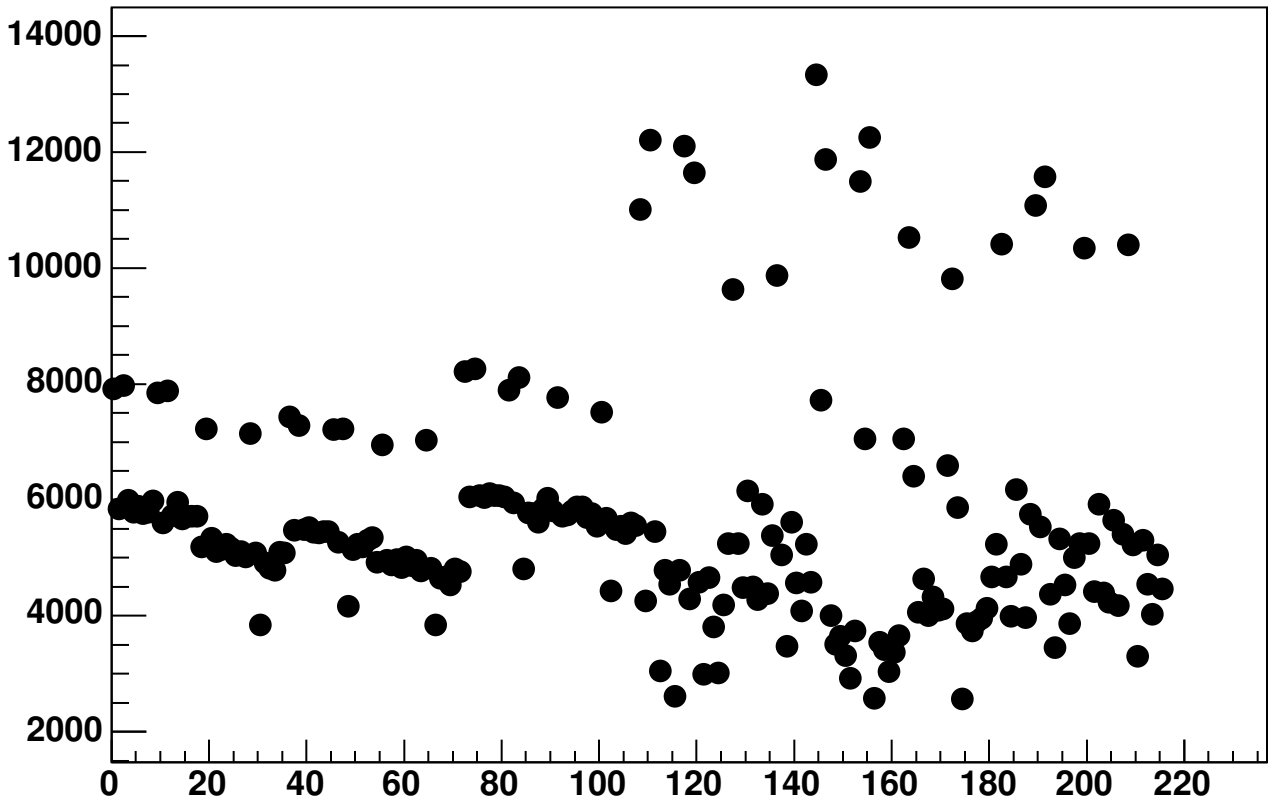
Enable 0, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



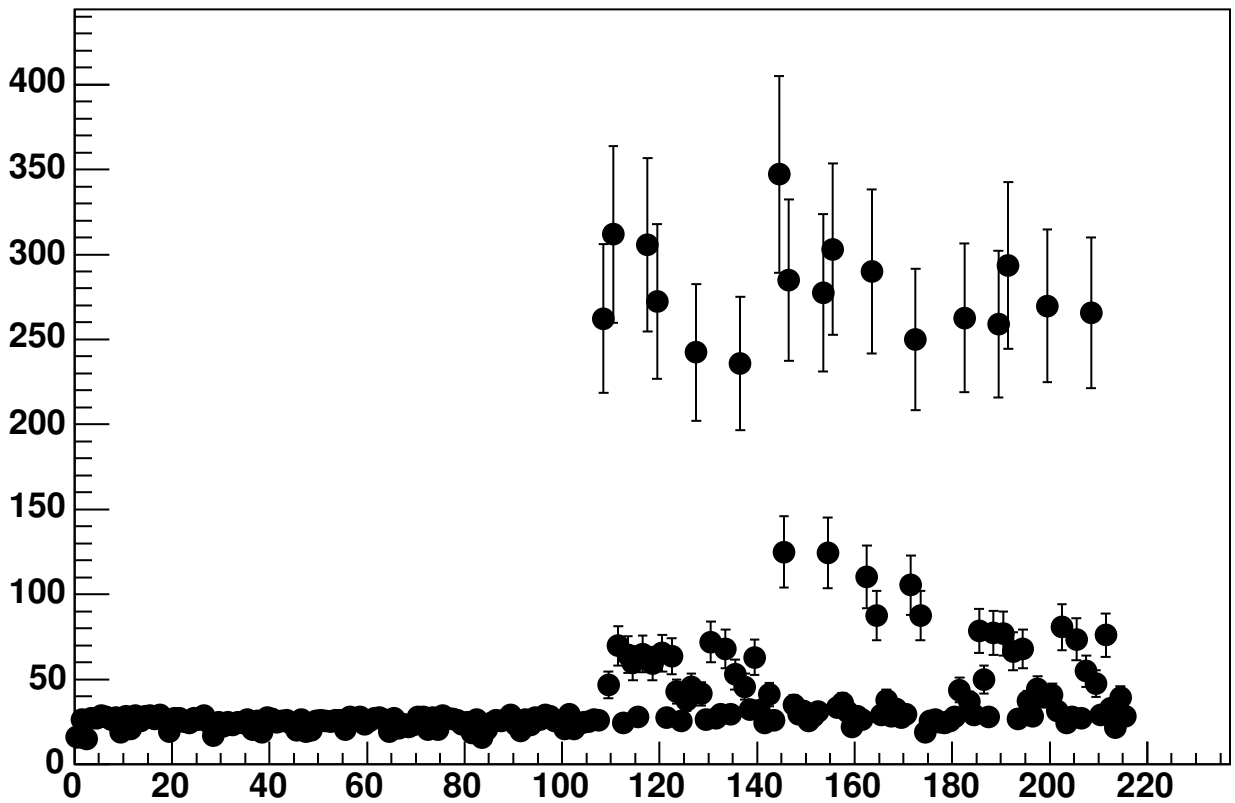
Enable 0, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



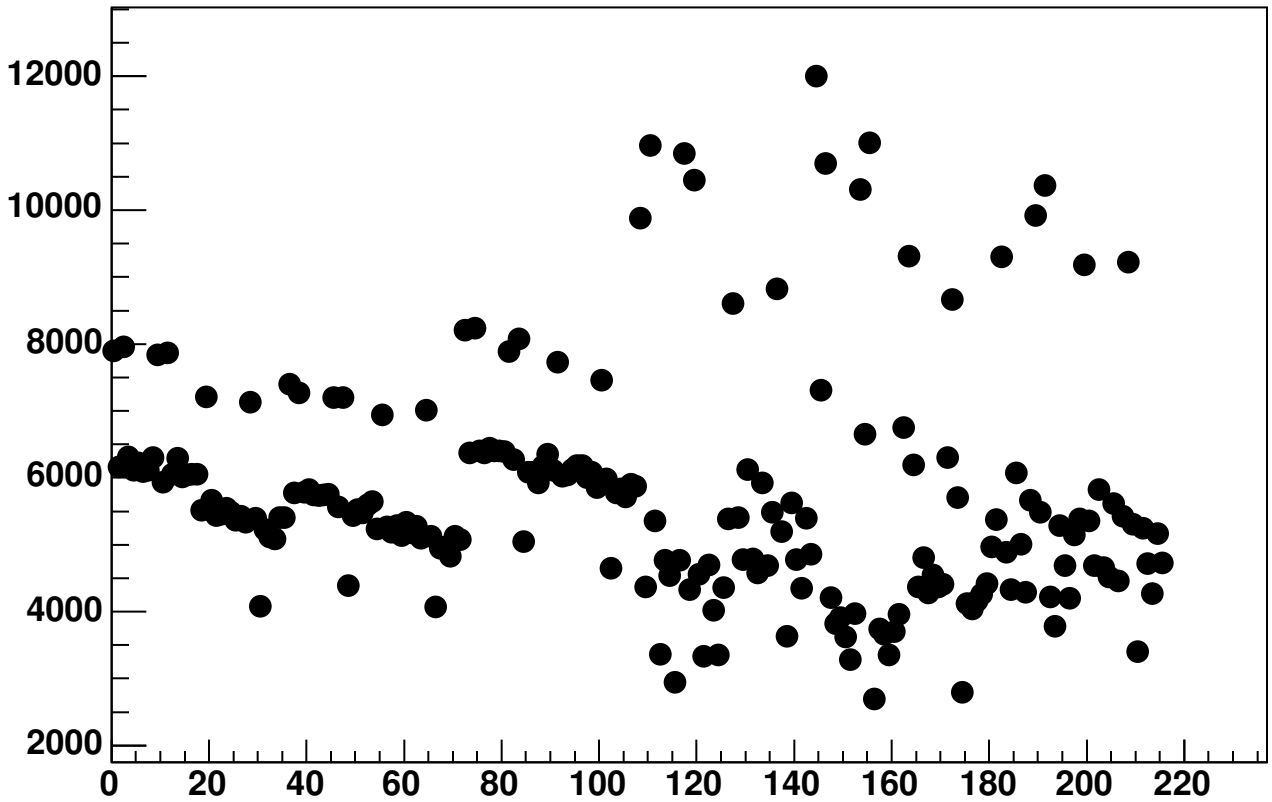
Enable 0, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



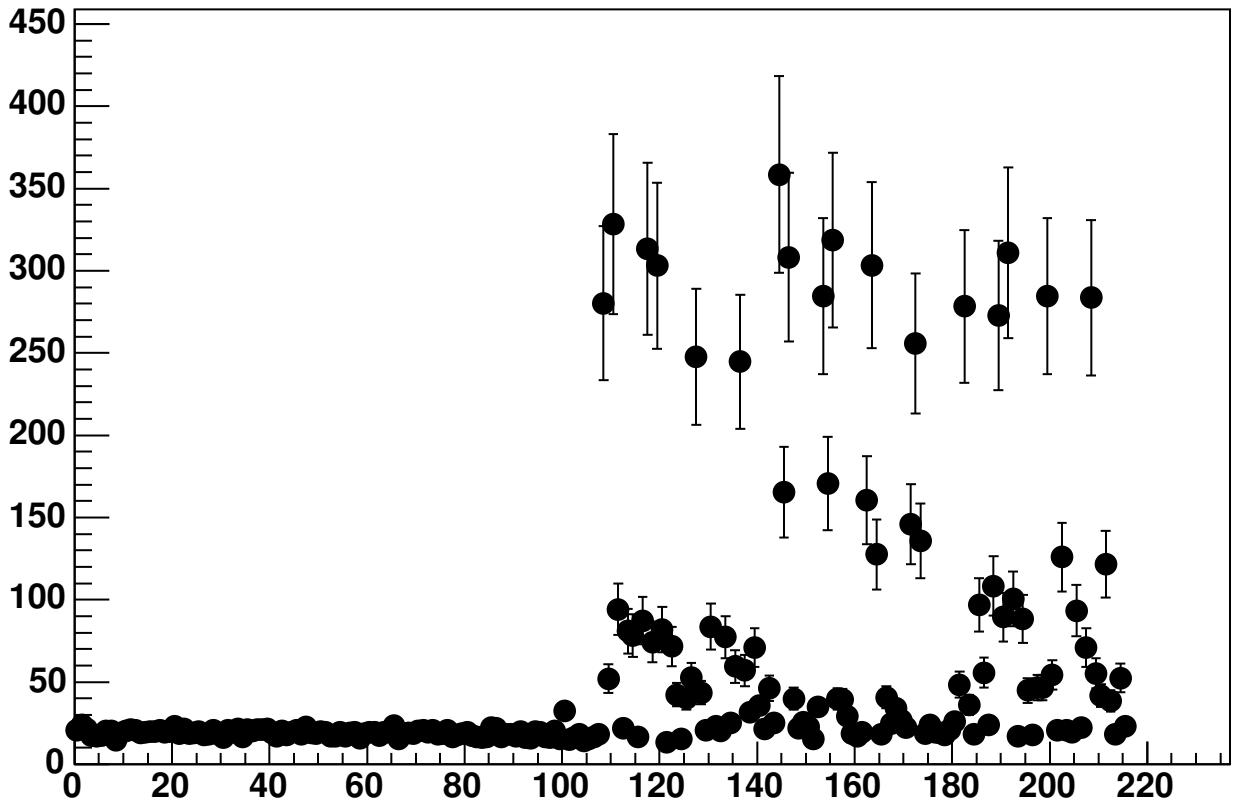
Enable 0, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



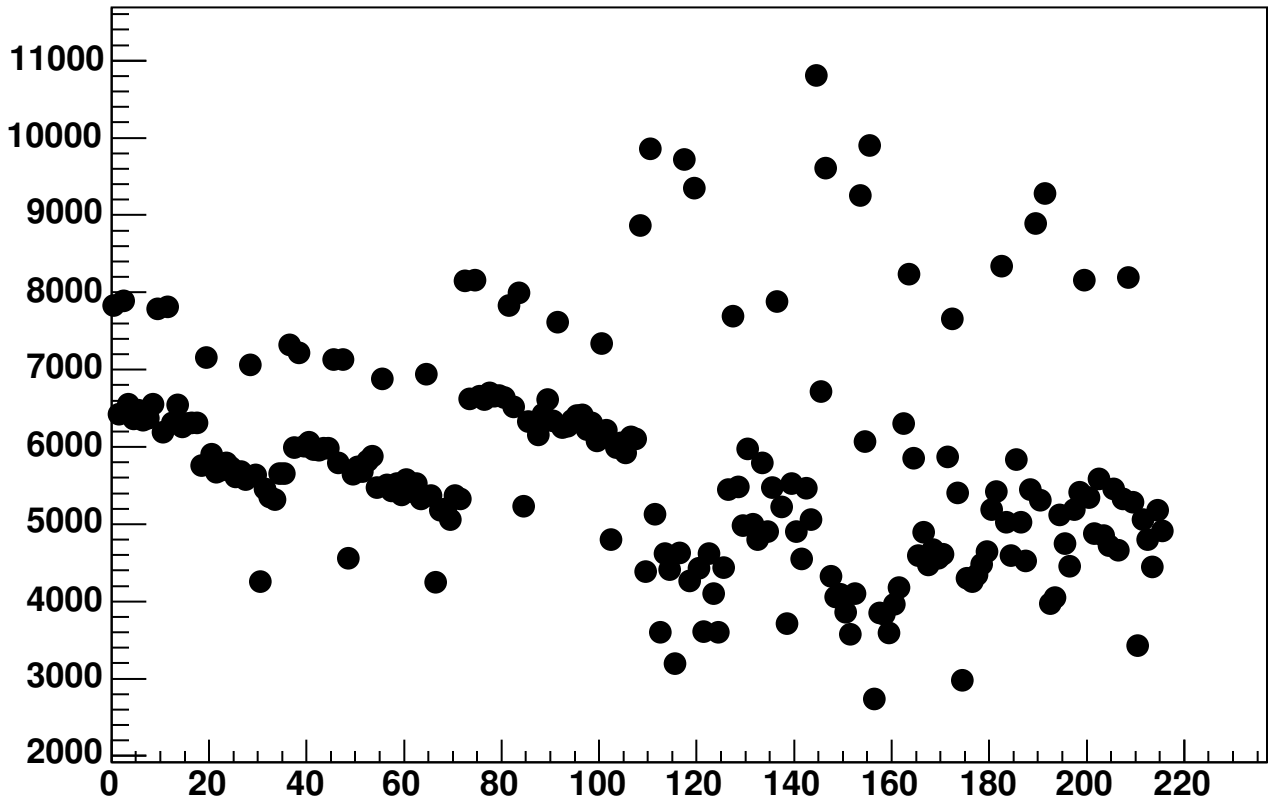
Enable 0, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



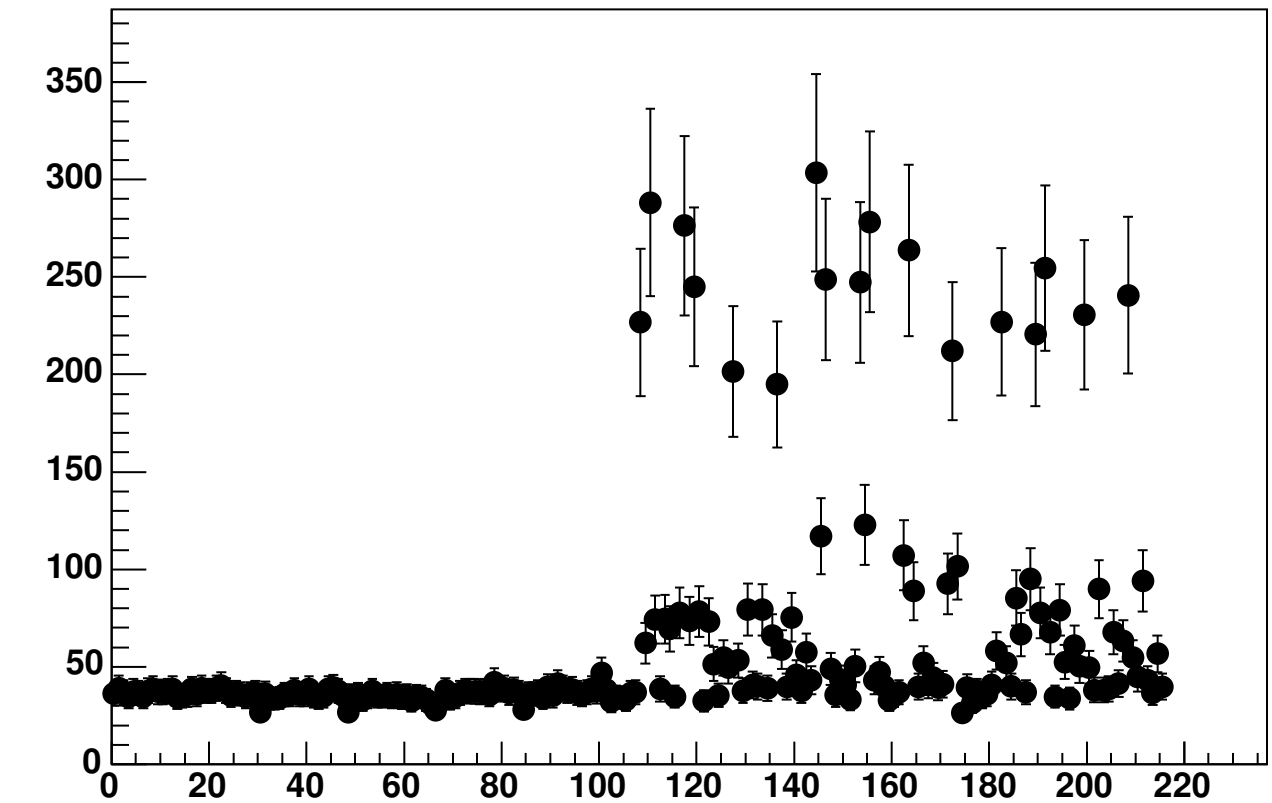
Enable 0, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



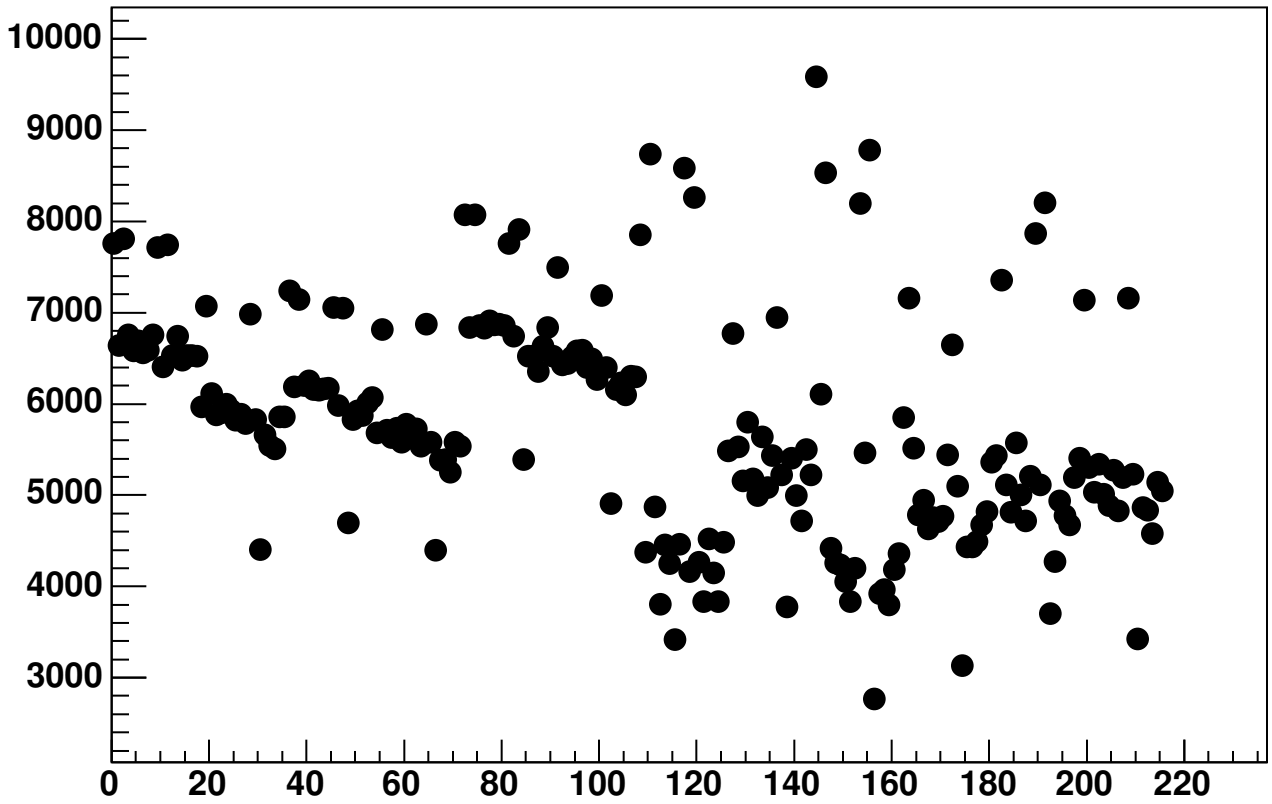
Enable 0, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



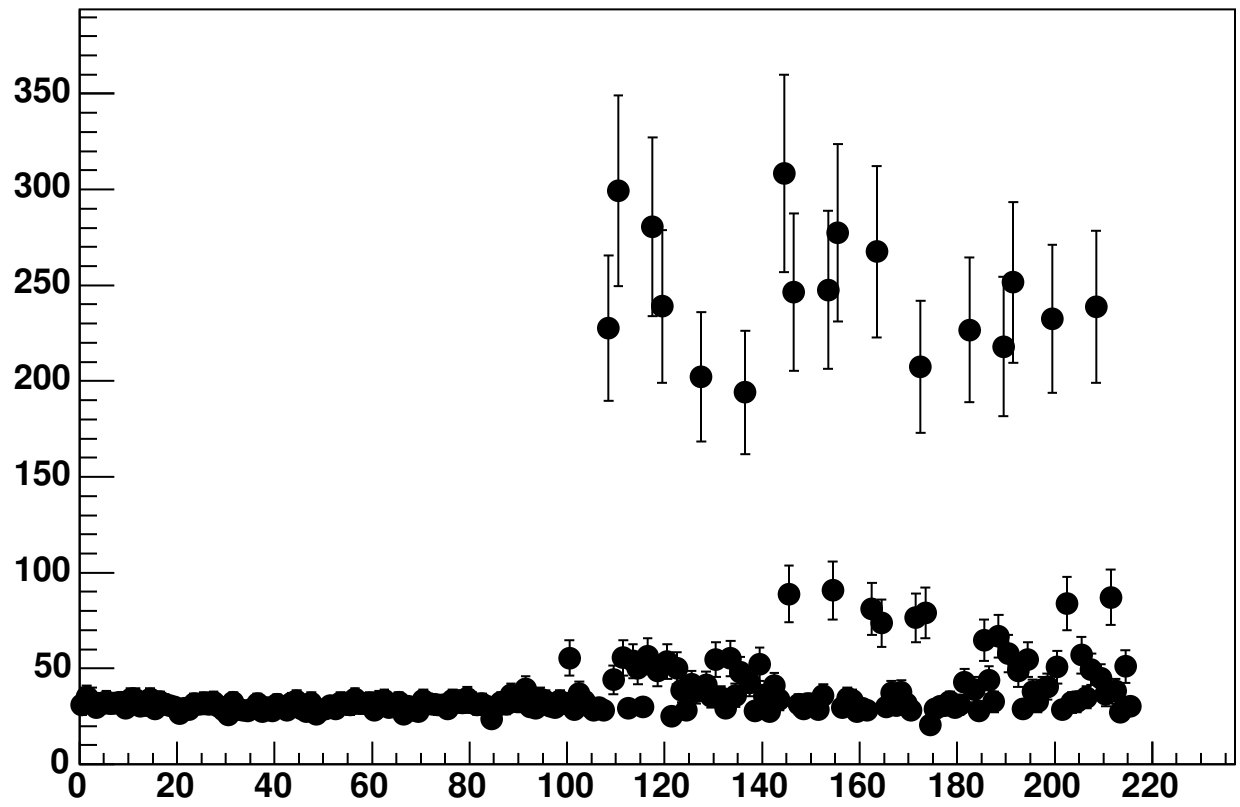
Enable 0, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



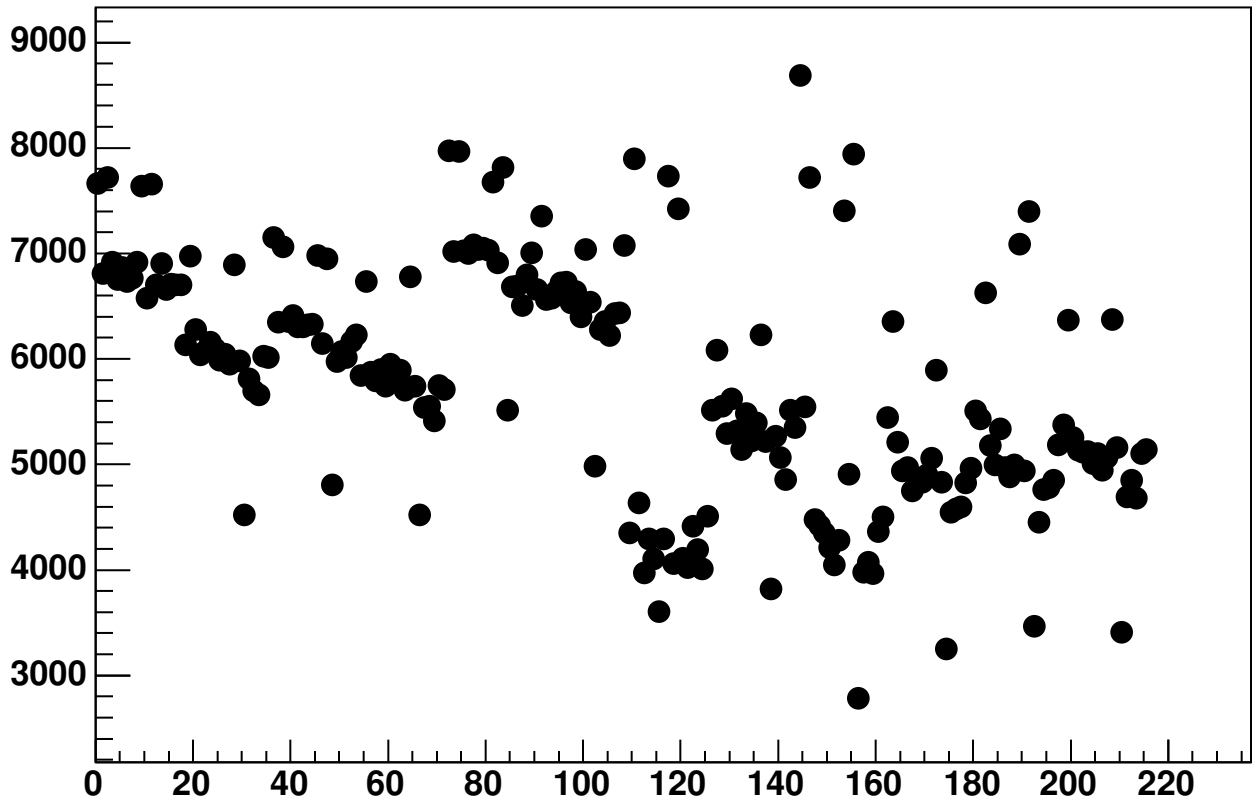
Enable 0, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



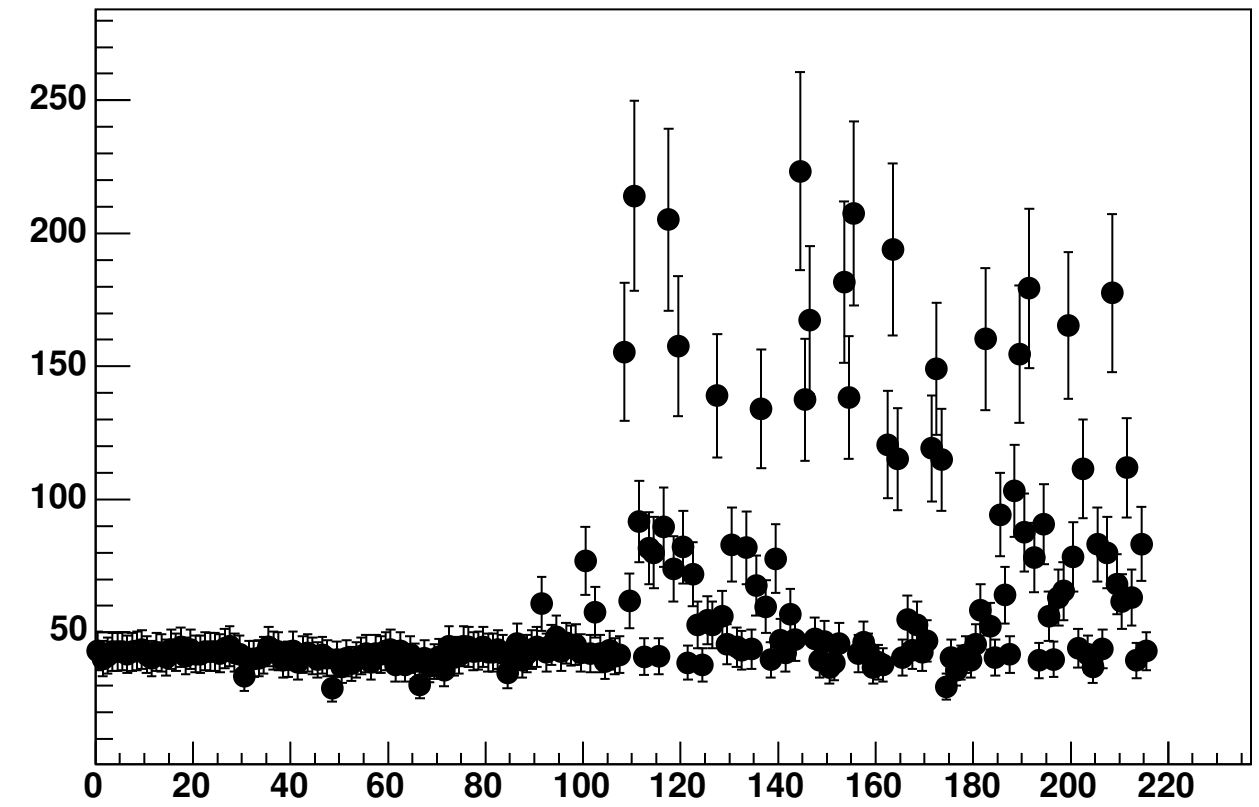
Enable 0, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



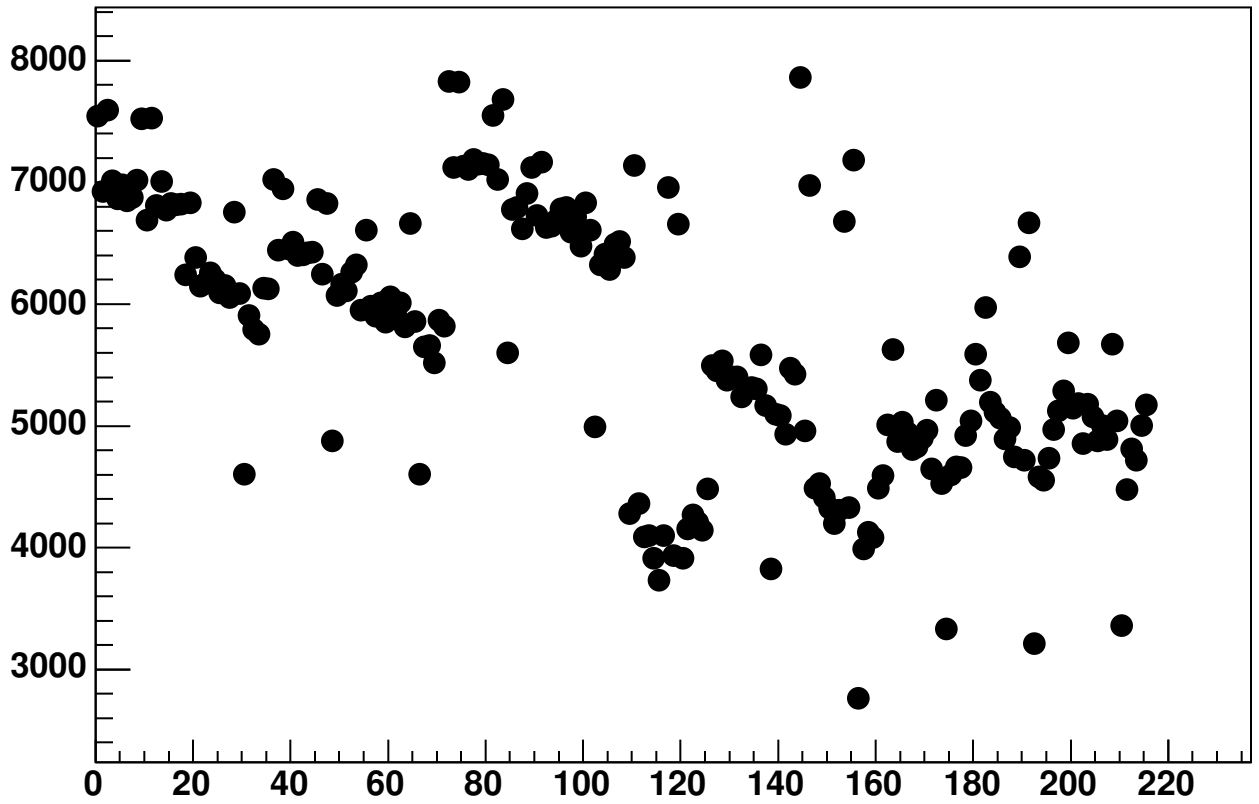
Enable 0, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



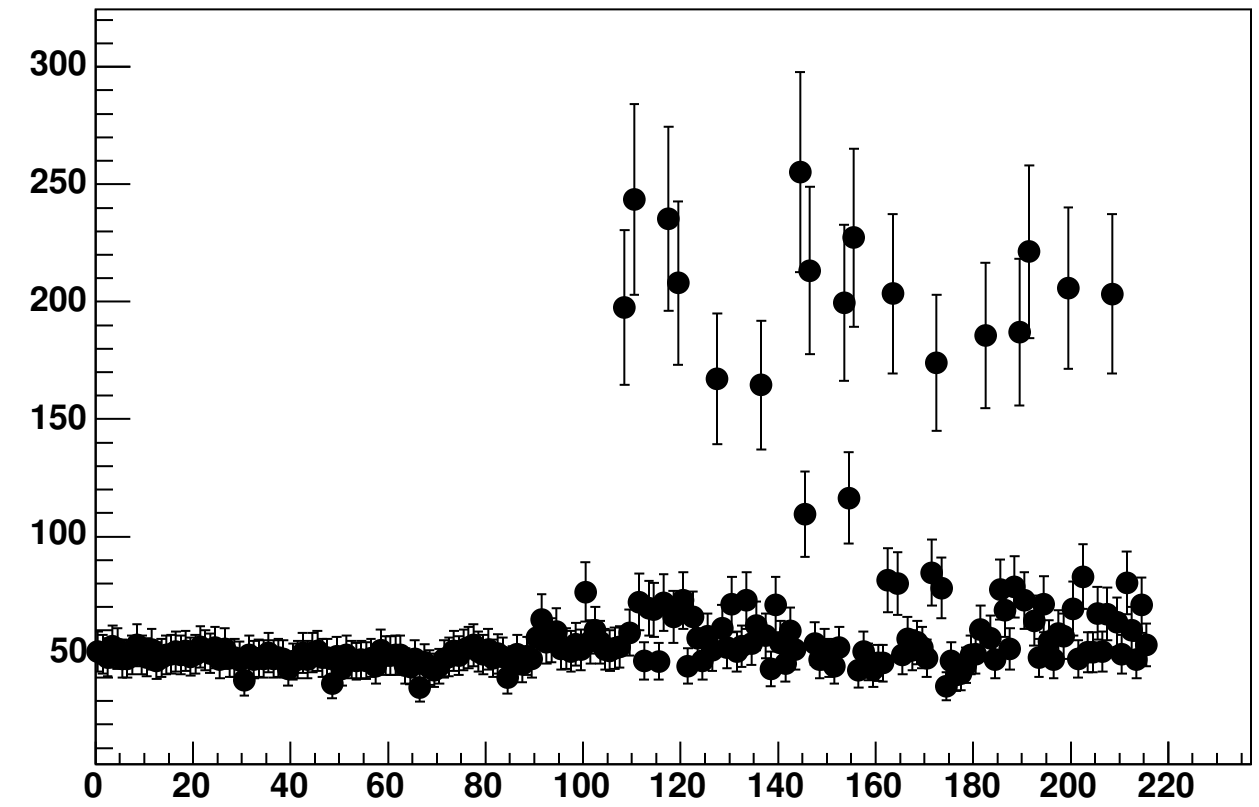
Enable 0, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



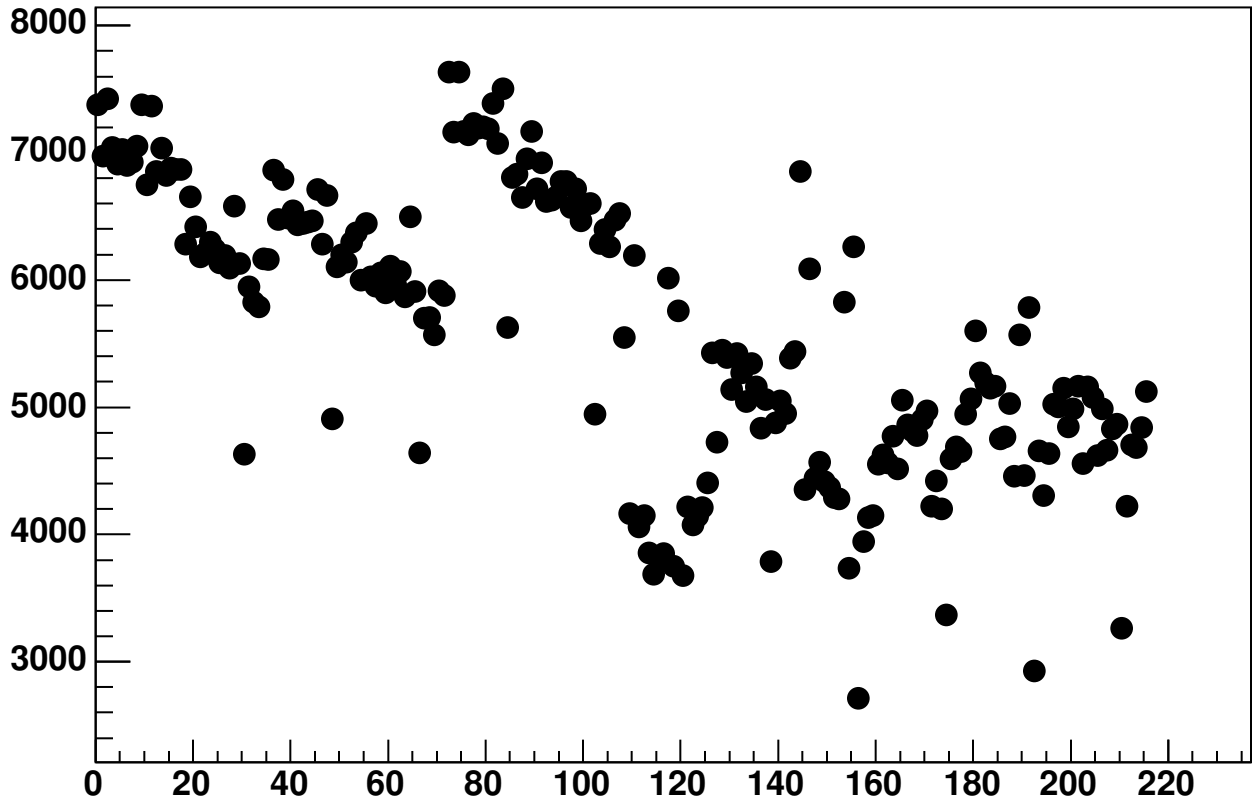
Enable 0, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



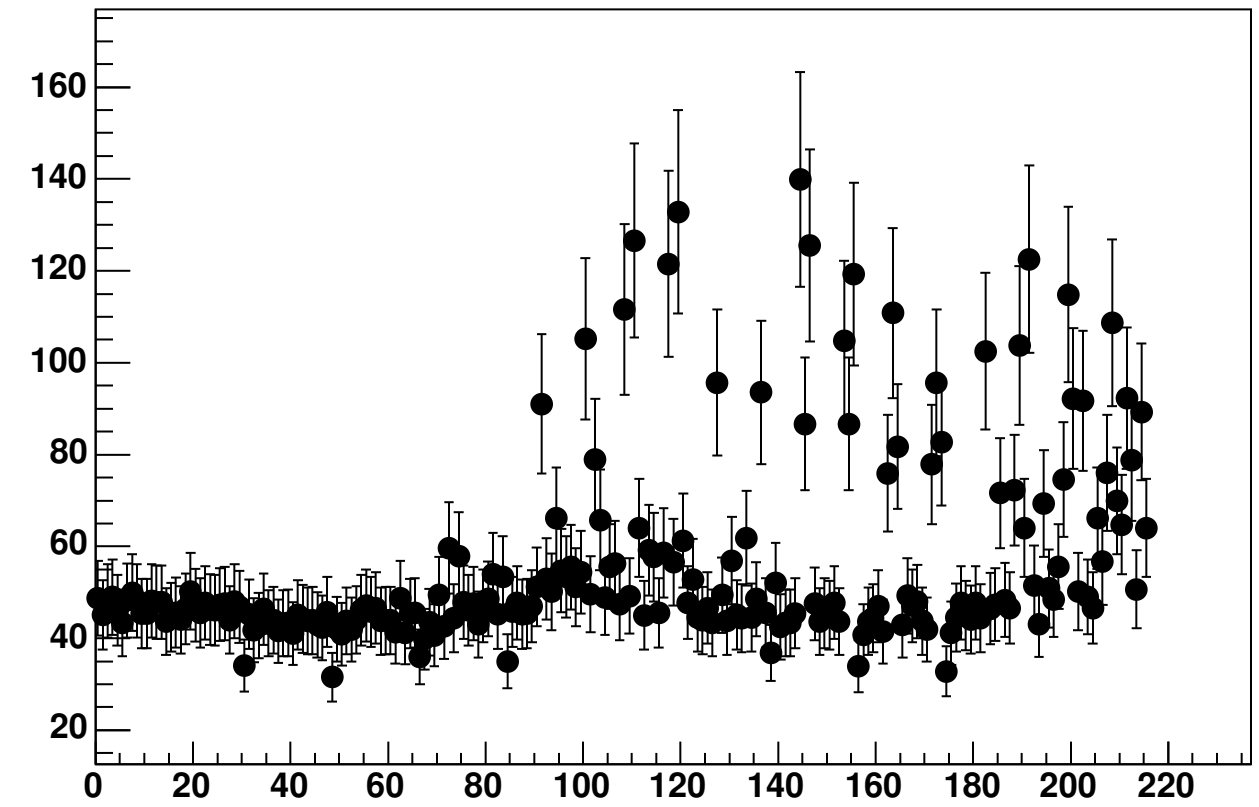
Enable 0, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



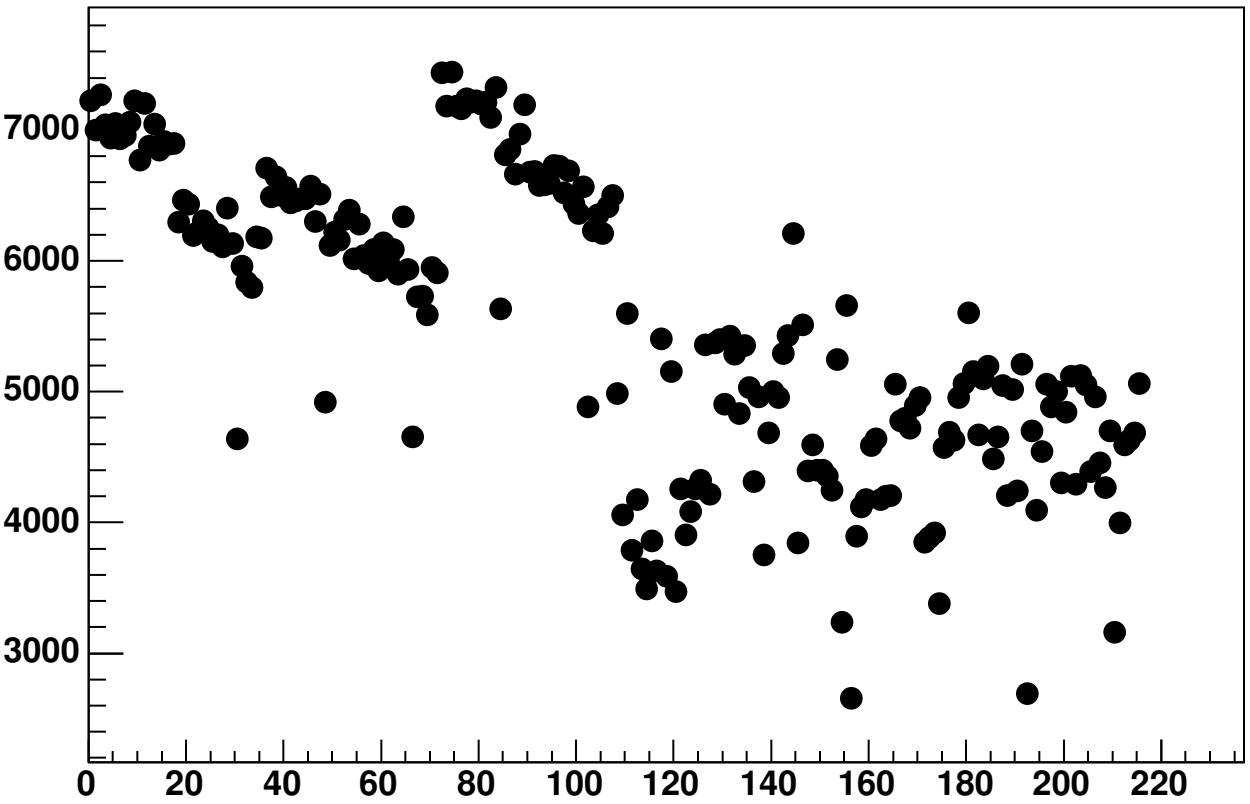
Enable 0, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



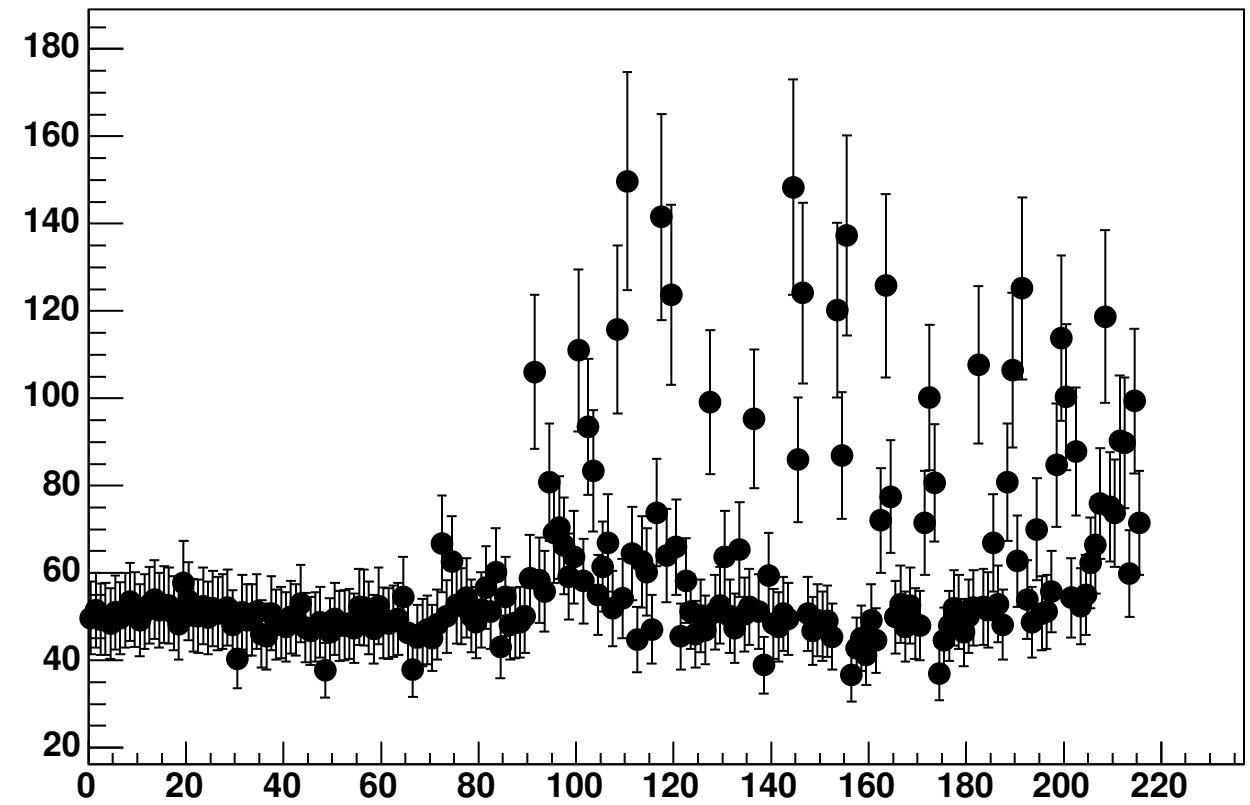
Enable 0, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



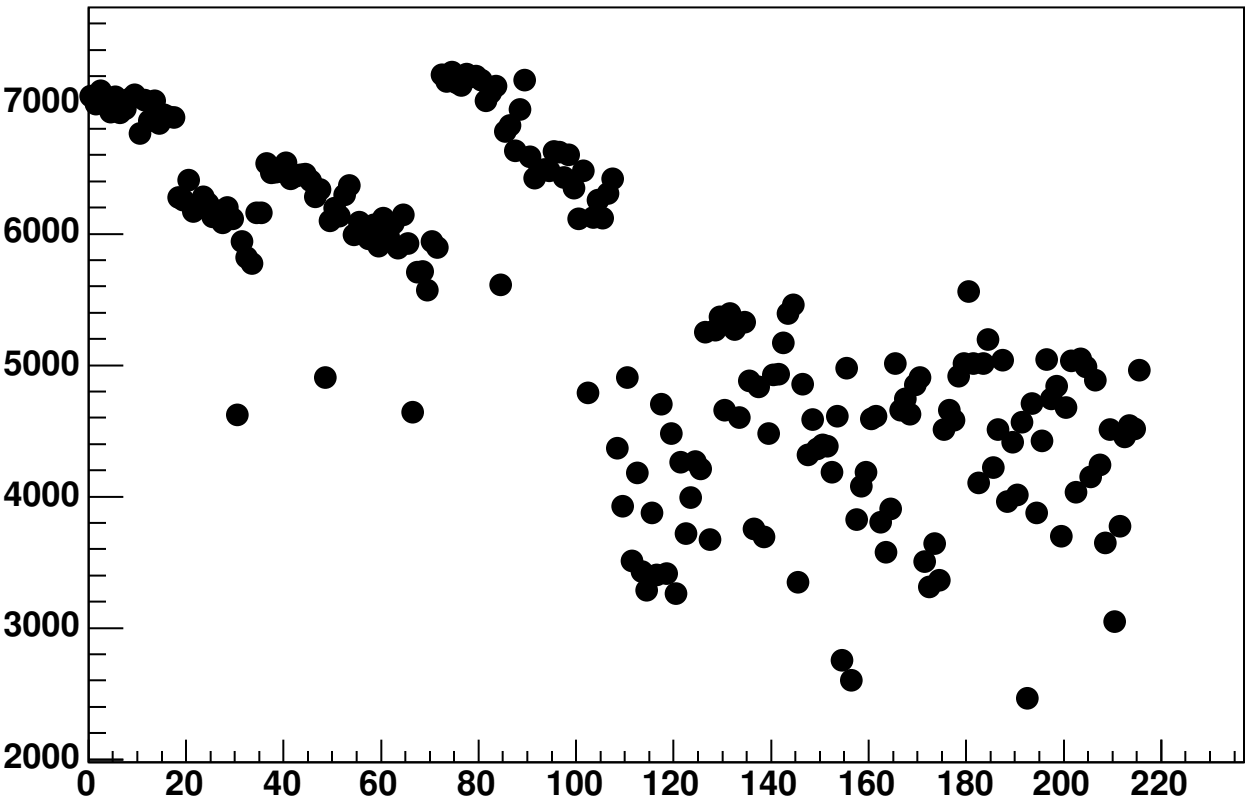
Enable 0, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



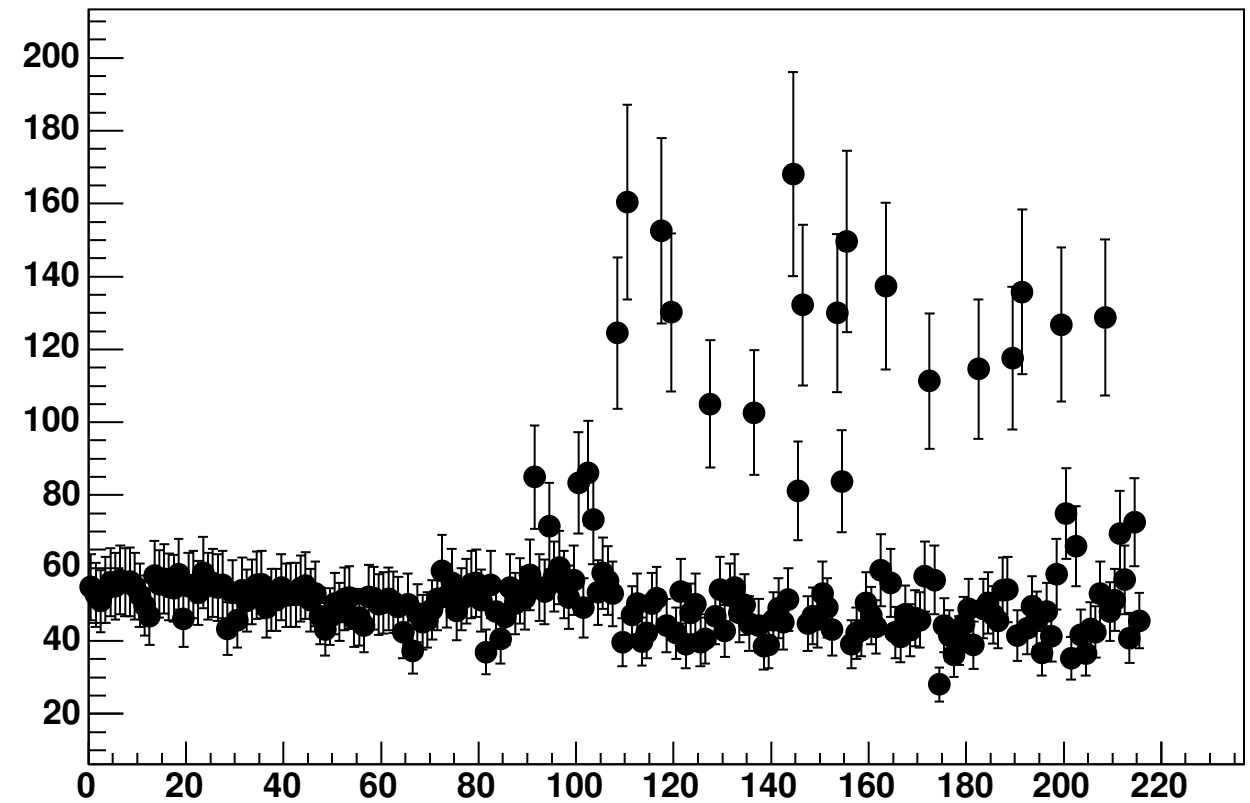
Enable 0, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



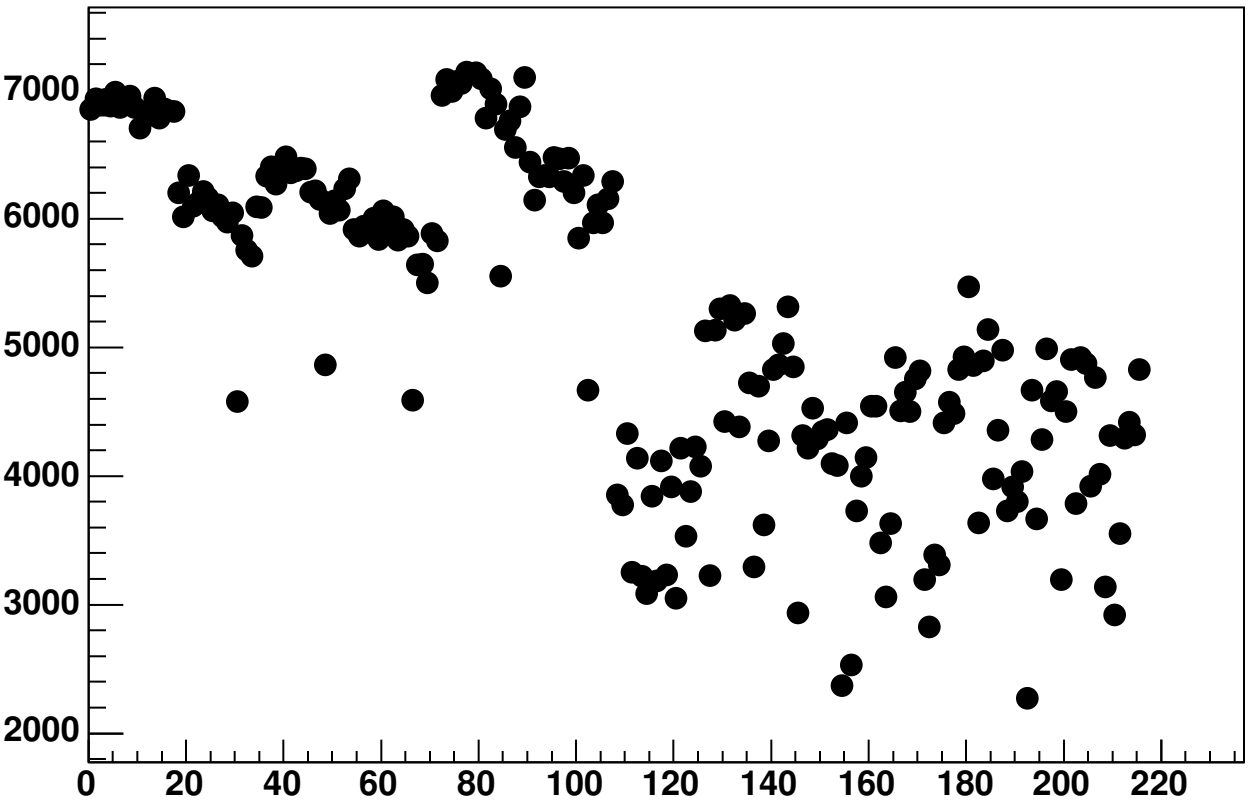
Enable 0, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



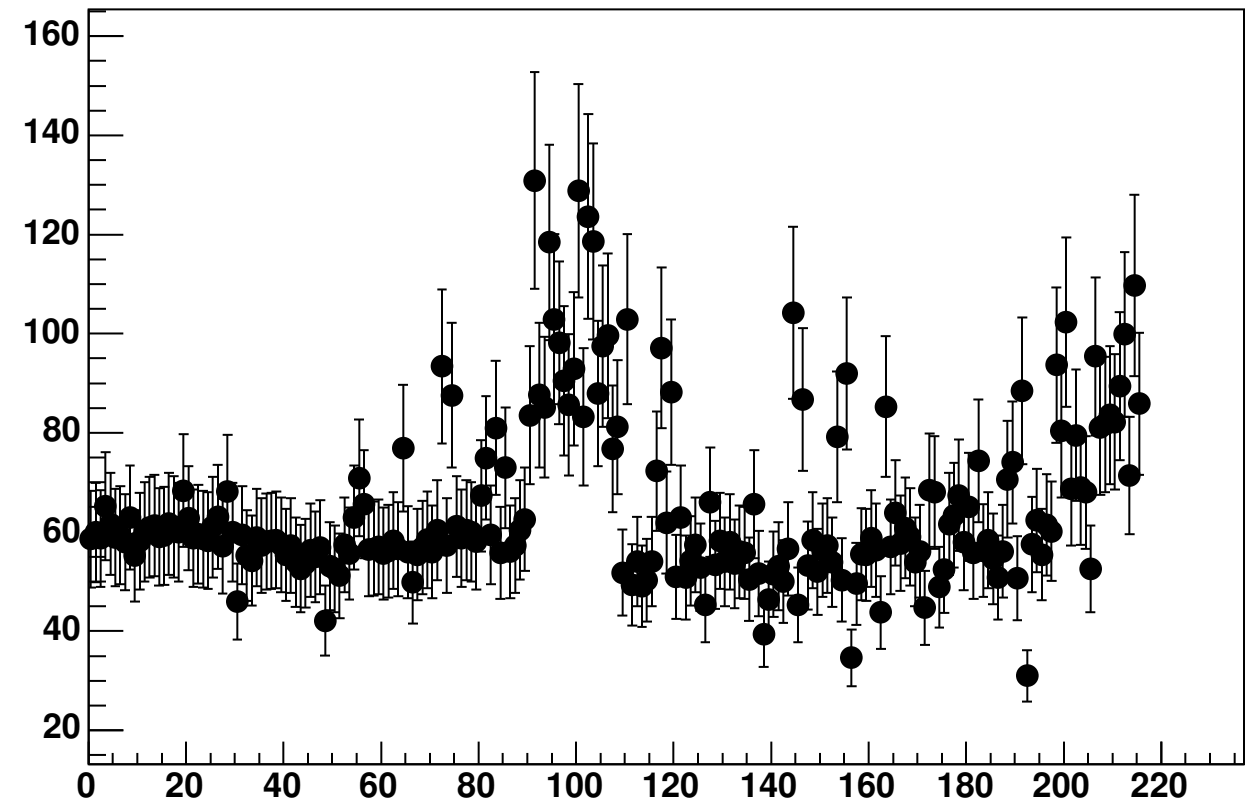
Enable 0, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



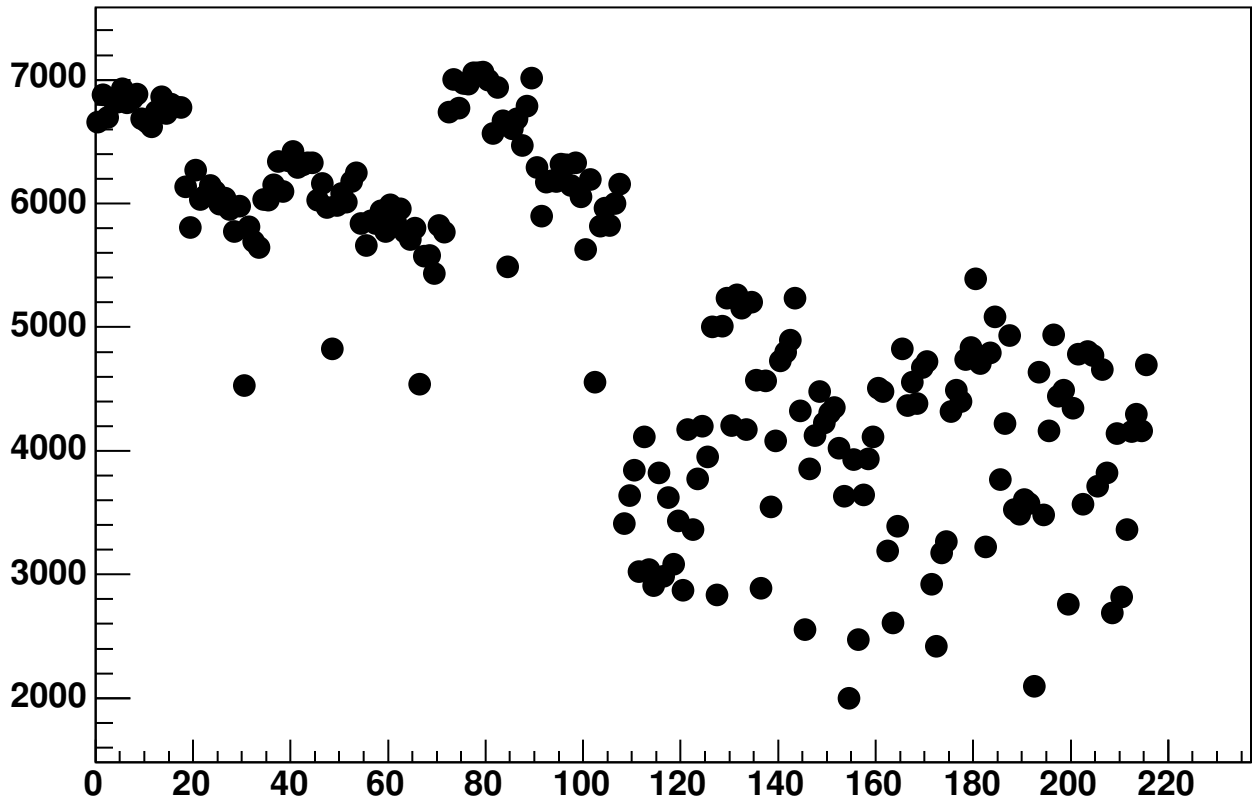
Enable 0, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



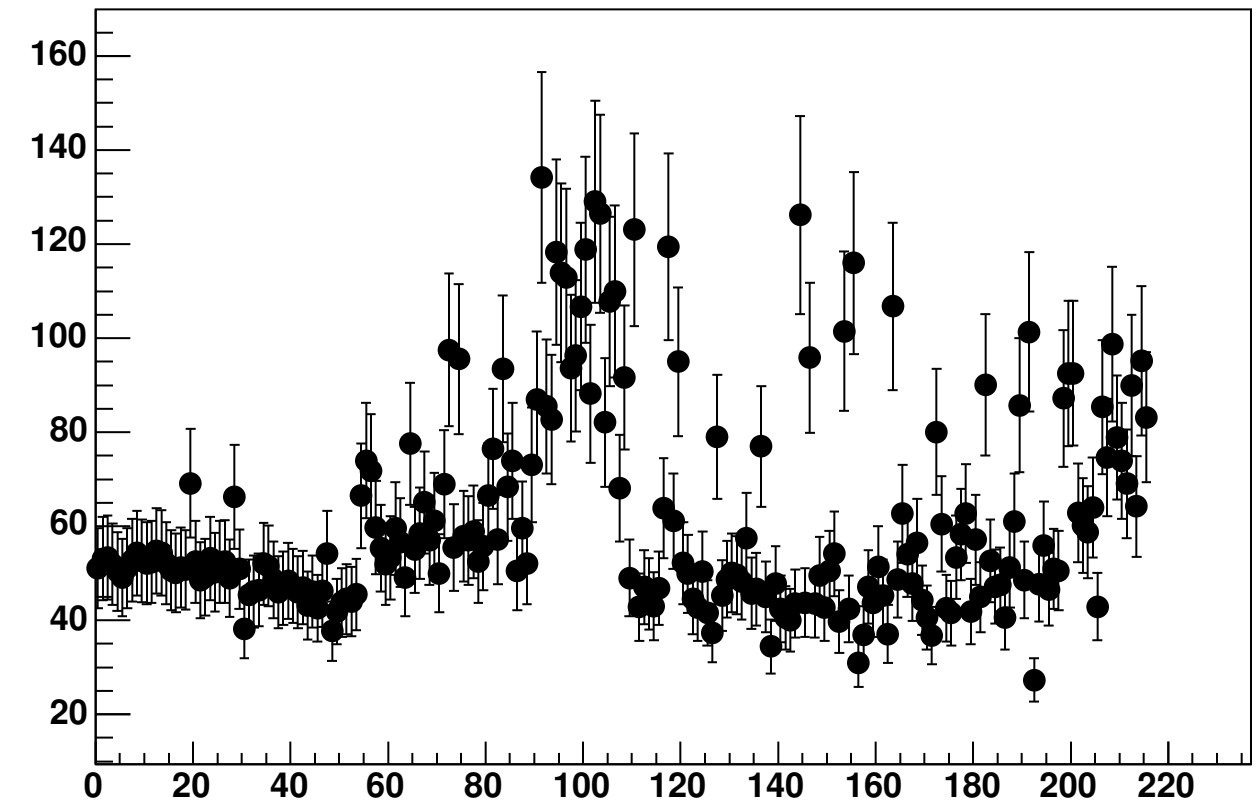
Enable 0, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



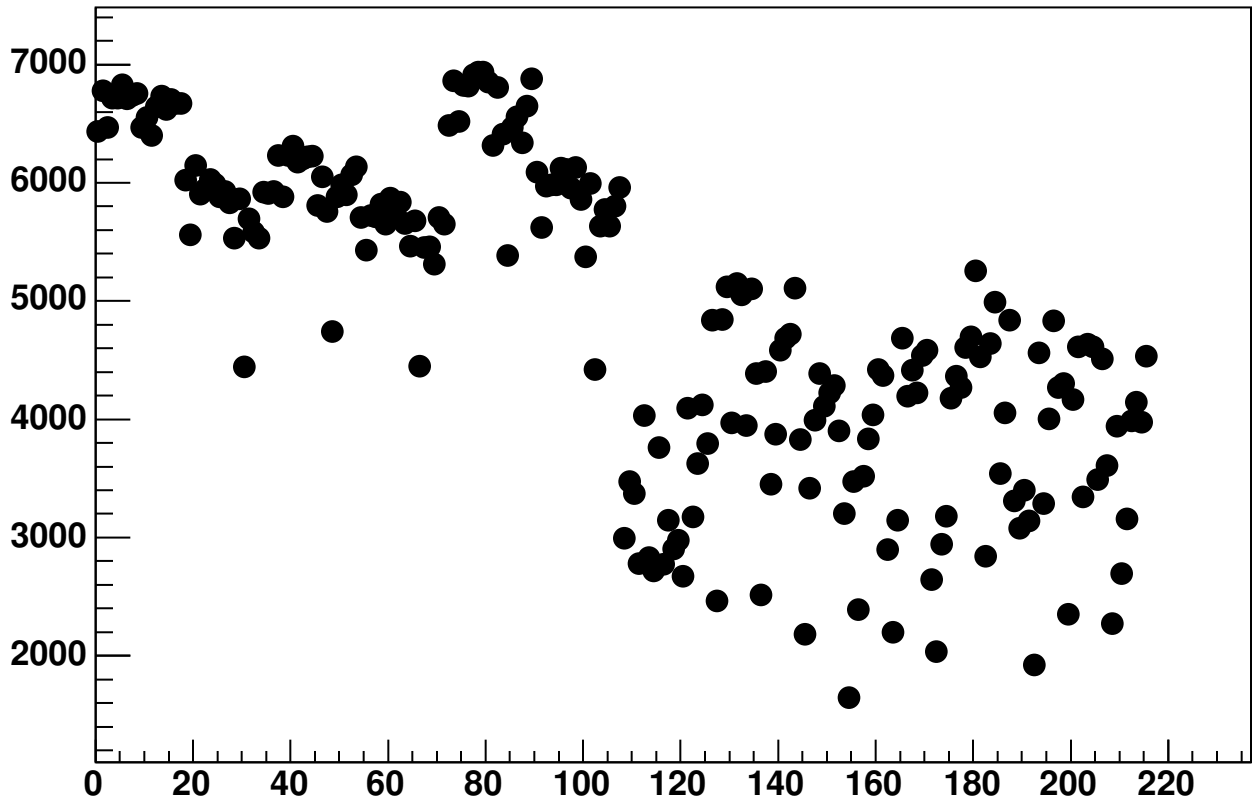
Enable 0, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



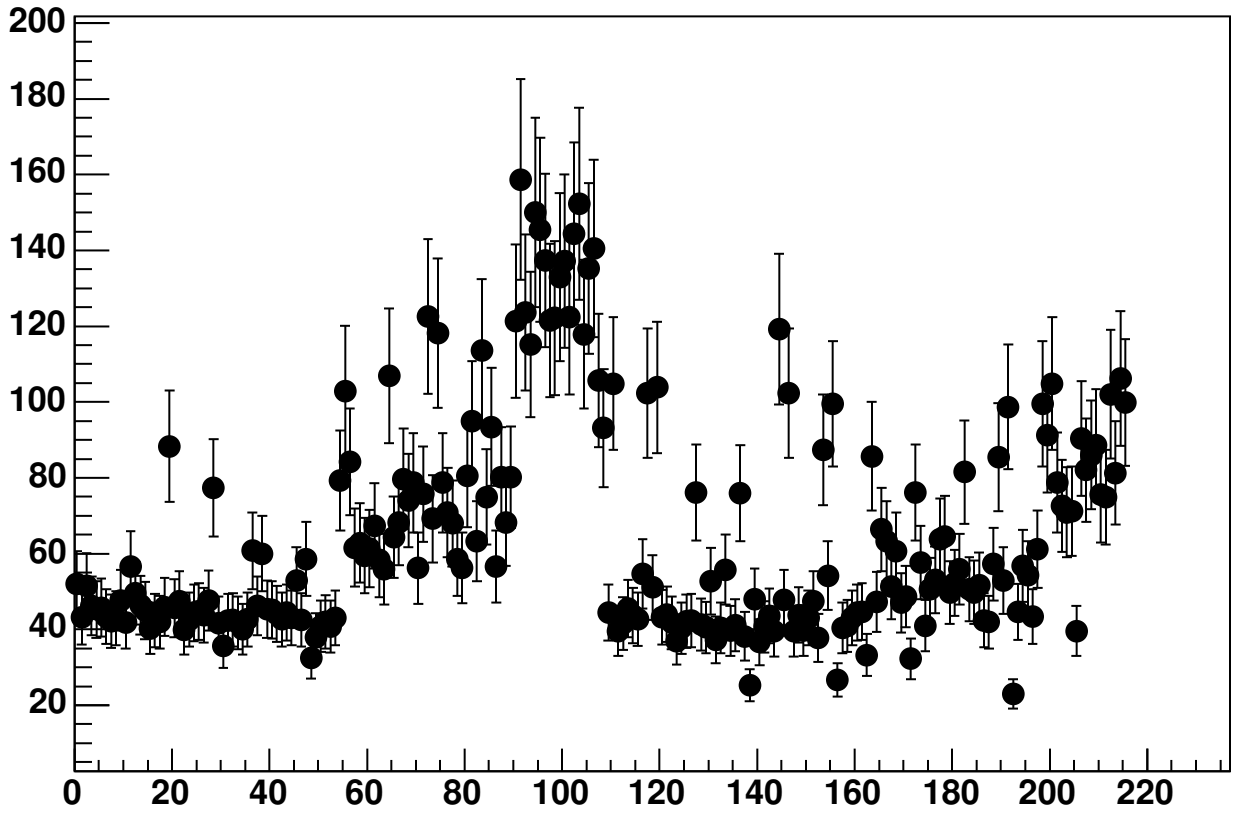
Enable 0, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



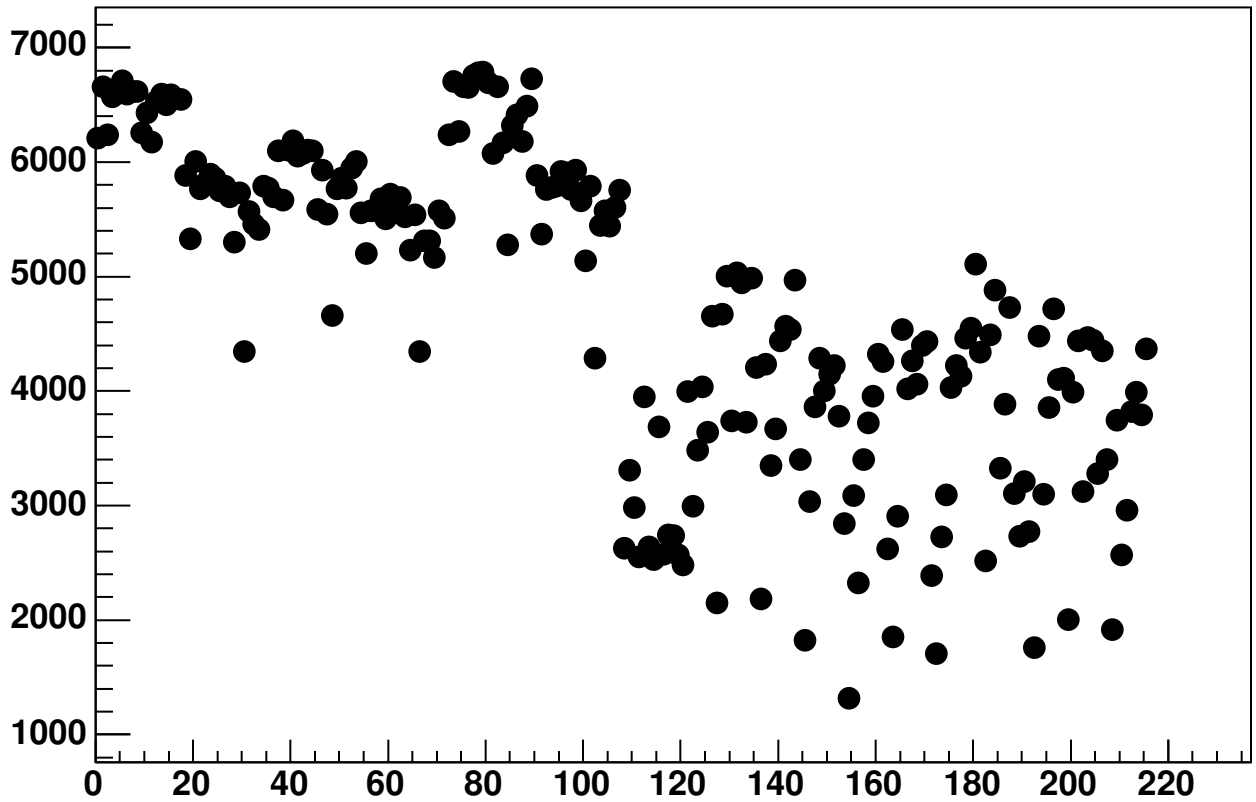
Enable 0, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



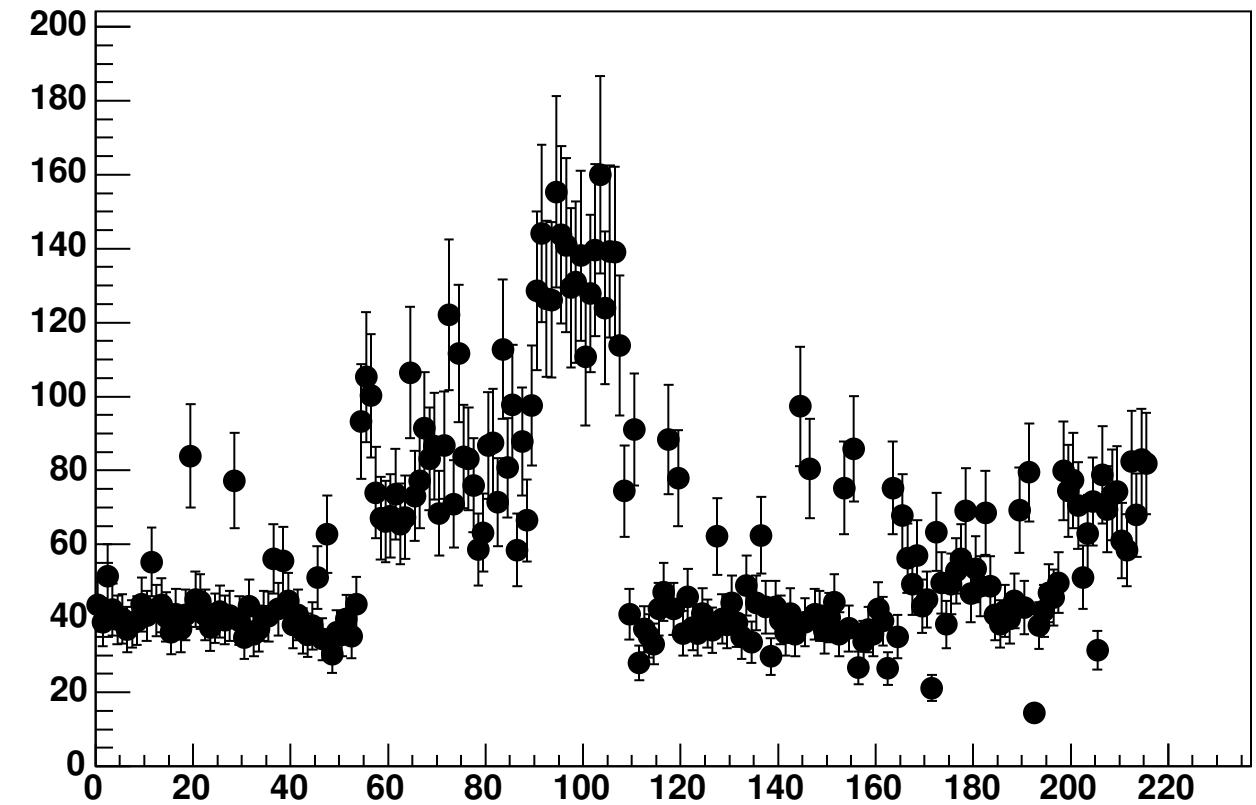
Enable 0, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



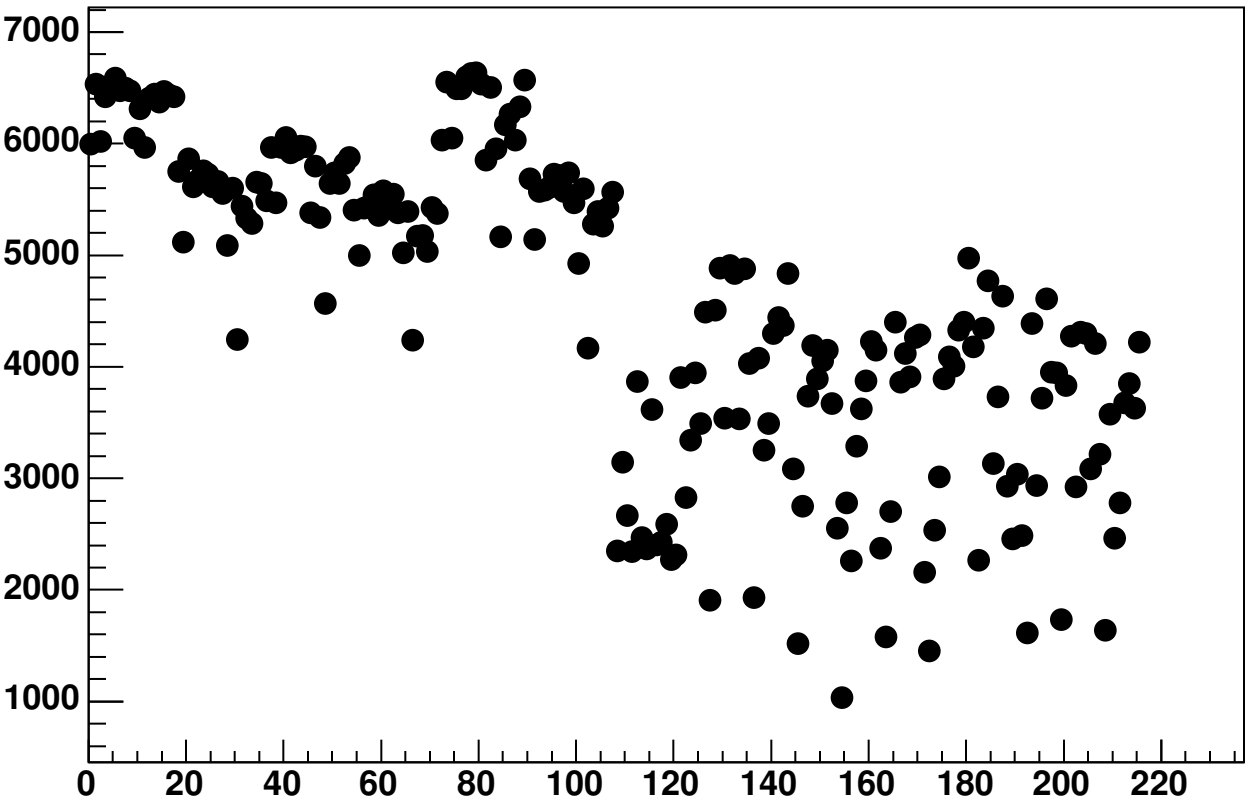
Enable 0, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



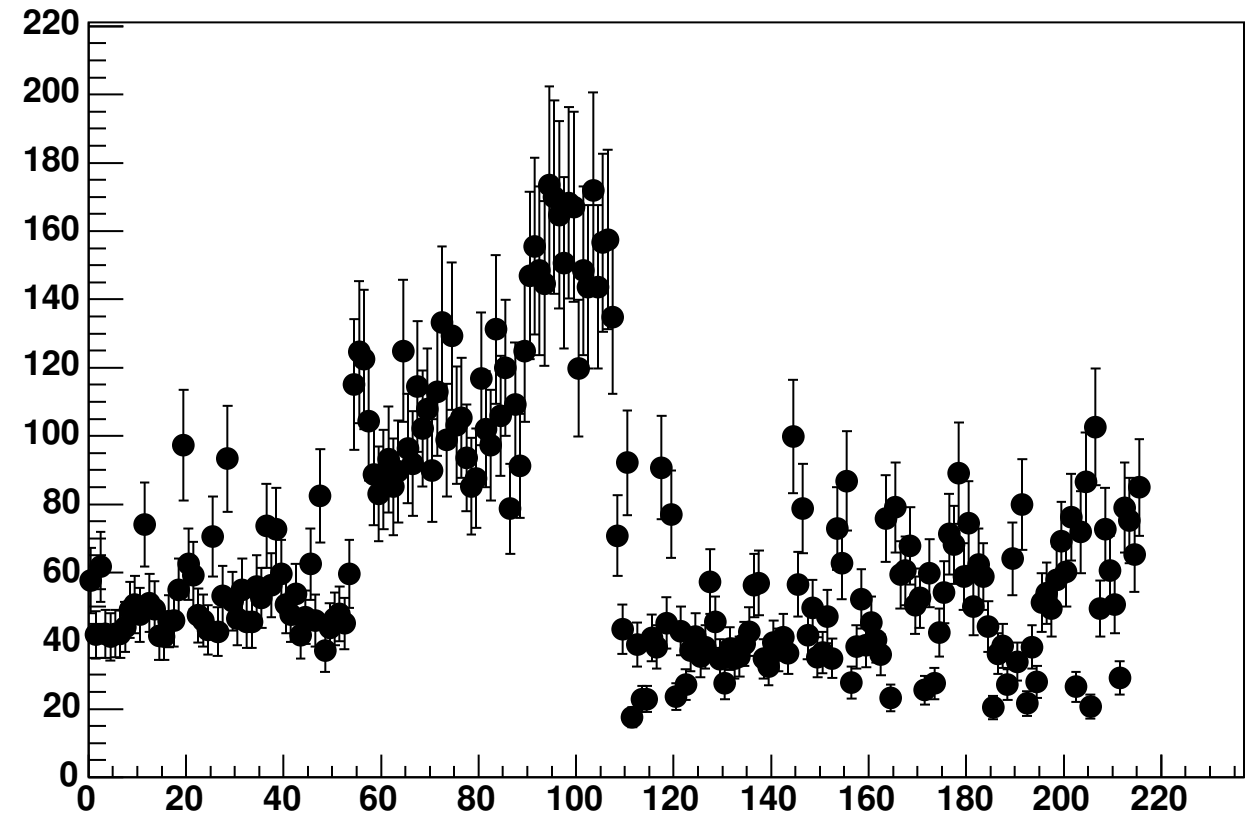
Enable 0, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



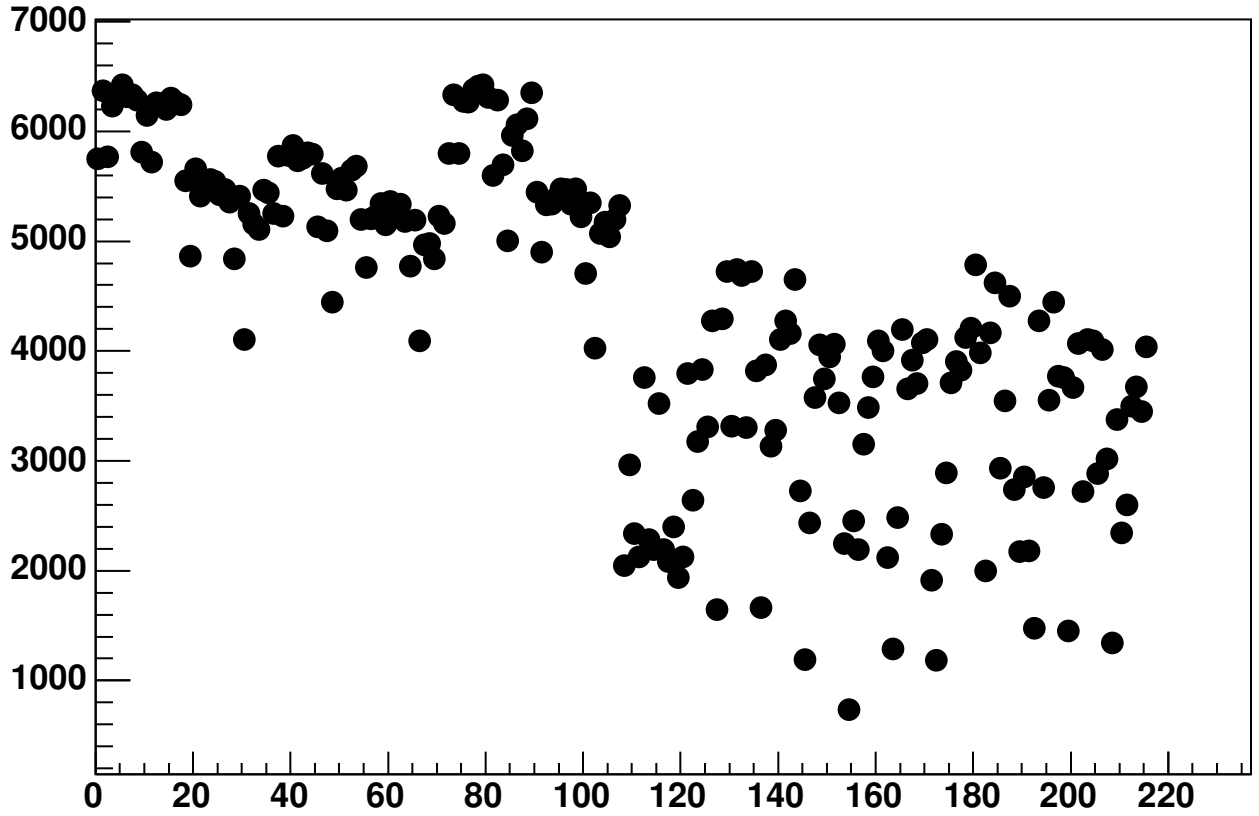
Enable 0, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



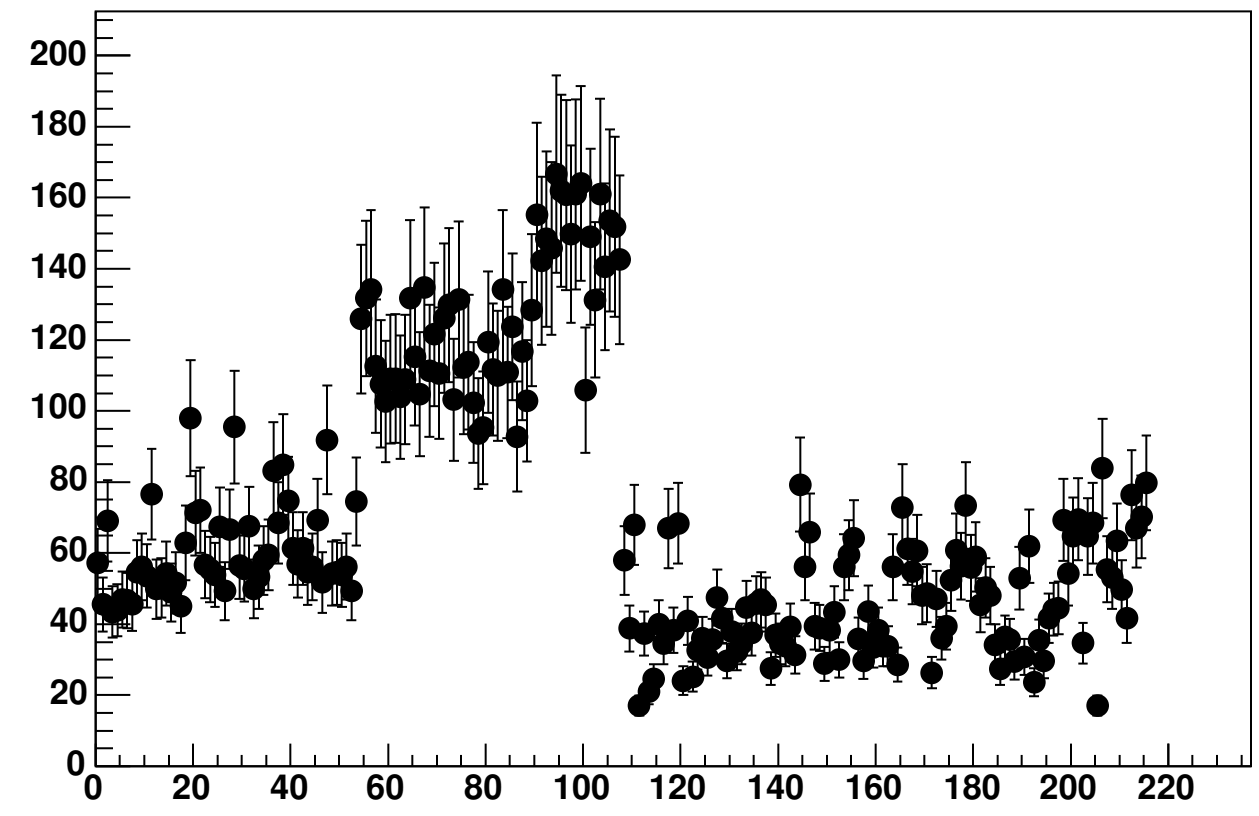
Enable 0, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



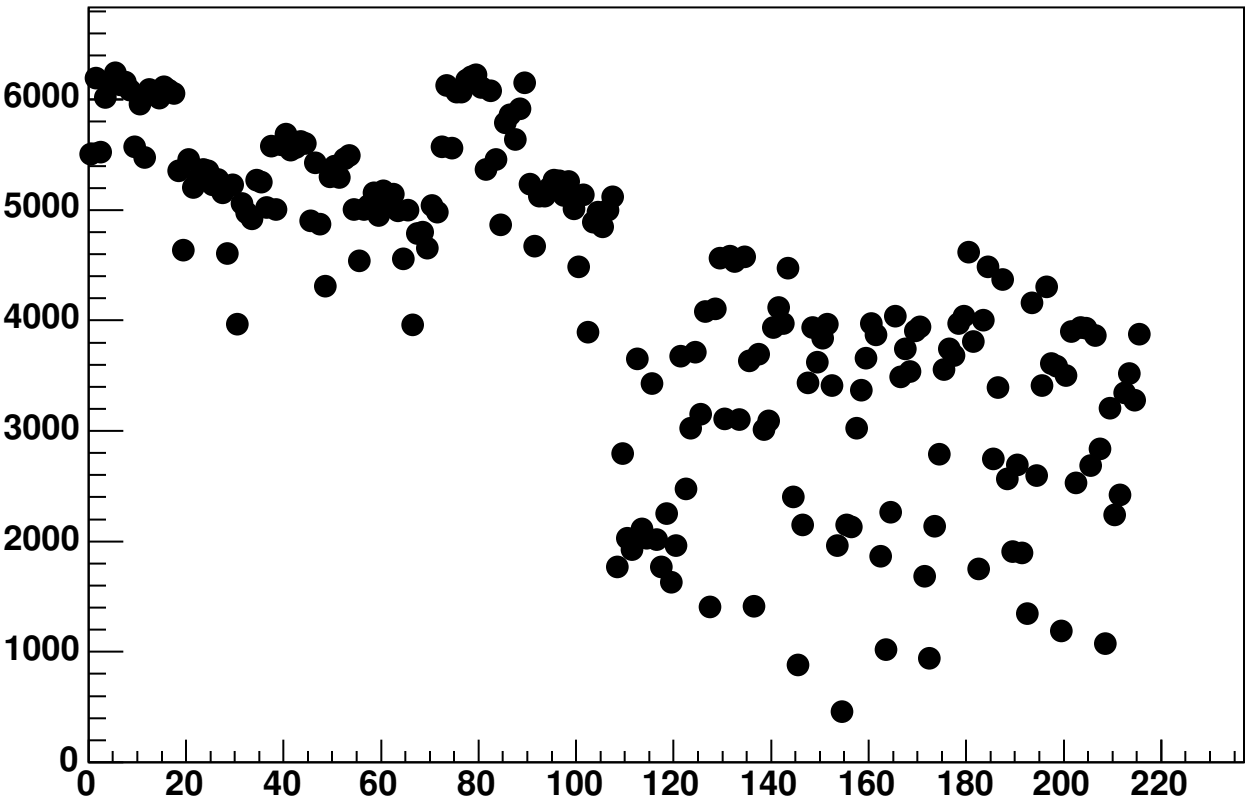
Enable 0, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



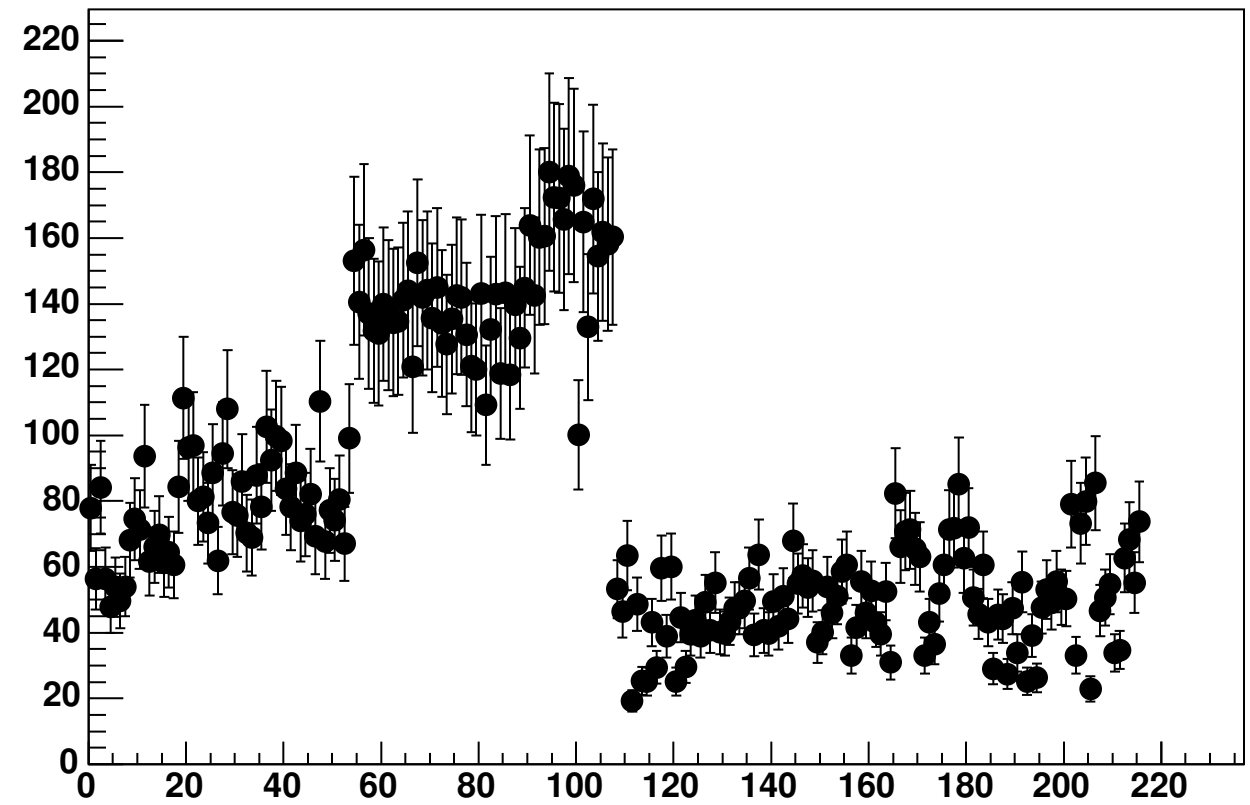
Enable 0, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



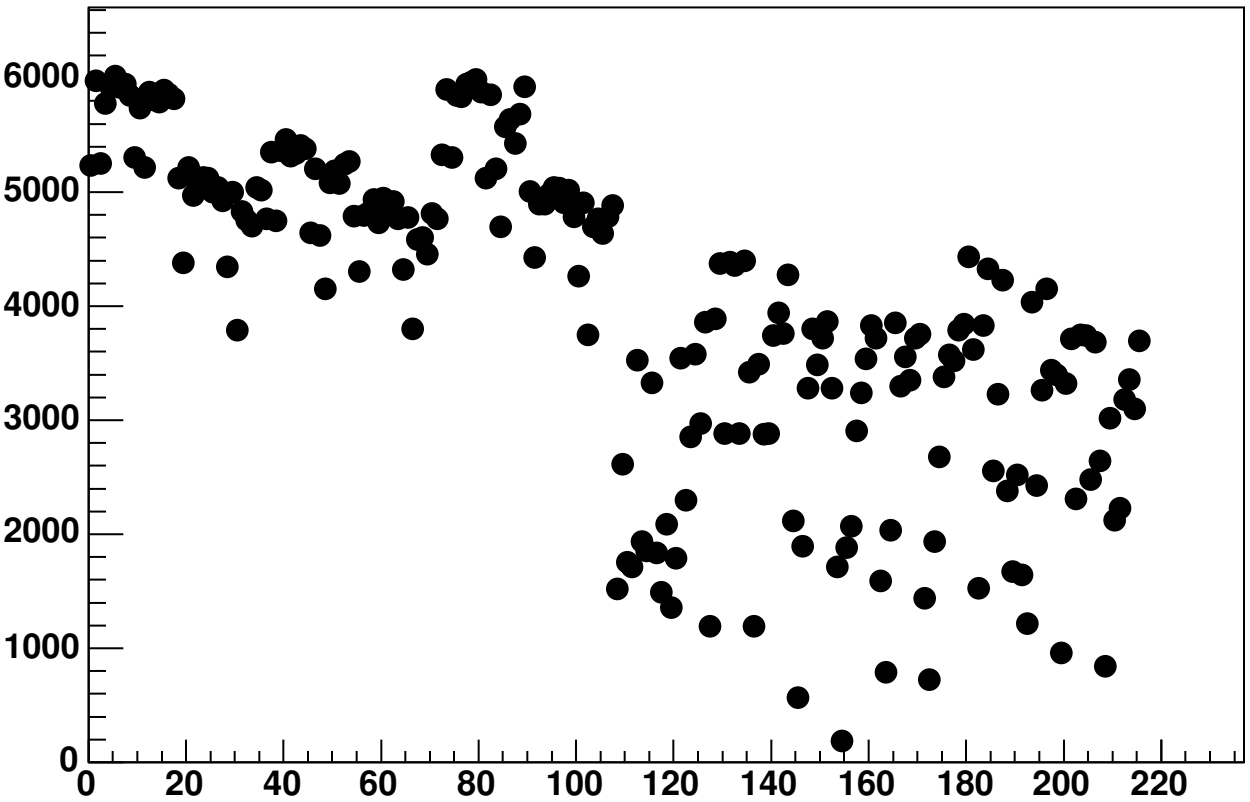
Enable 0, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



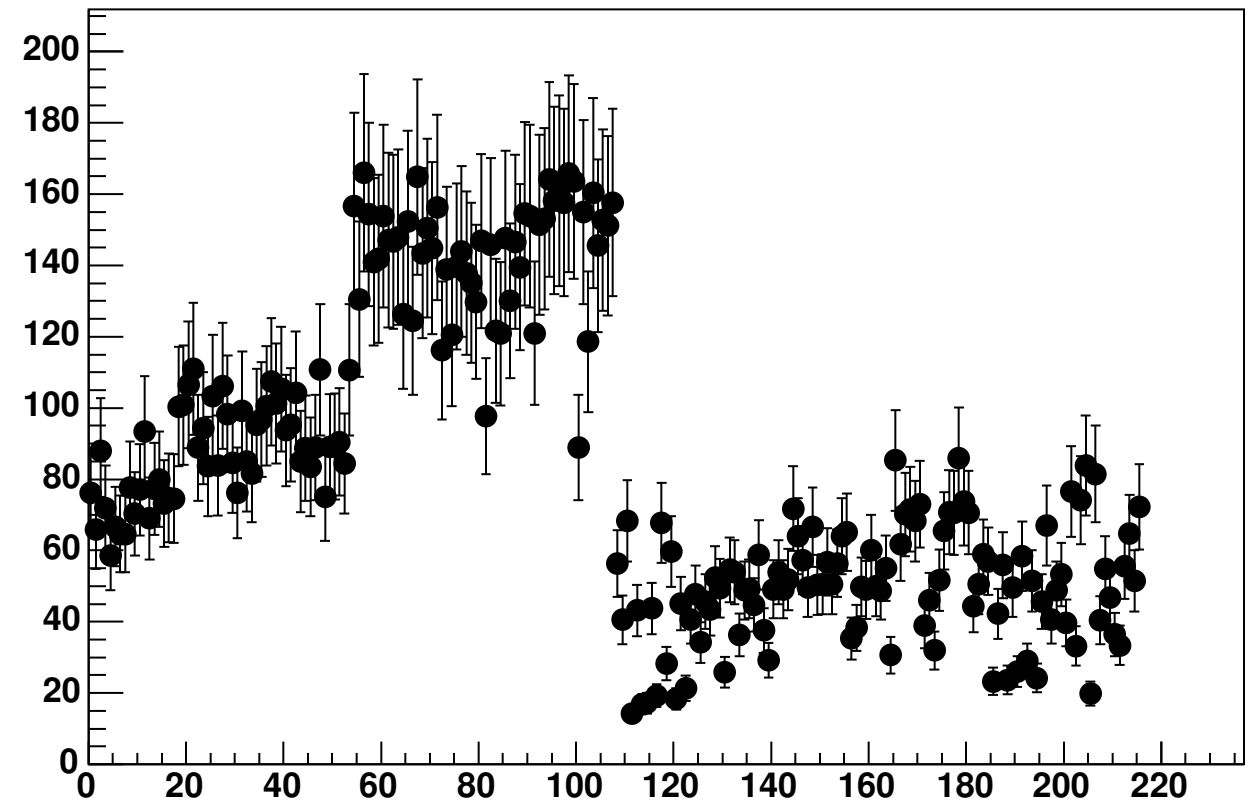
Enable 0, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



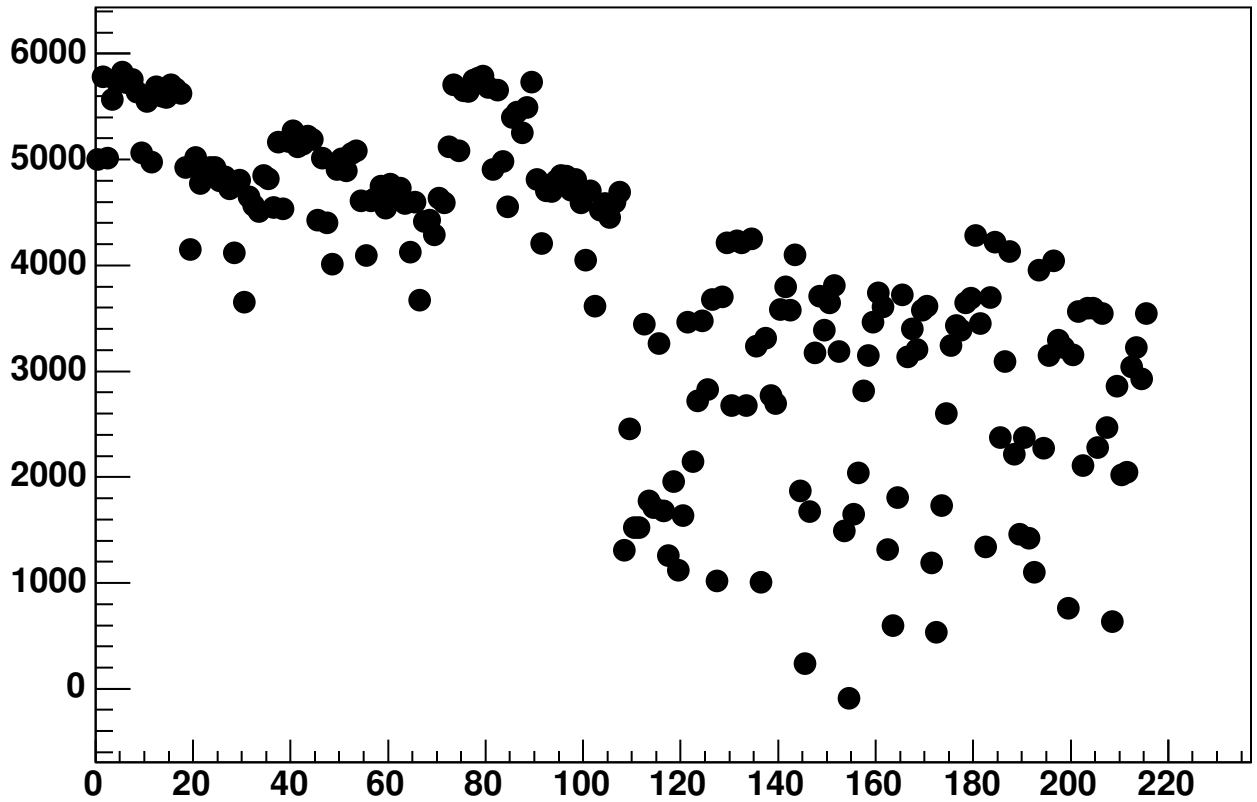
Enable 0, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



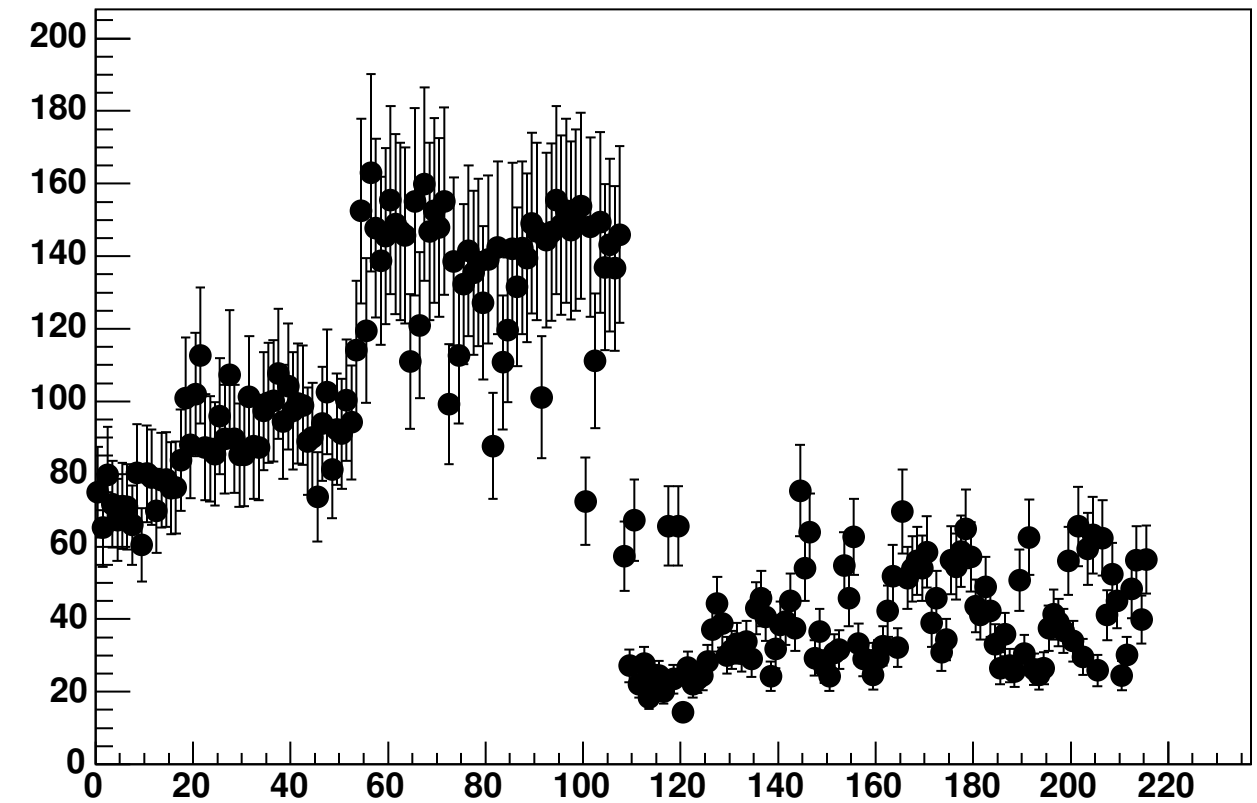
Enable 0, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



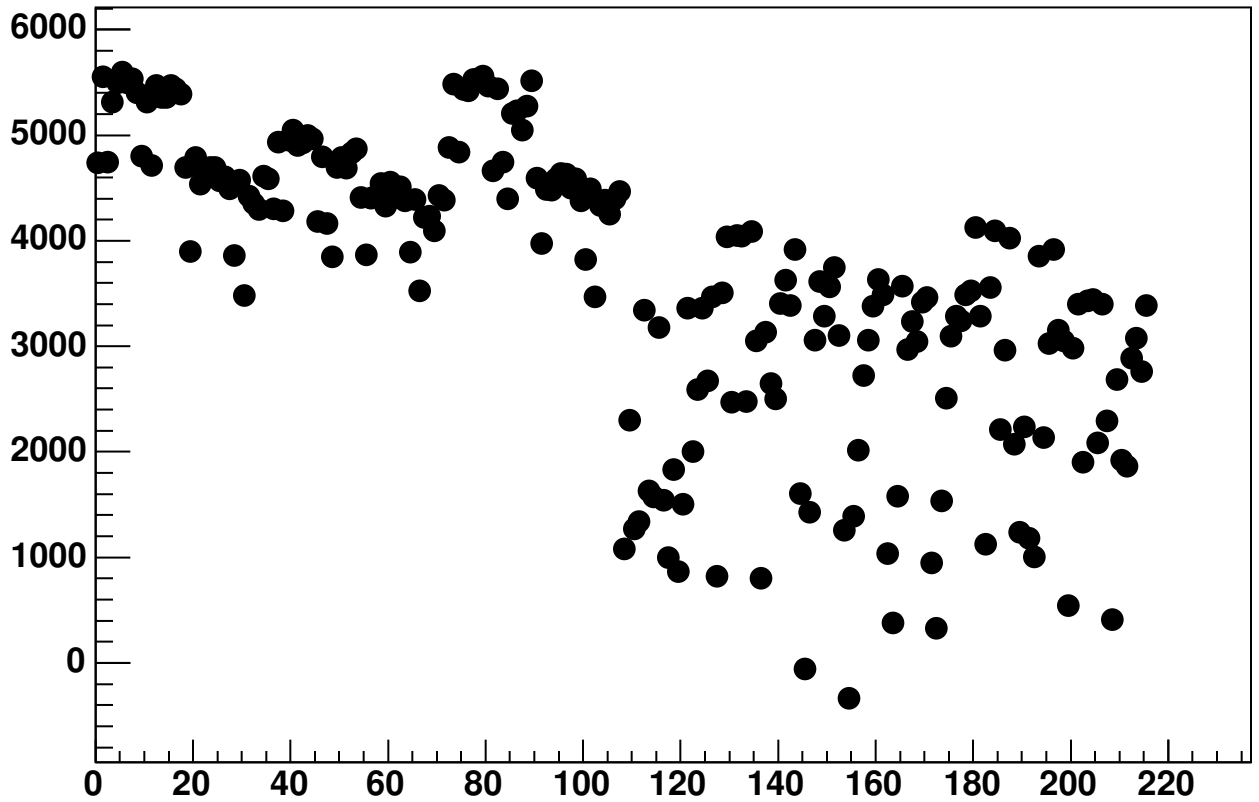
Enable 0, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



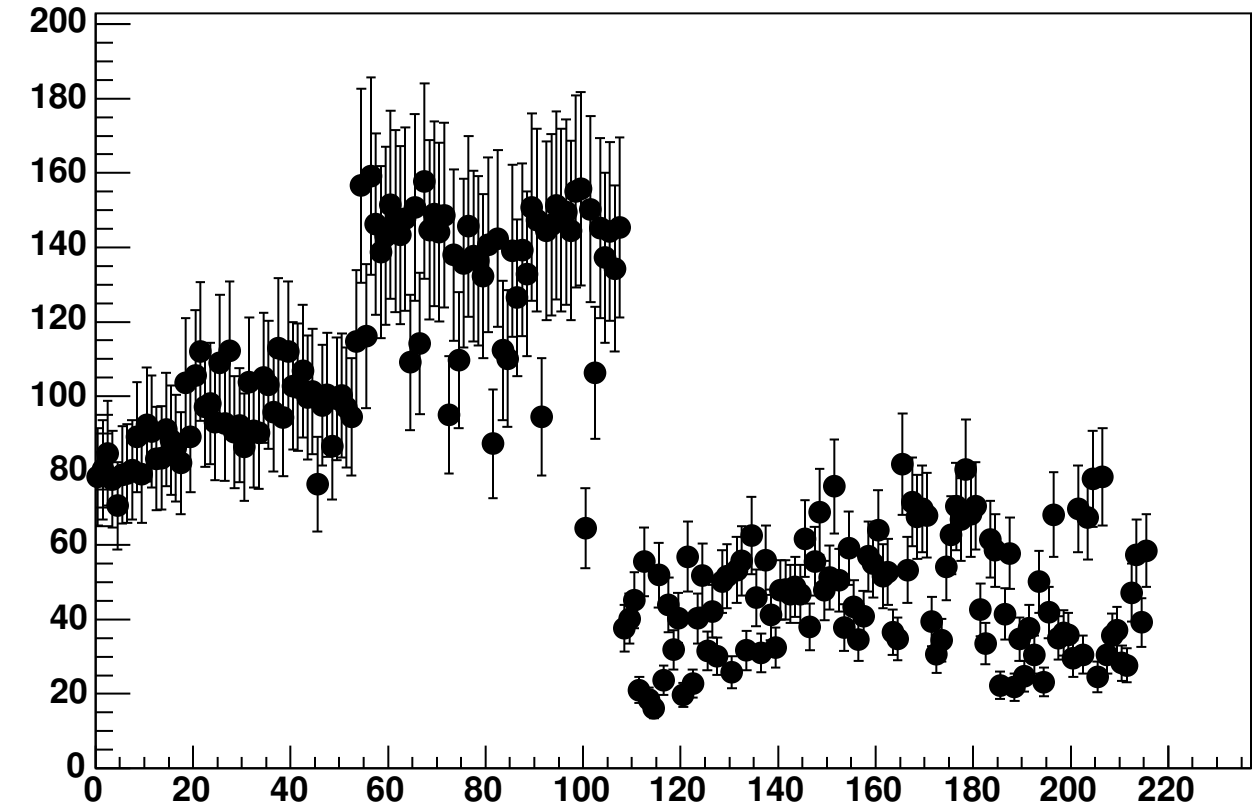
Enable 0, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



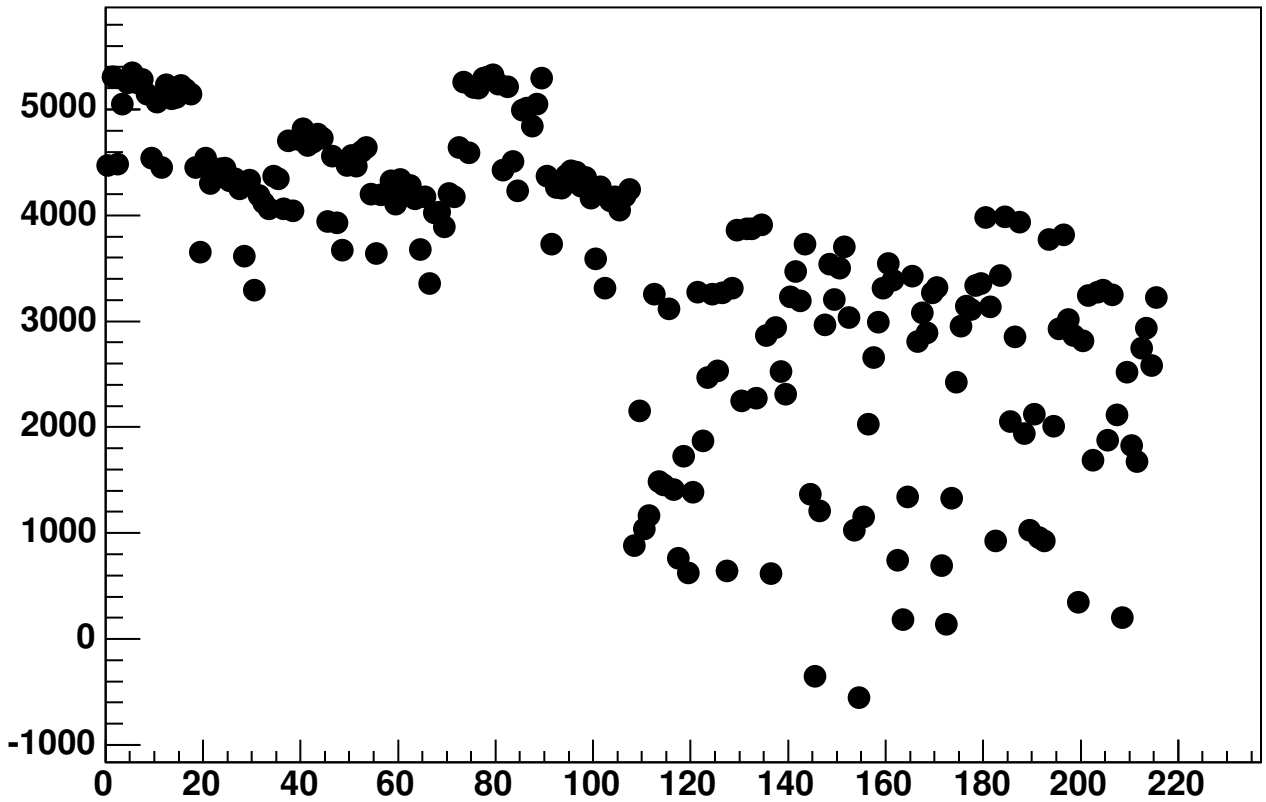
Enable 0, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



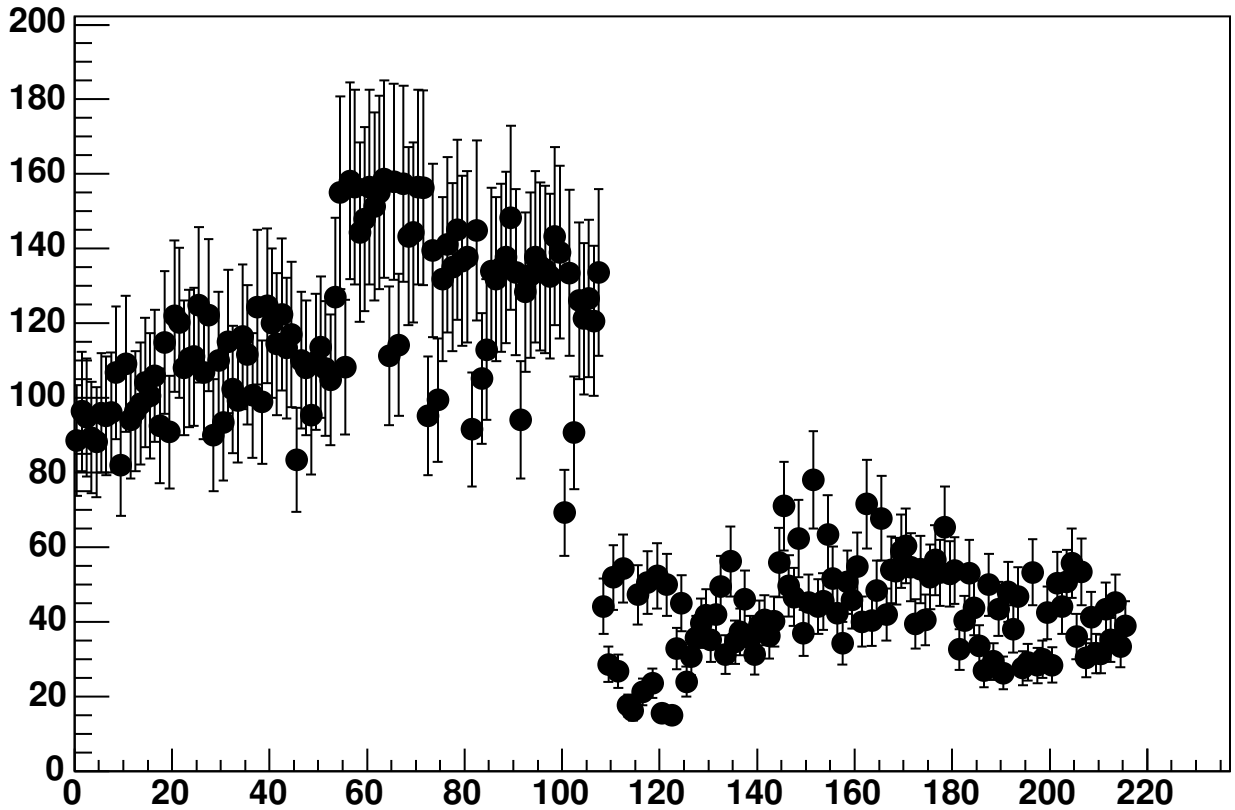
Enable 0, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



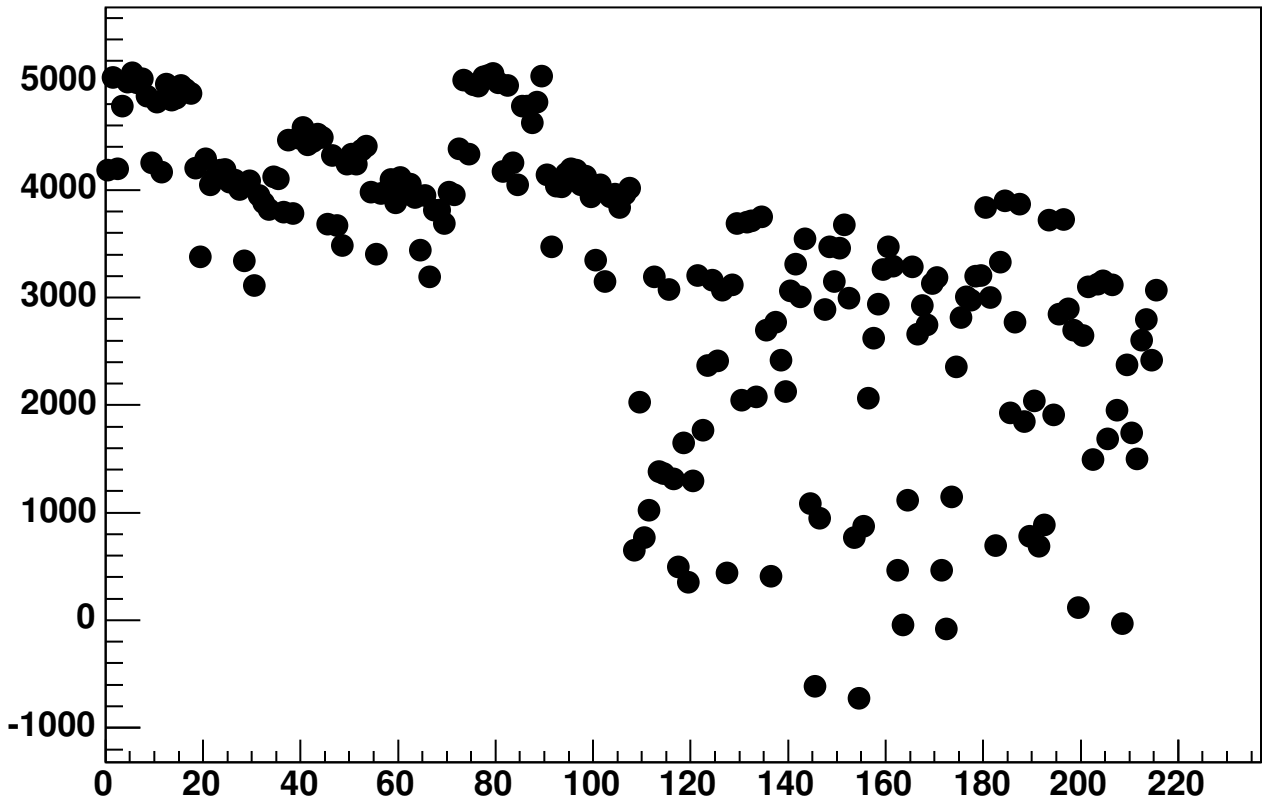
Enable 0, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



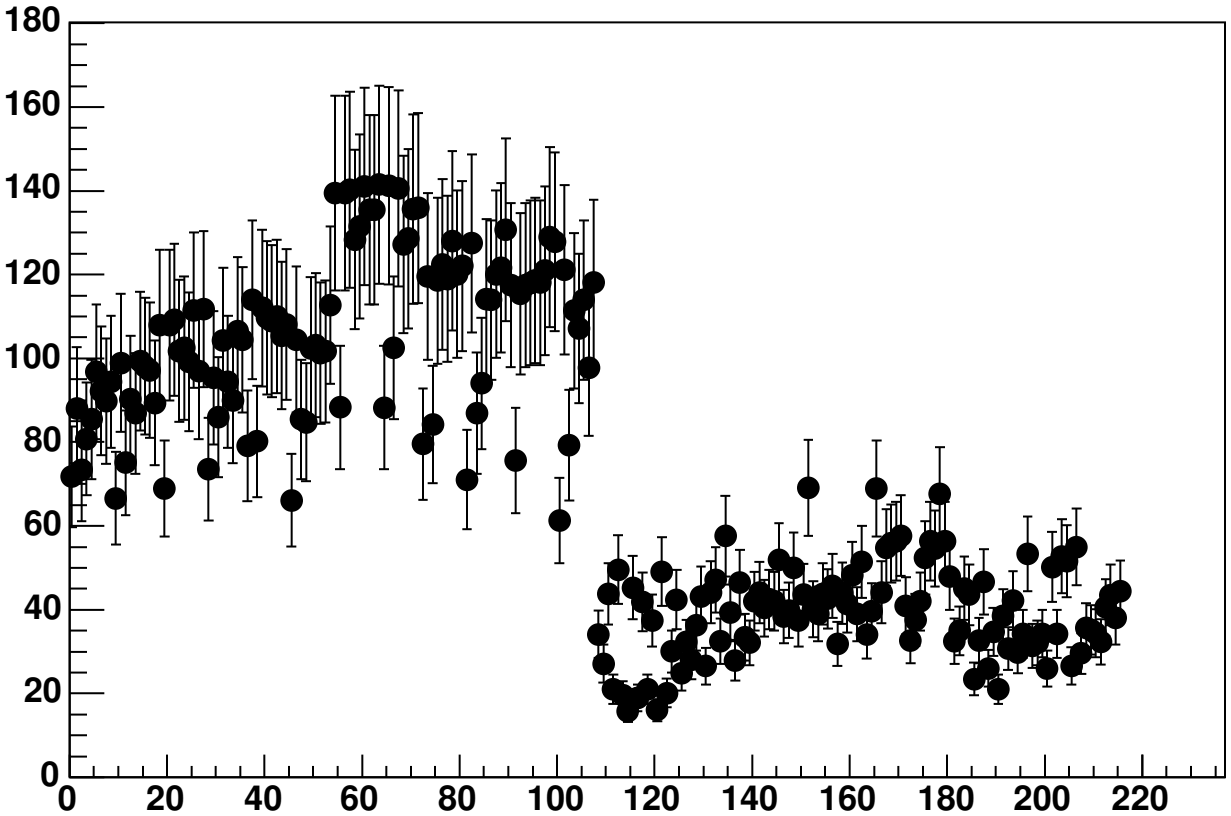
Enable 0, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



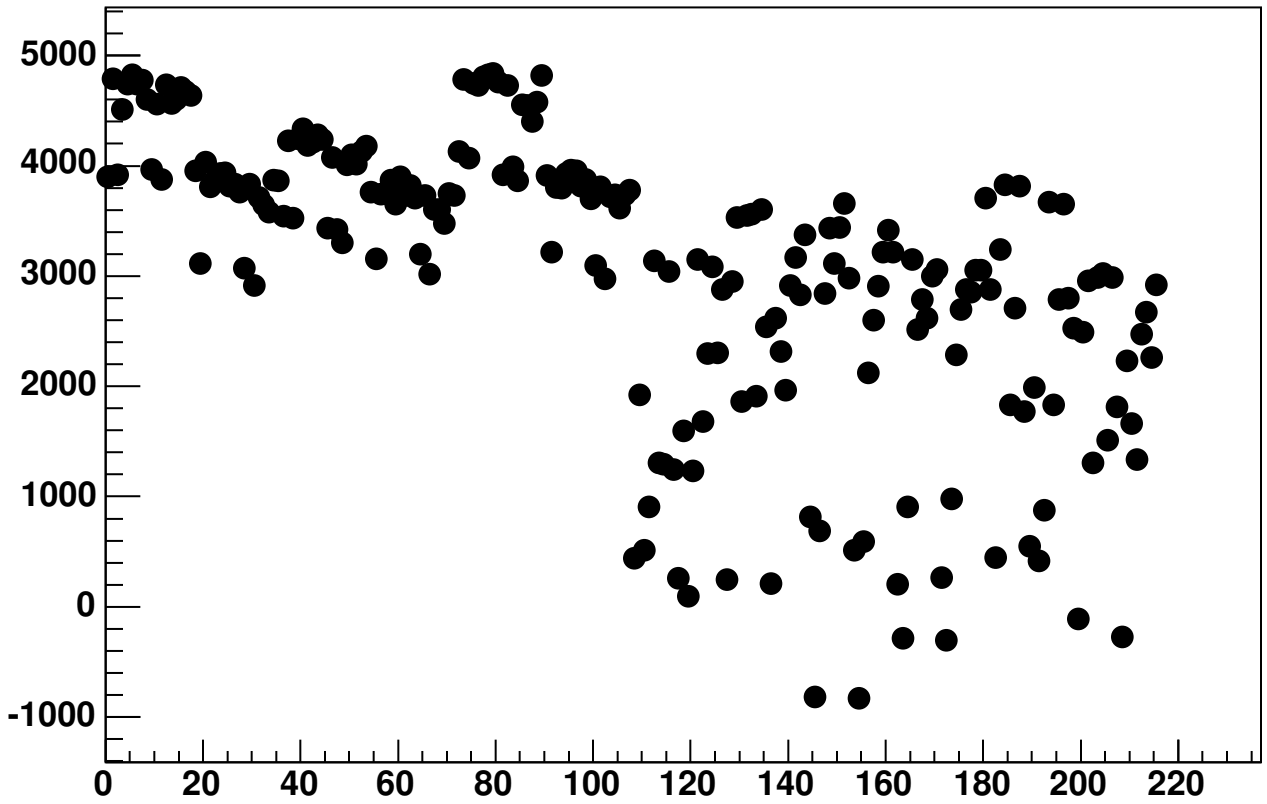
Enable 0, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



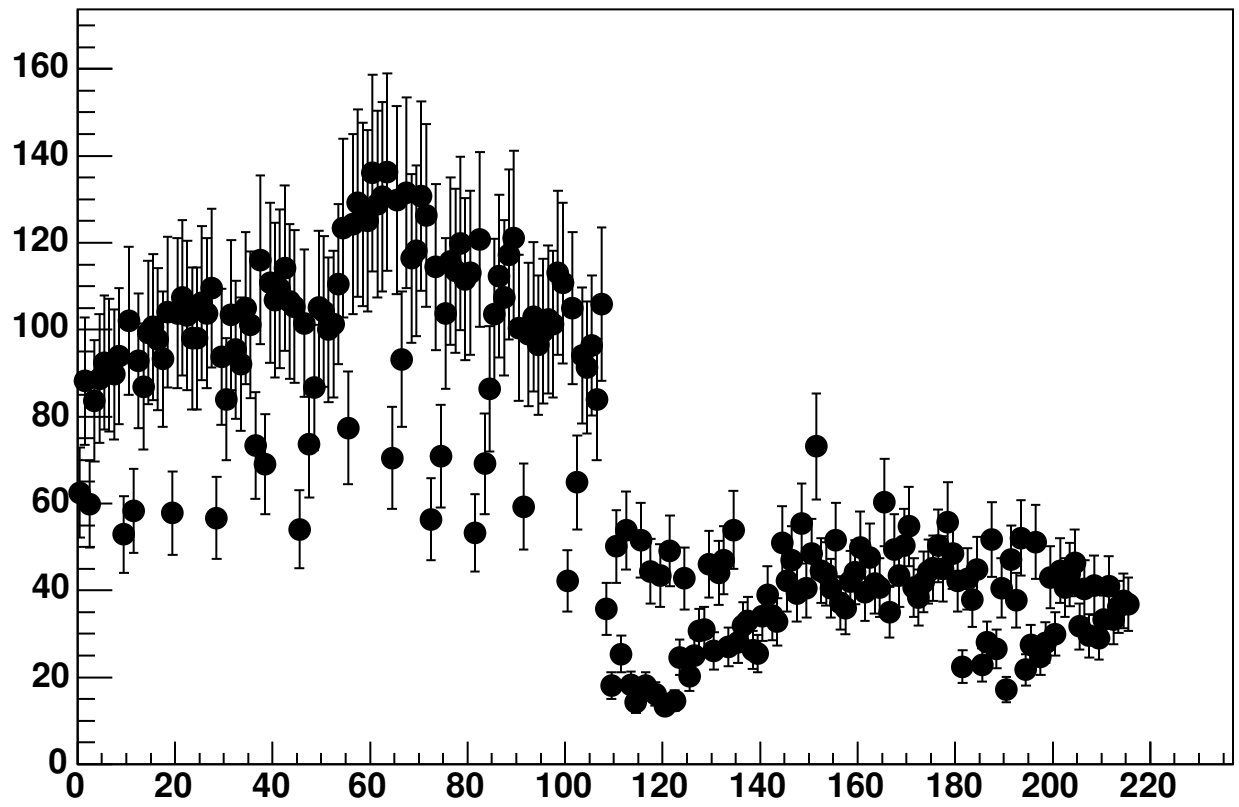
Enable 0, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



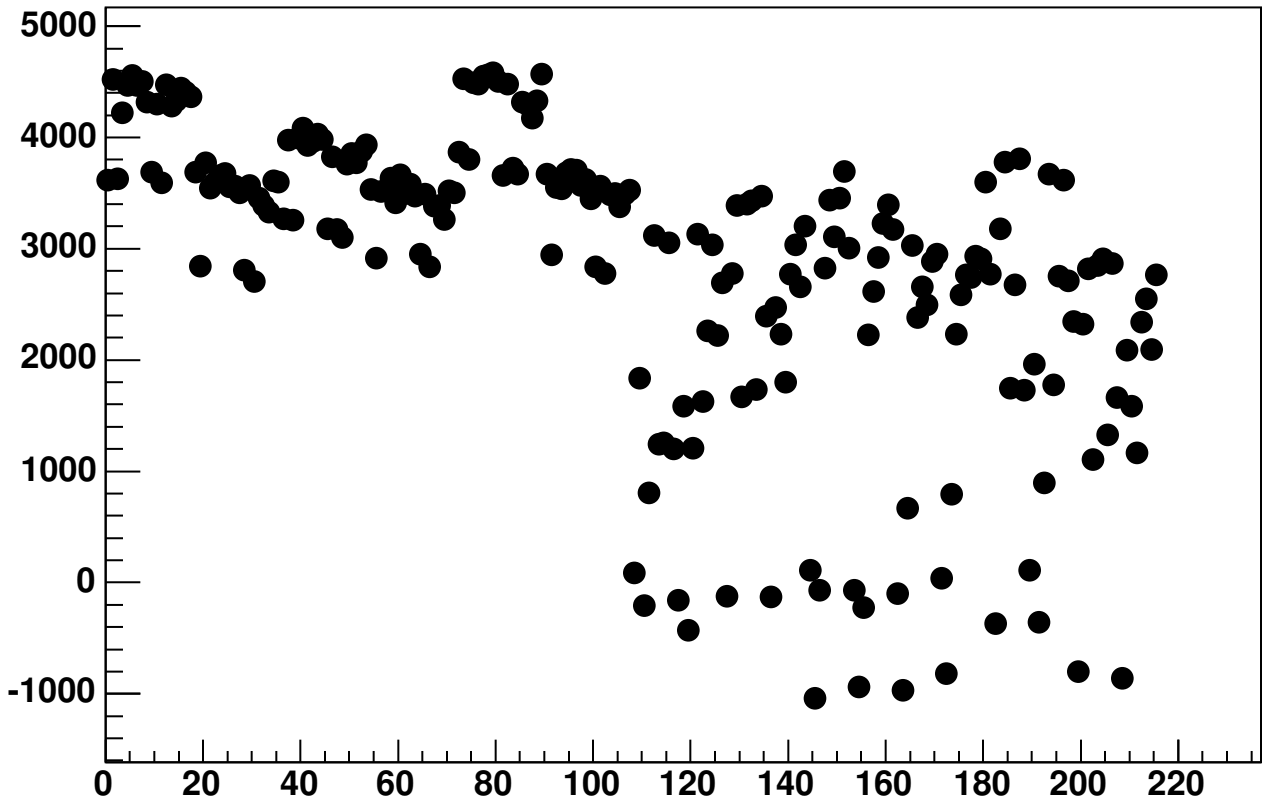
Enable 0, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



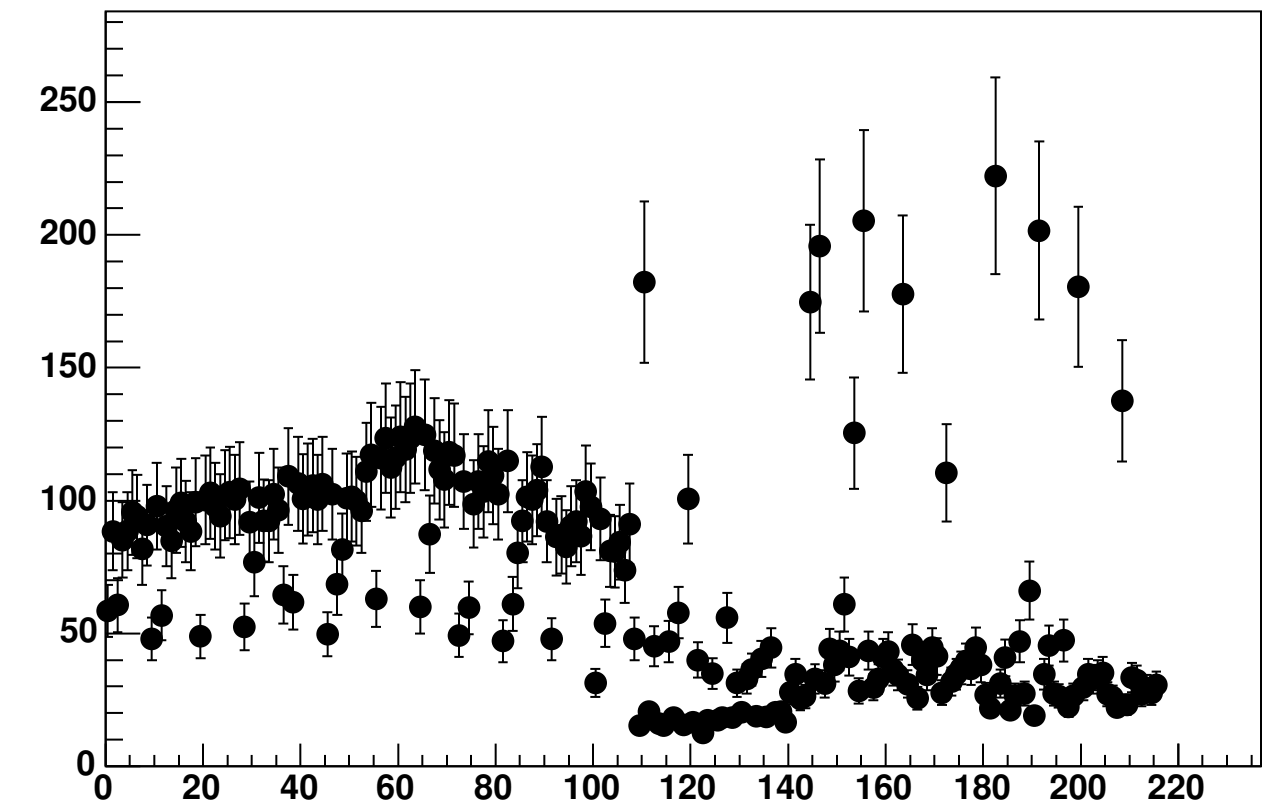
Enable 0, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



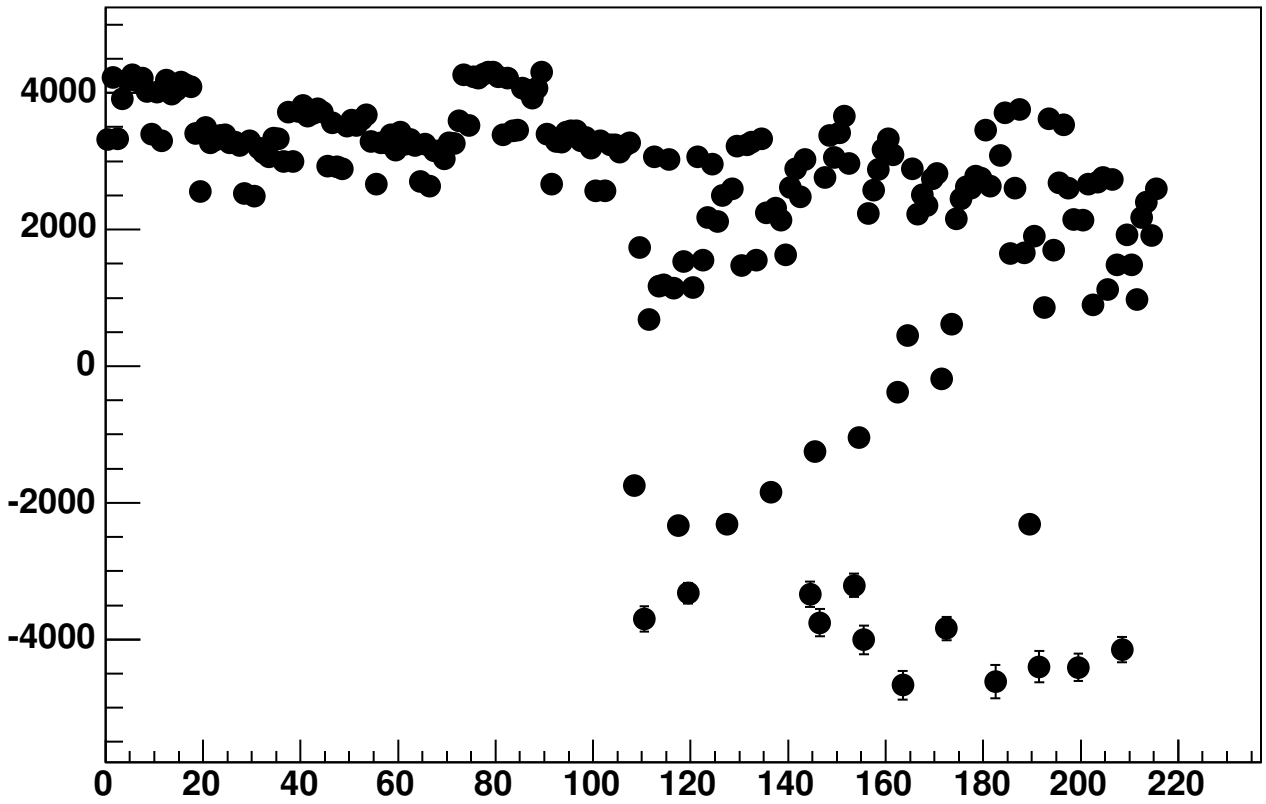
Enable 0, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



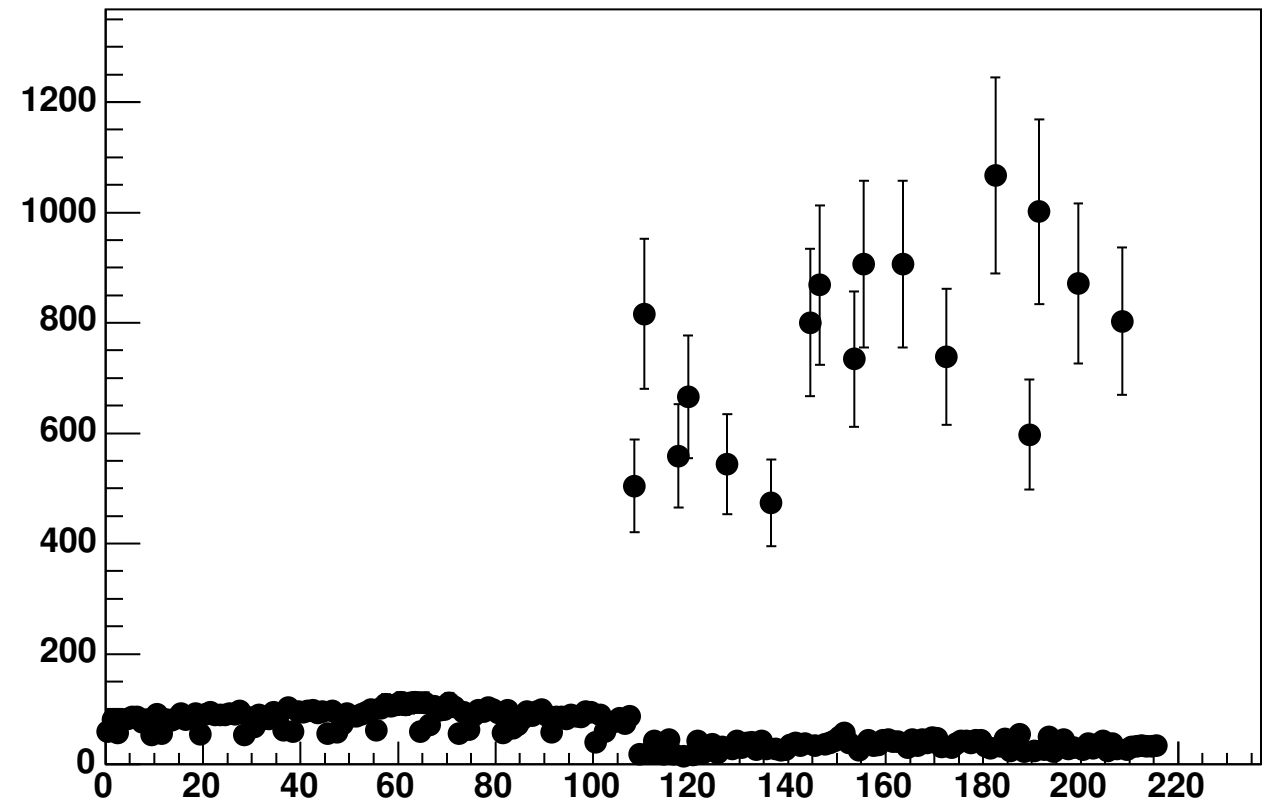
Enable 0, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



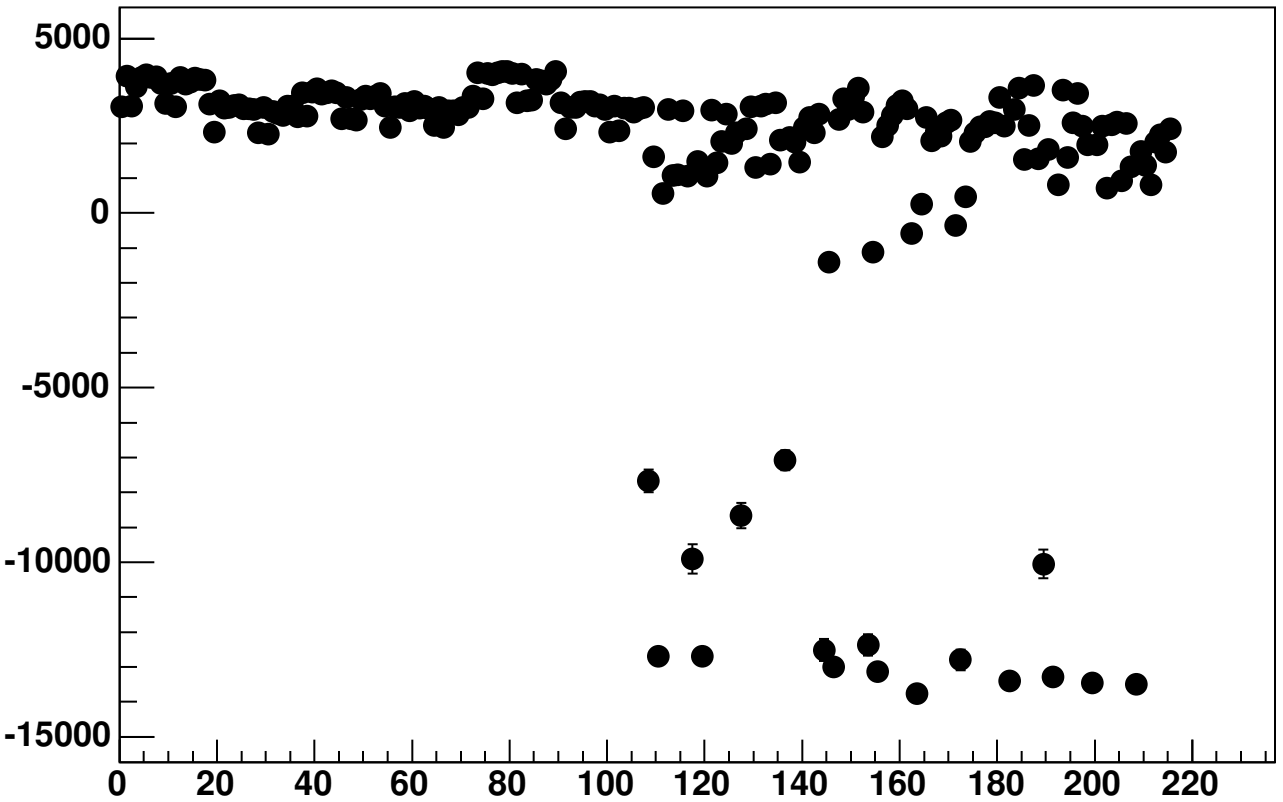
Enable 0, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



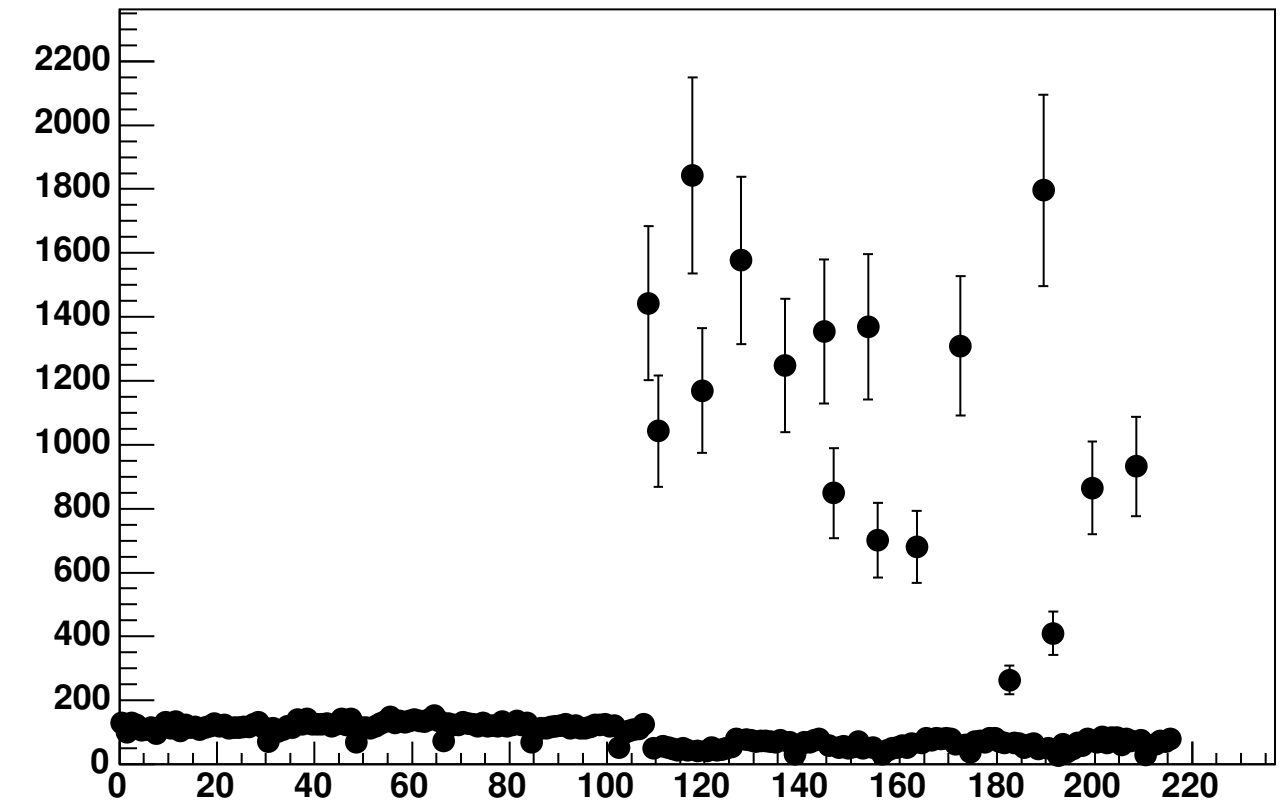
Enable 0, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



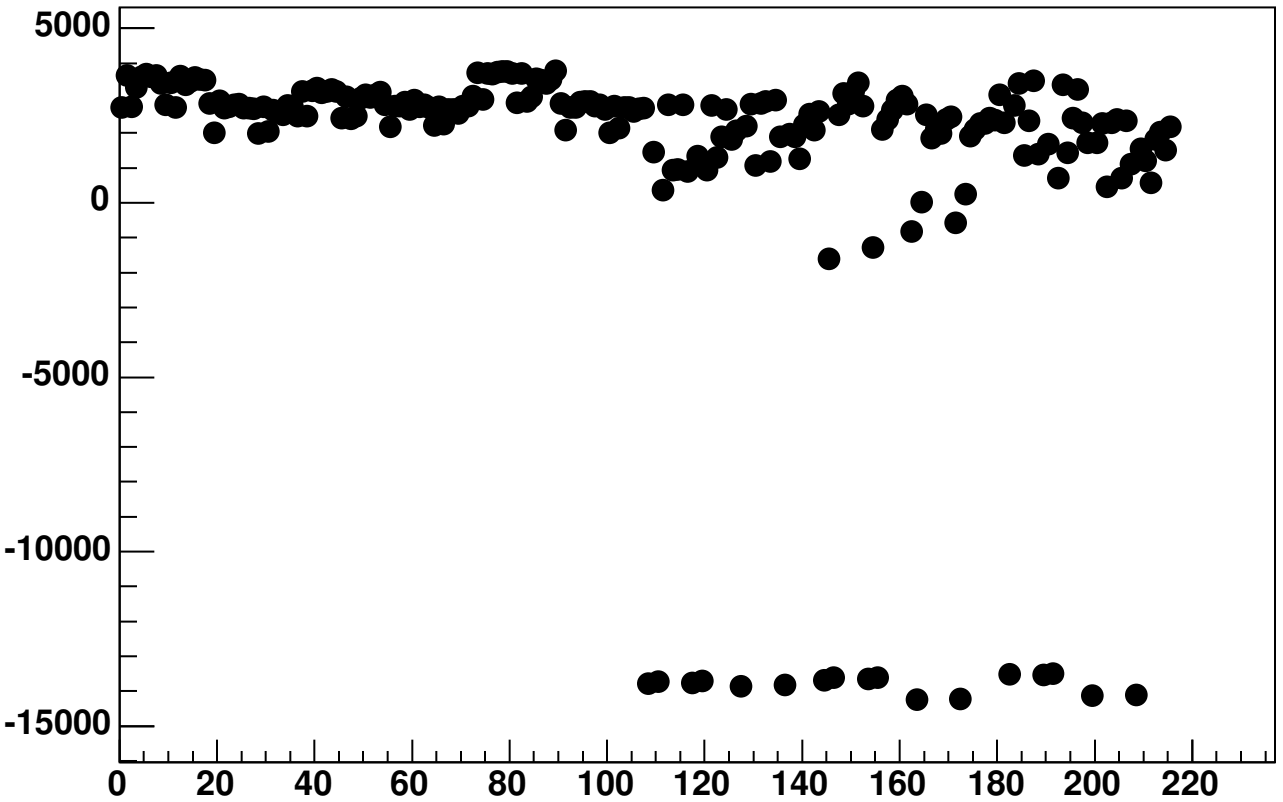
Enable 0, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



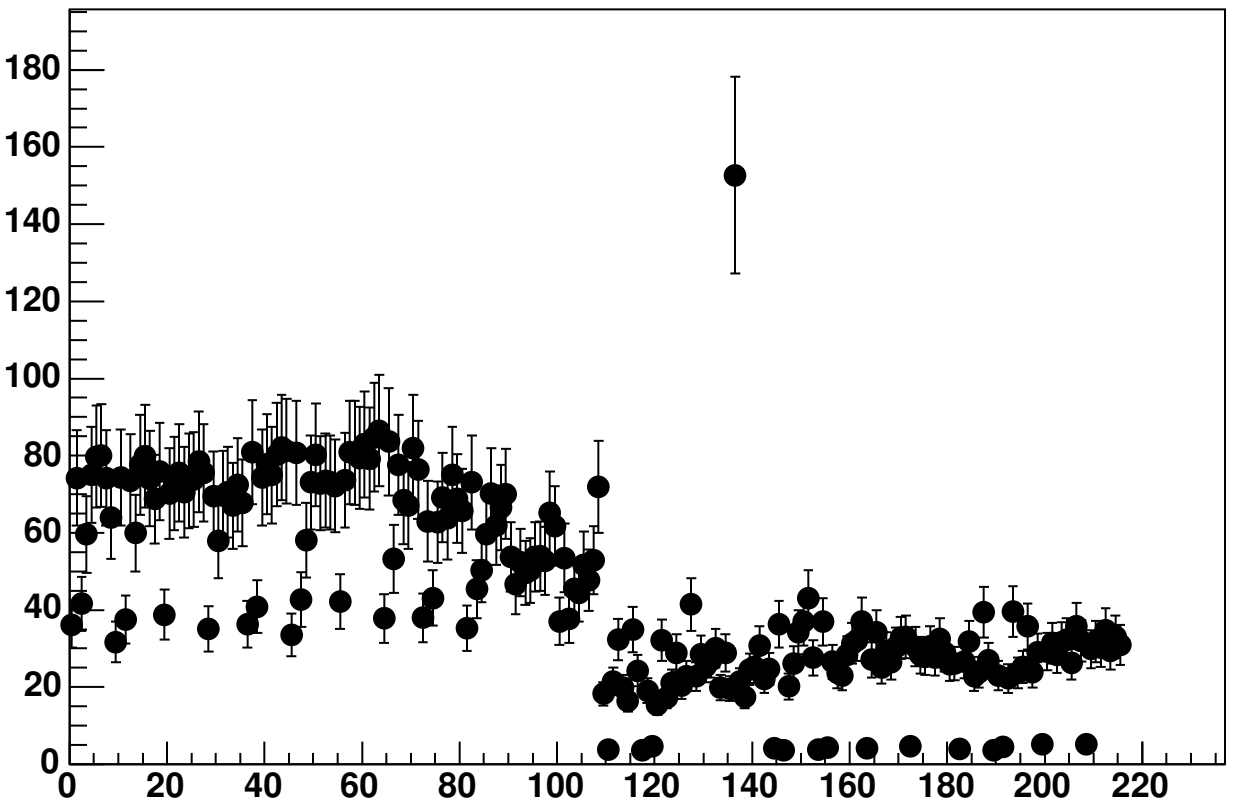
Enable 0, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



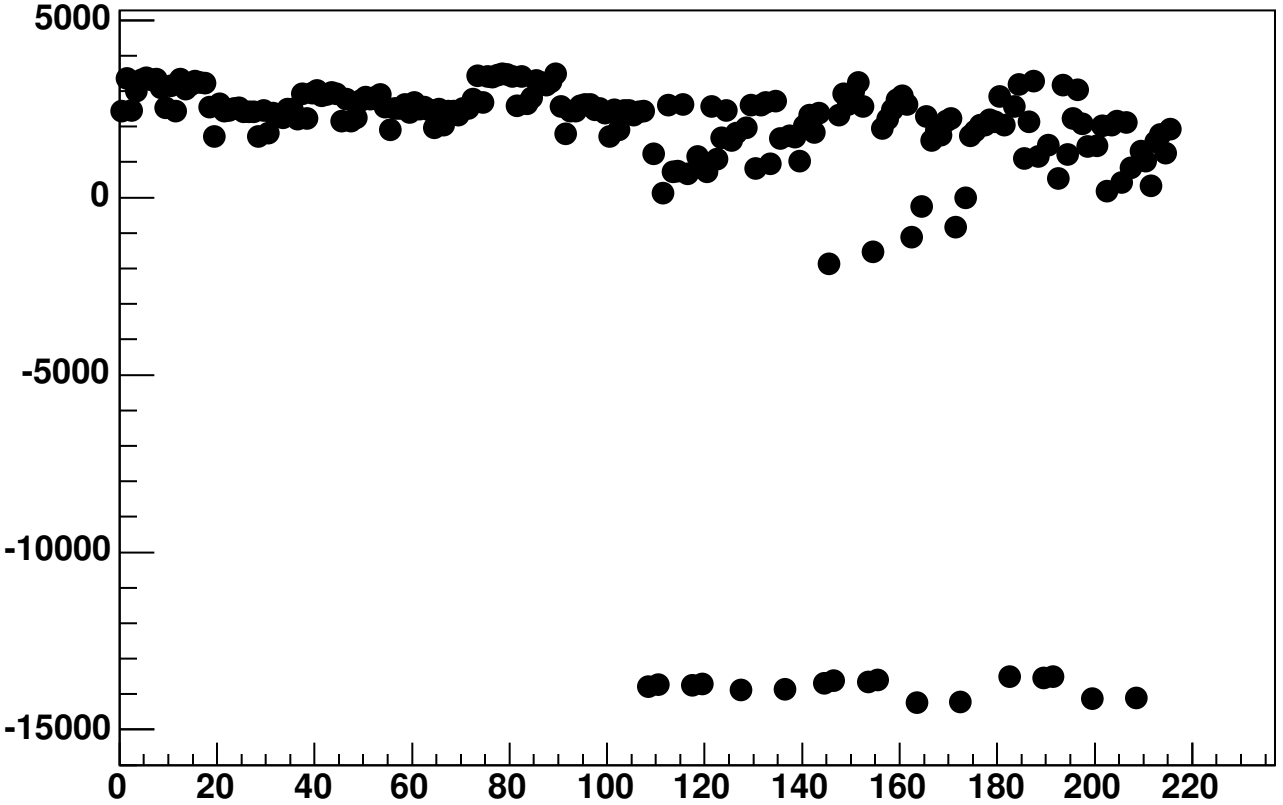
Enable 0, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



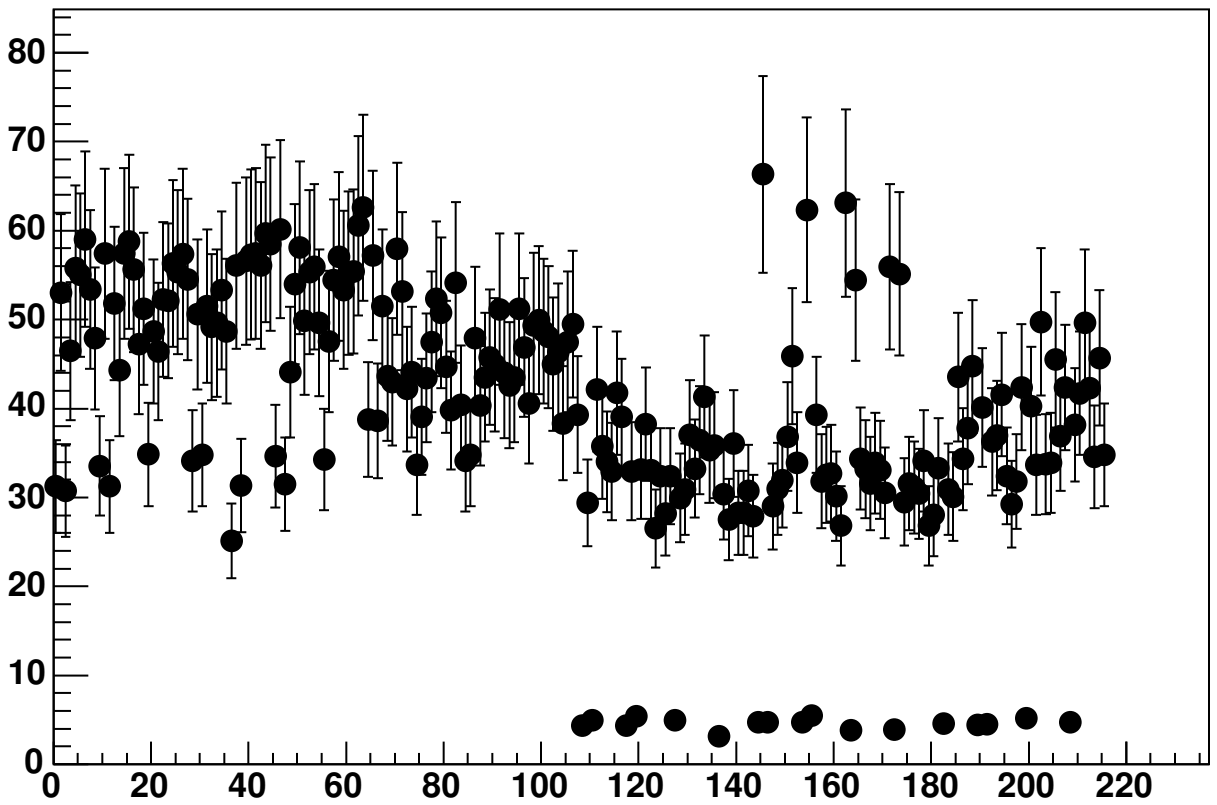
Enable 0, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



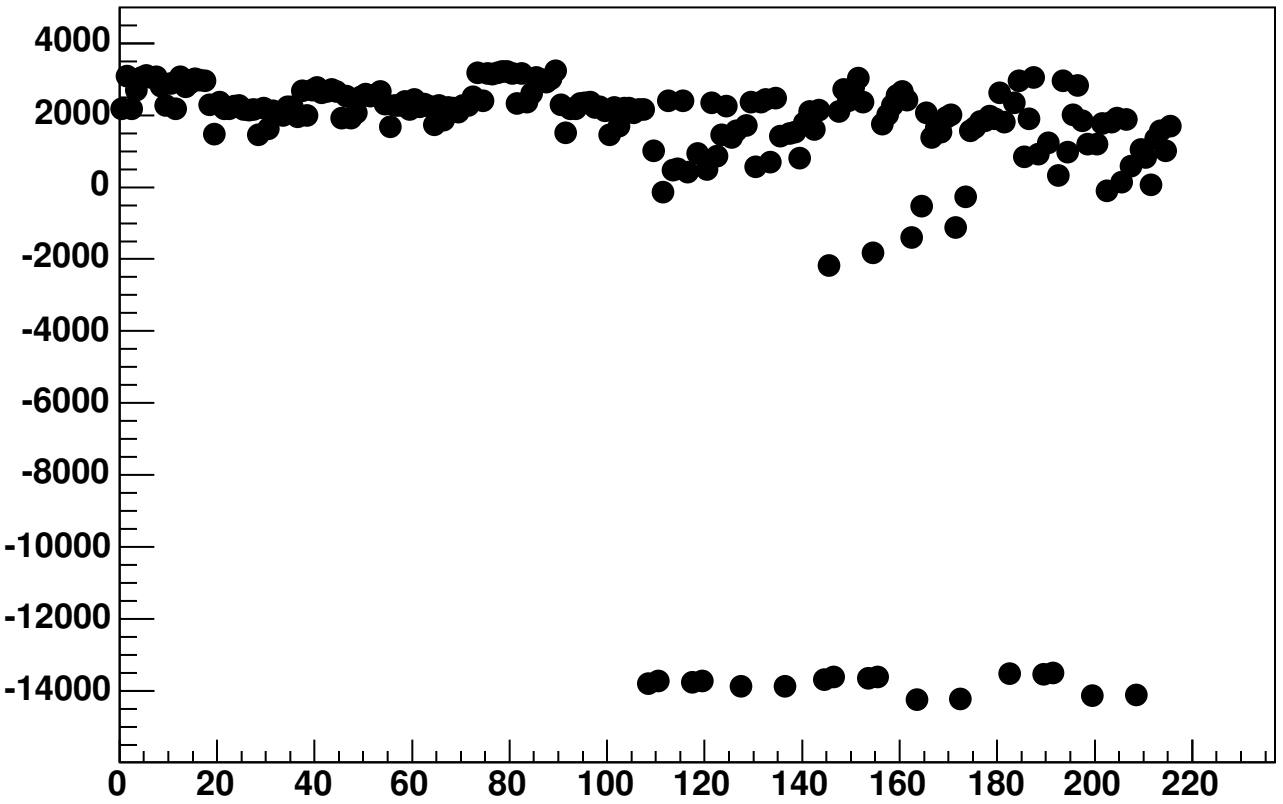
Enable 0, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



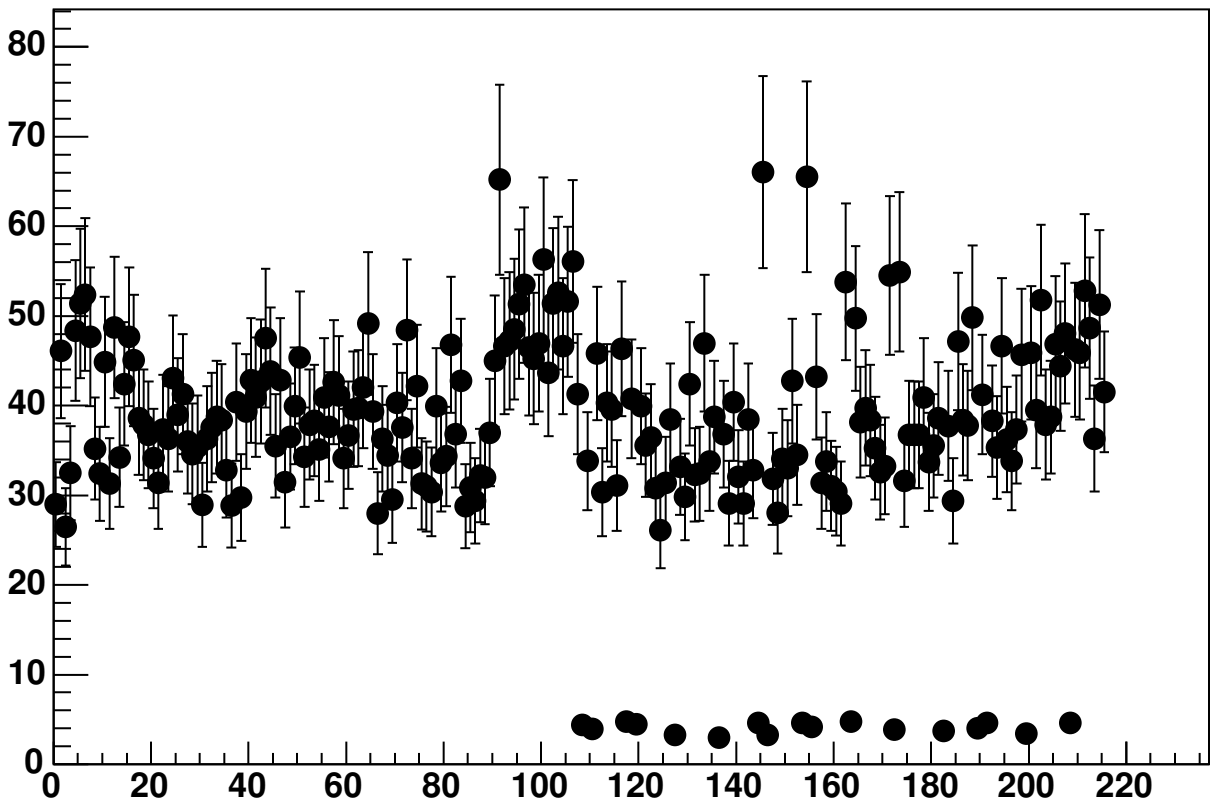
Enable 0, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



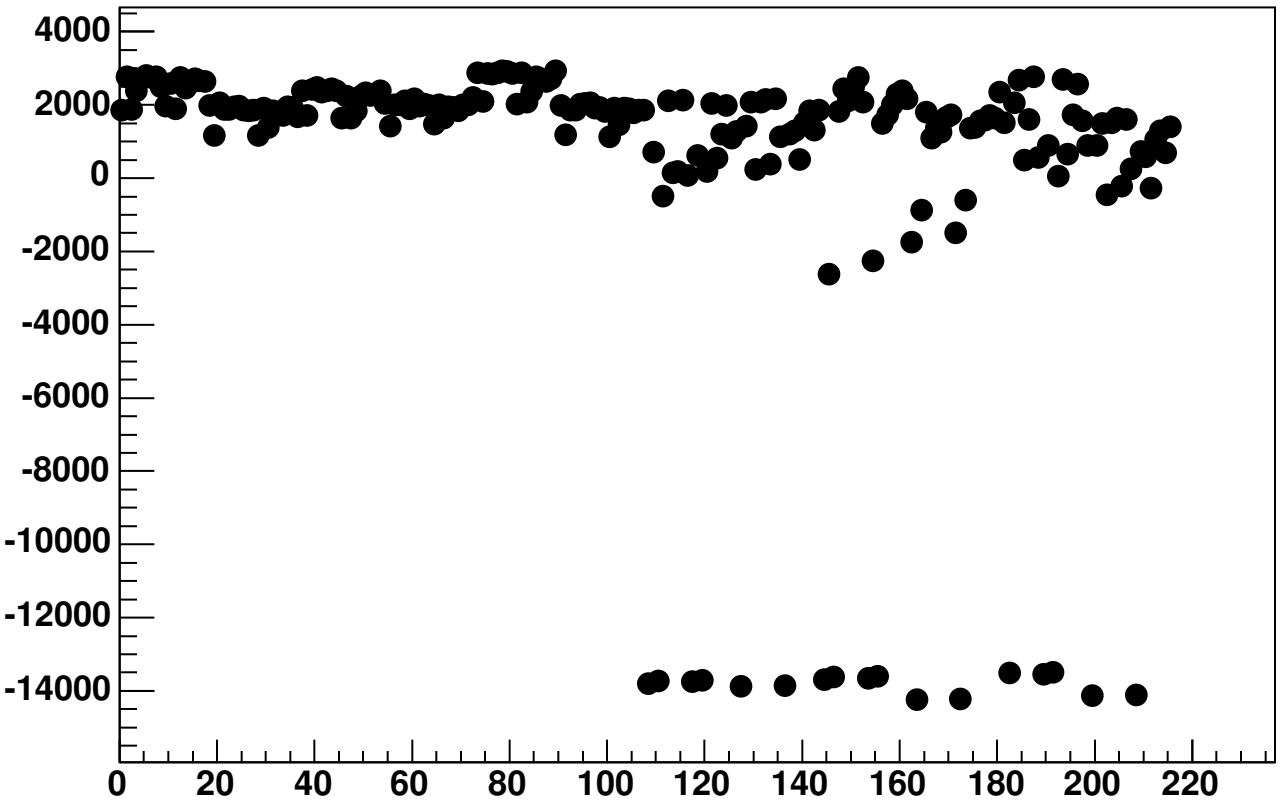
Enable 0, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



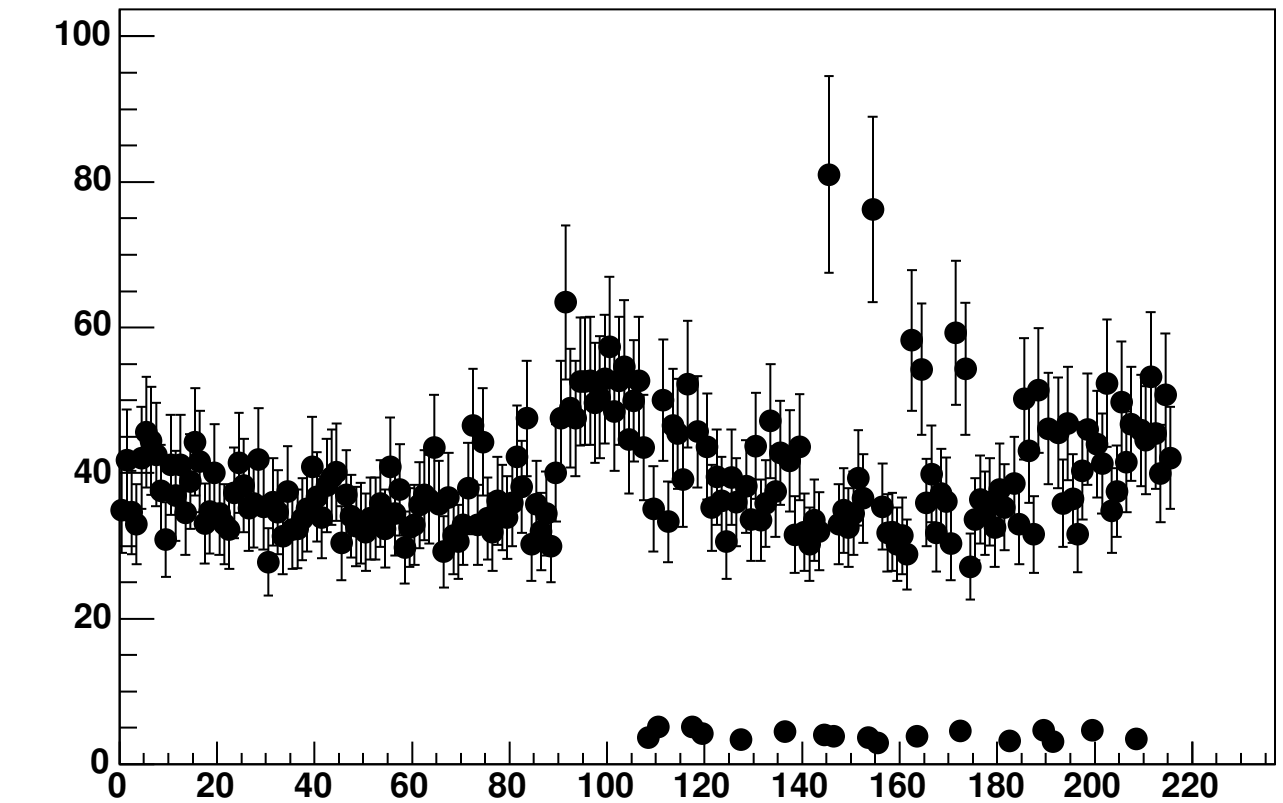
Enable 0, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan



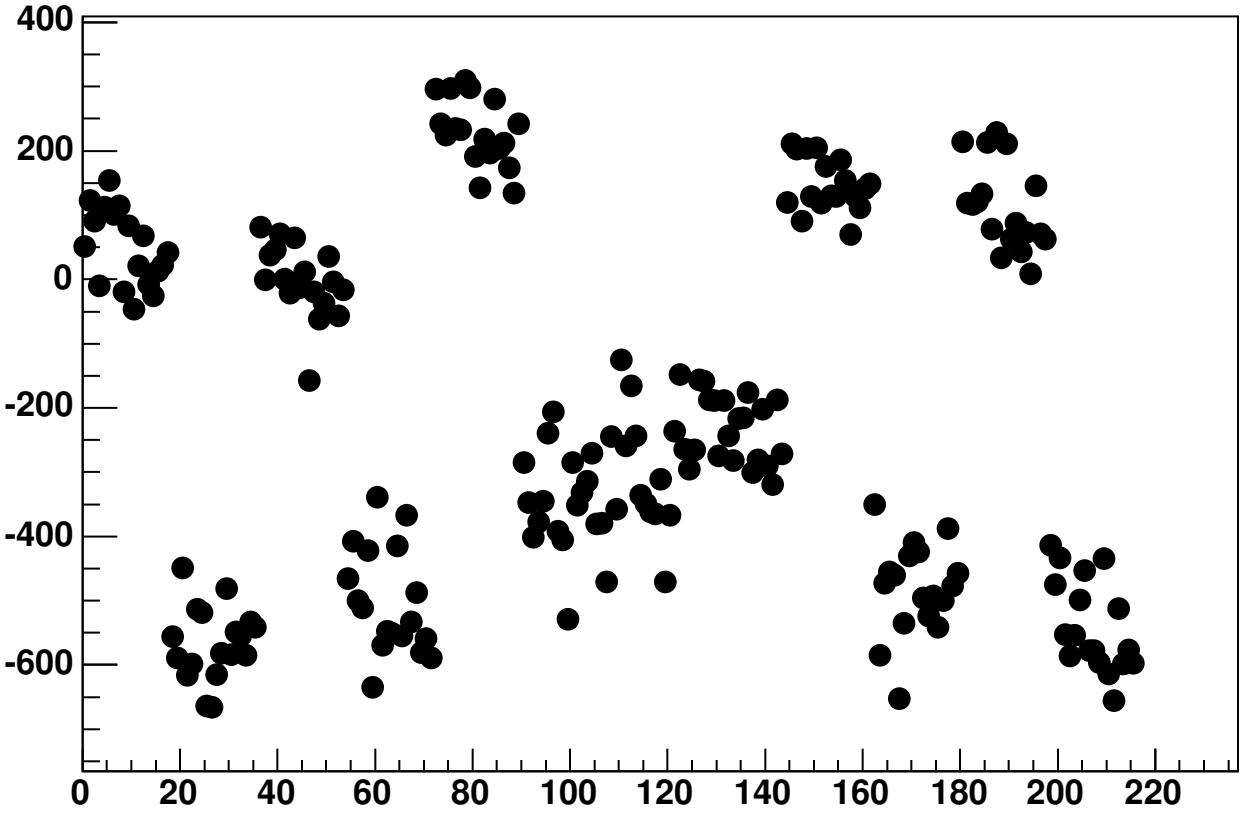
Enable 0, DAC=4000, Hold=245, ADC Mean vs 18*Chip+Chan



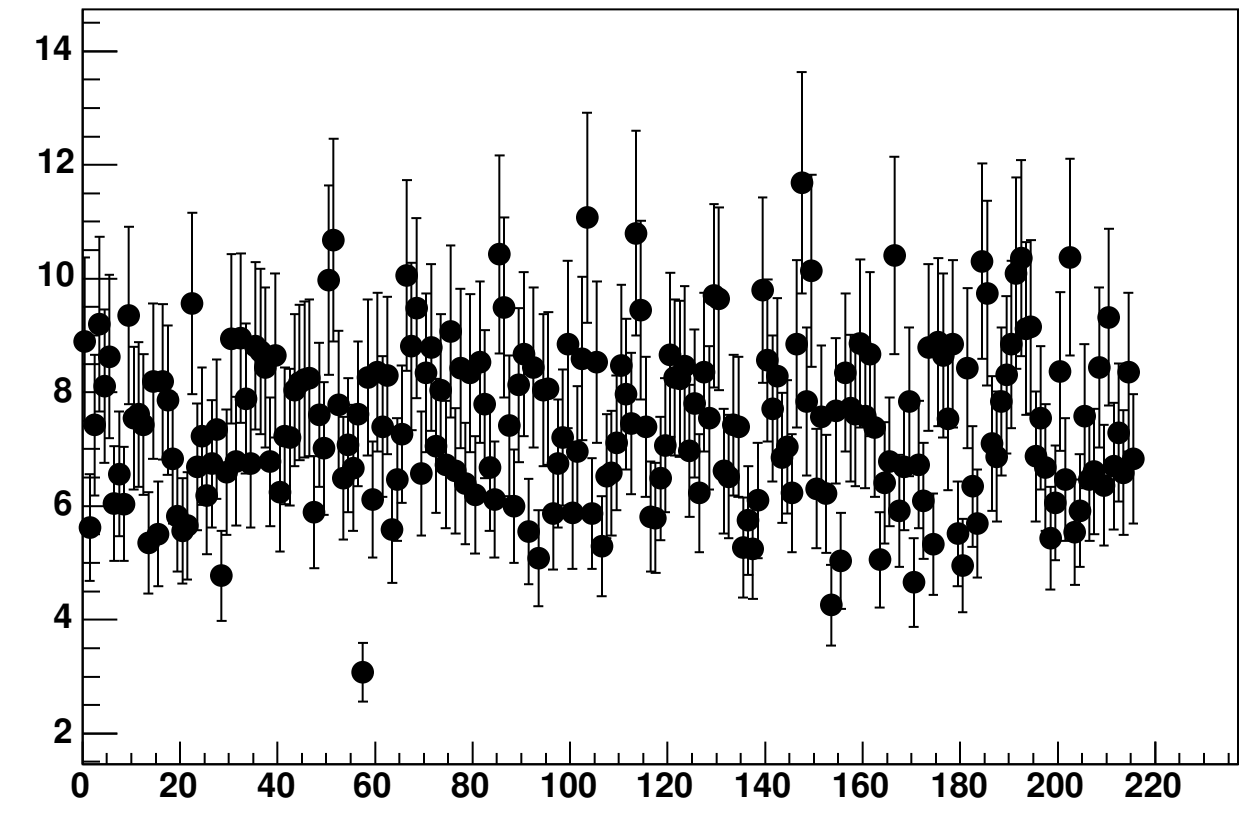
Enable 0, DAC=4000, Hold=245, ADC Noise vs 18*Chip+Chan



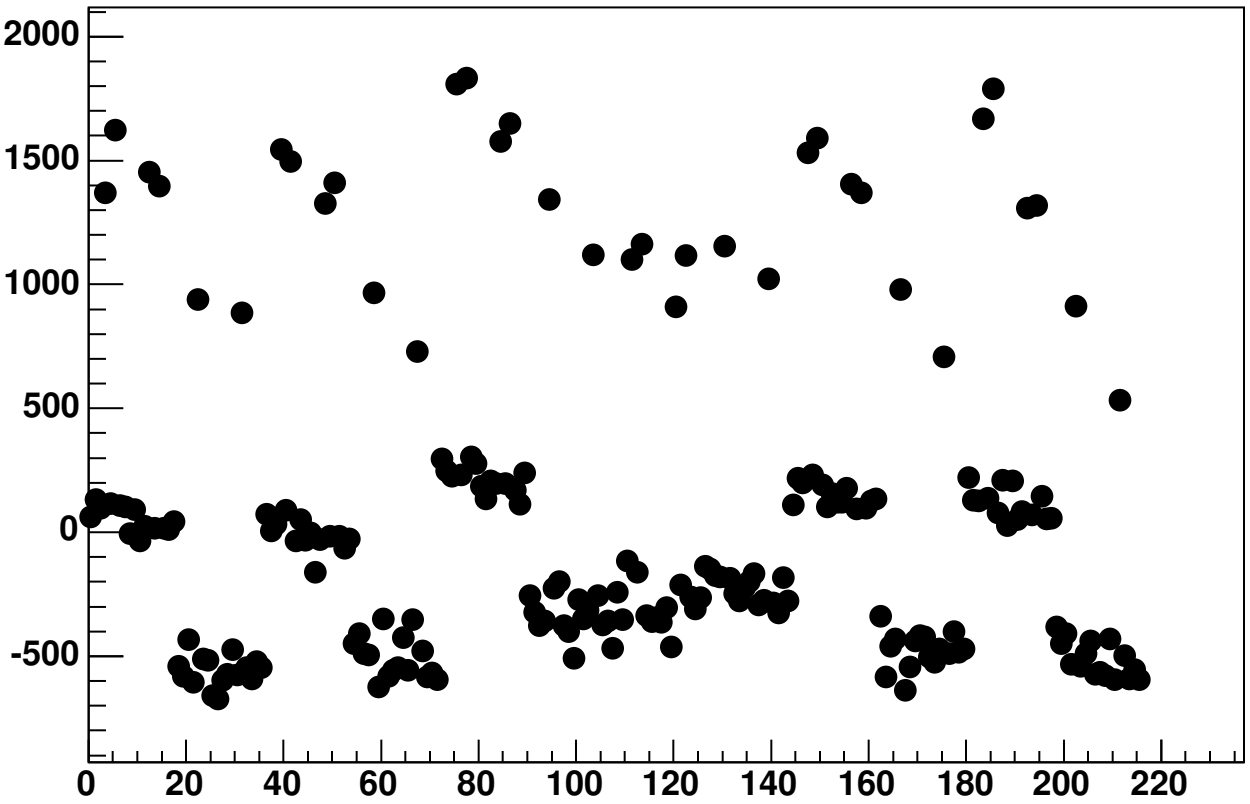
Enable 1, DAC=4000, Hold=0, ADC Mean vs 18*Chip+Chan



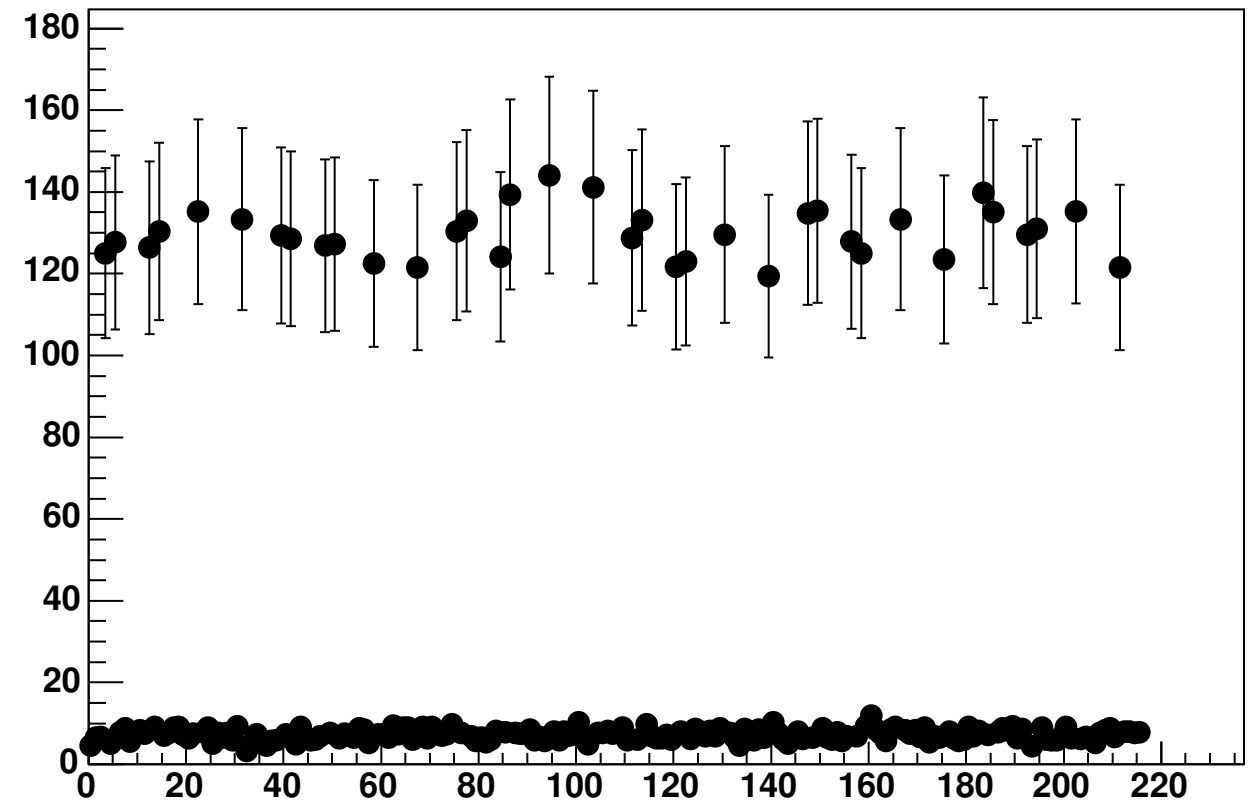
Enable 1, DAC=4000, Hold=0, ADC Noise vs 18*Chip+Chan



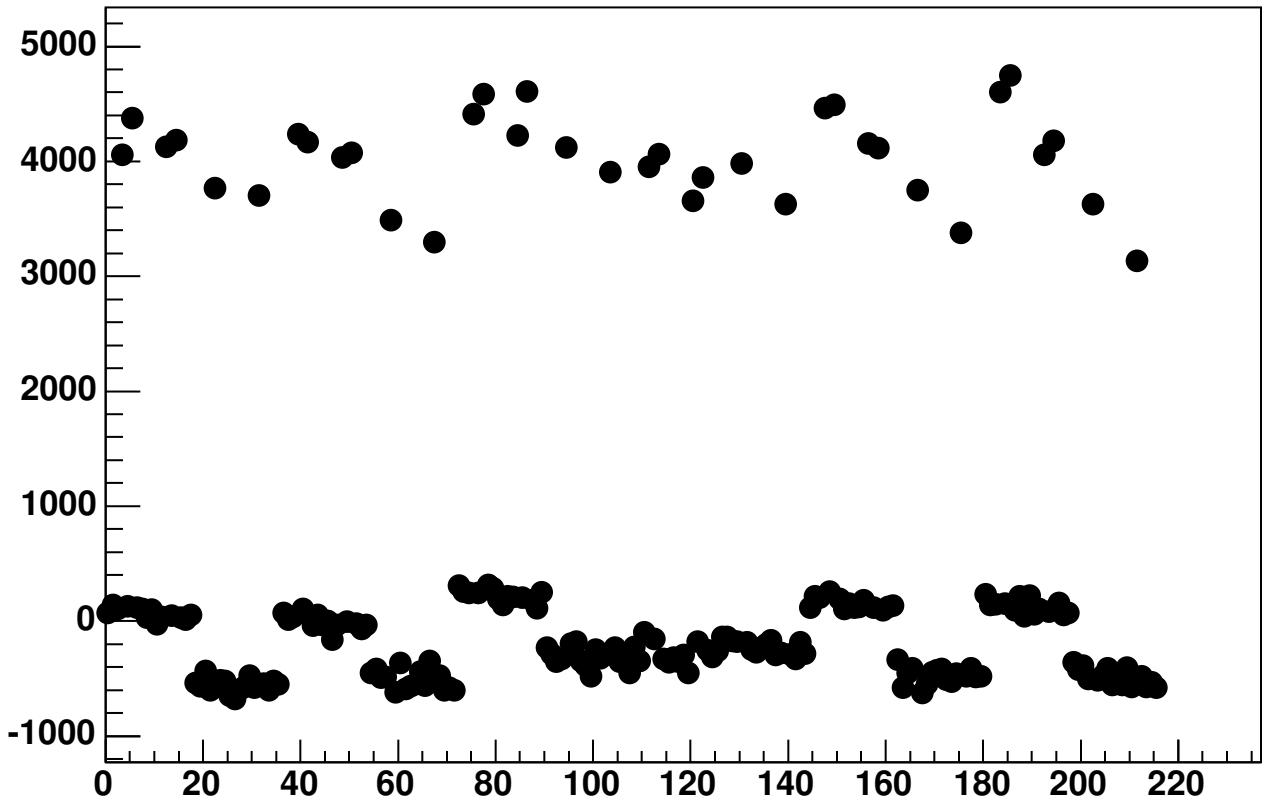
Enable 1, DAC=4000, Hold=5, ADC Mean vs 18*Chip+Chan



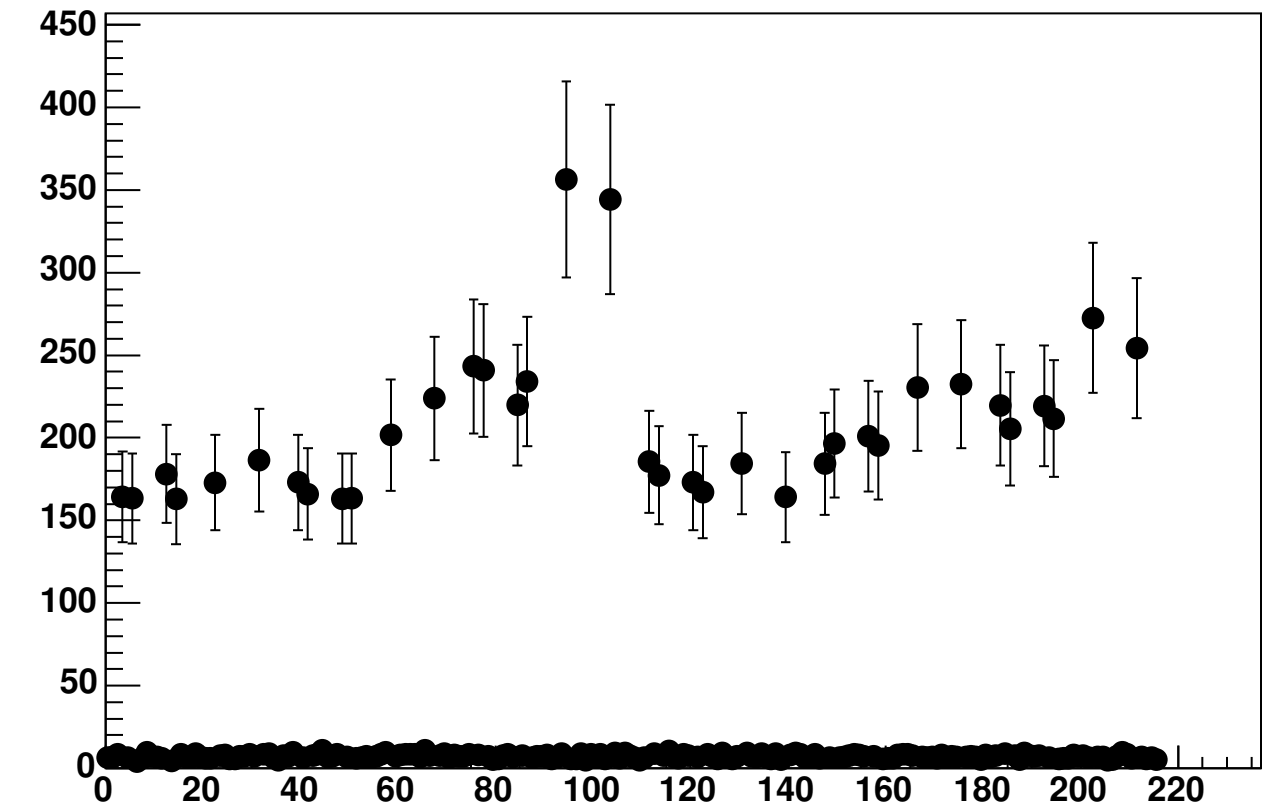
Enable 1, DAC=4000, Hold=5, ADC Noise vs 18*Chip+Chan



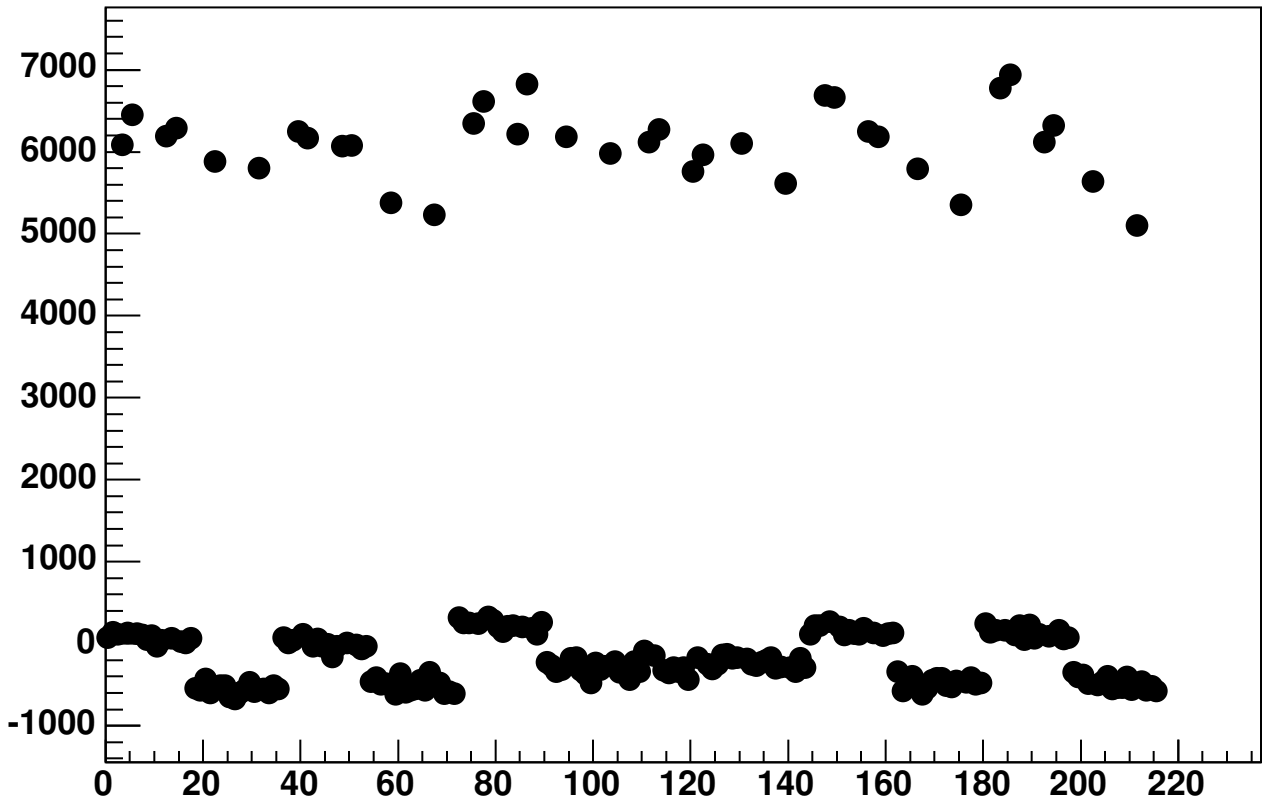
Enable 1, DAC=4000, Hold=10, ADC Mean vs 18*Chip+Chan



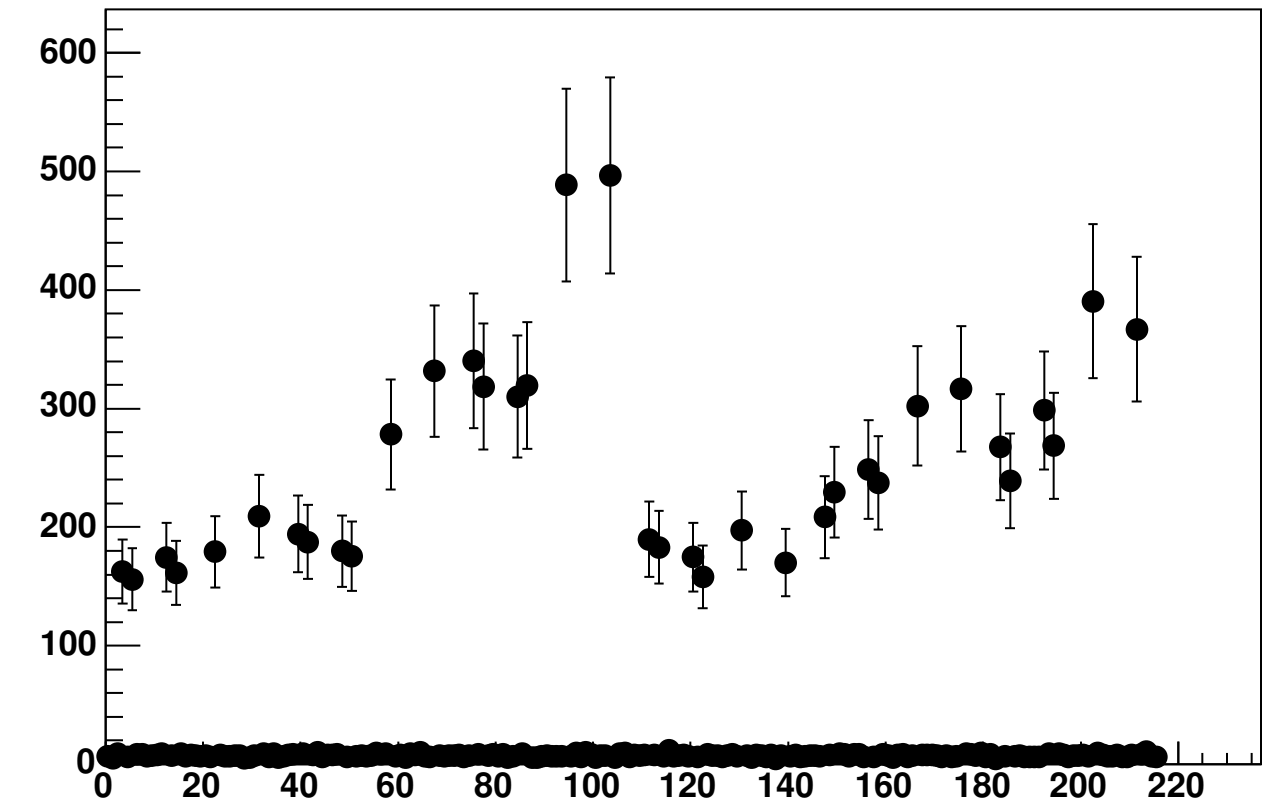
Enable 1, DAC=4000, Hold=10, ADC Noise vs 18*Chip+Chan



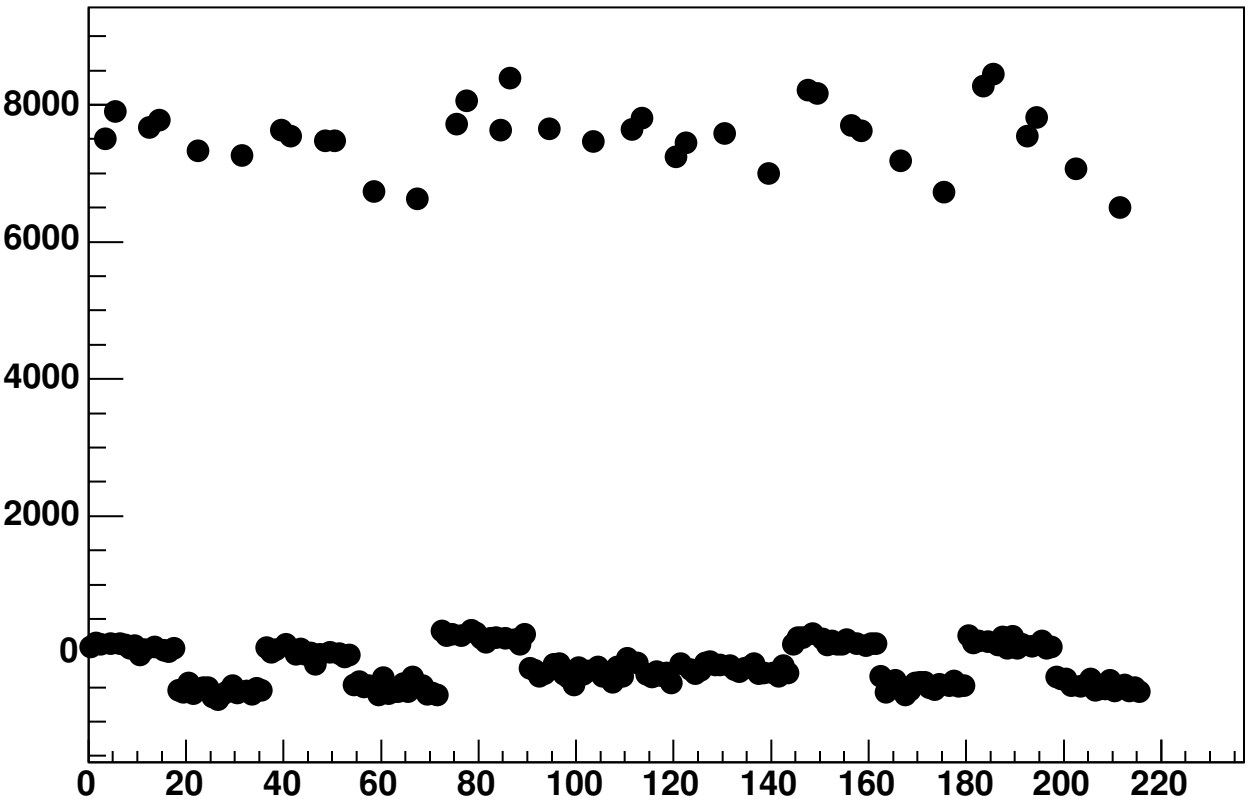
Enable 1, DAC=4000, Hold=15, ADC Mean vs 18*Chip+Chan



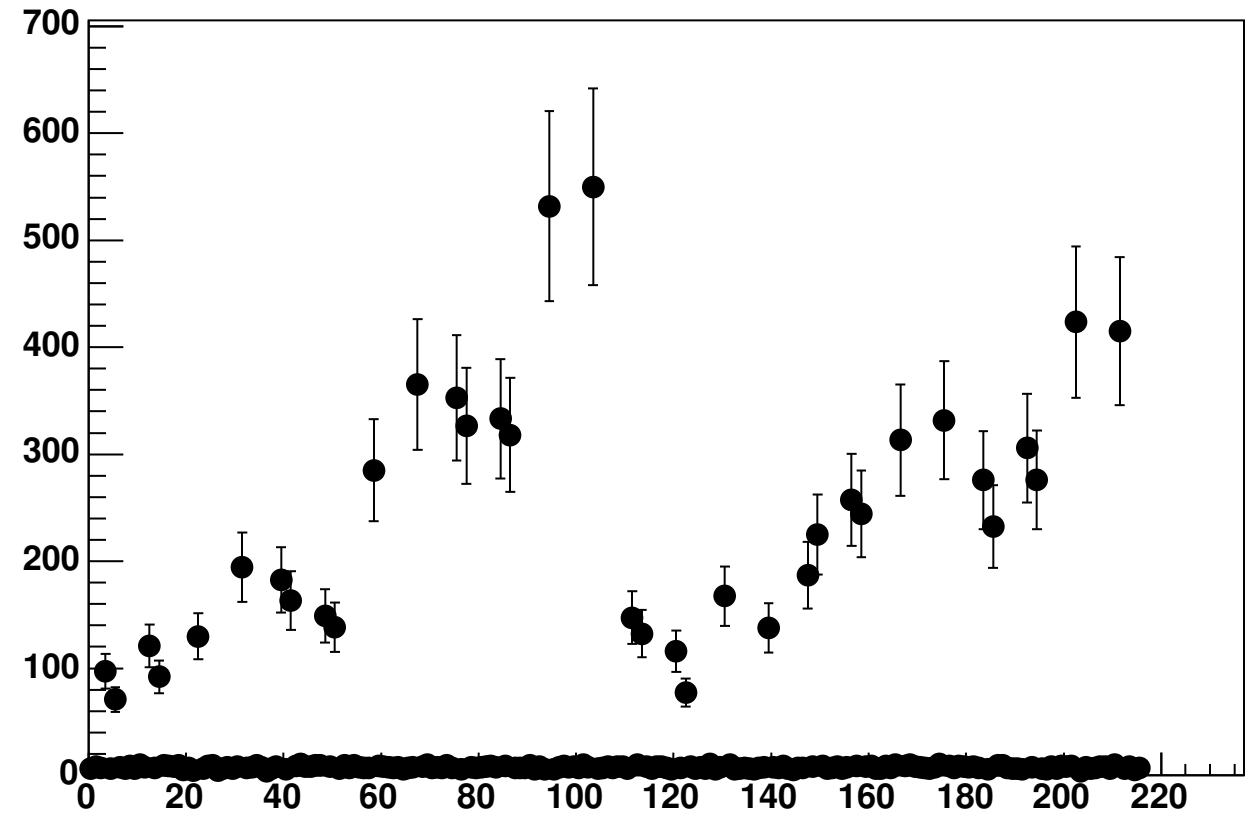
Enable 1, DAC=4000, Hold=15, ADC Noise vs 18*Chip+Chan



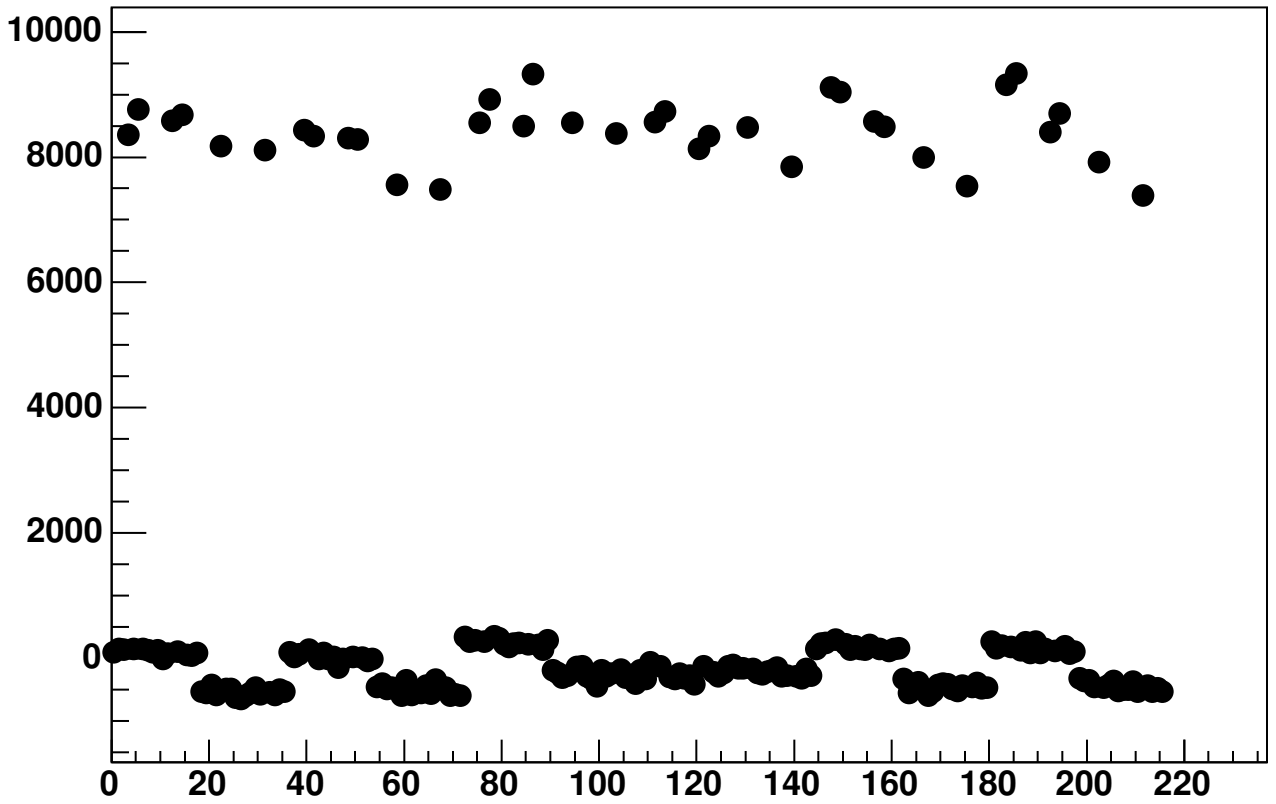
Enable 1, DAC=4000, Hold=20, ADC Mean vs 18*Chip+Chan



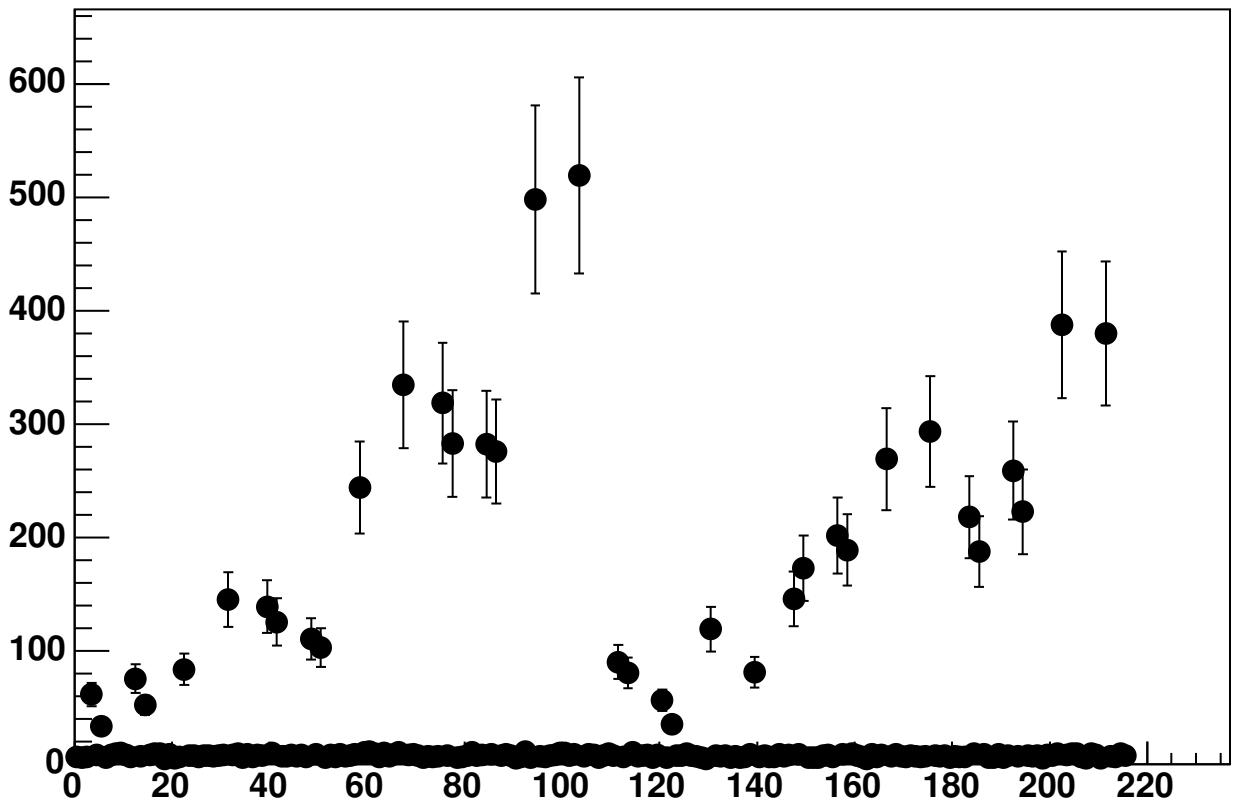
Enable 1, DAC=4000, Hold=20, ADC Noise vs 18*Chip+Chan



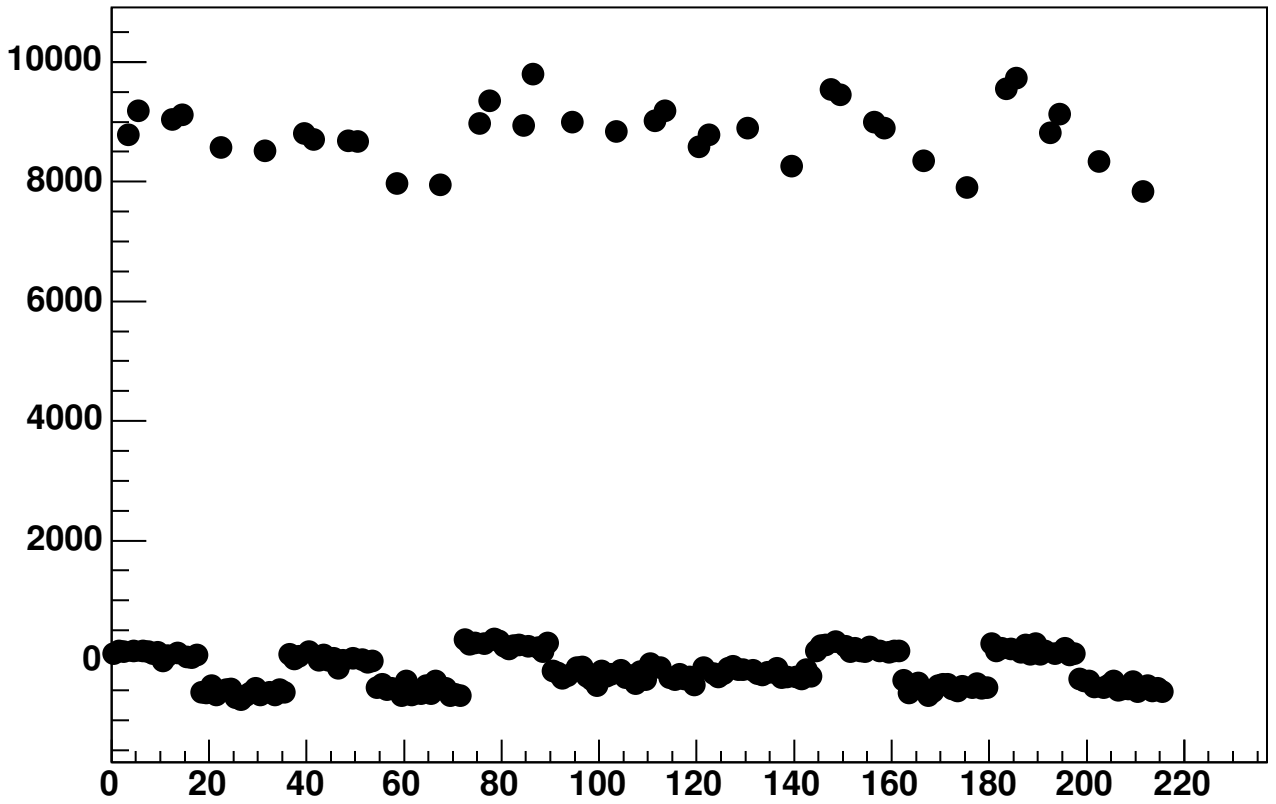
Enable 1, DAC=4000, Hold=25, ADC Mean vs 18*Chip+Chan



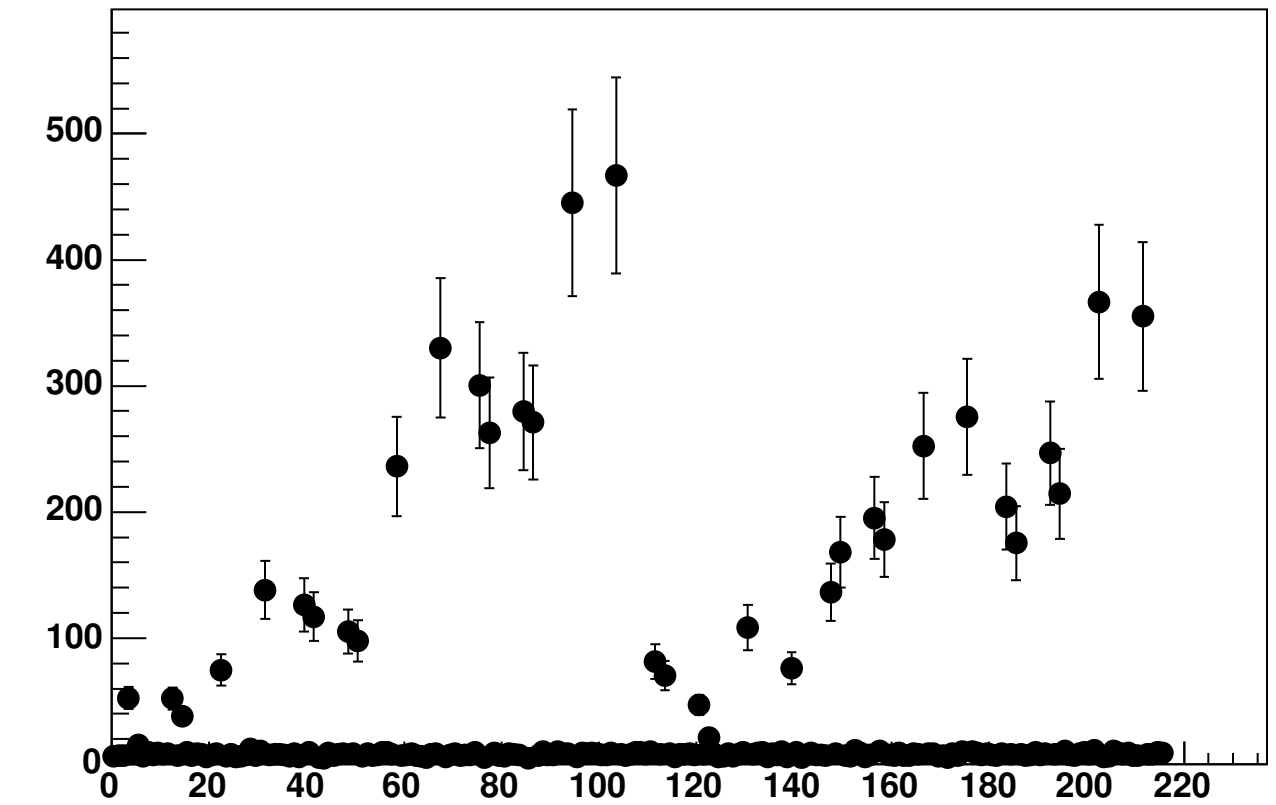
Enable 1, DAC=4000, Hold=25, ADC Noise vs 18*Chip+Chan



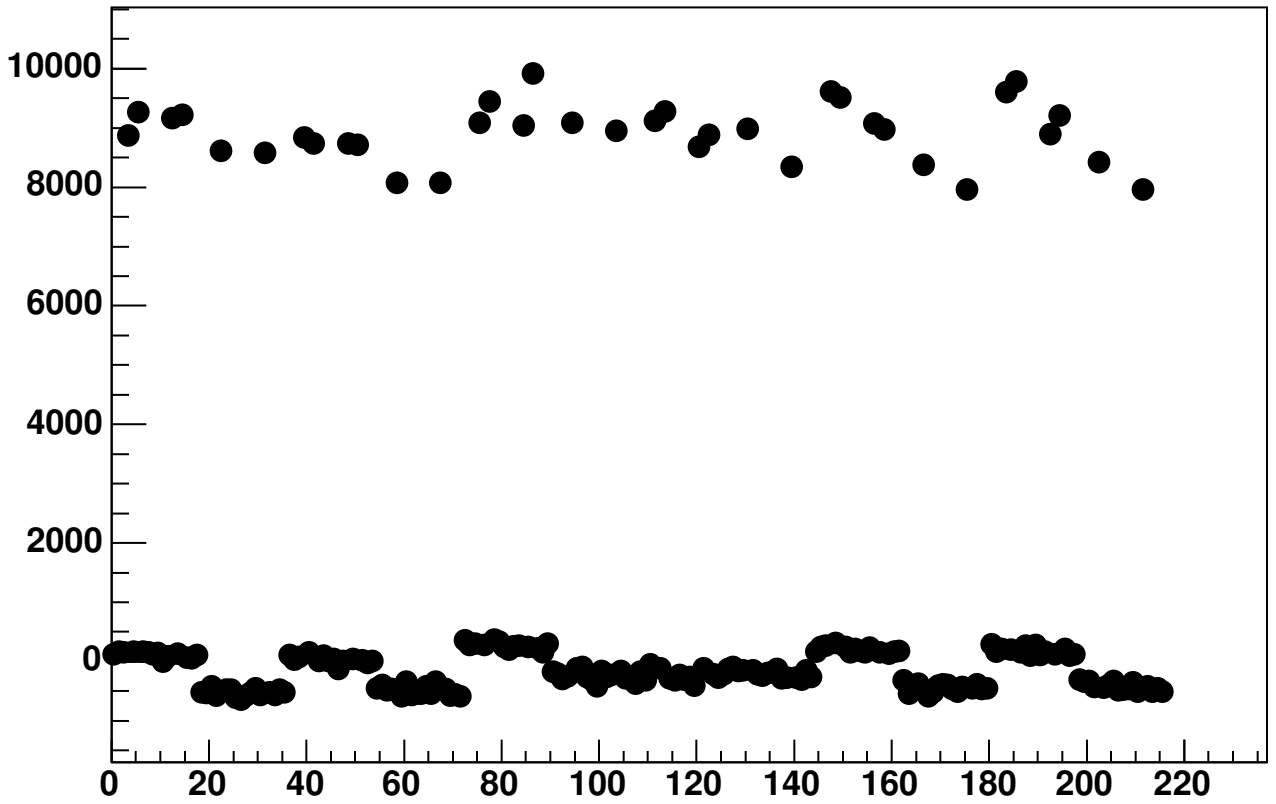
Enable 1, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



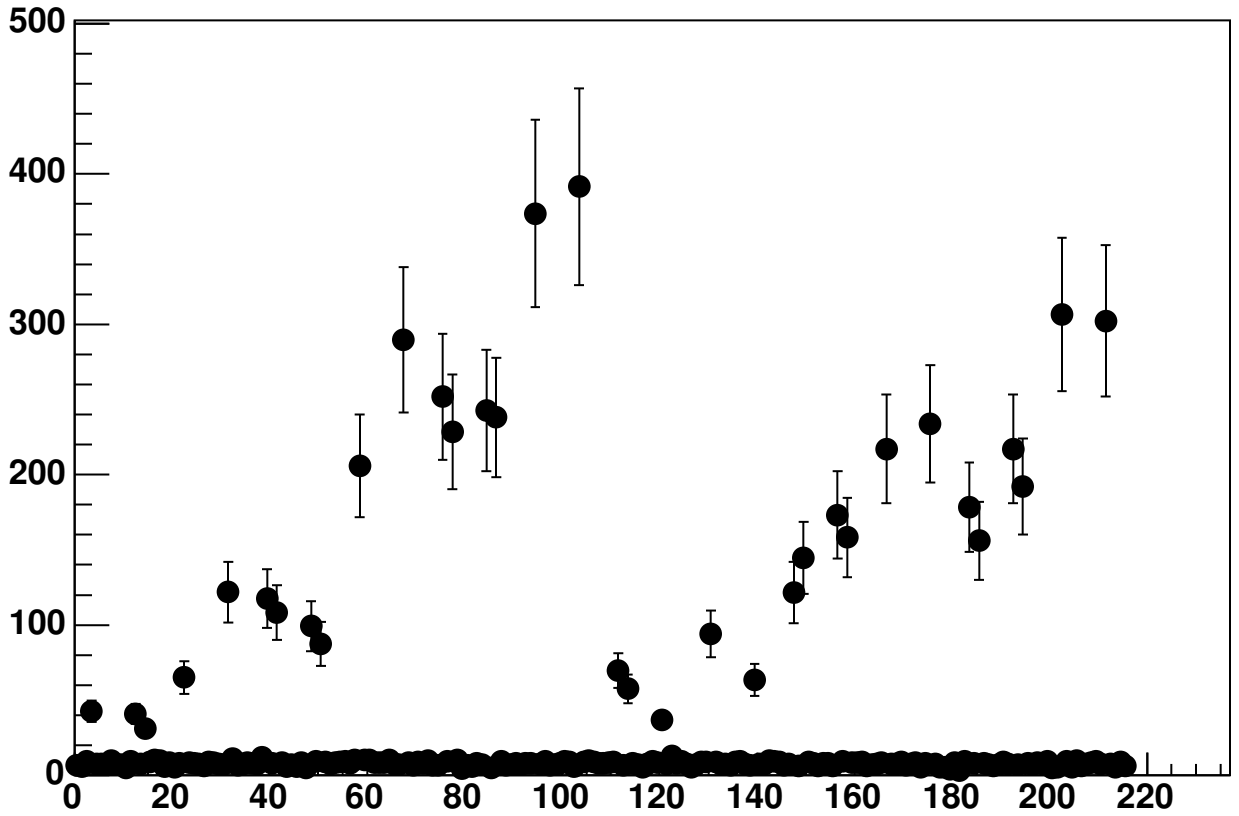
Enable 1, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



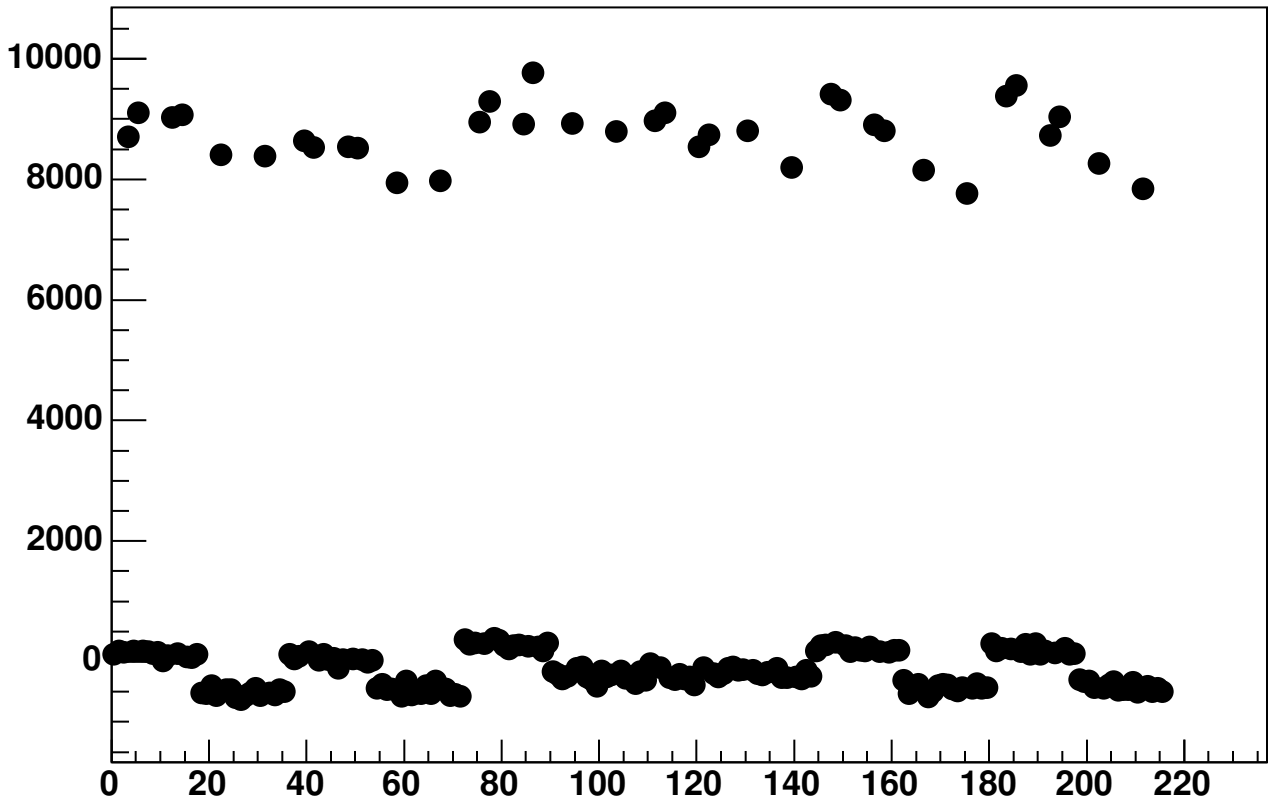
Enable 1, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



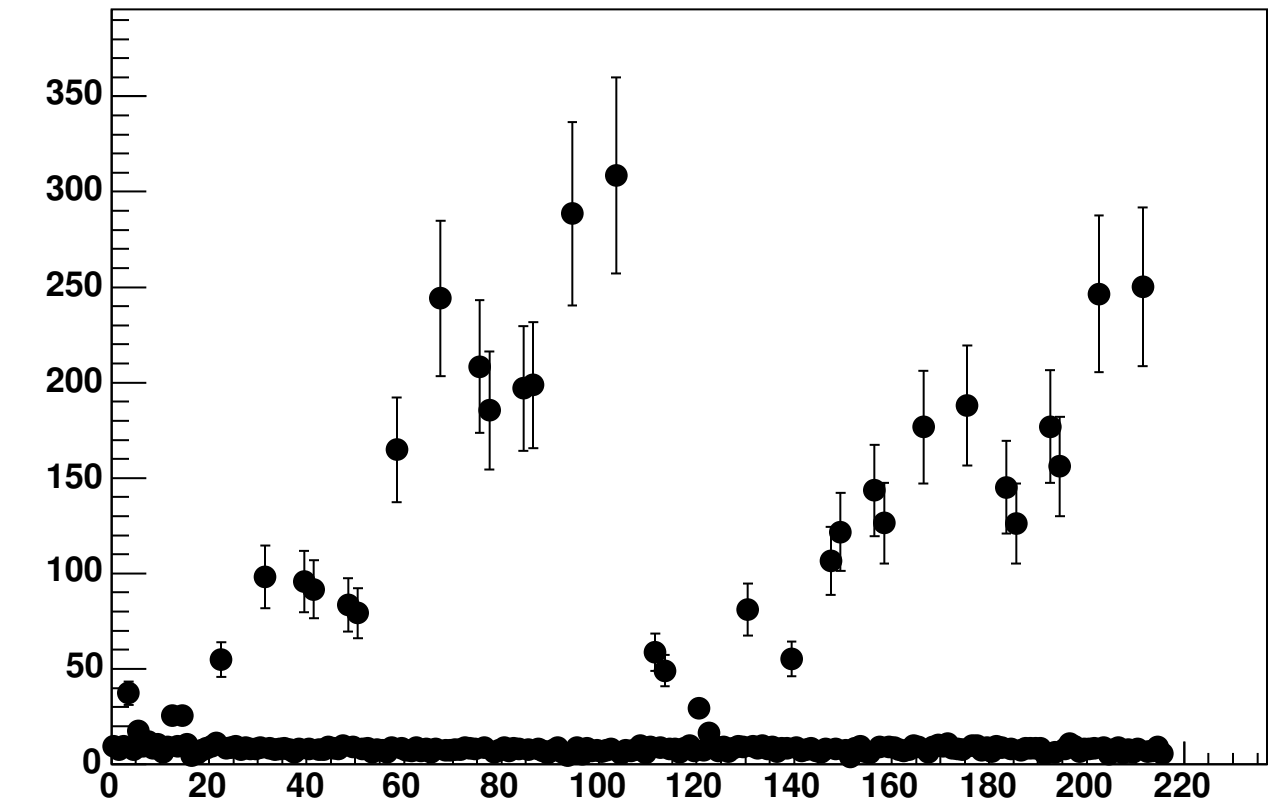
Enable 1, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



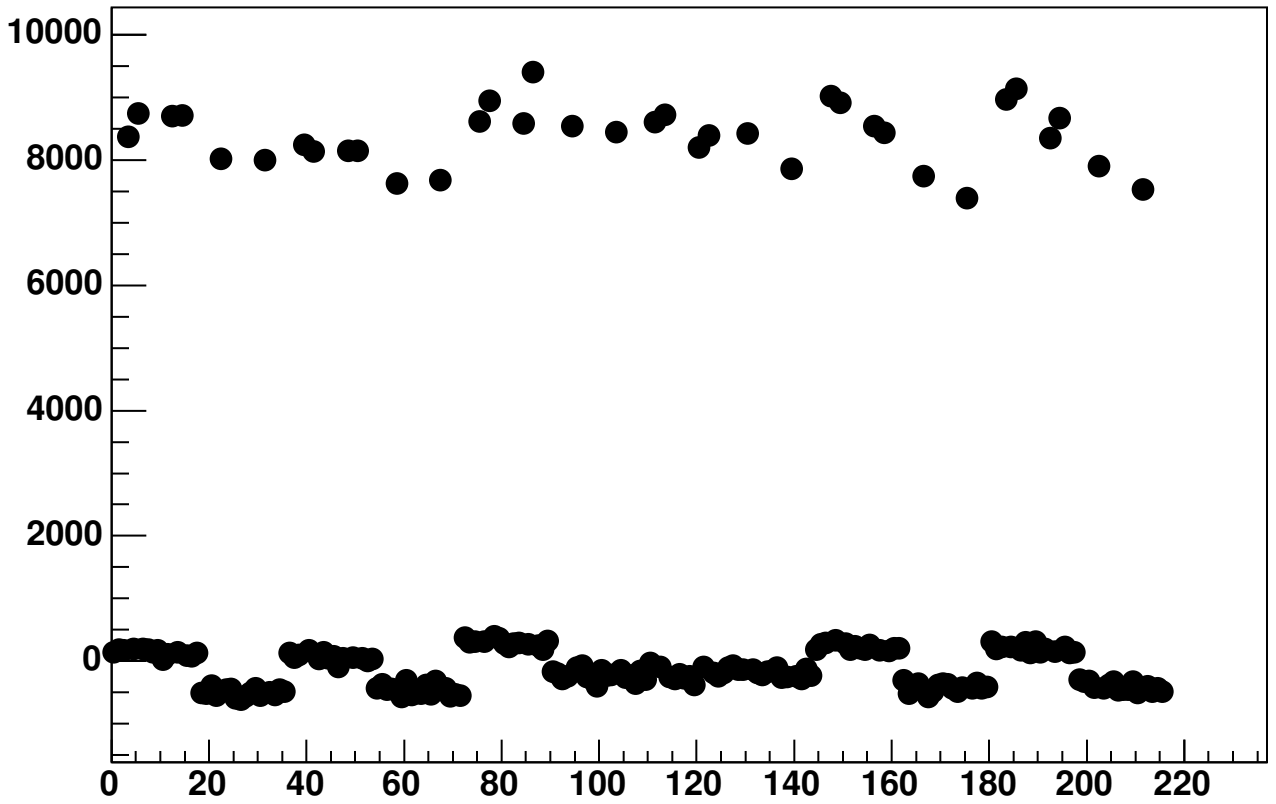
Enable 1, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



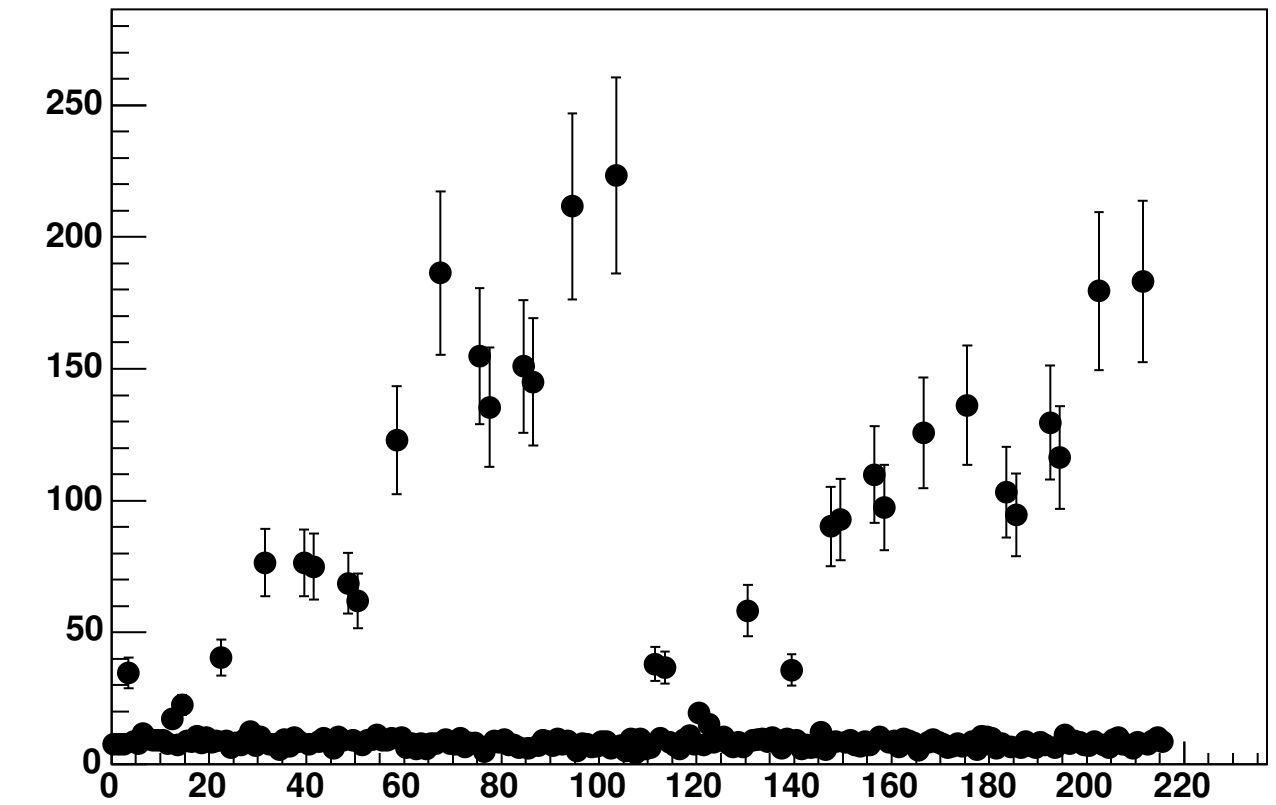
Enable 1, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



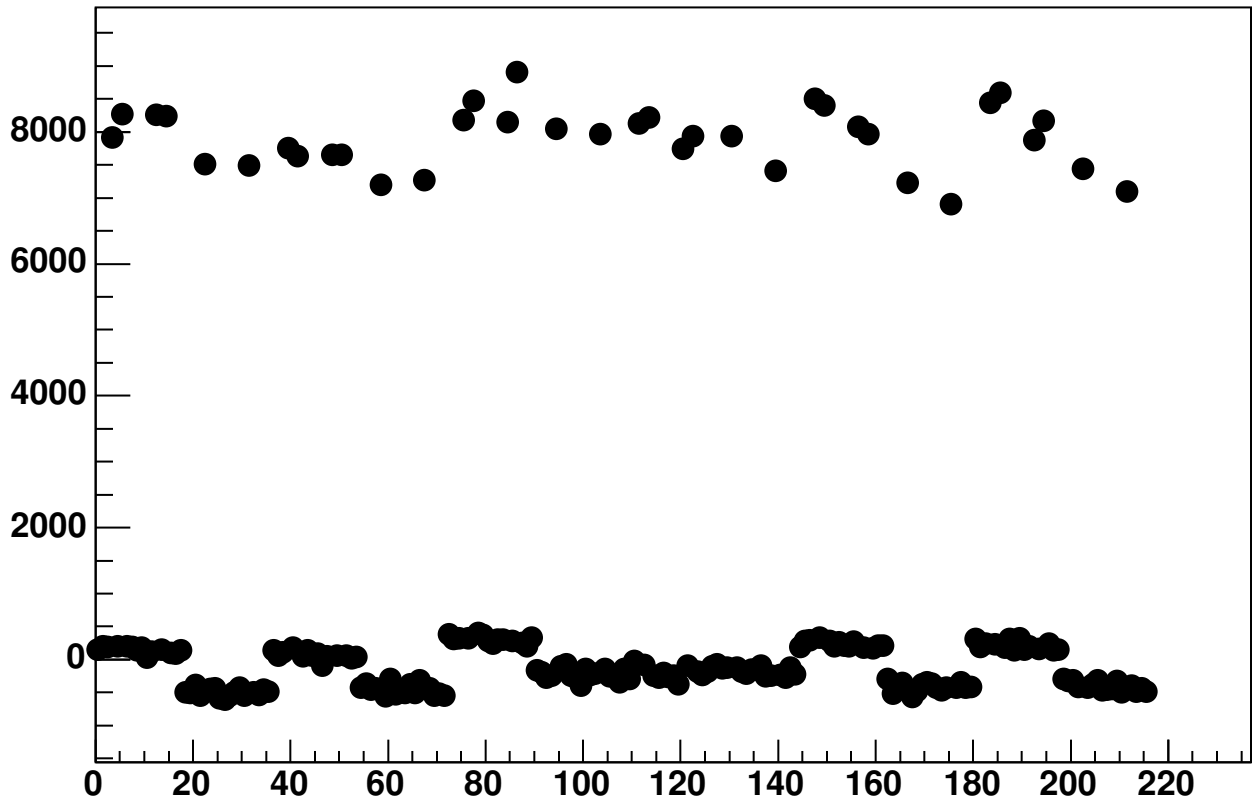
Enable 1, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



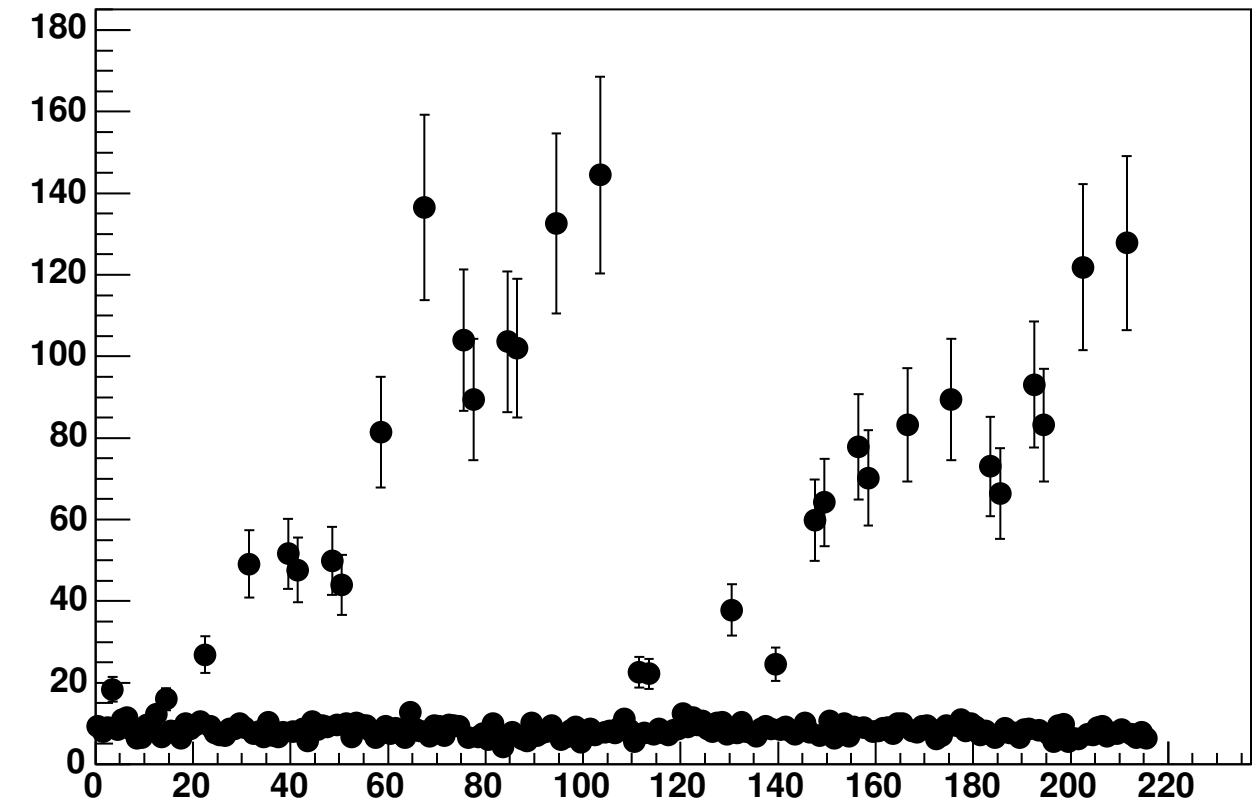
Enable 1, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



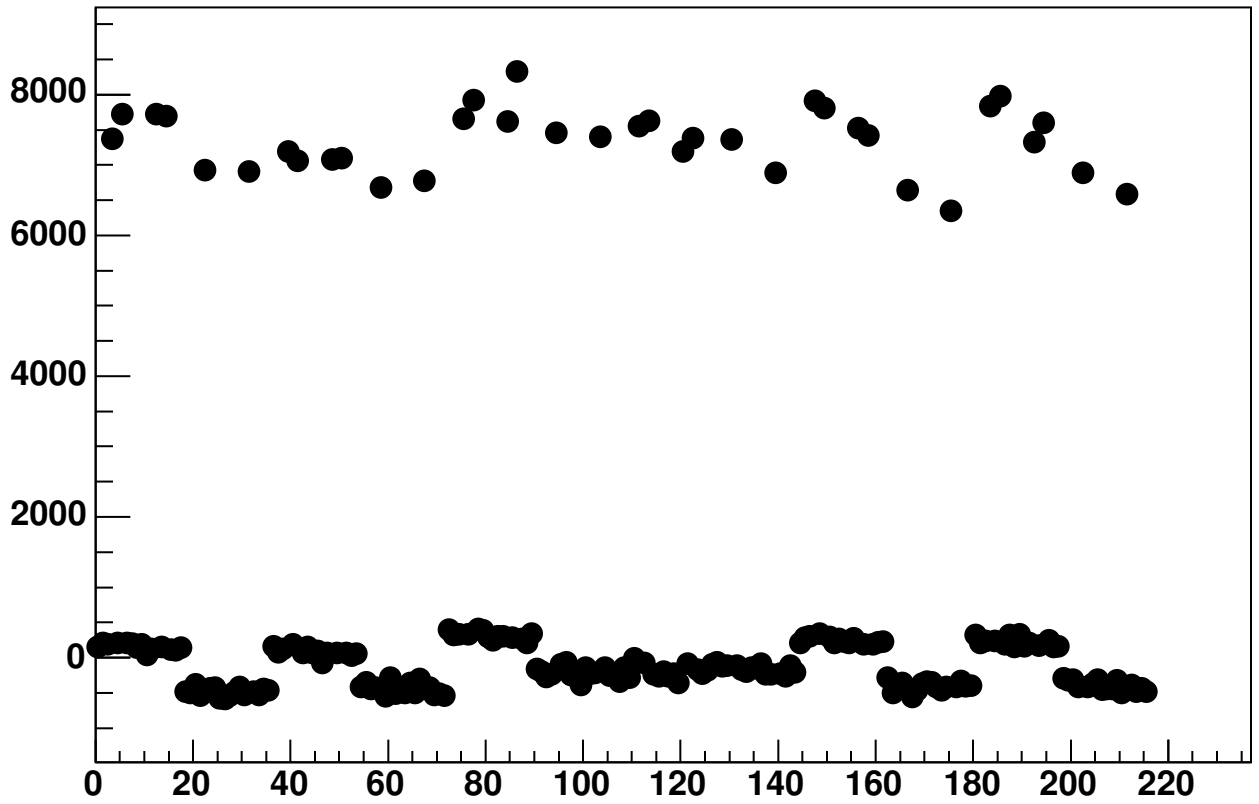
Enable 1, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



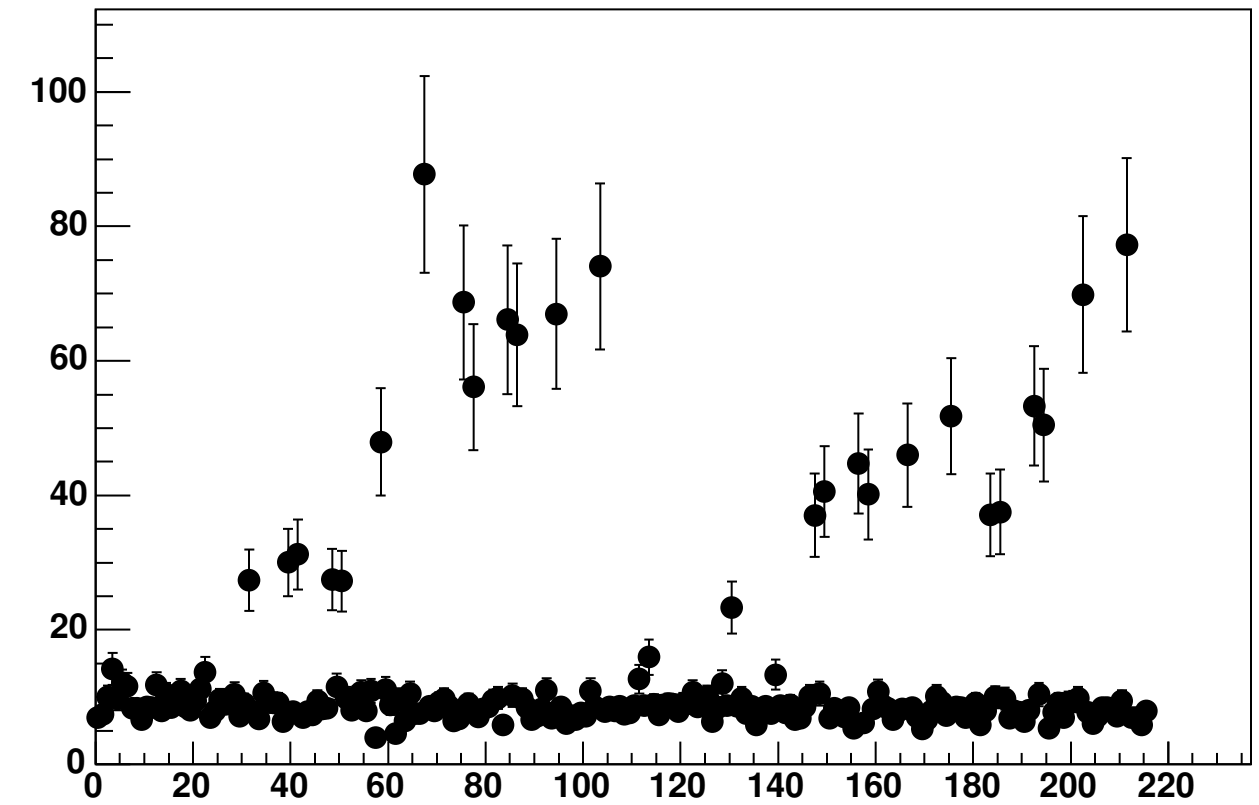
Enable 1, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



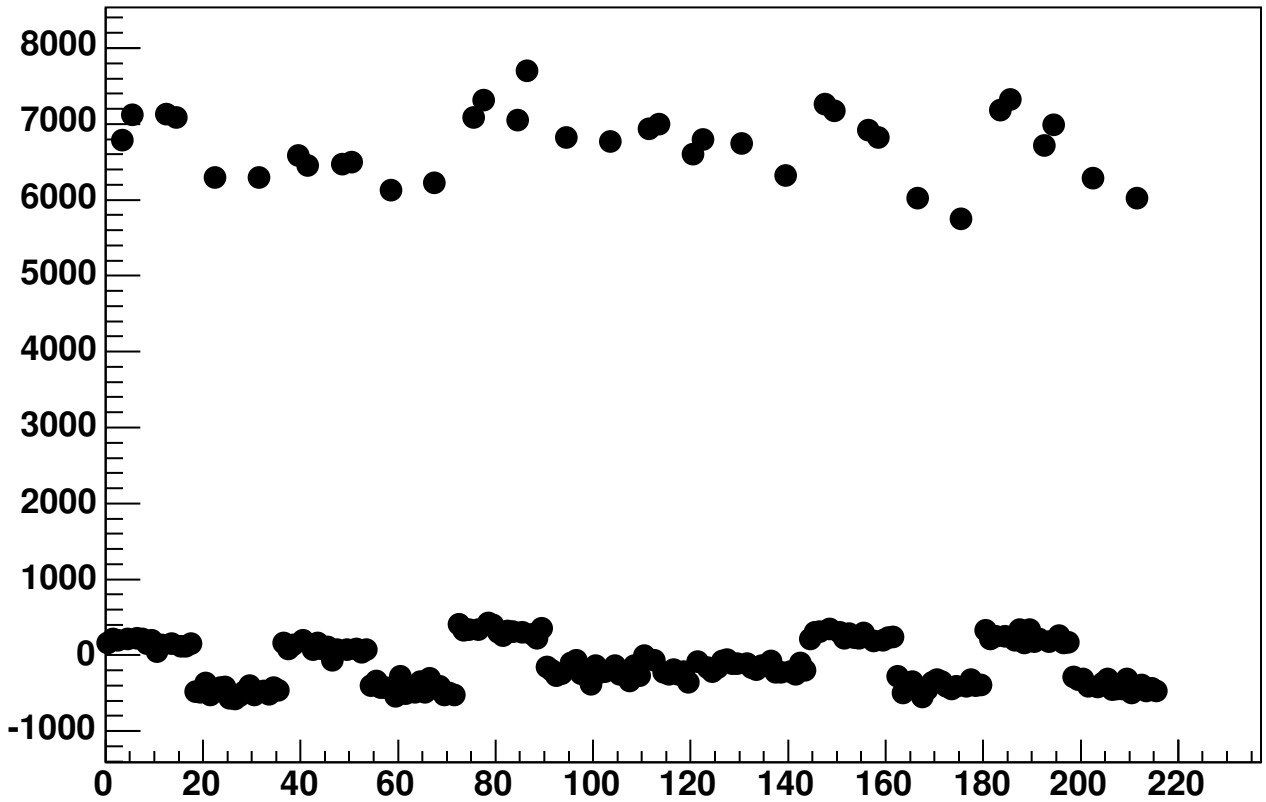
Enable 1, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



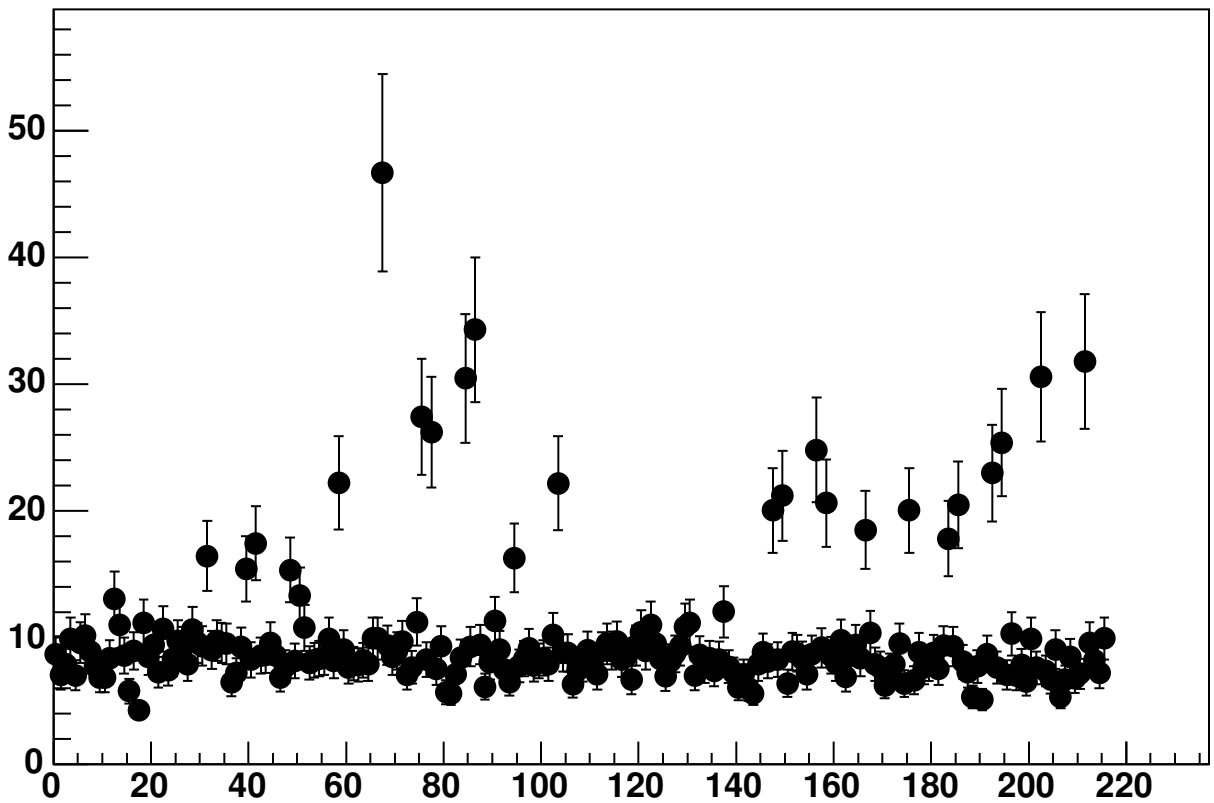
Enable 1, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



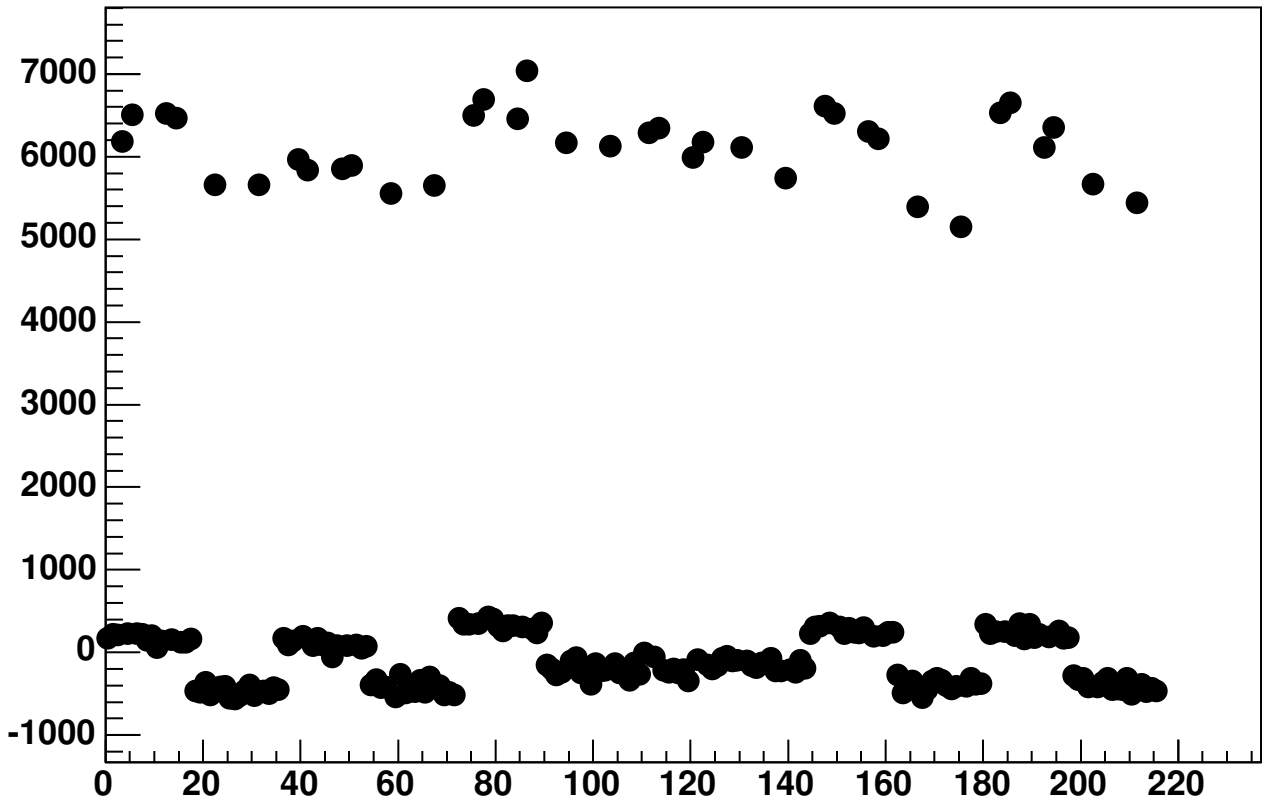
Enable 1, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



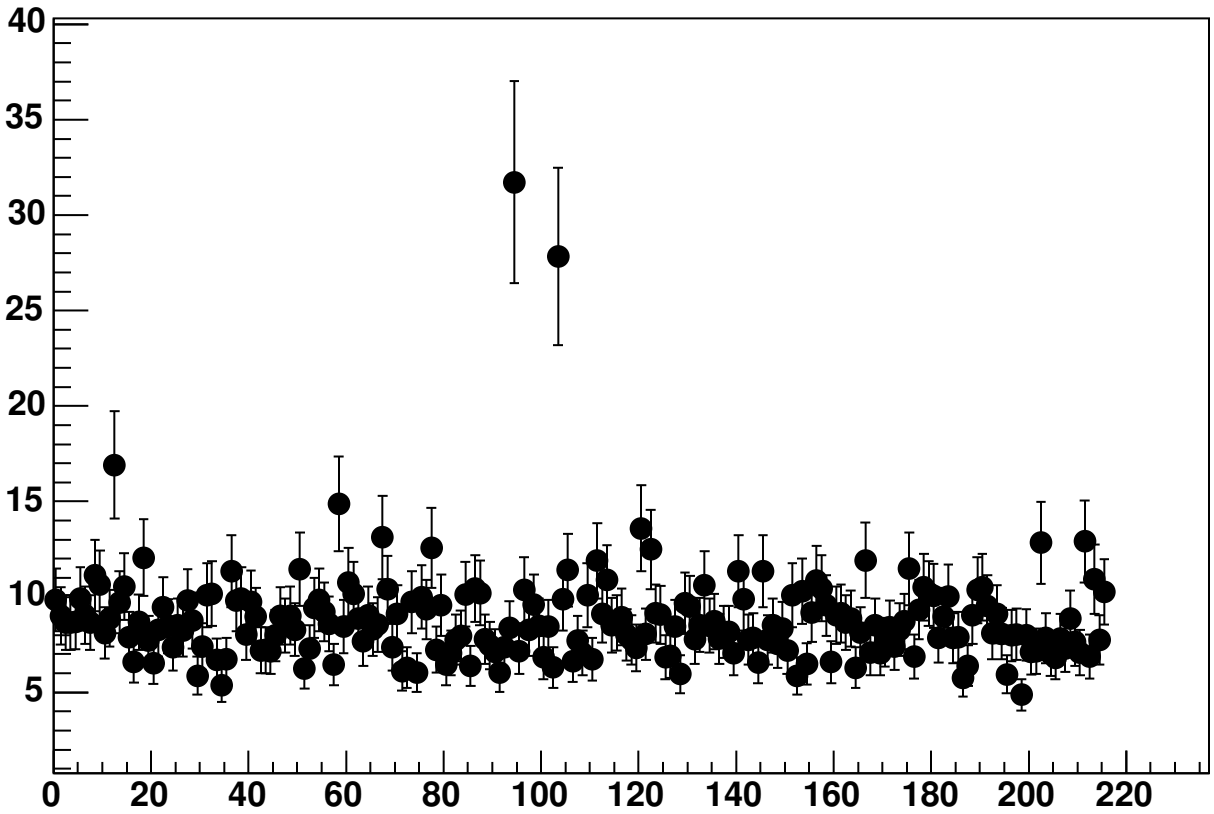
Enable 1, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



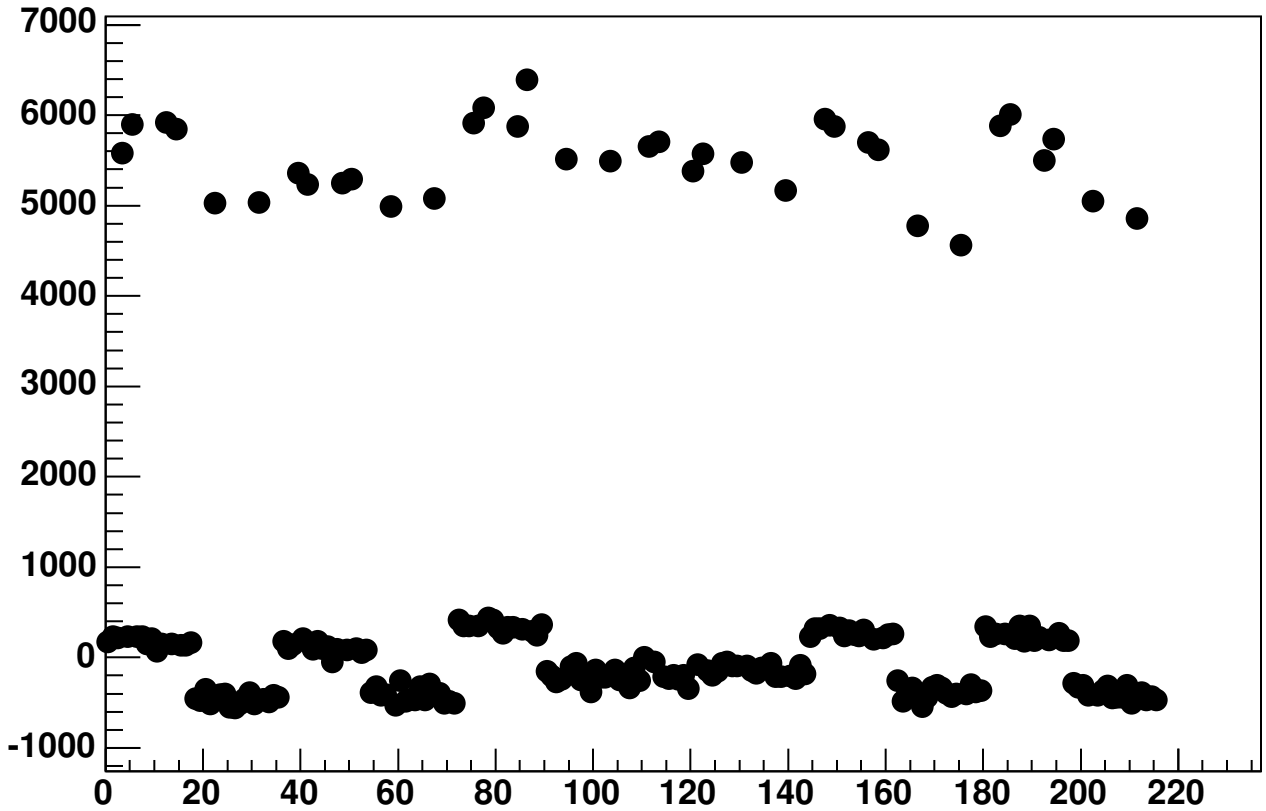
Enable 1, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



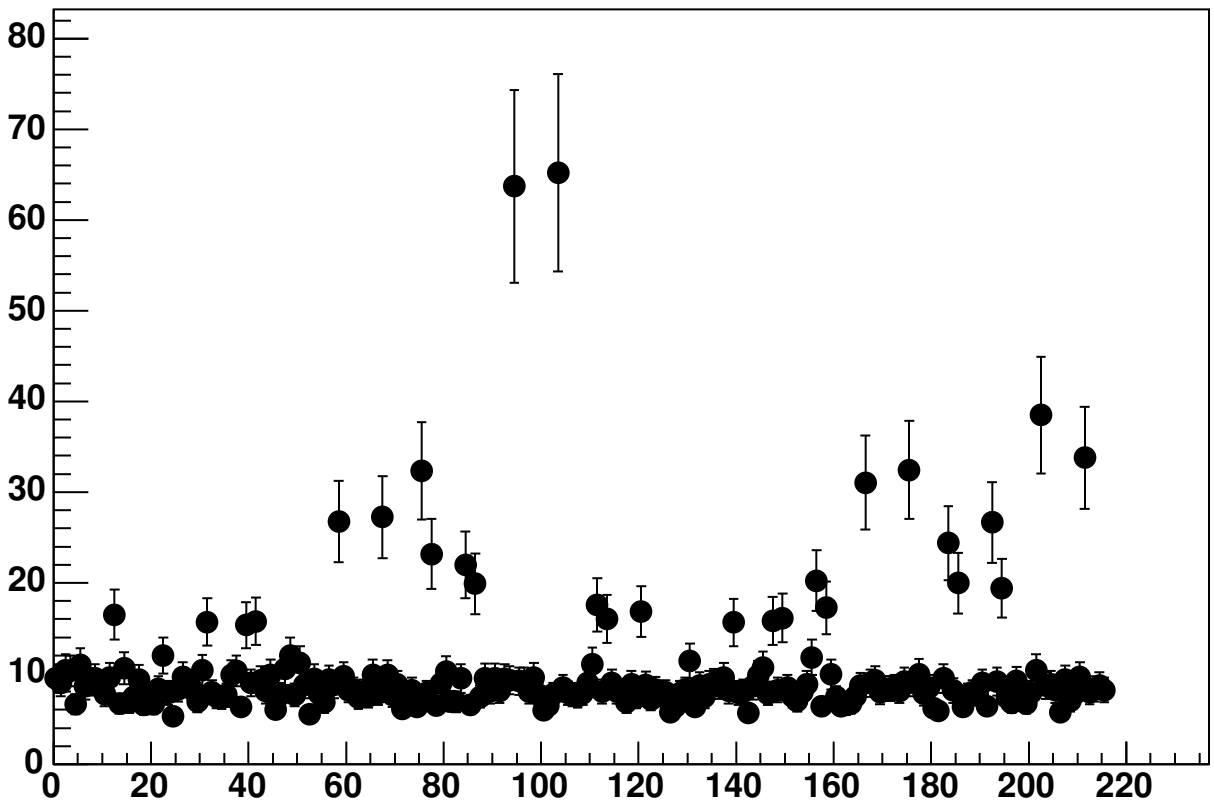
Enable 1, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



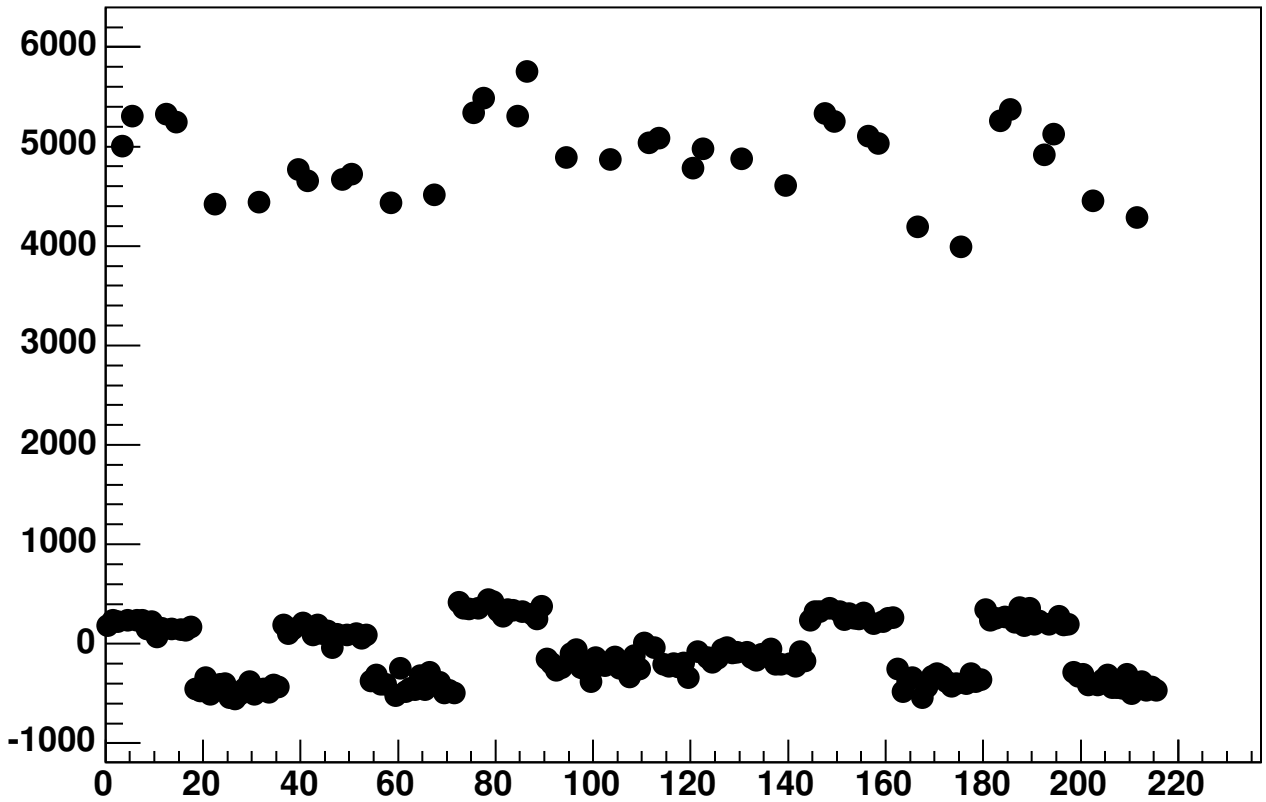
Enable 1, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



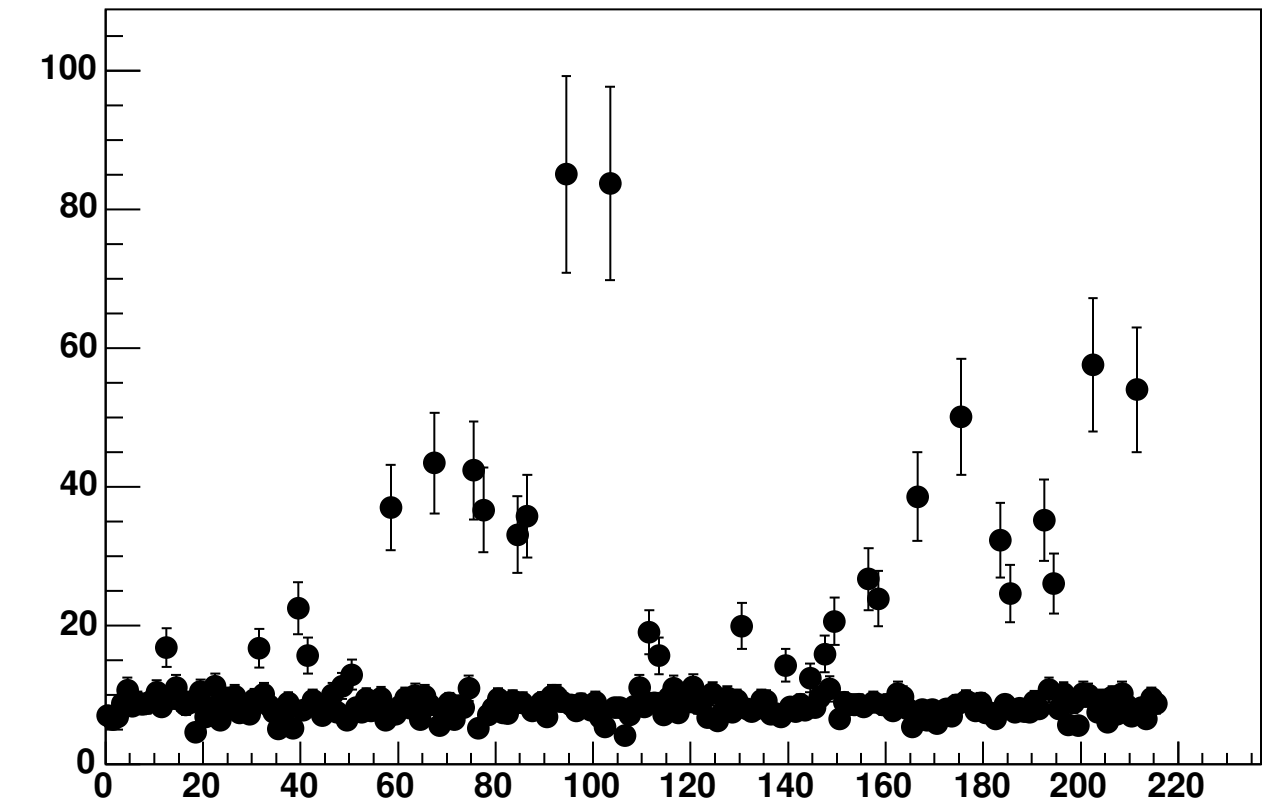
Enable 1, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



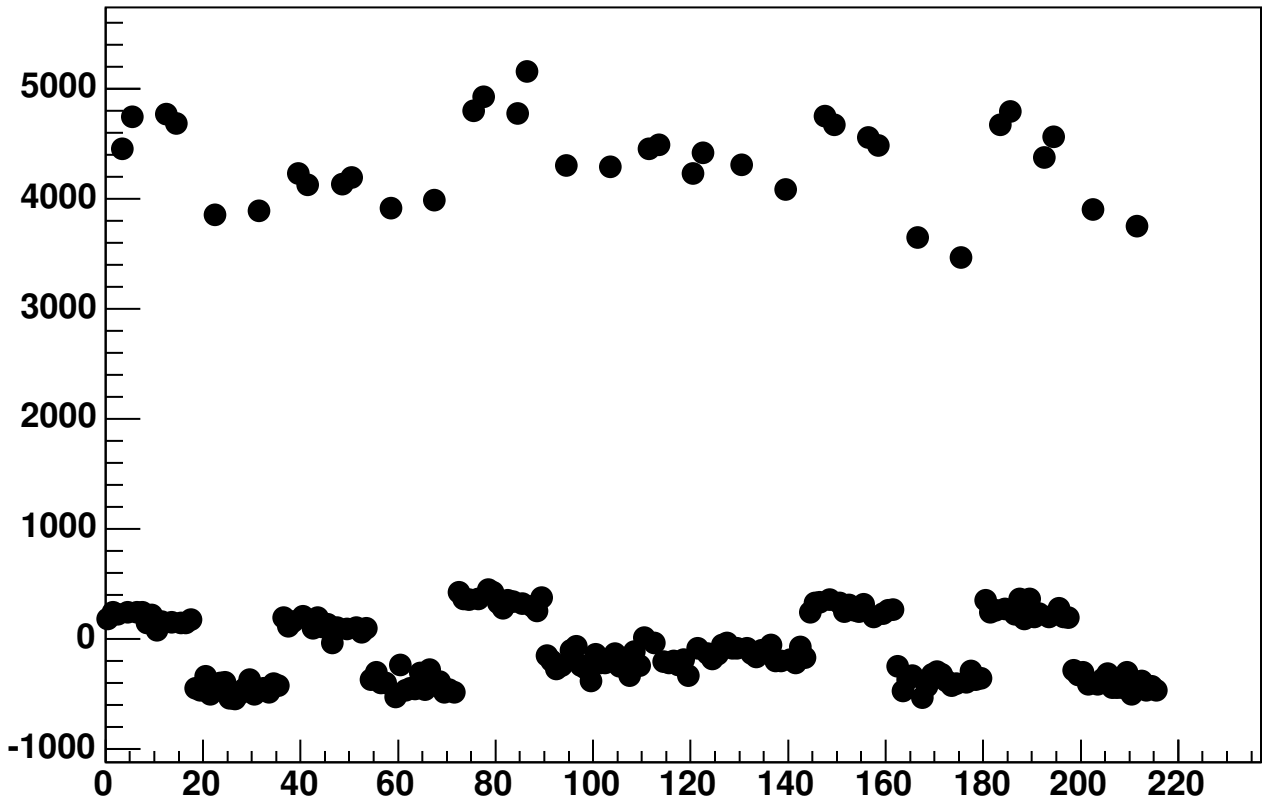
Enable 1, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



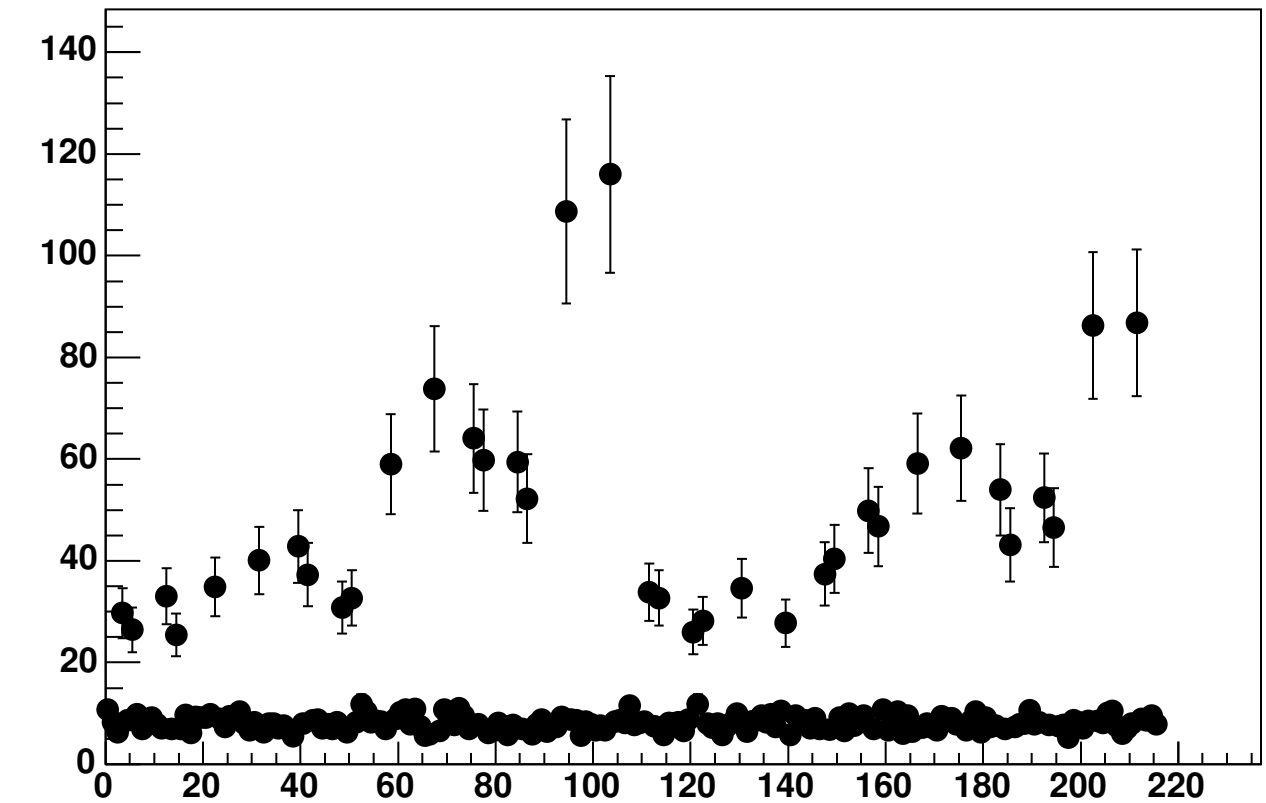
Enable 1, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



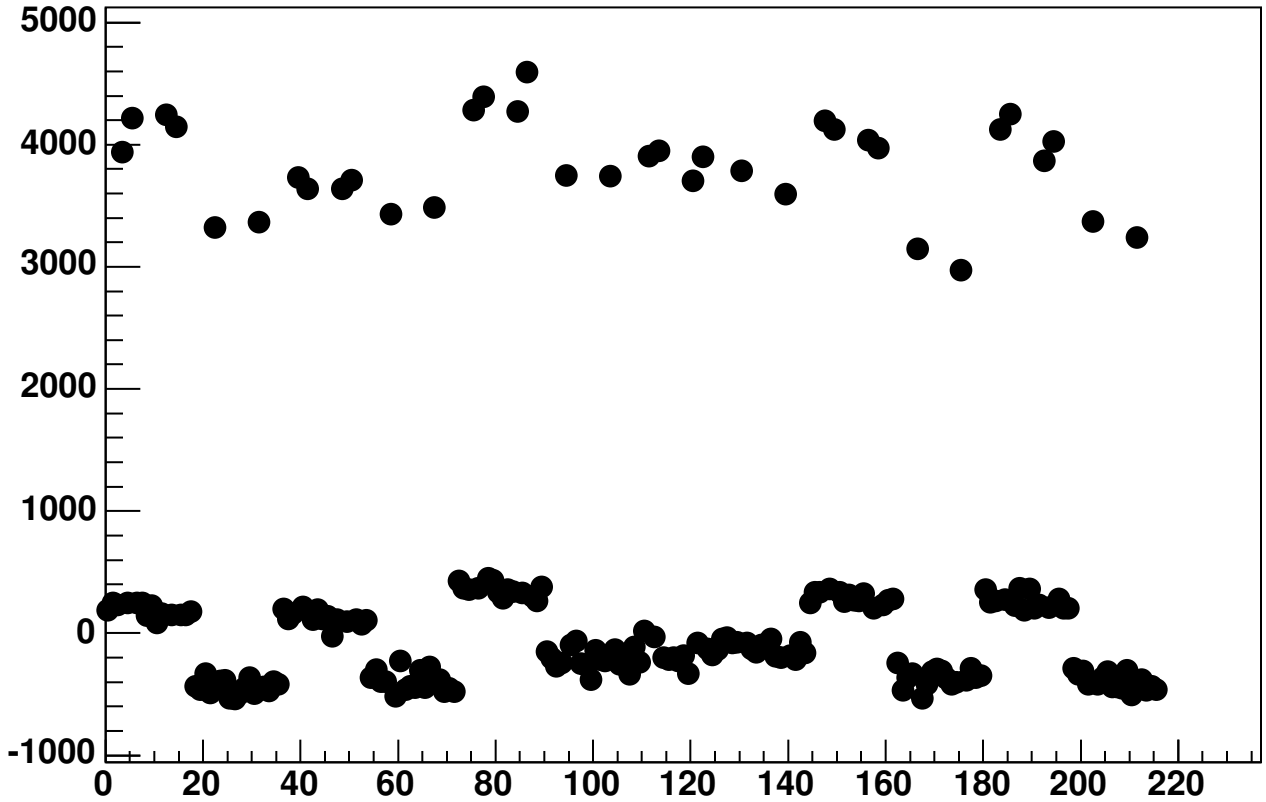
Enable 1, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



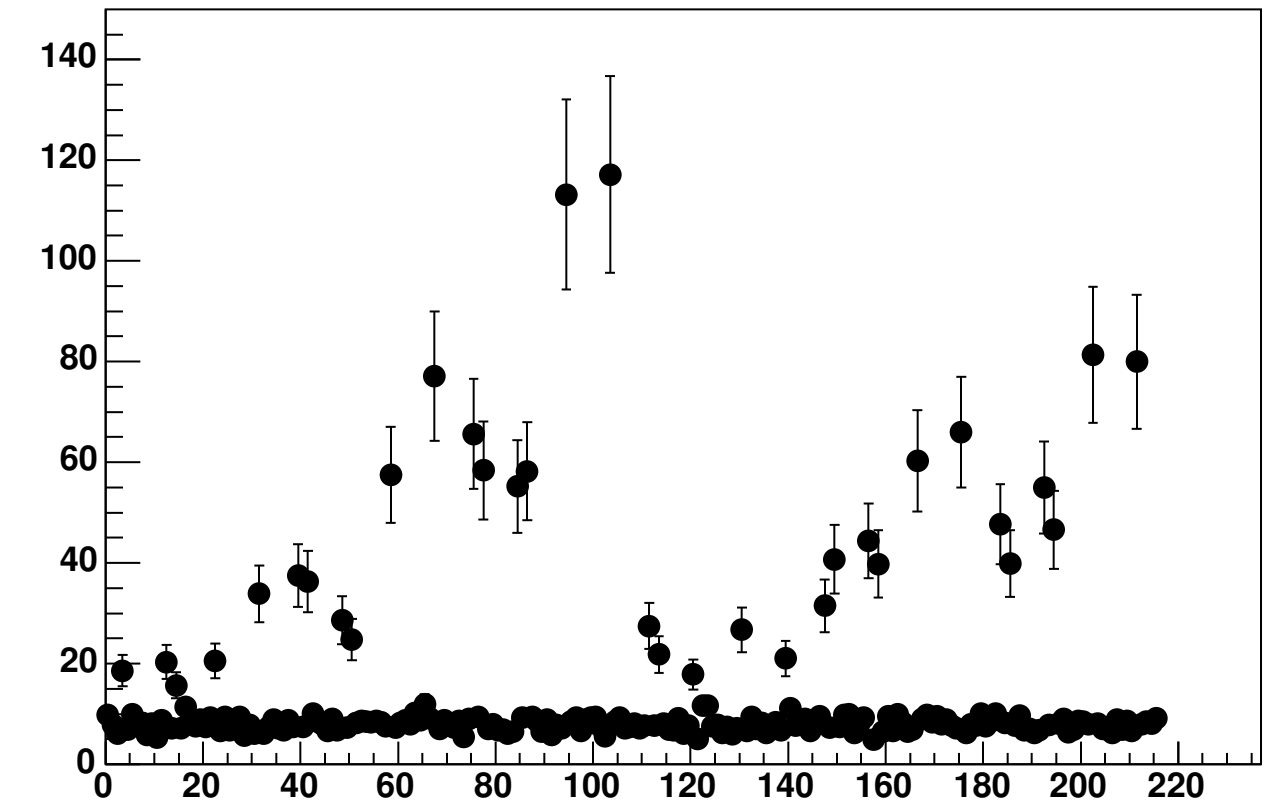
Enable 1, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



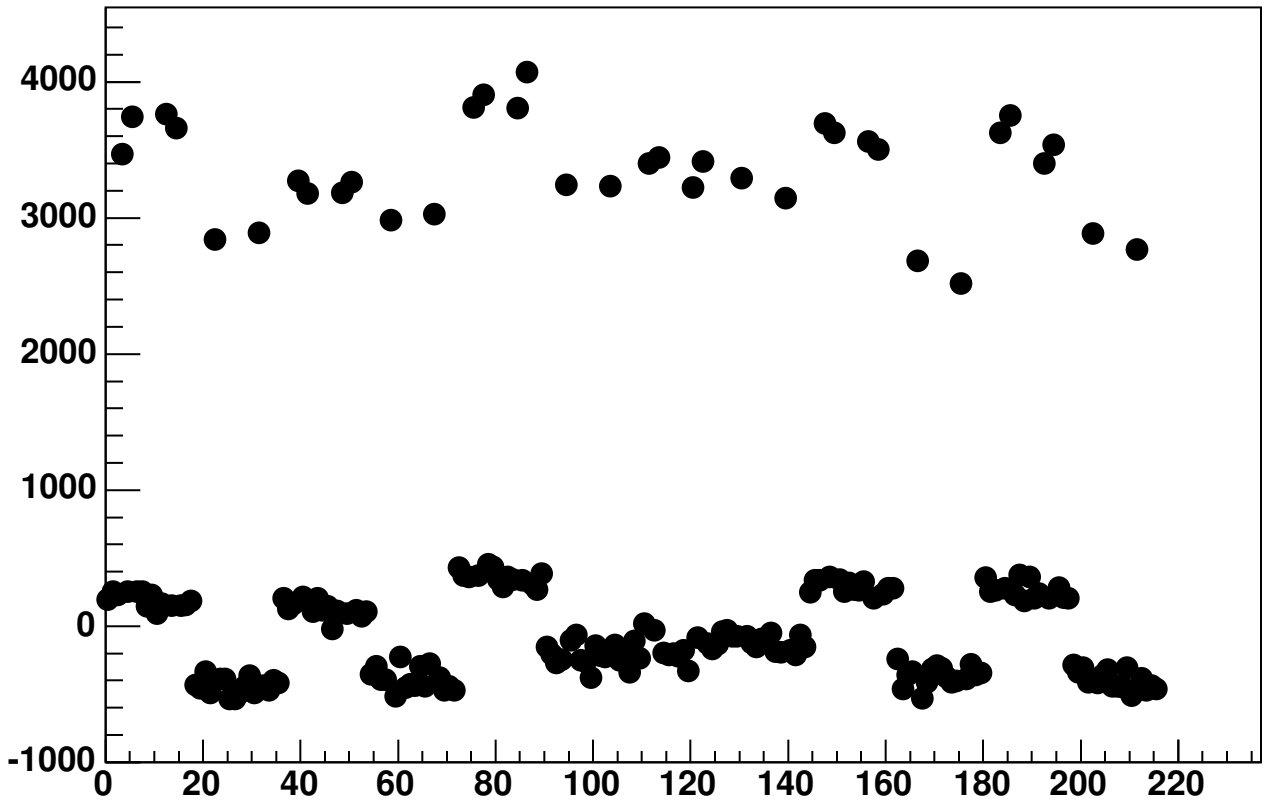
Enable 1, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



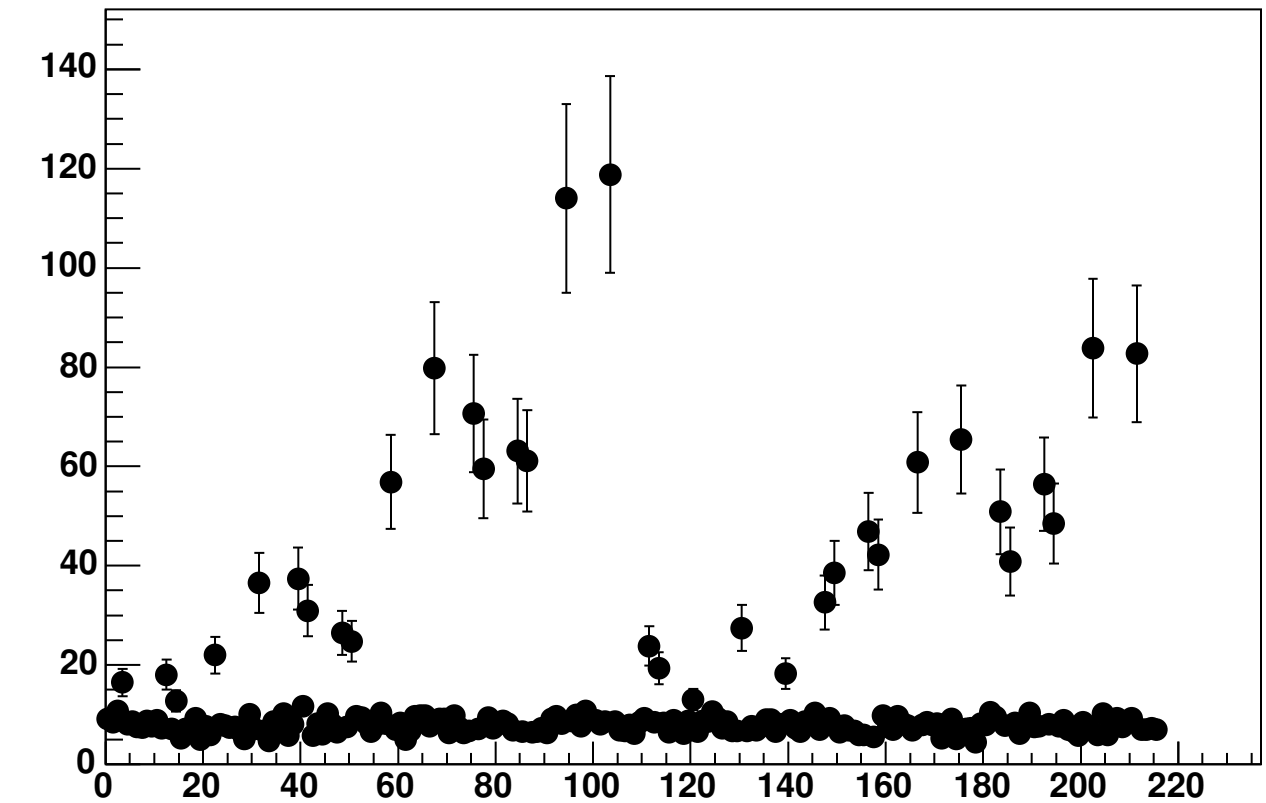
Enable 1, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



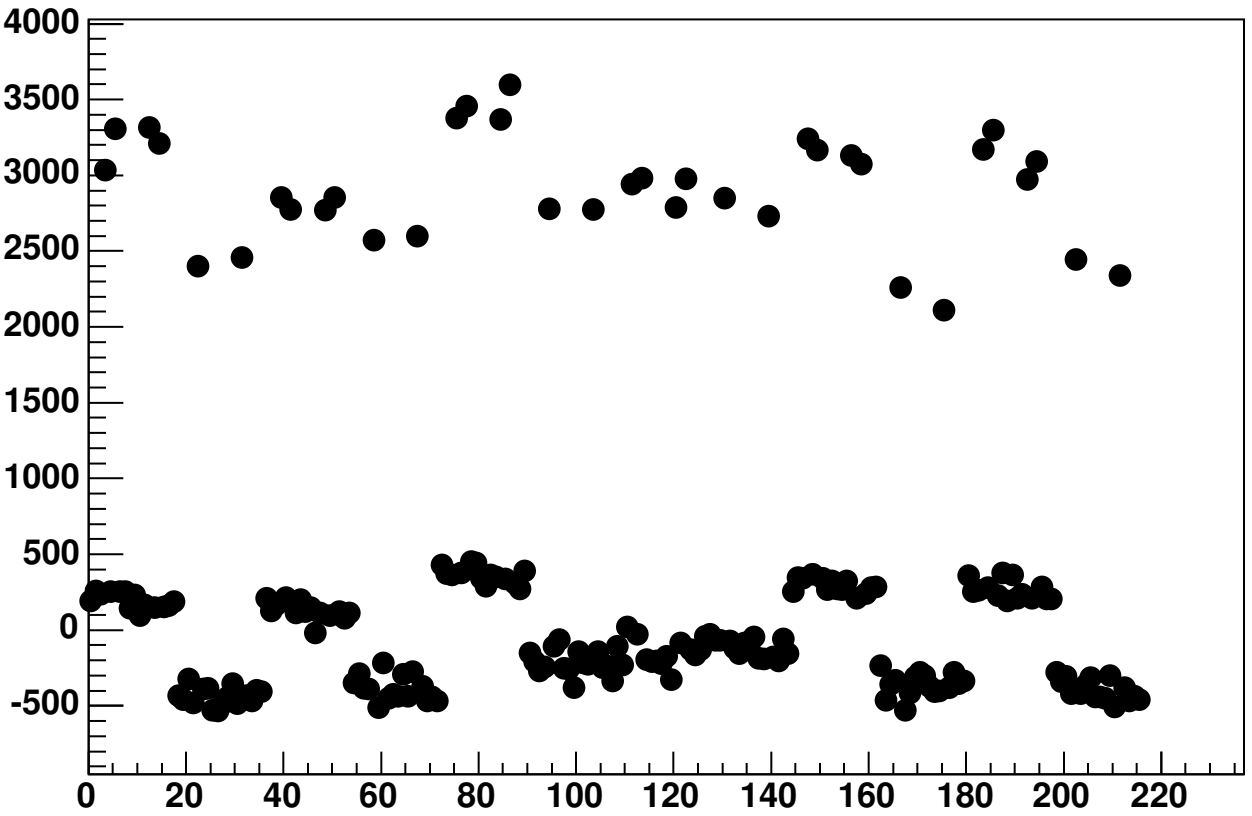
Enable 1, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



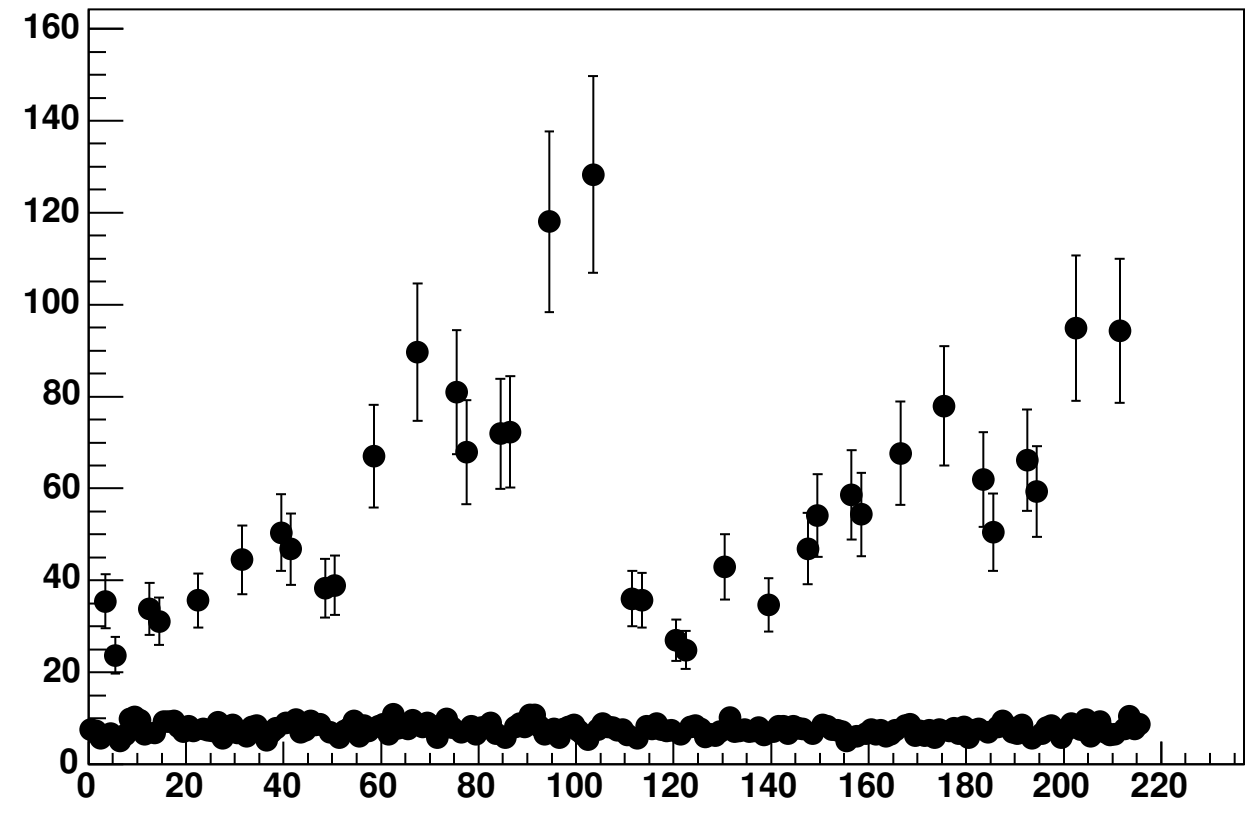
Enable 1, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



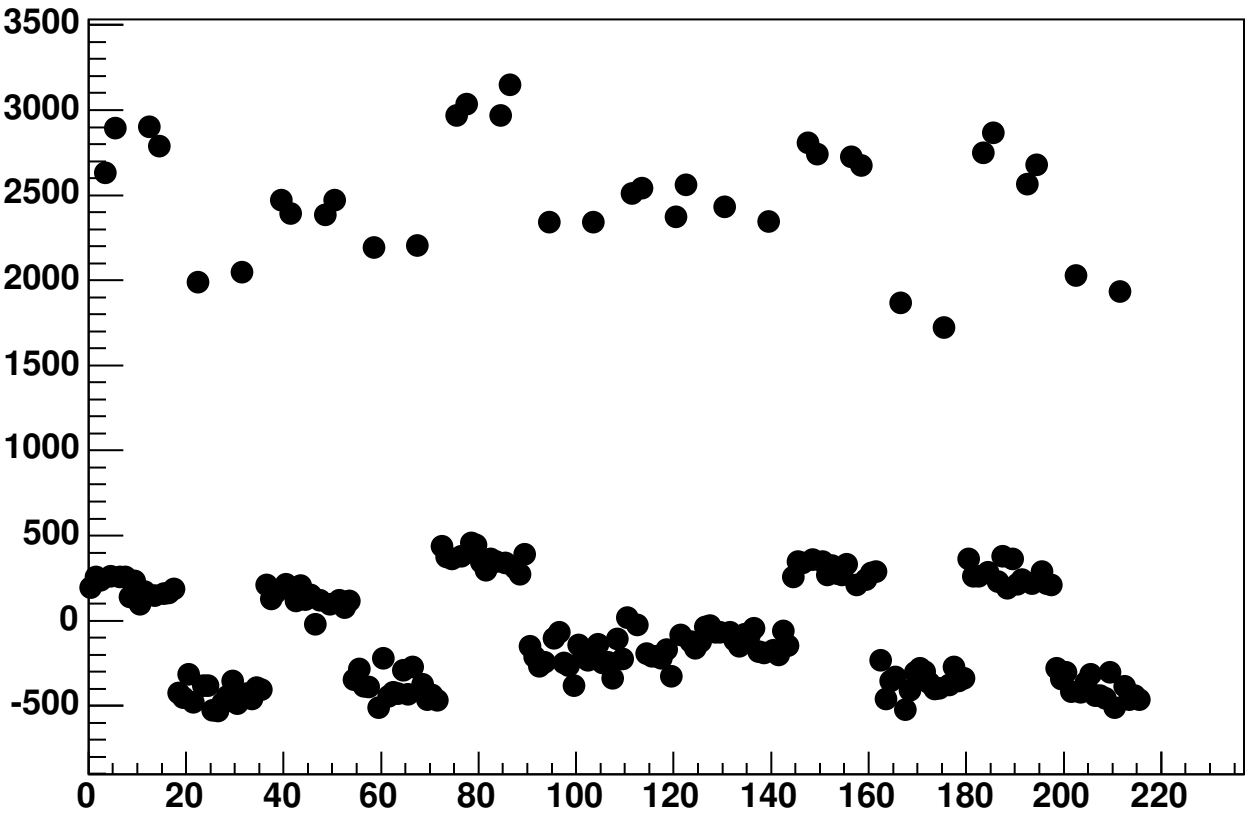
Enable 1, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



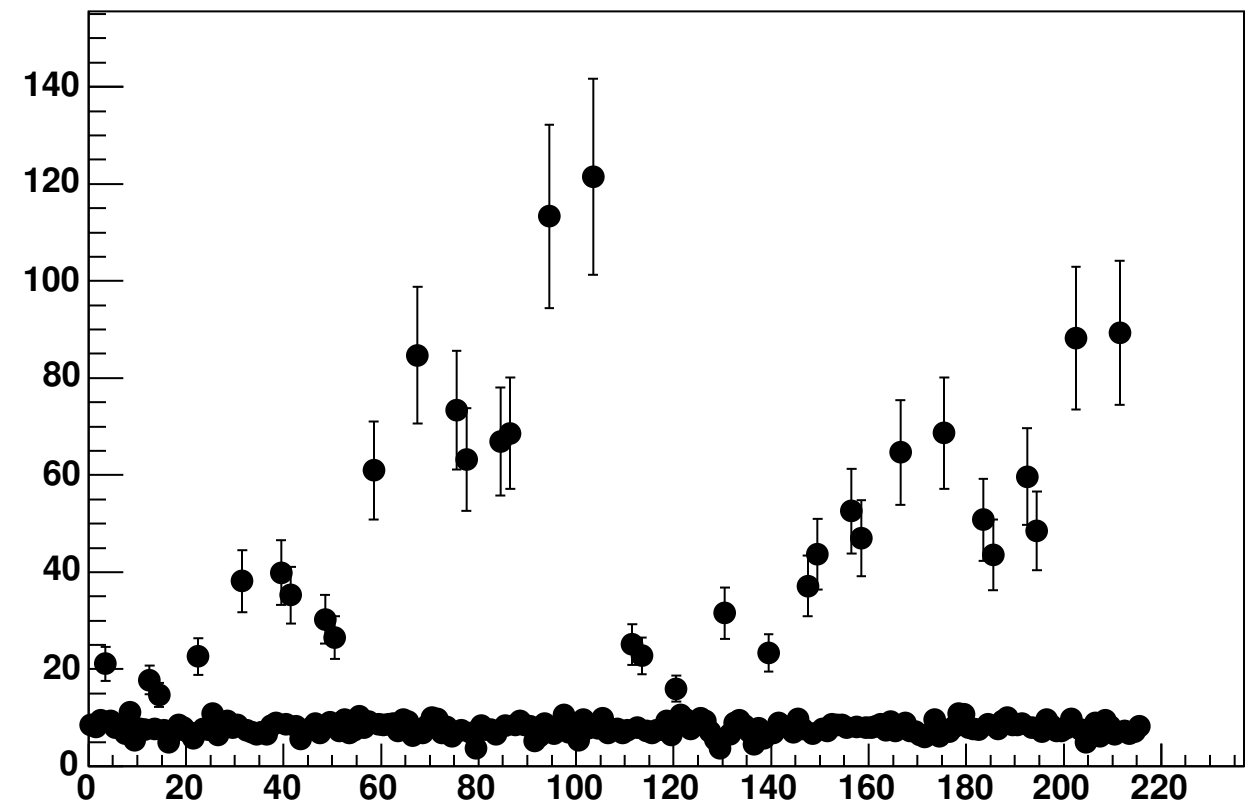
Enable 1, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



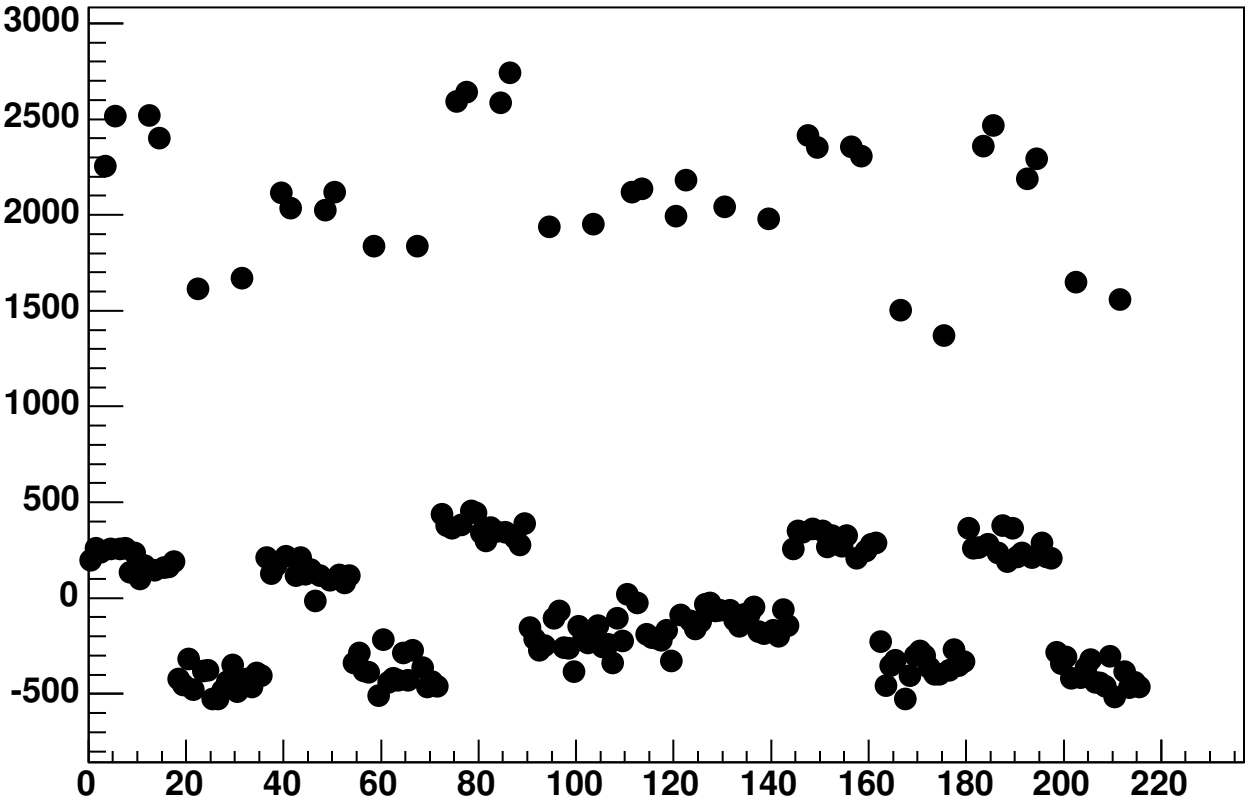
Enable 1, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



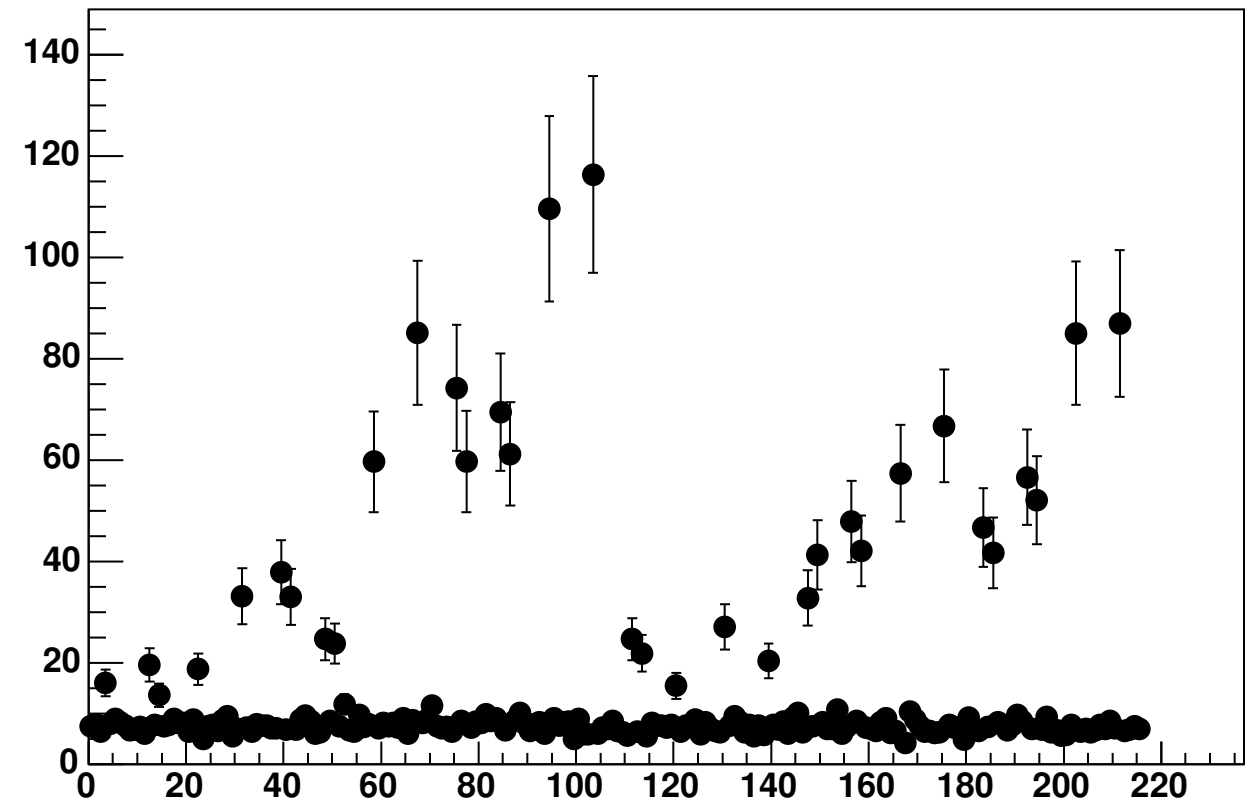
Enable 1, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



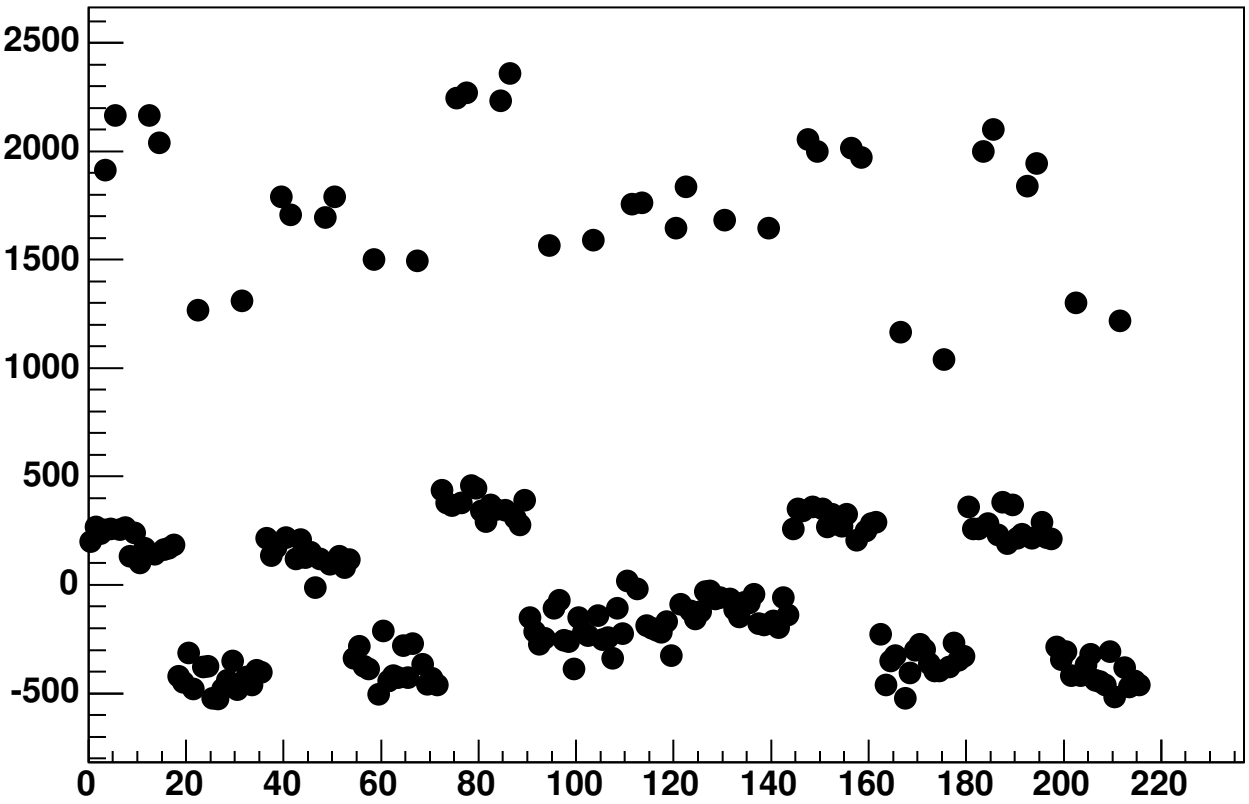
Enable 1, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



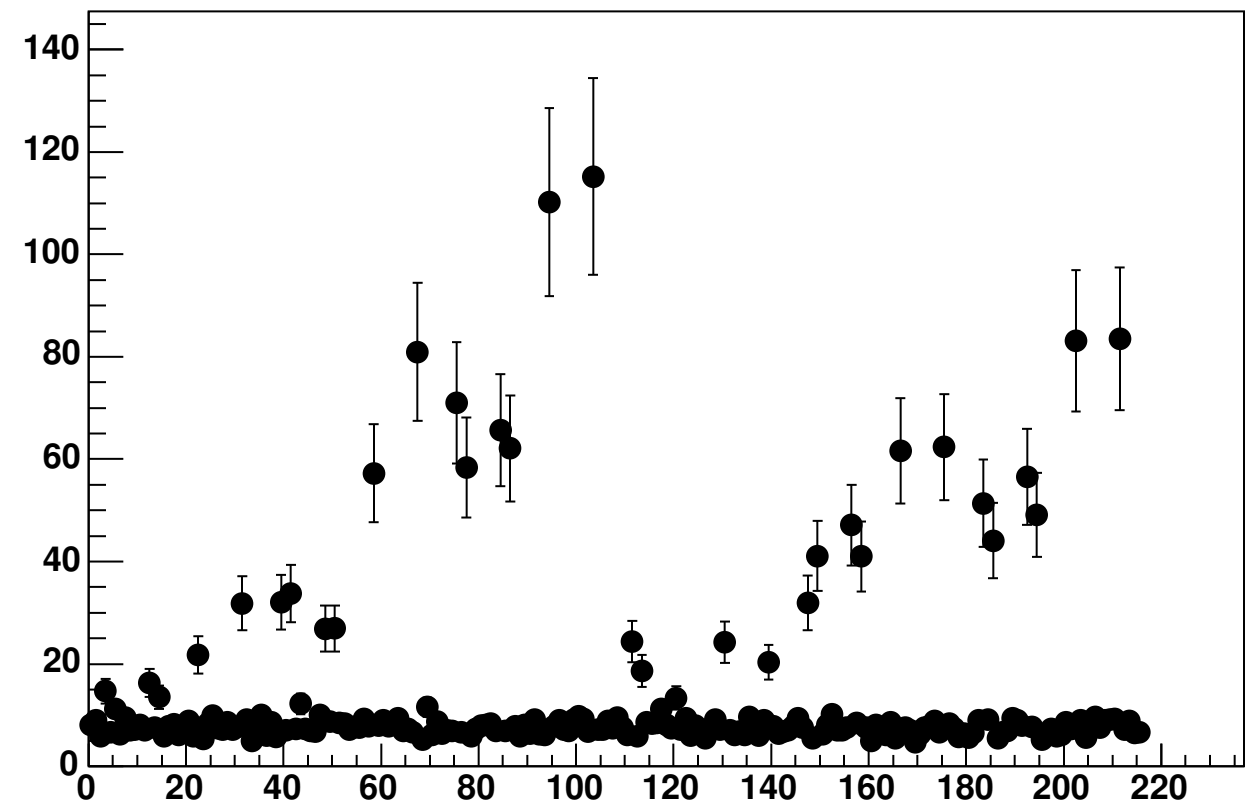
Enable 1, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



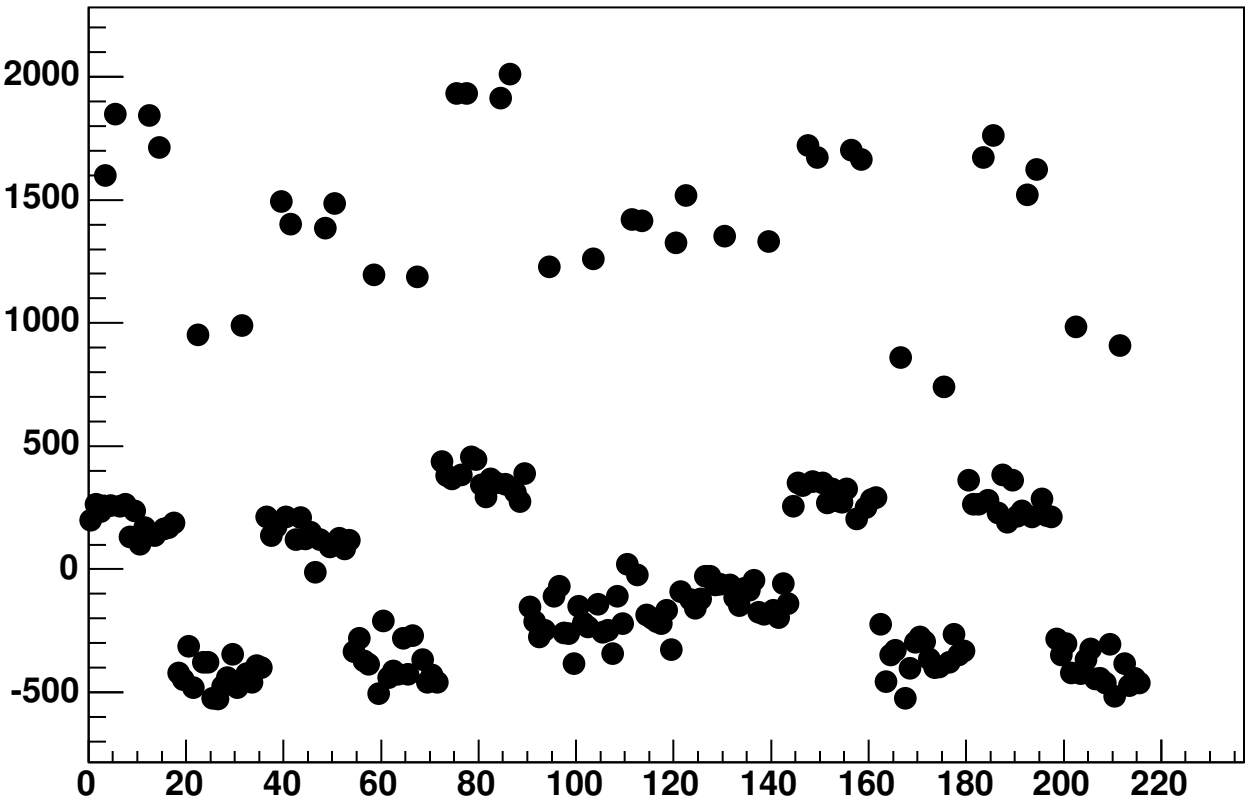
Enable 1, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



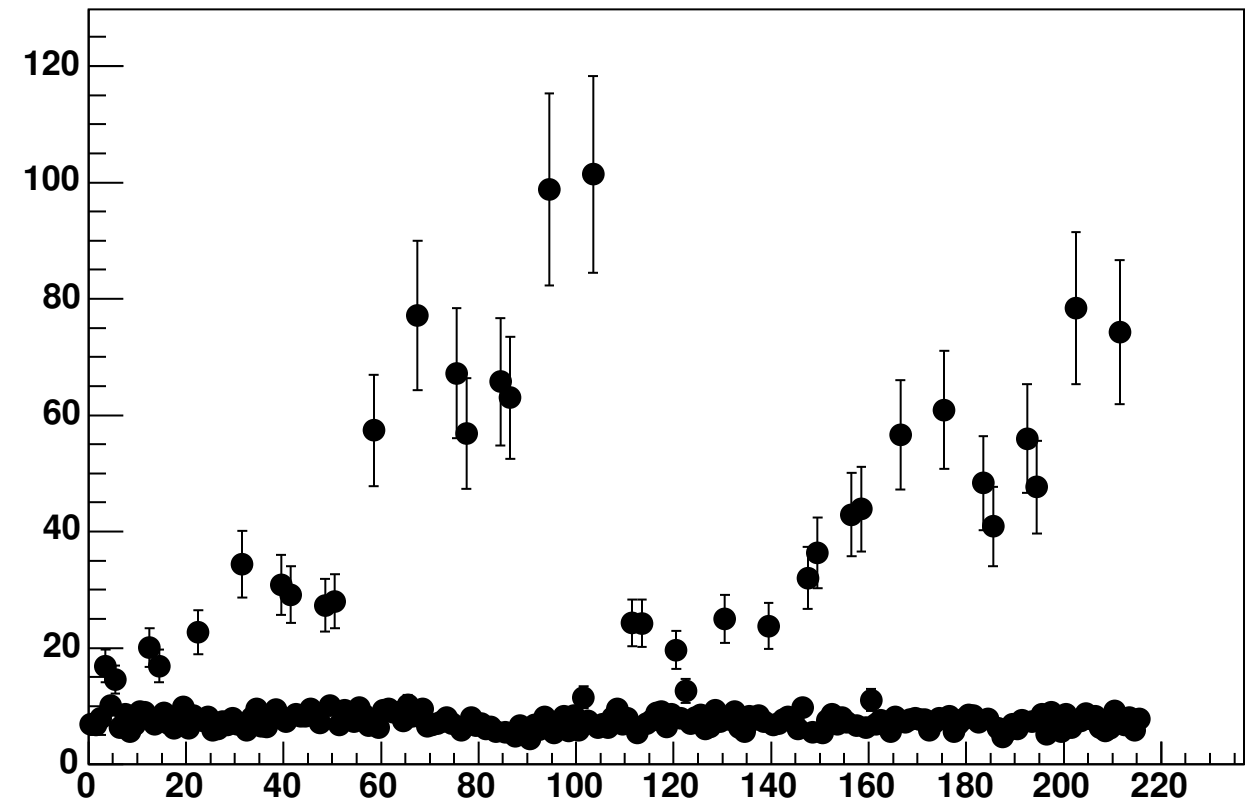
Enable 1, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



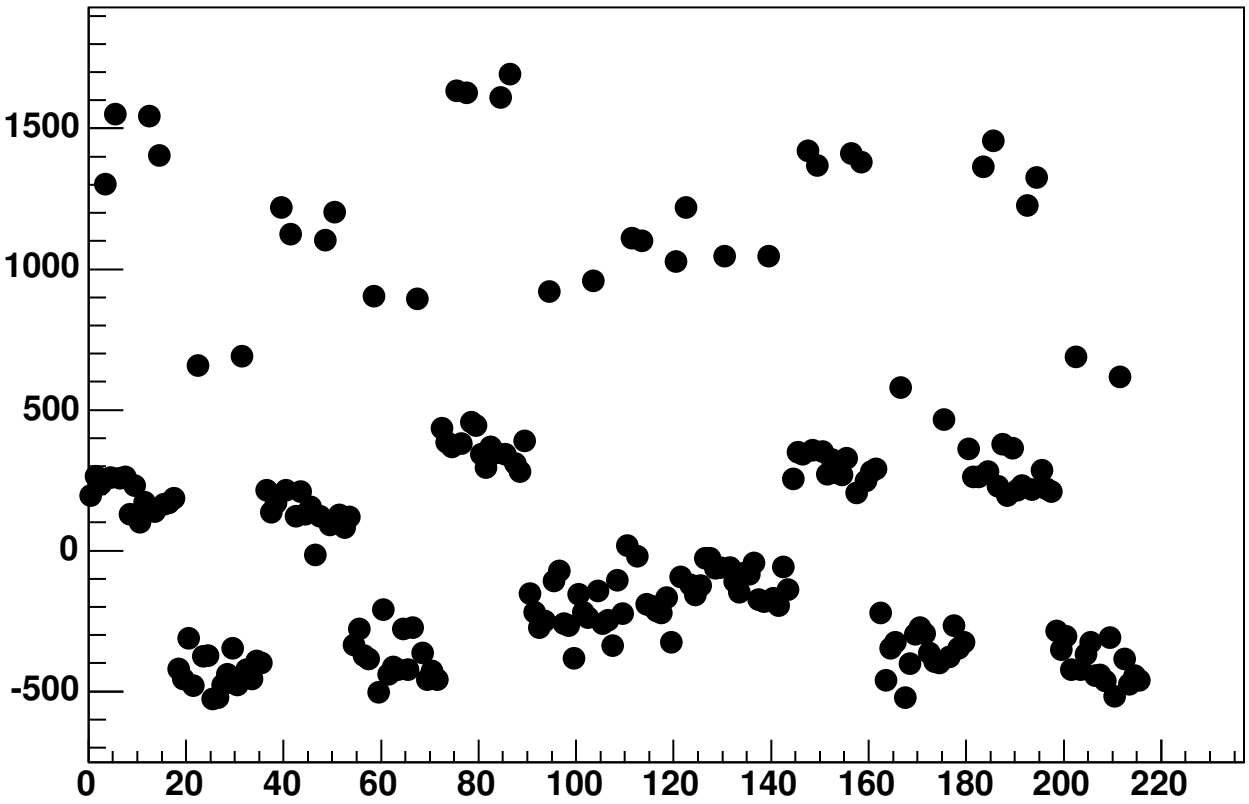
Enable 1, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



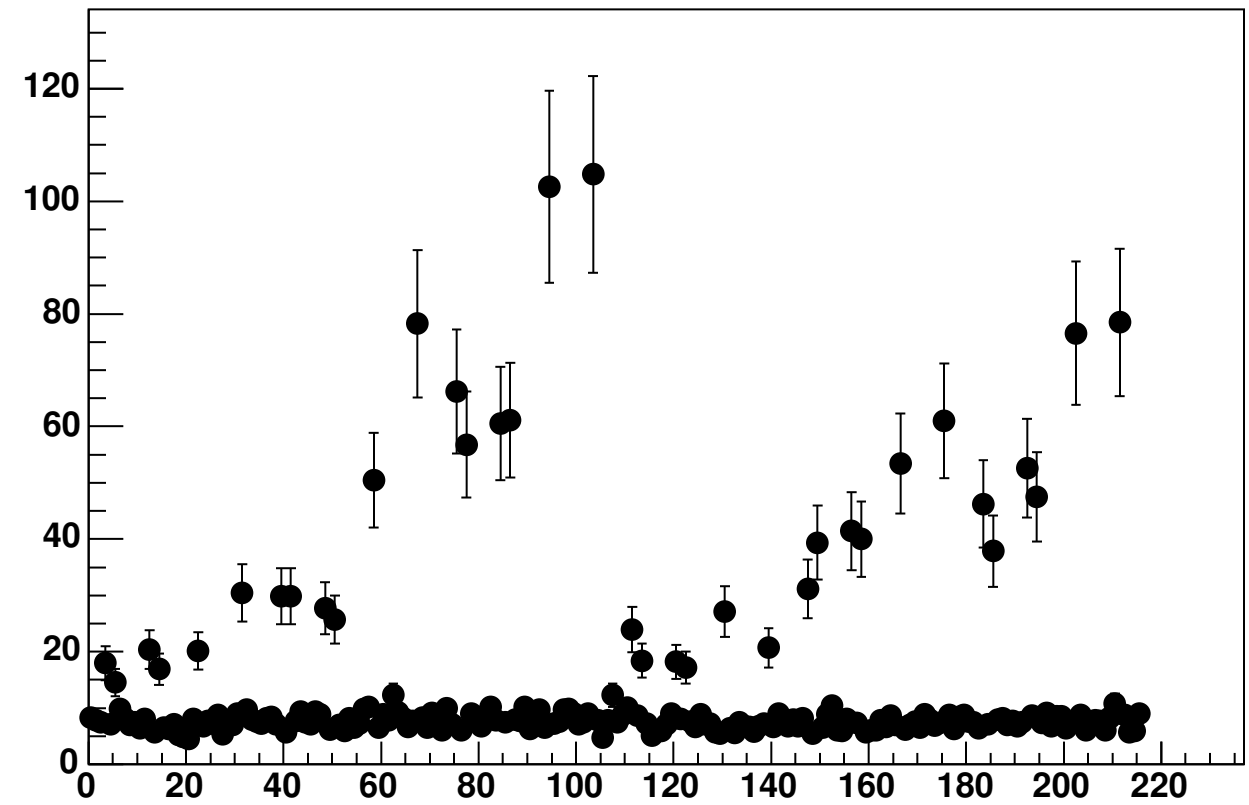
Enable 1, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



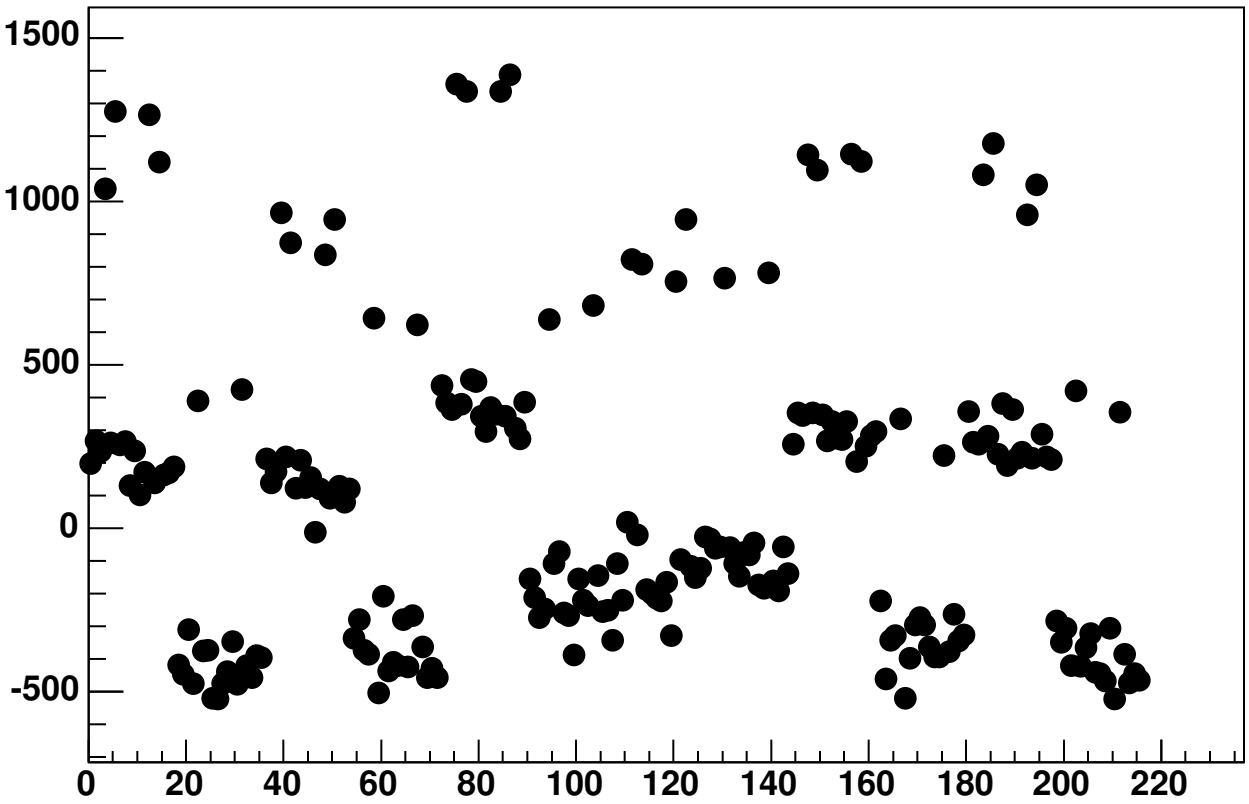
Enable 1, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



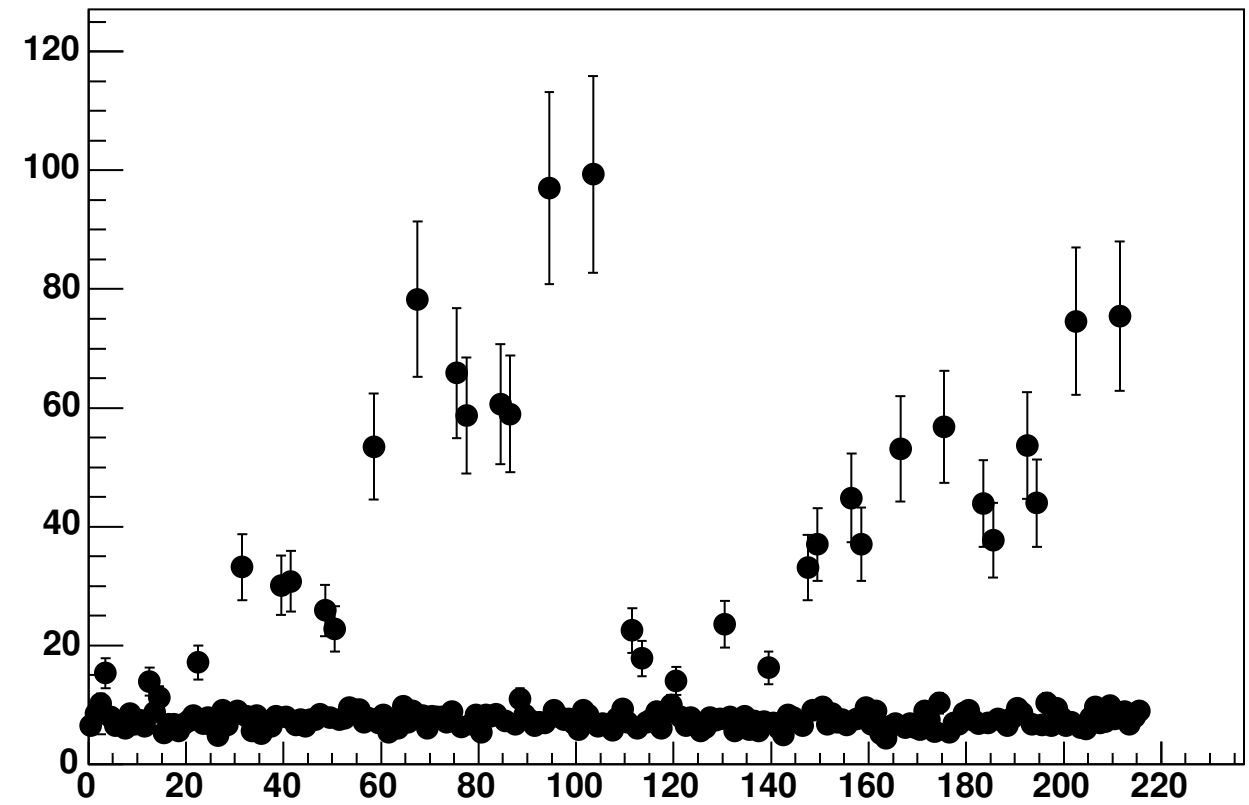
Enable 1, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



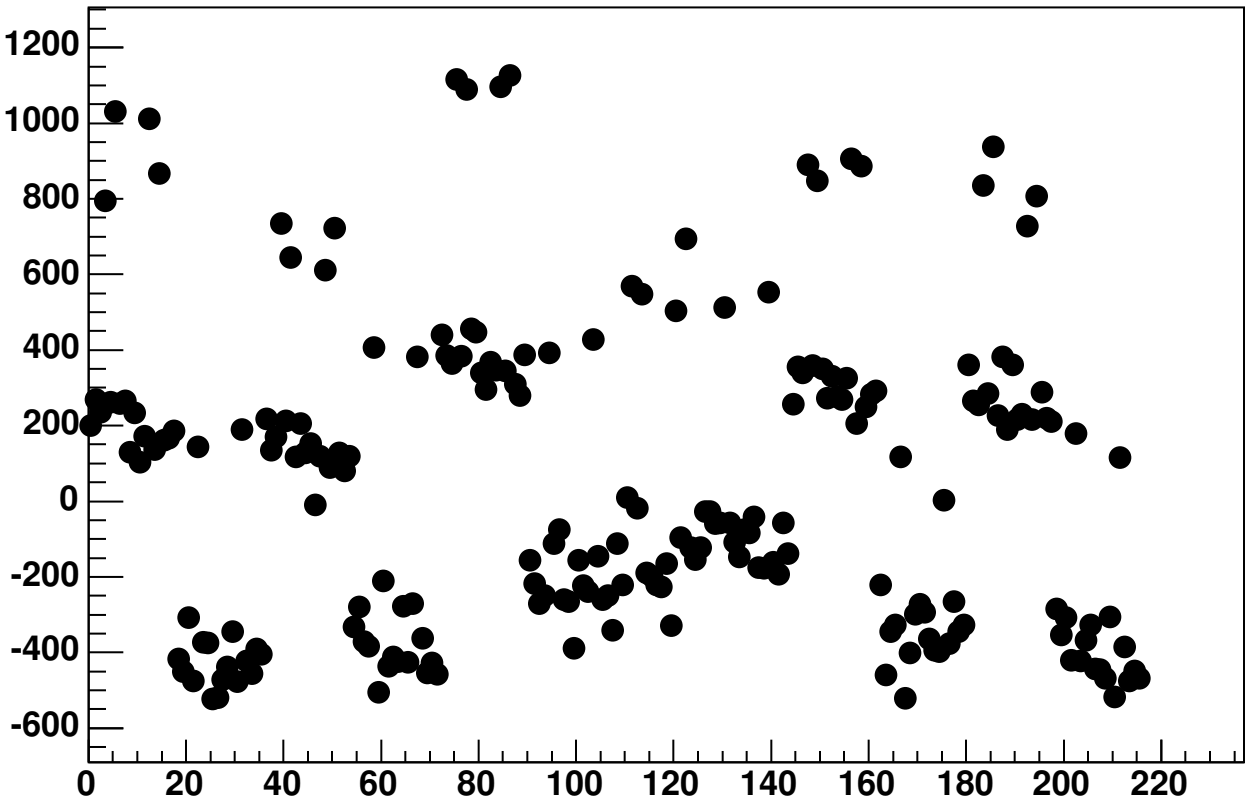
Enable 1, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



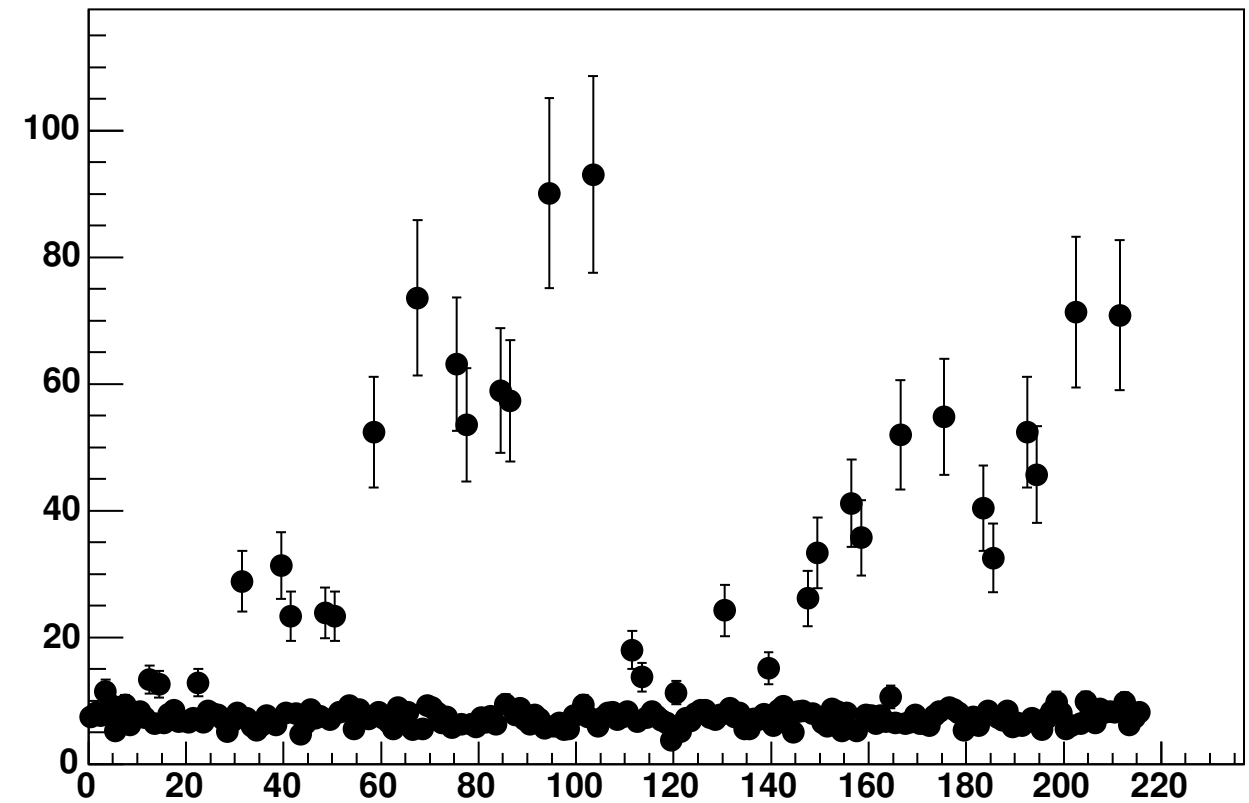
Enable 1, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



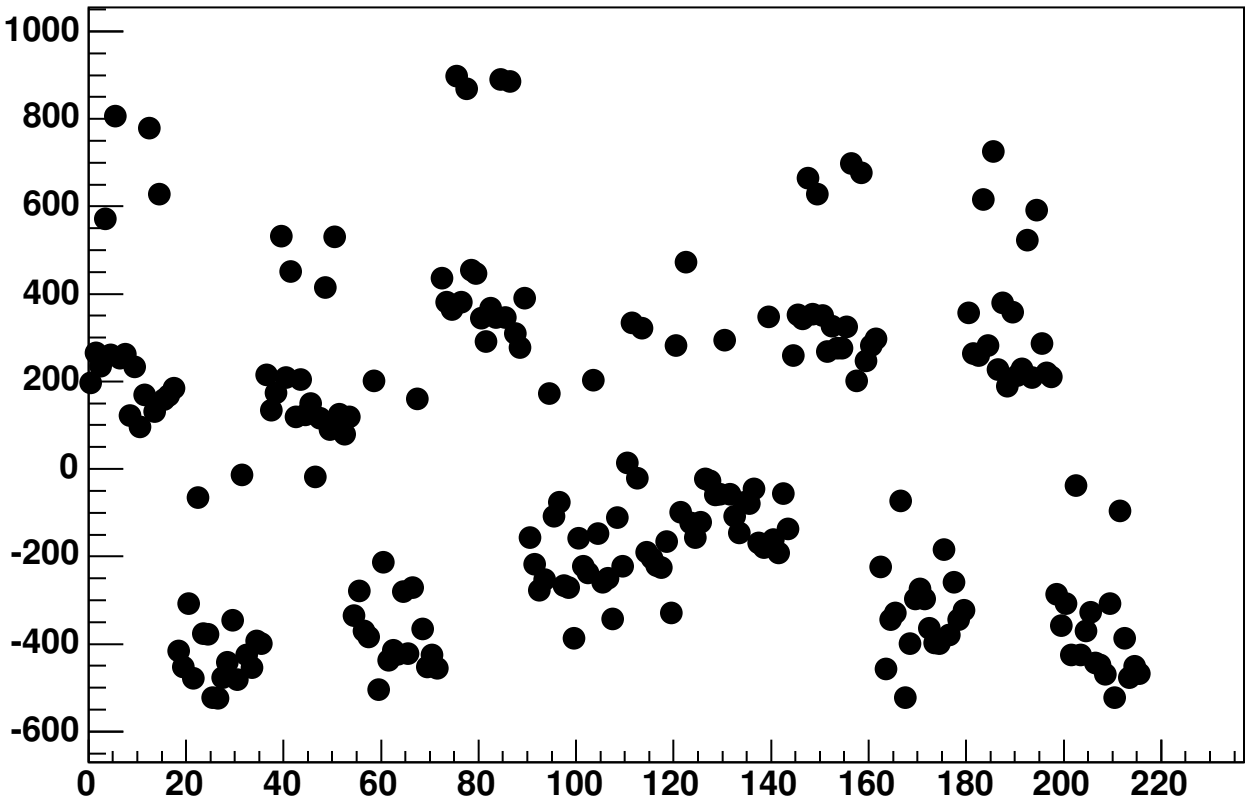
Enable 1, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



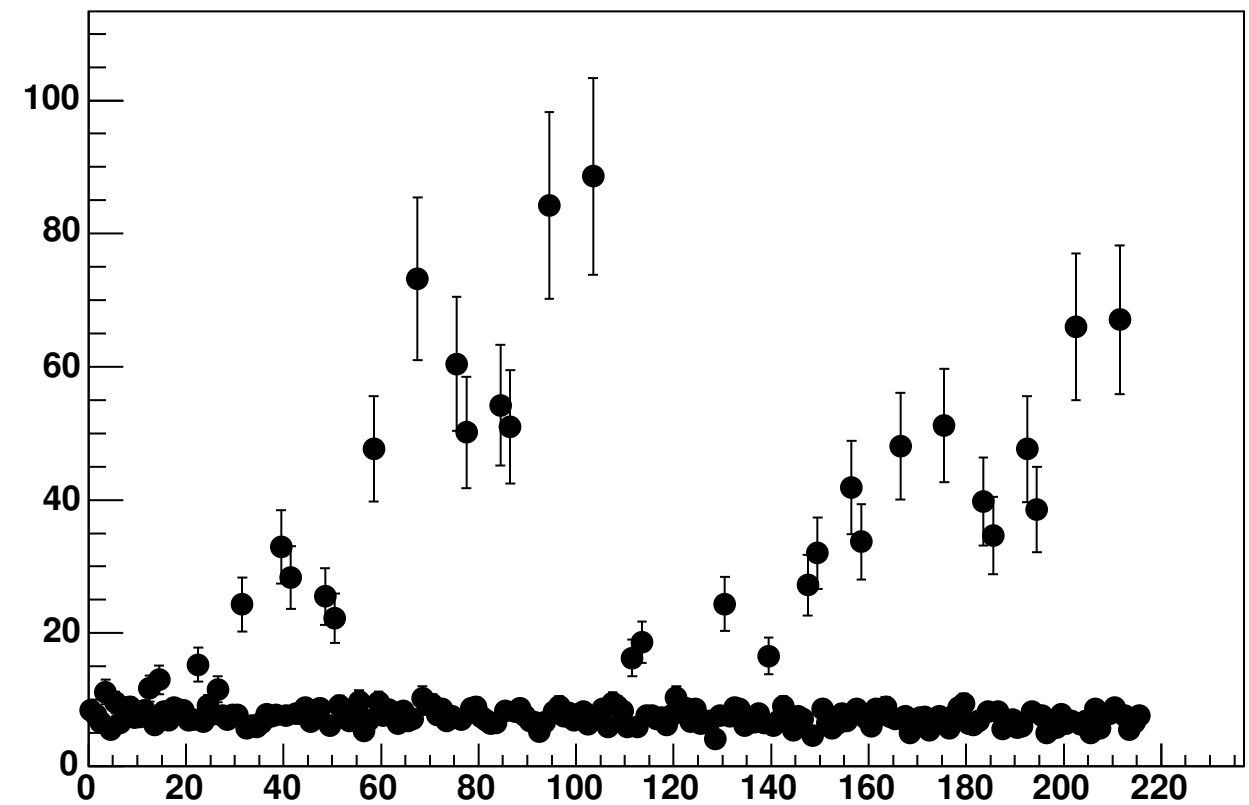
Enable 1, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



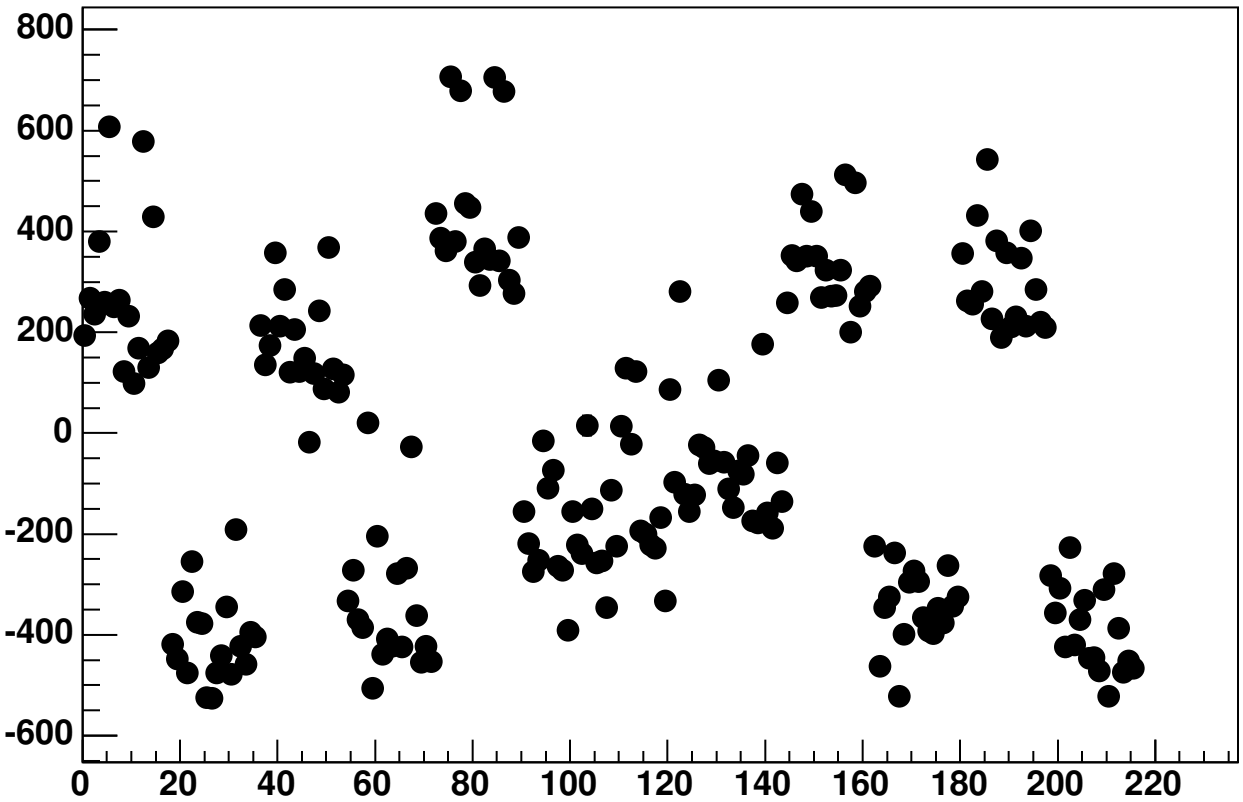
Enable 1, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



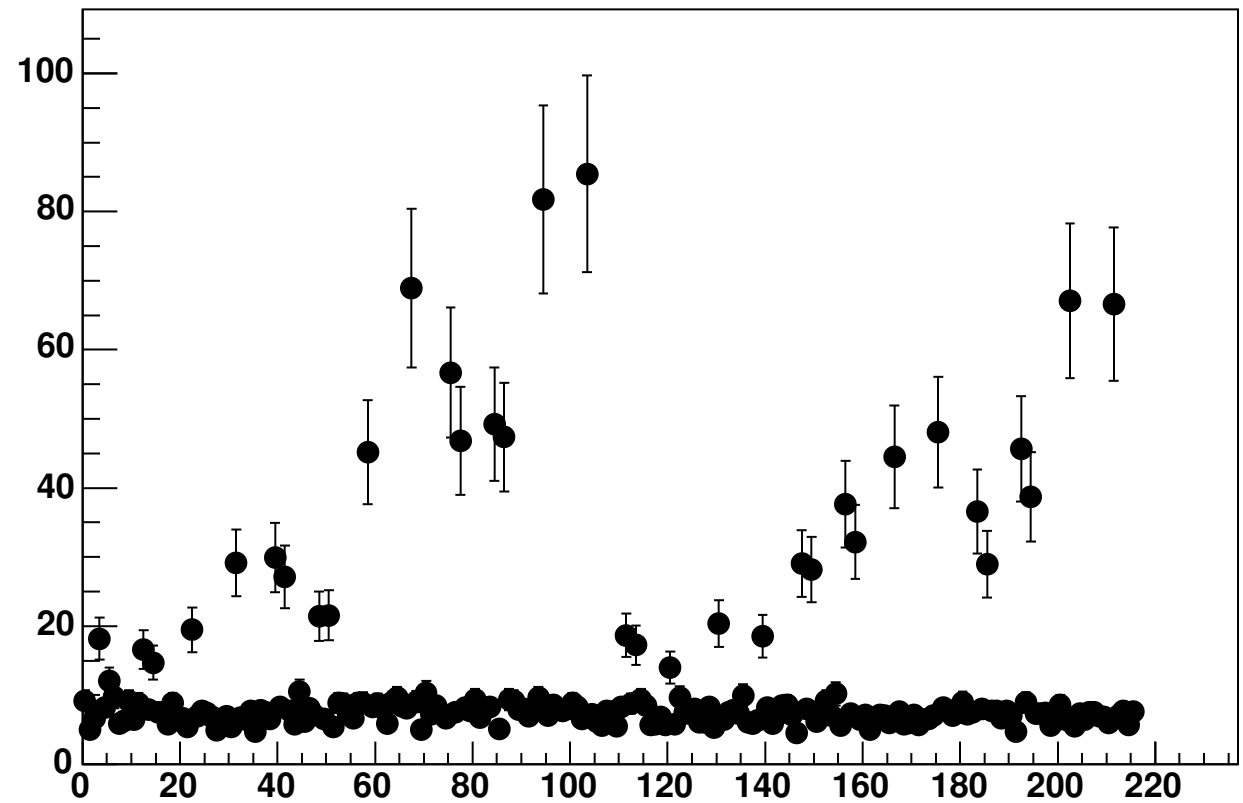
Enable 1, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



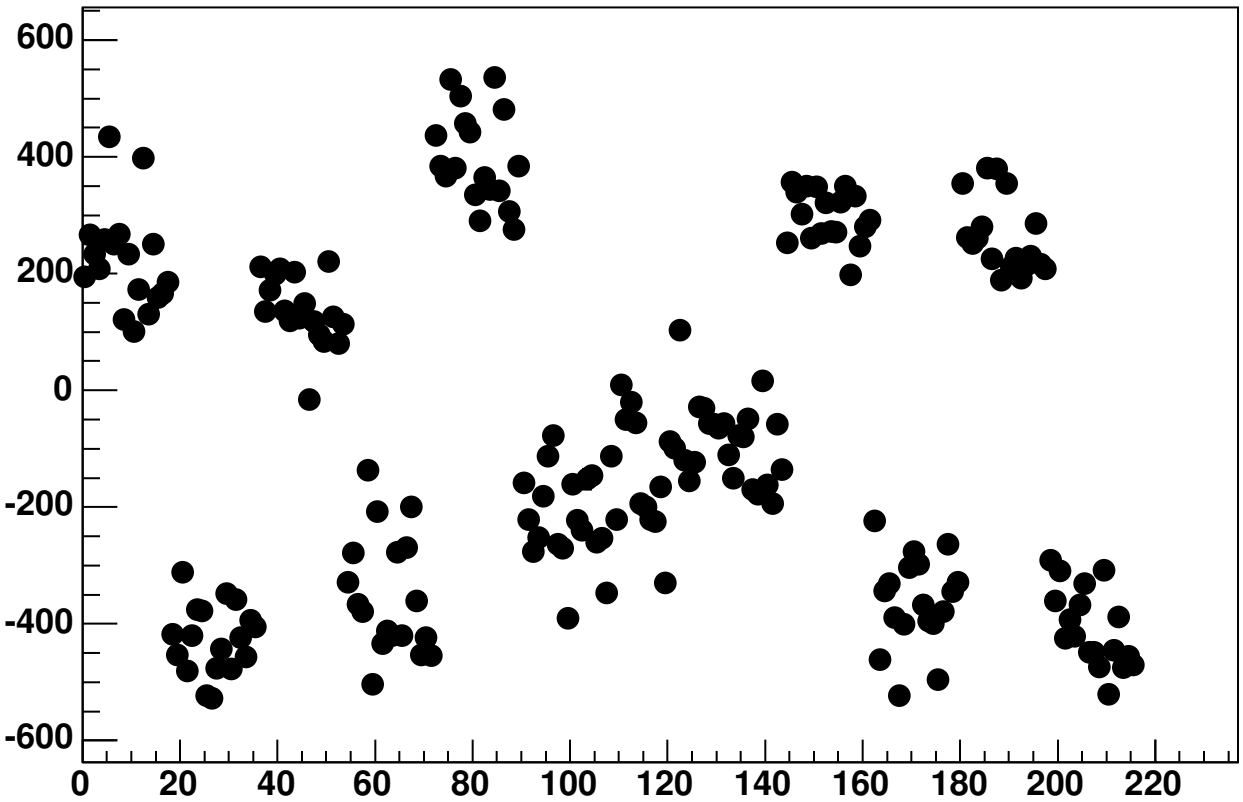
Enable 1, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



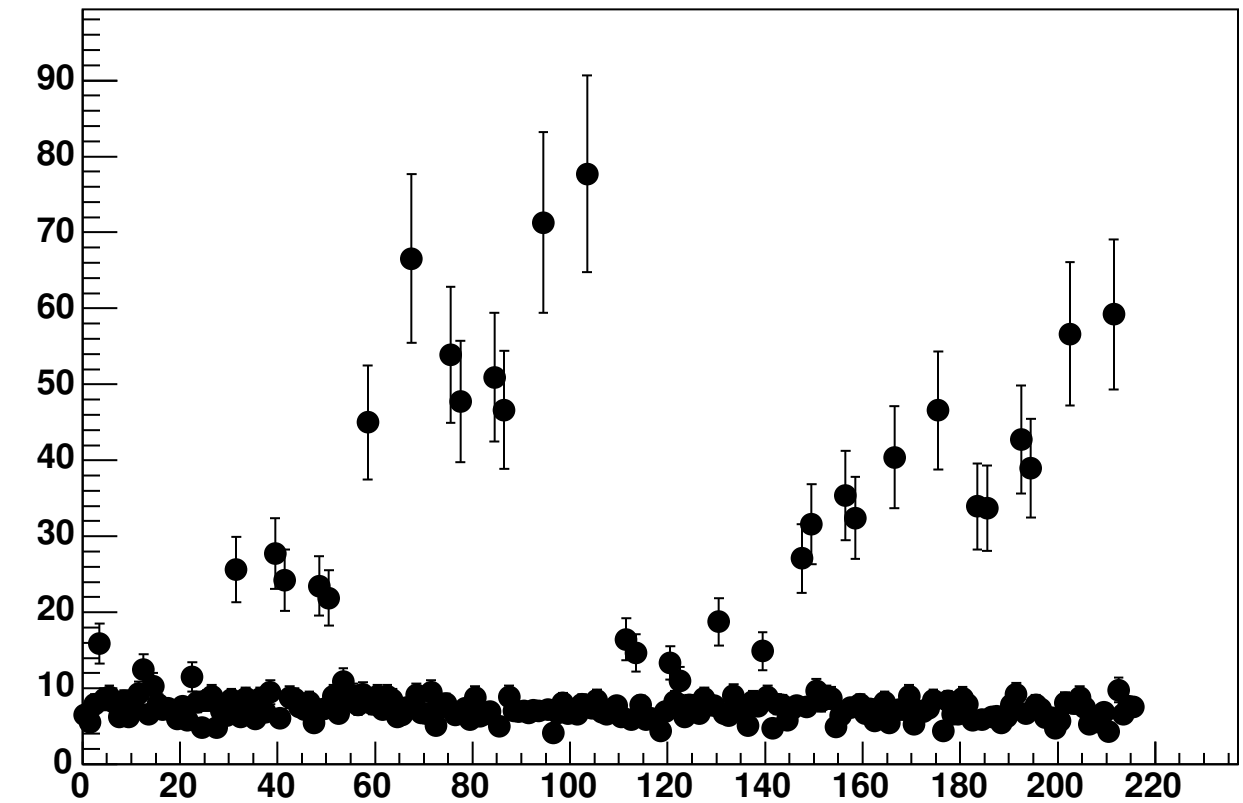
Enable 1, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



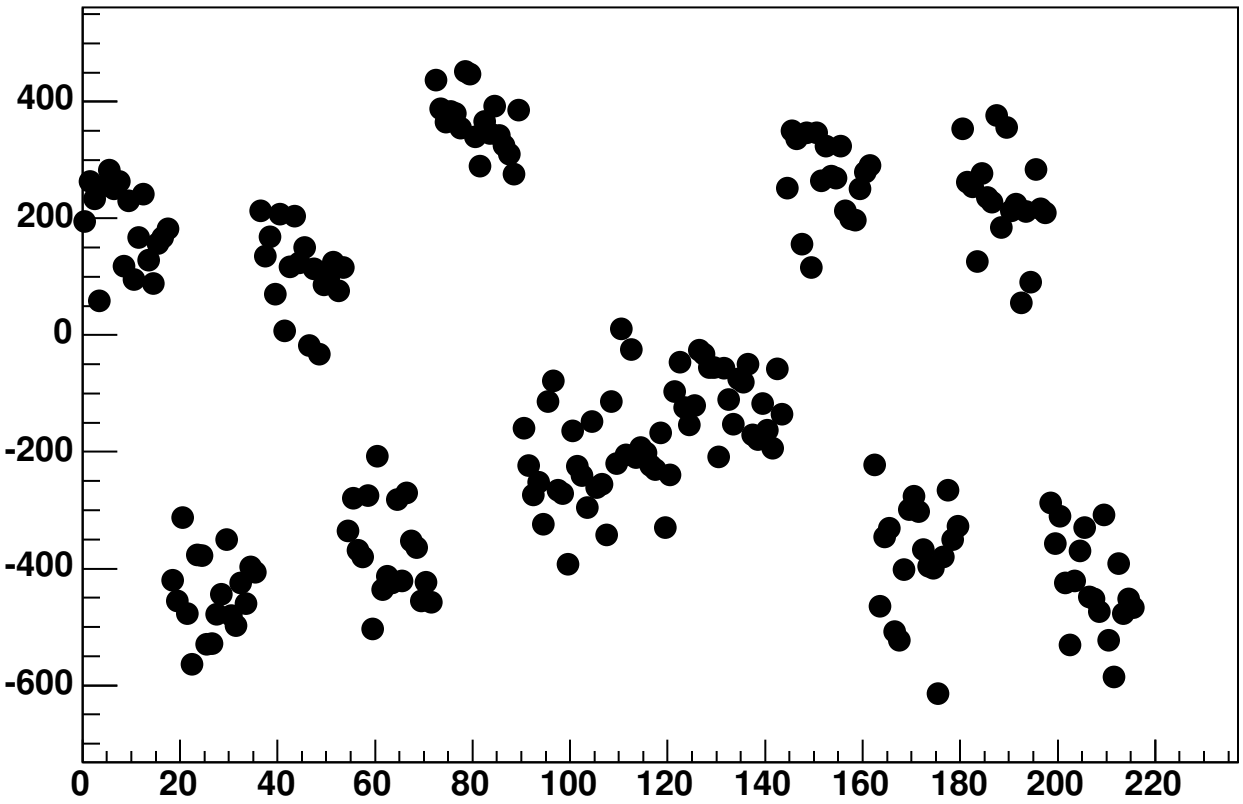
Enable 1, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



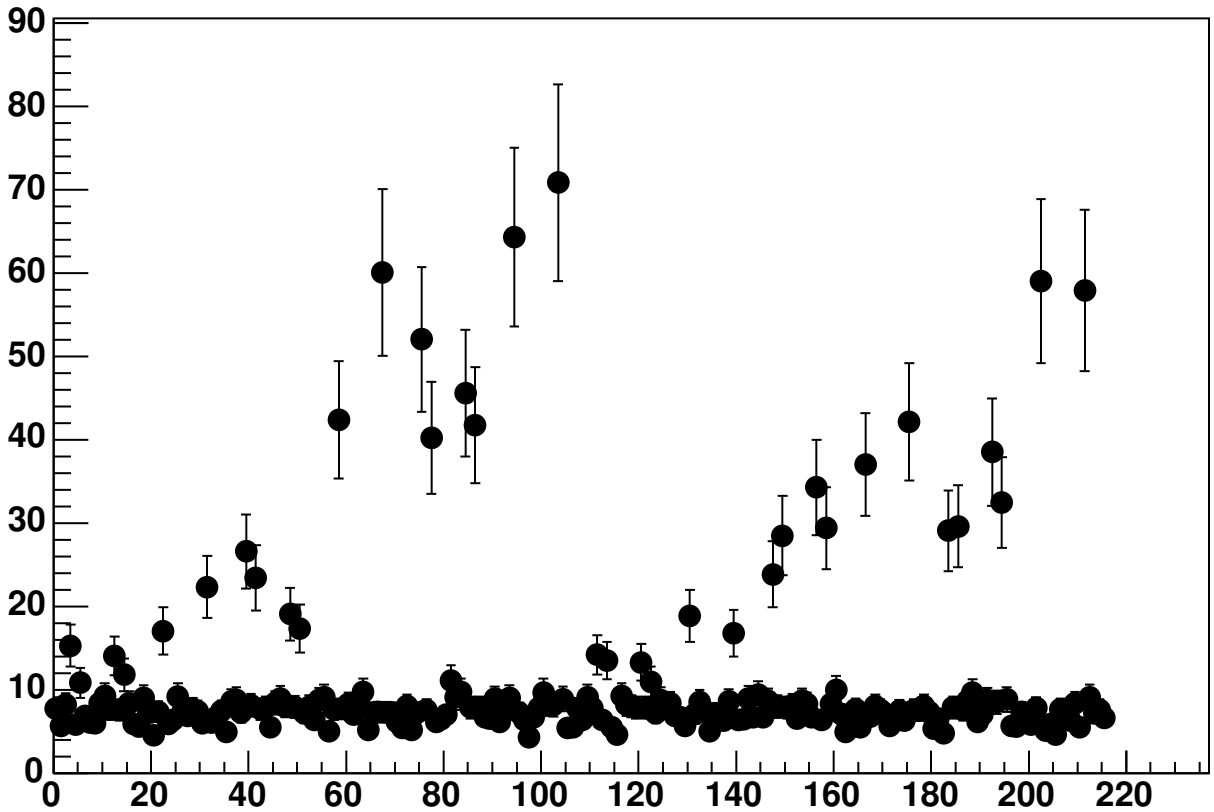
Enable 1, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



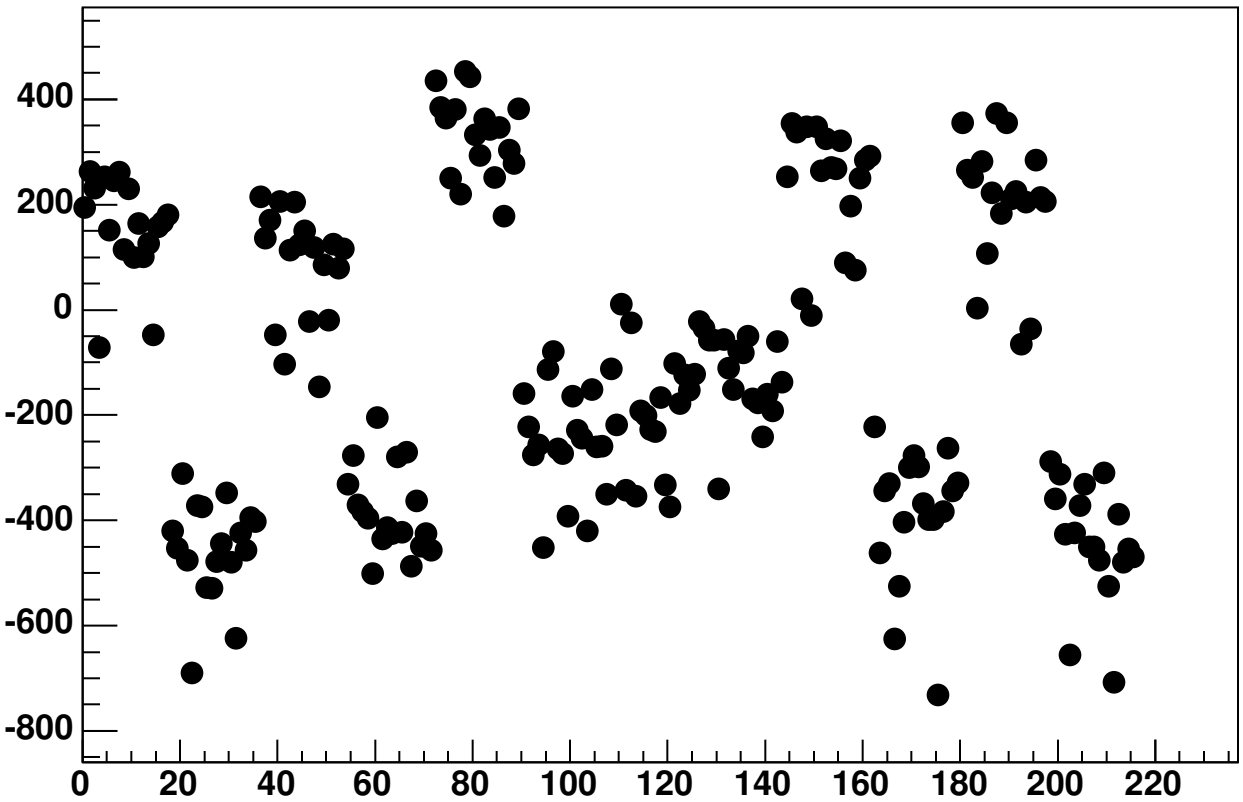
Enable 1, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



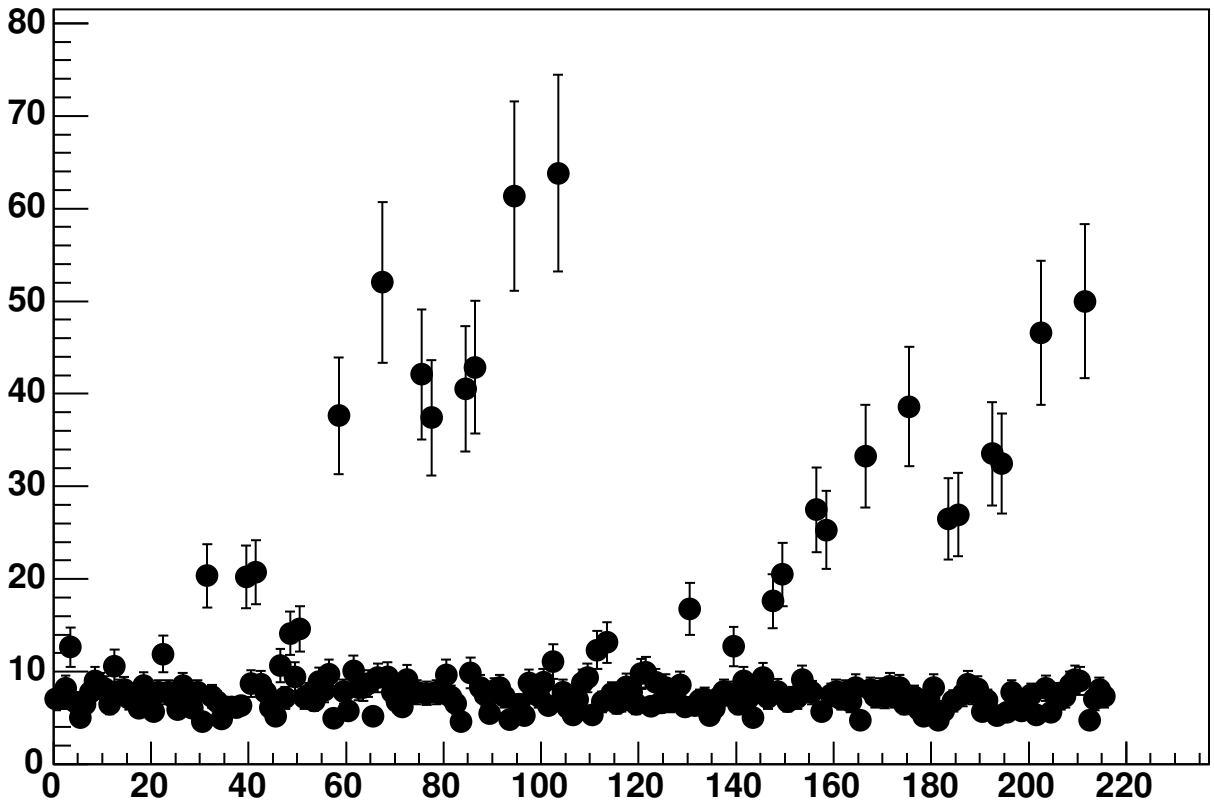
Enable 1, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



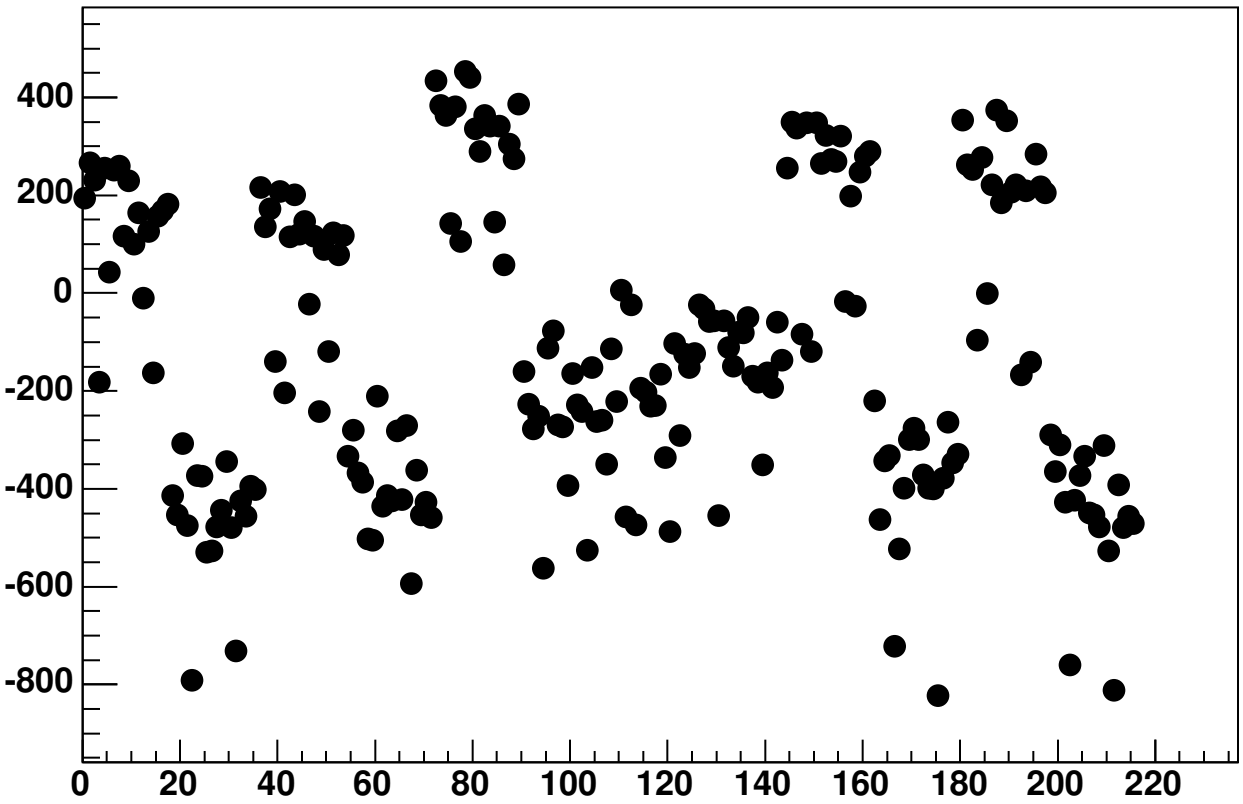
Enable 1, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



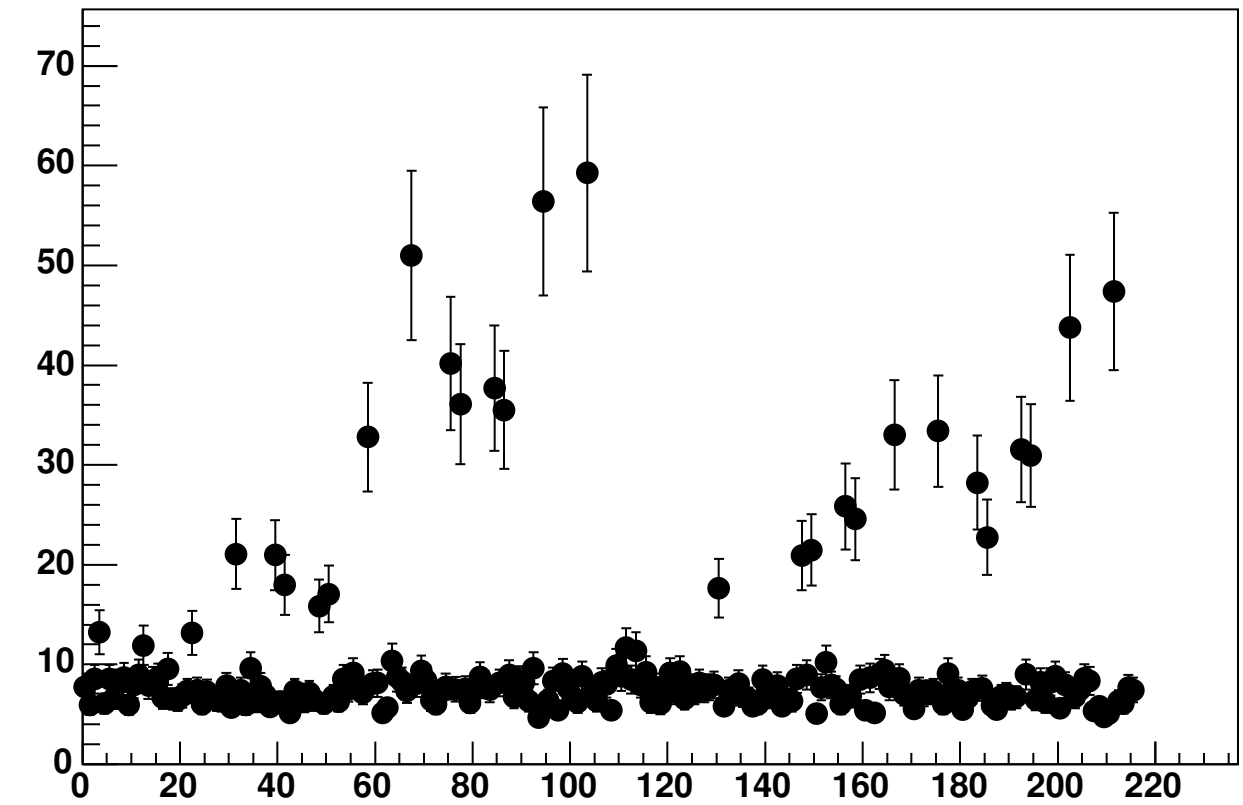
Enable 1, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



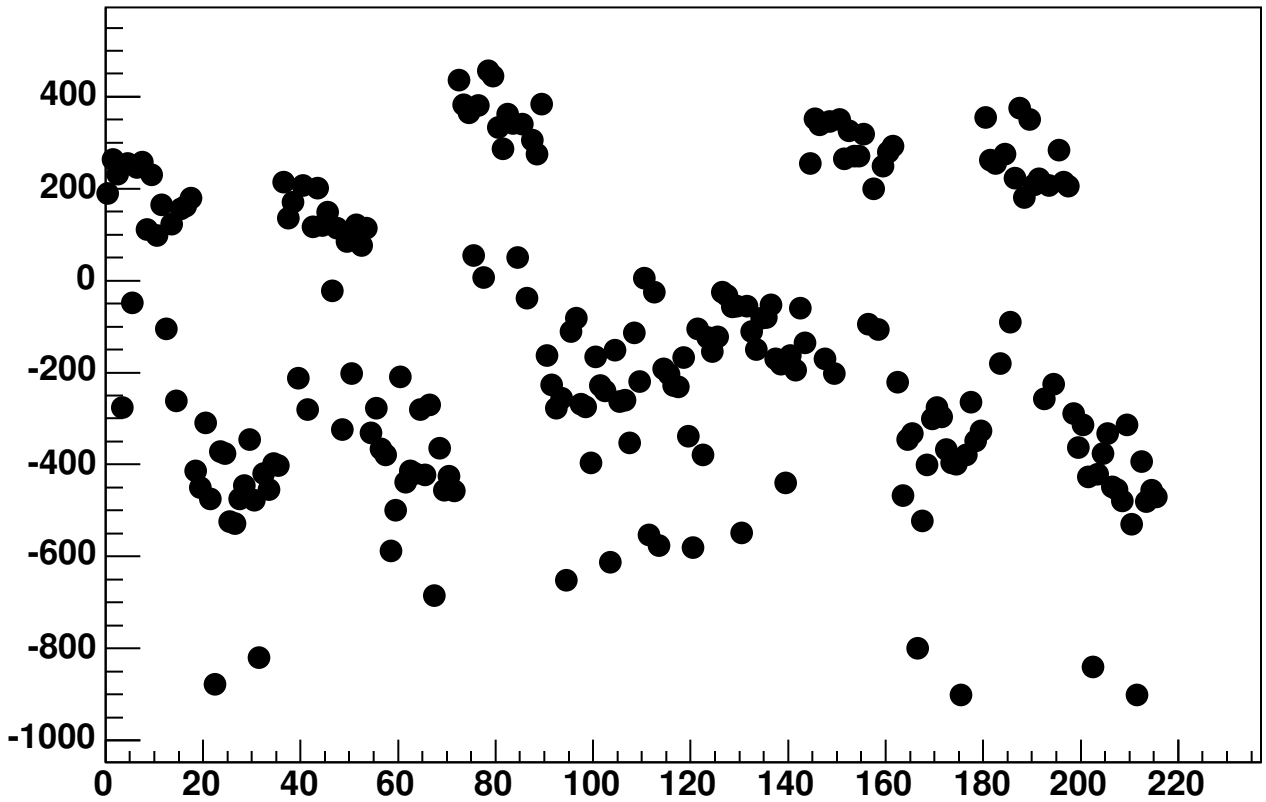
Enable 1, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



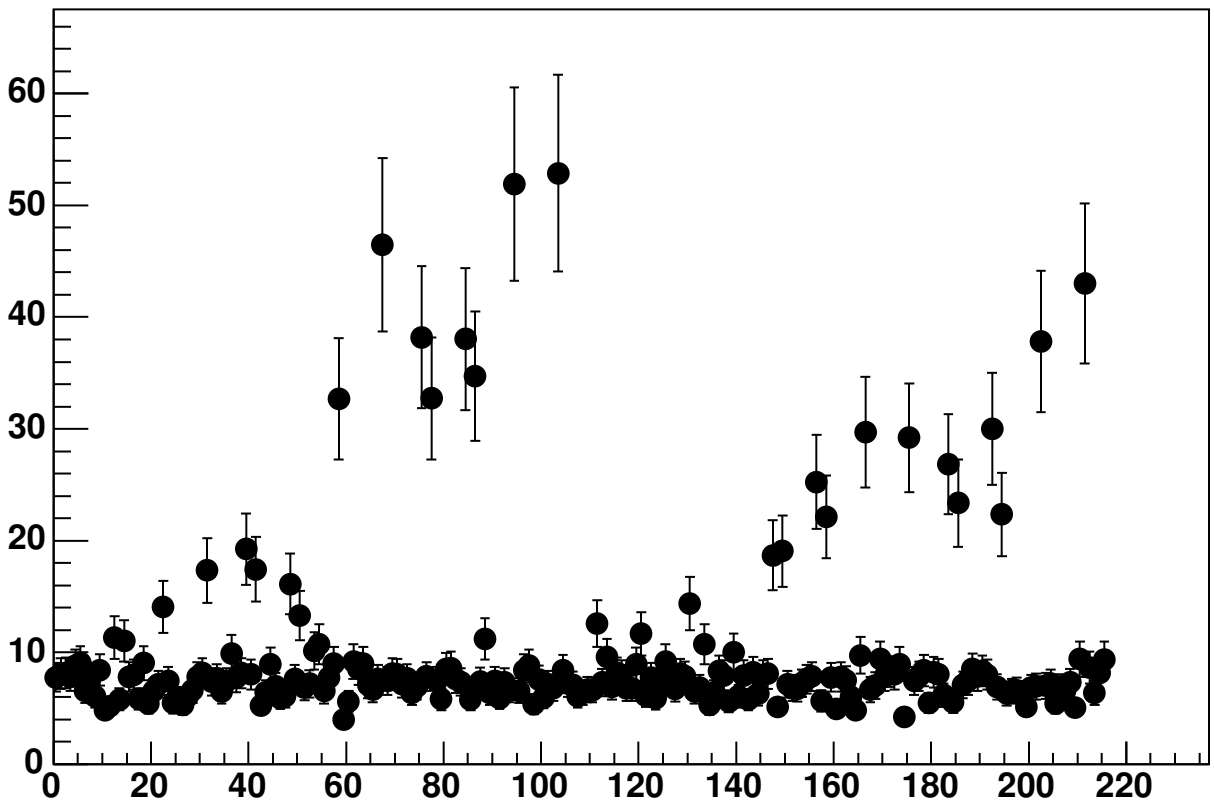
Enable 1, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



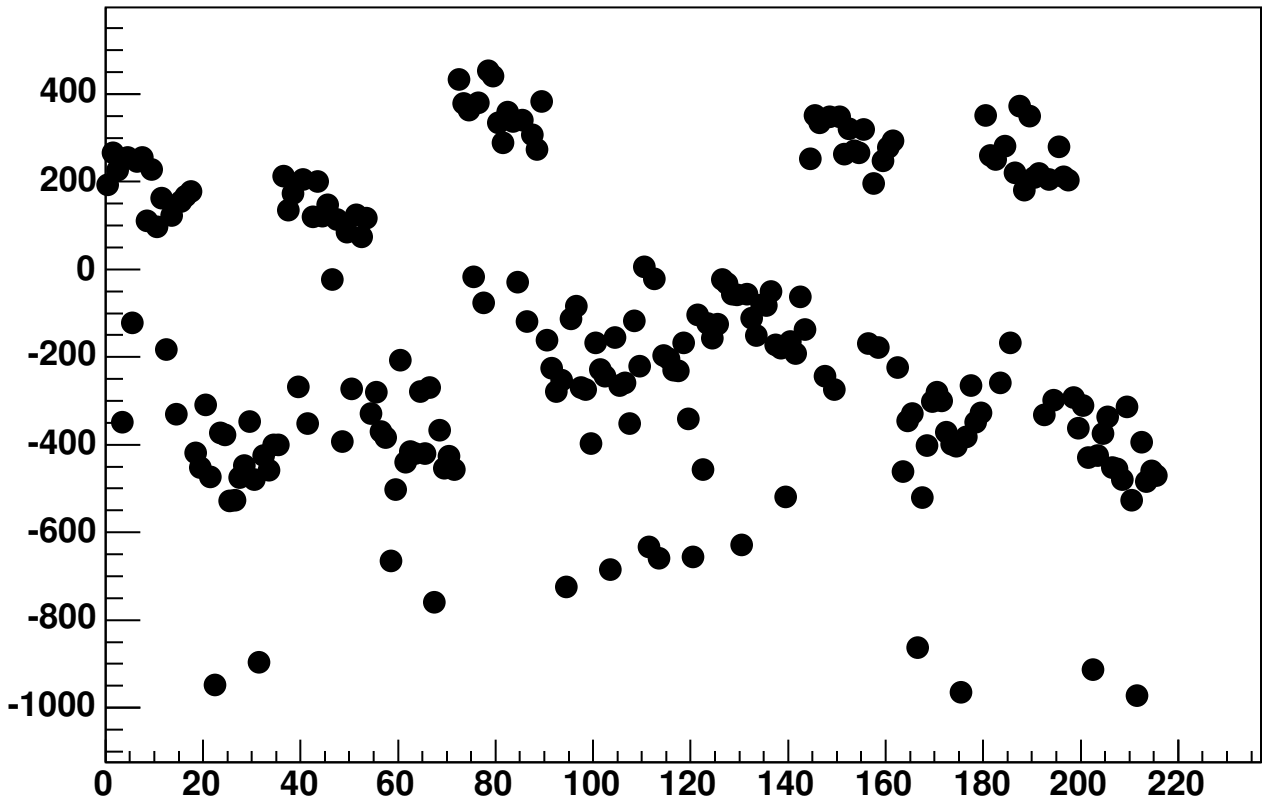
Enable 1, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



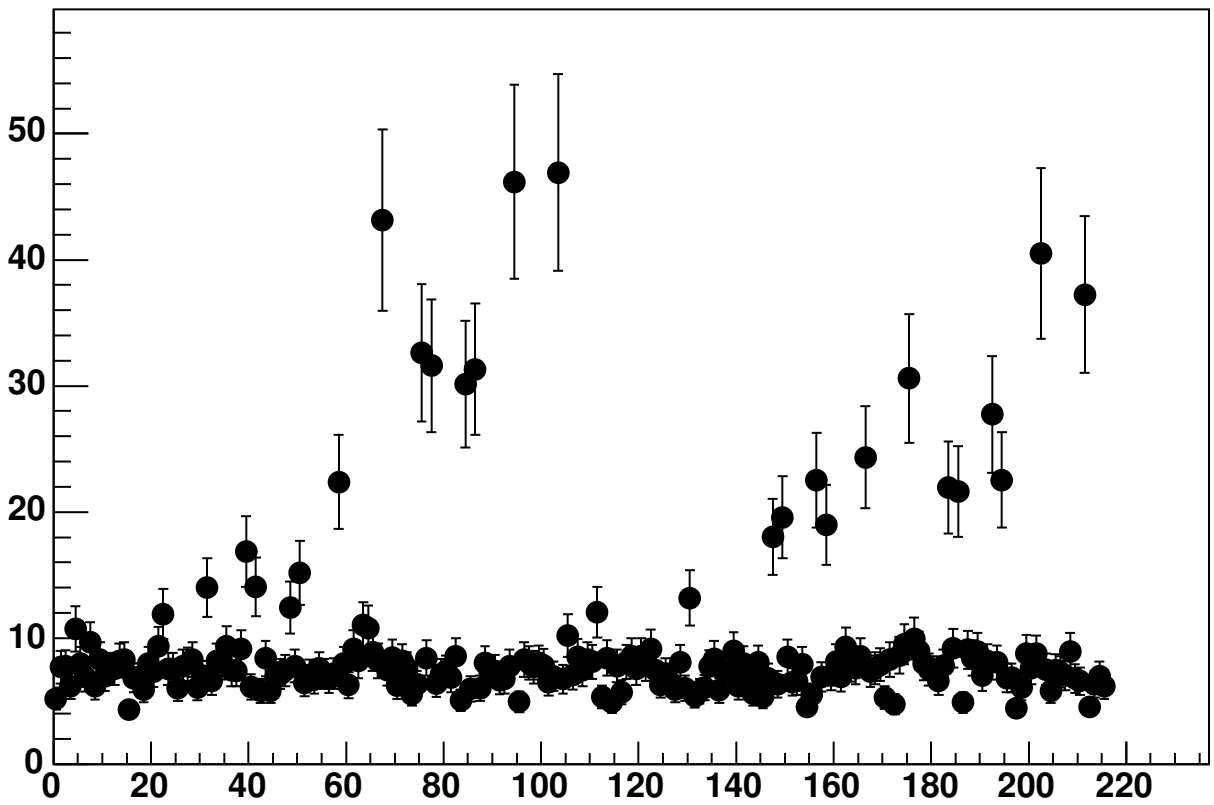
Enable 1, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



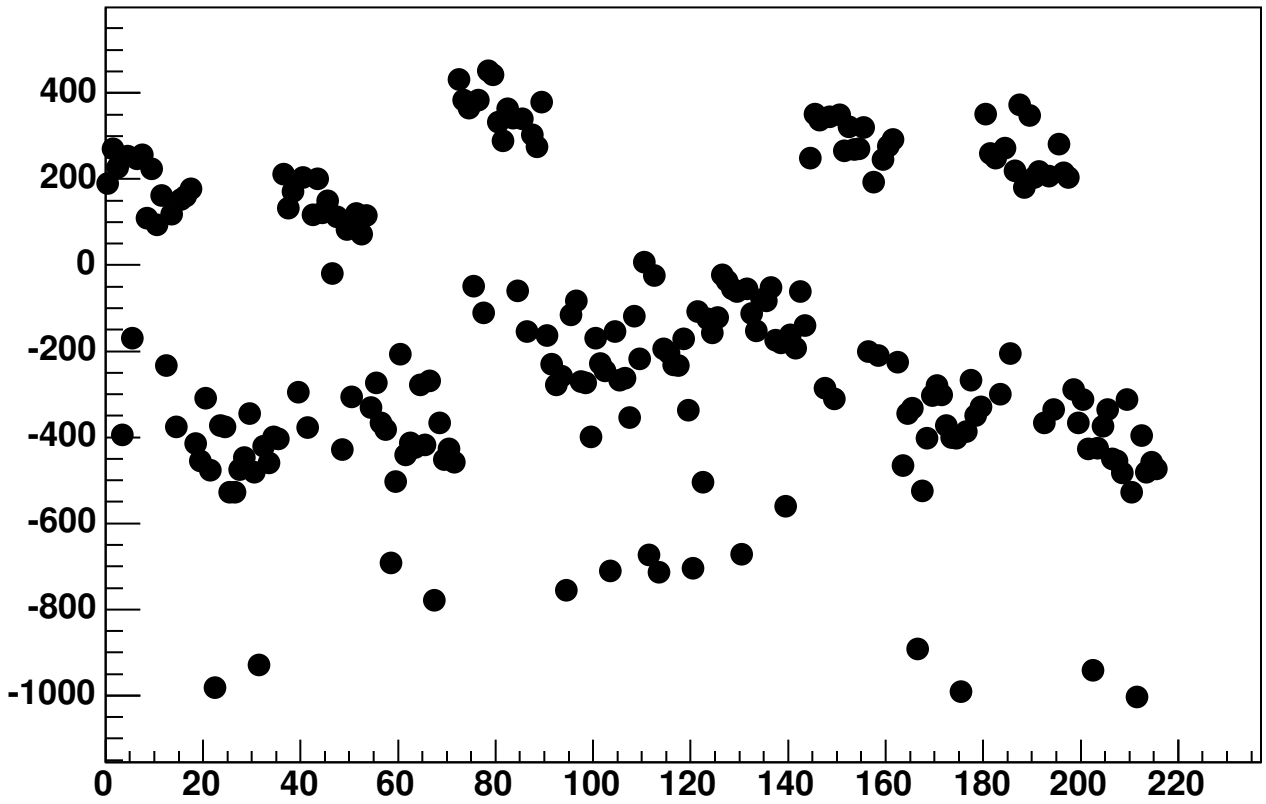
Enable 1, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



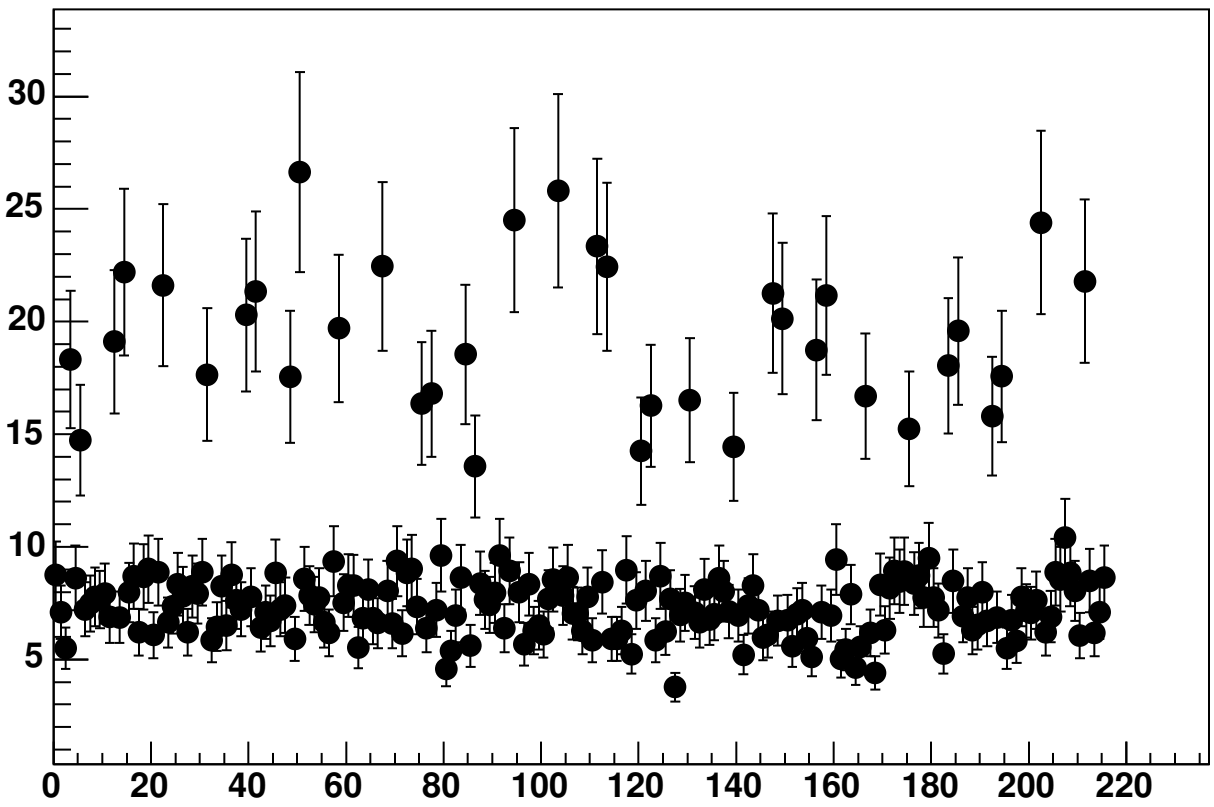
Enable 1, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



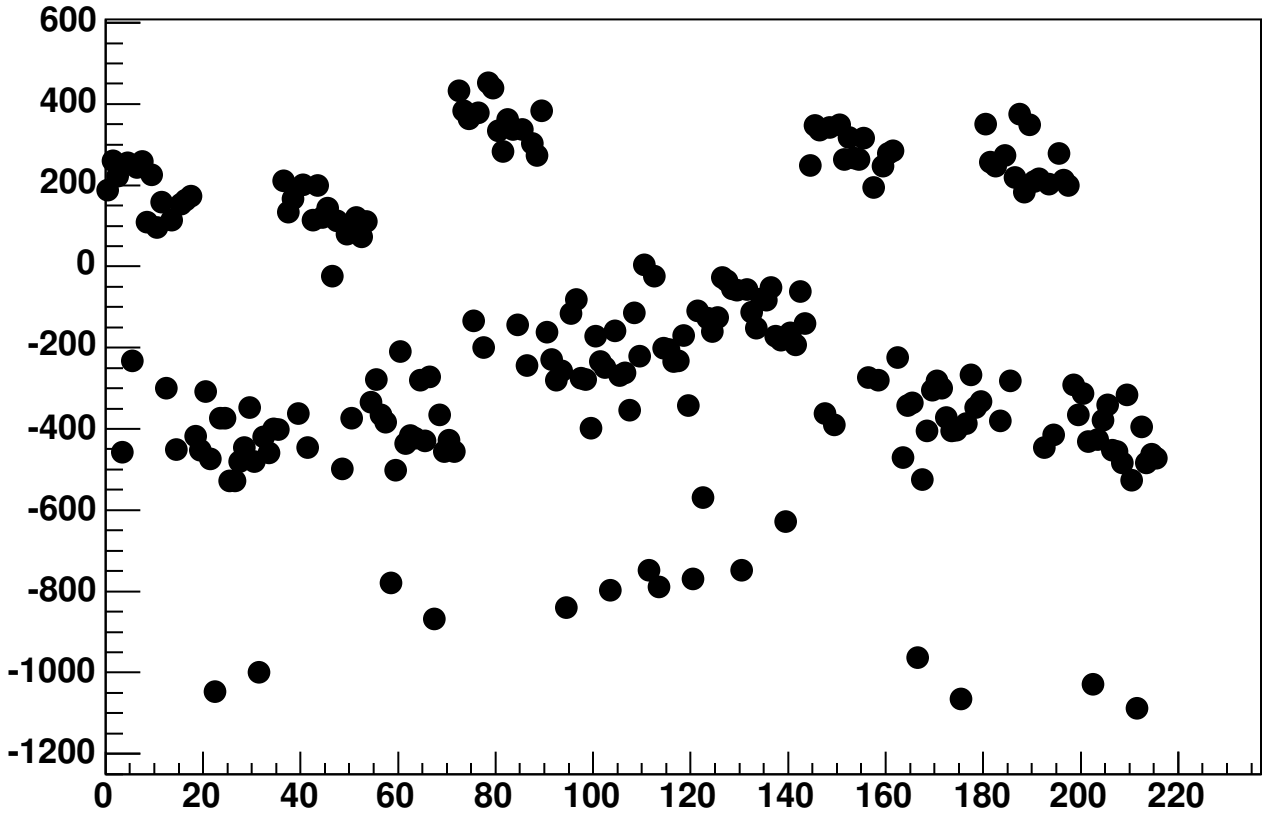
Enable 1, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



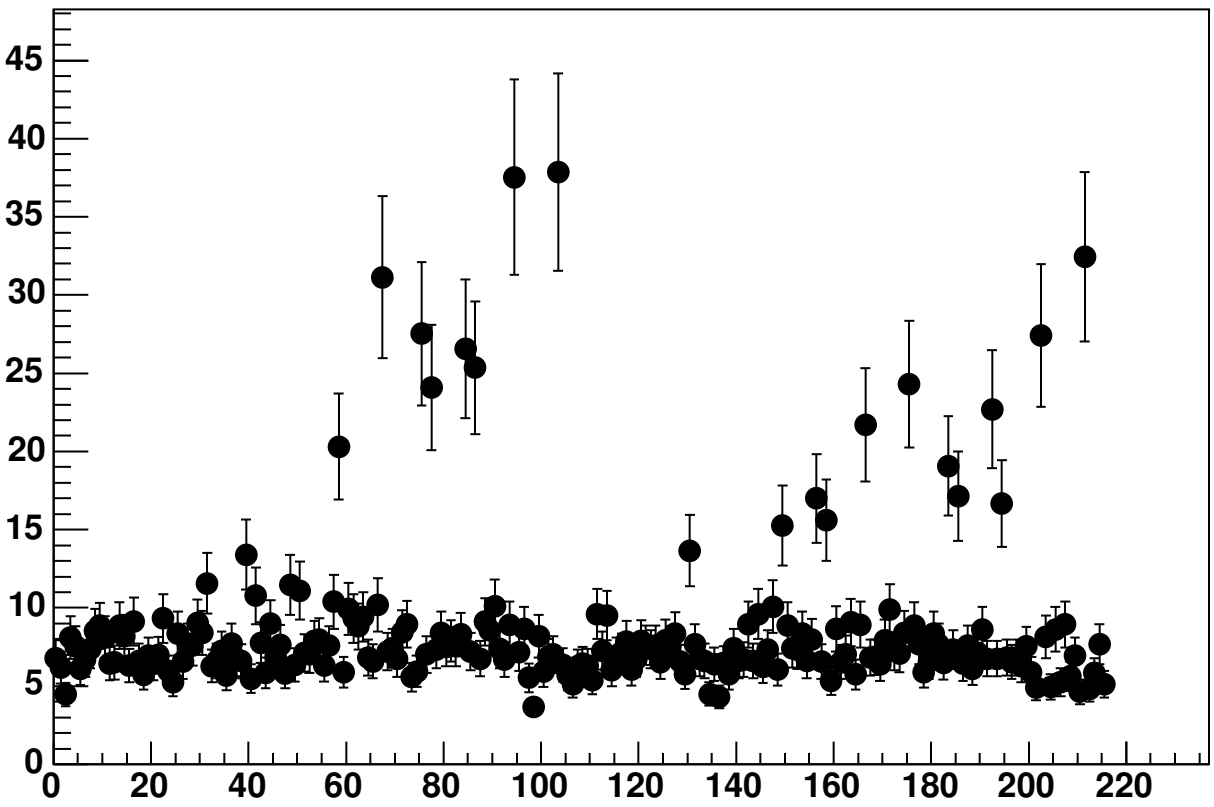
Enable 1, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



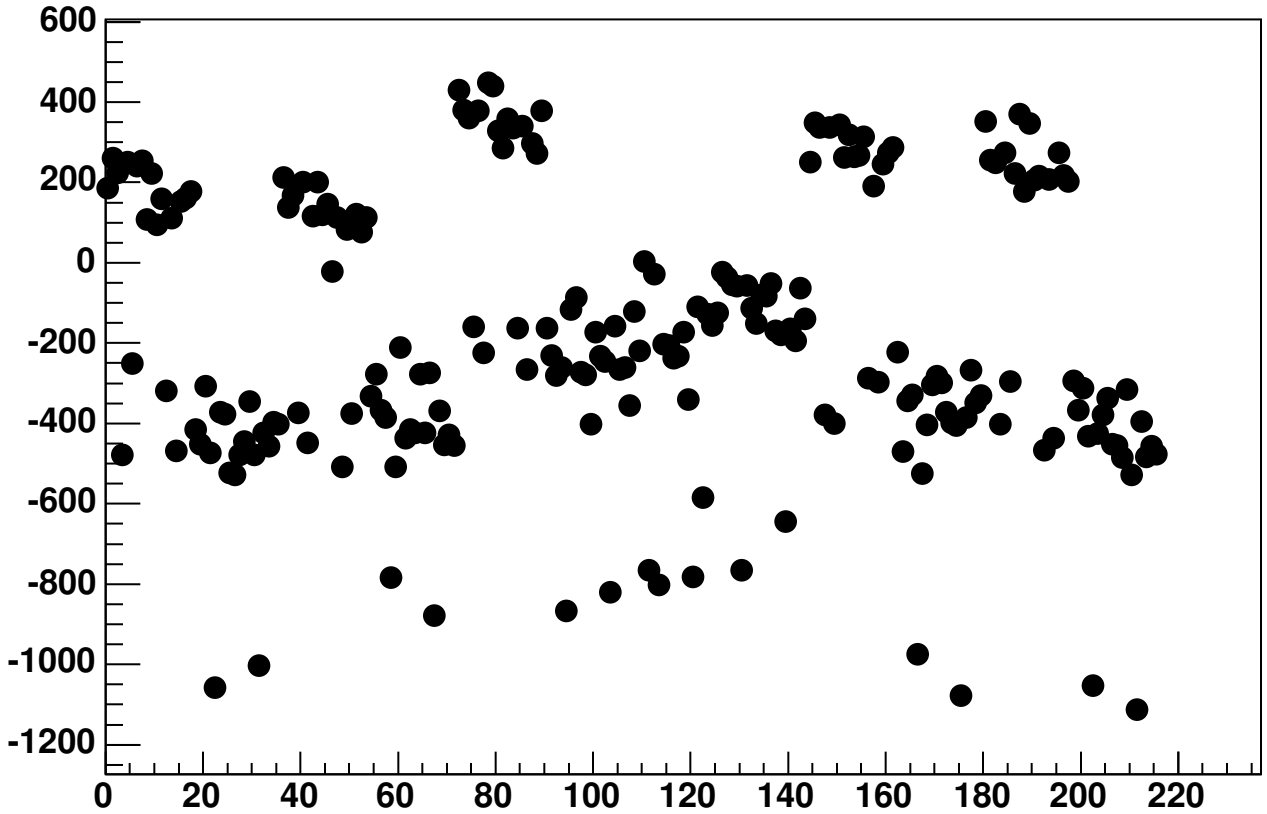
Enable 1, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



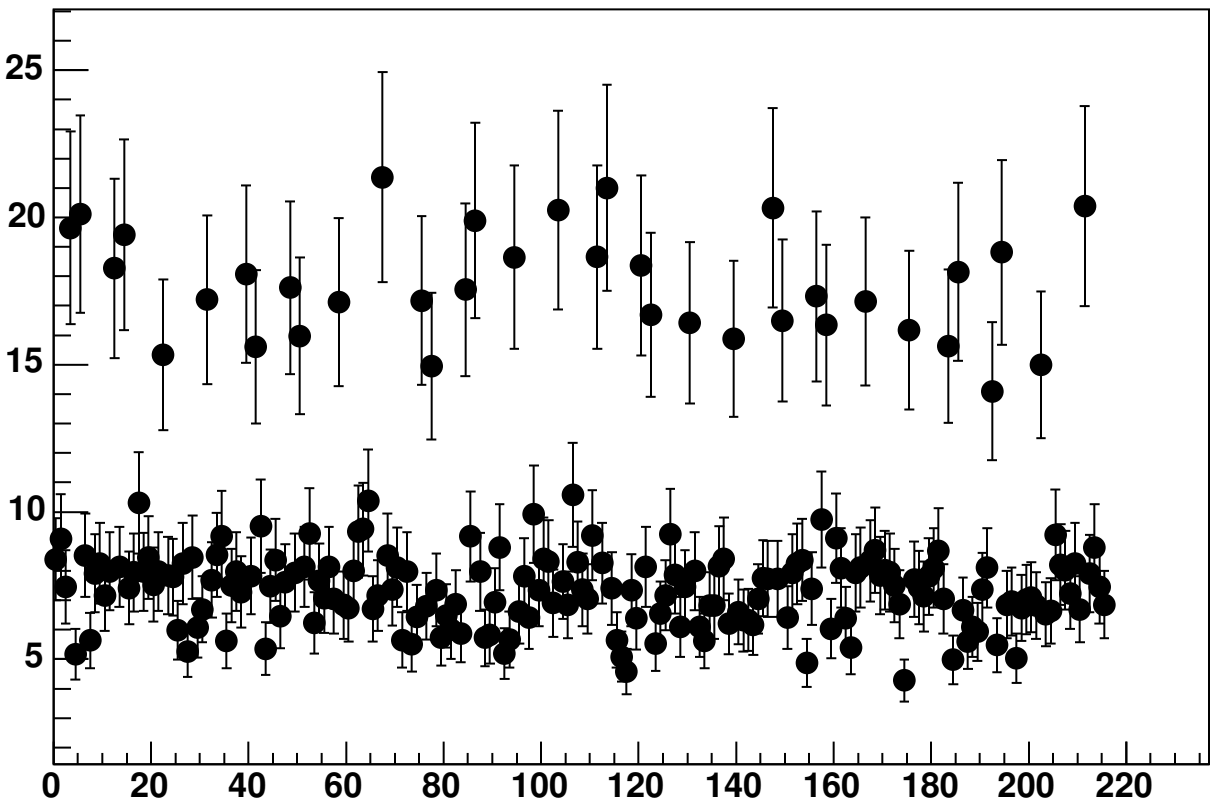
Enable 1, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



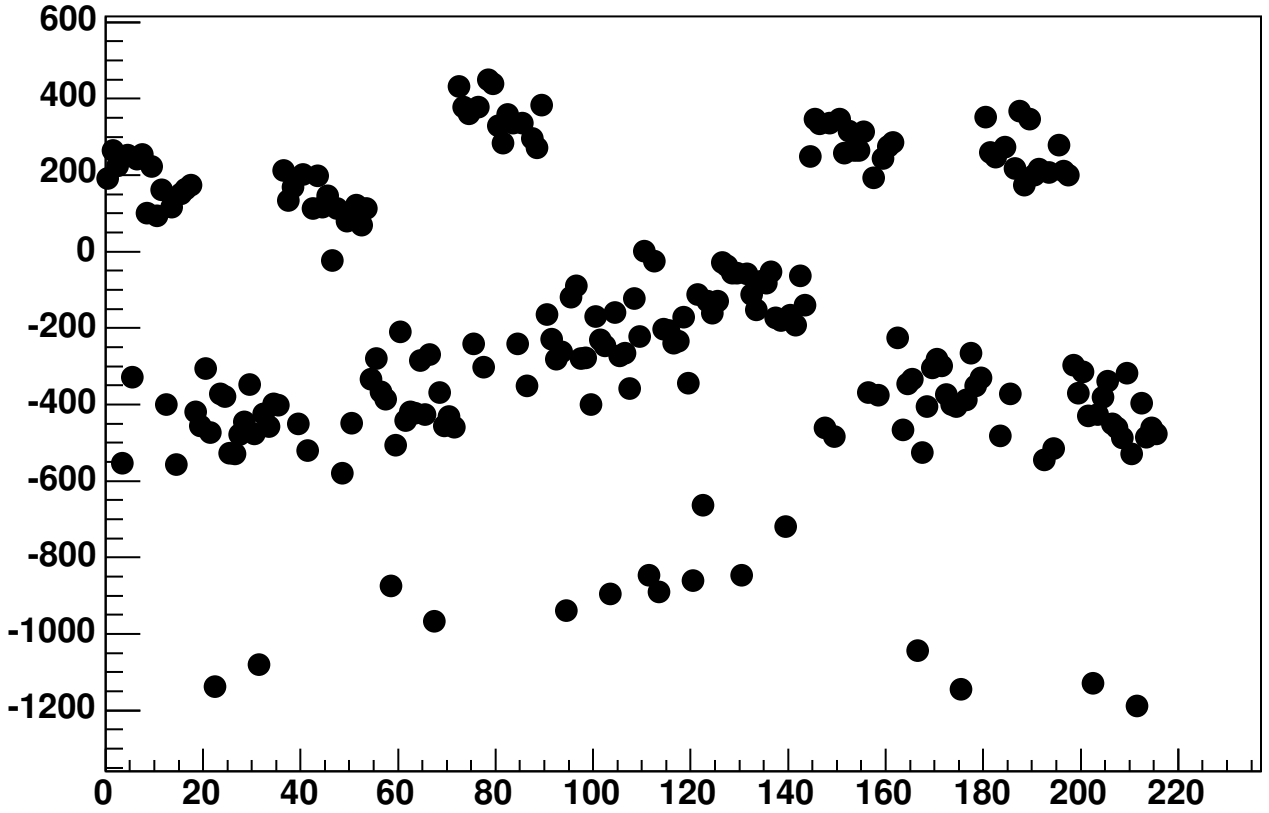
Enable 1, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



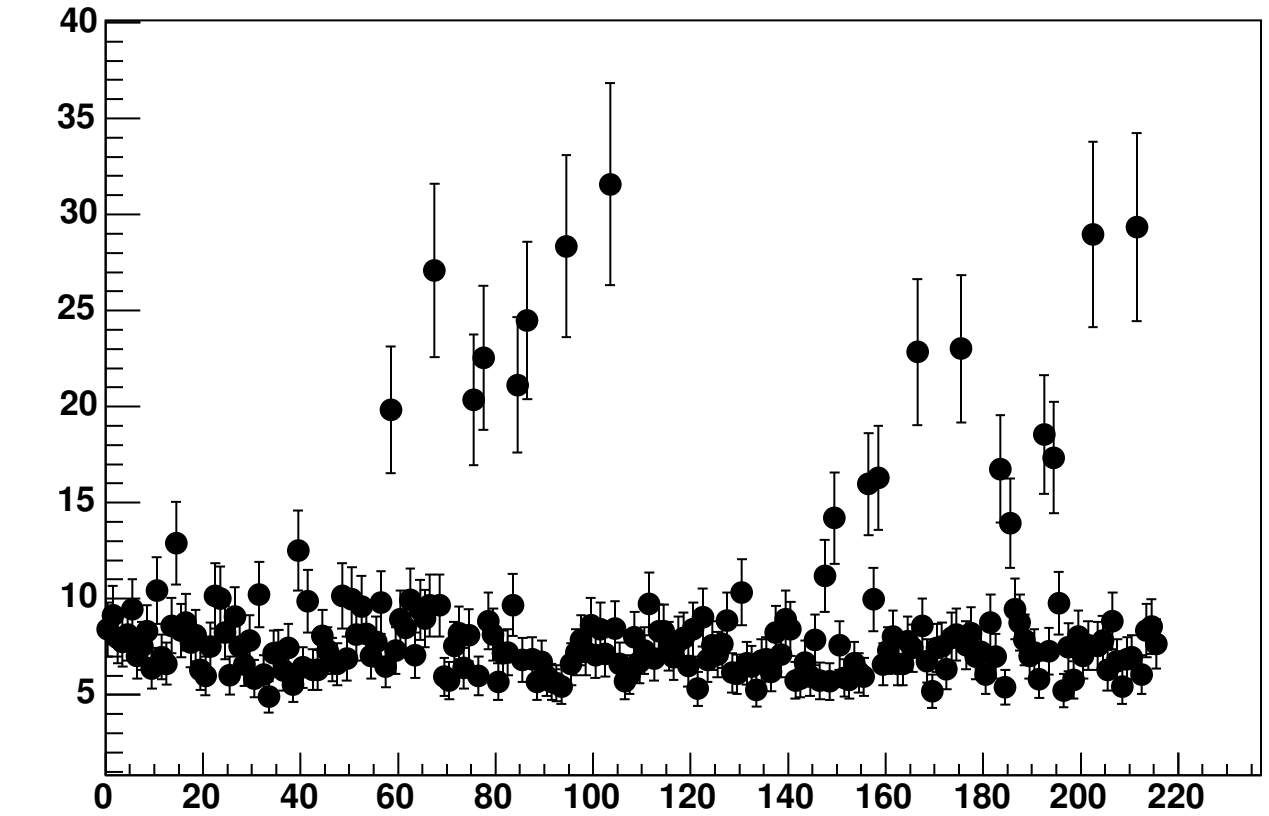
Enable 1, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



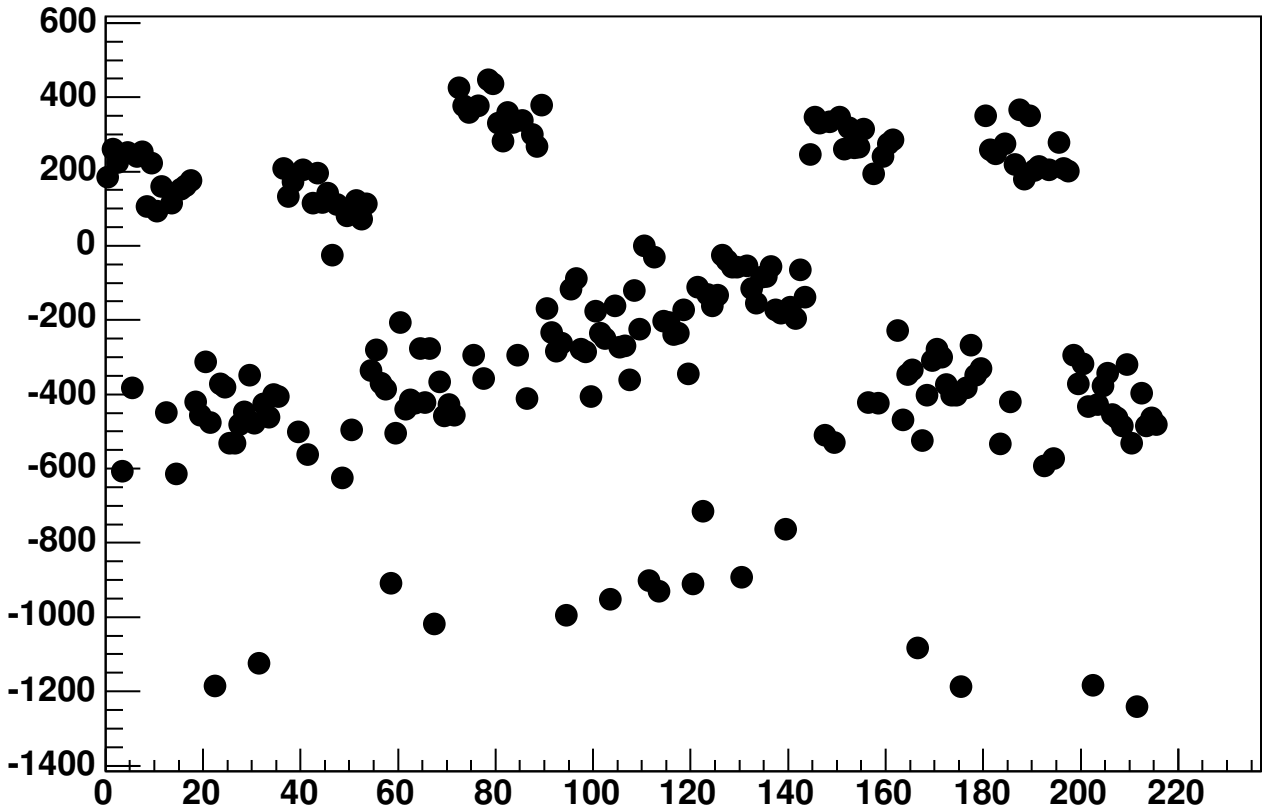
Enable 1, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



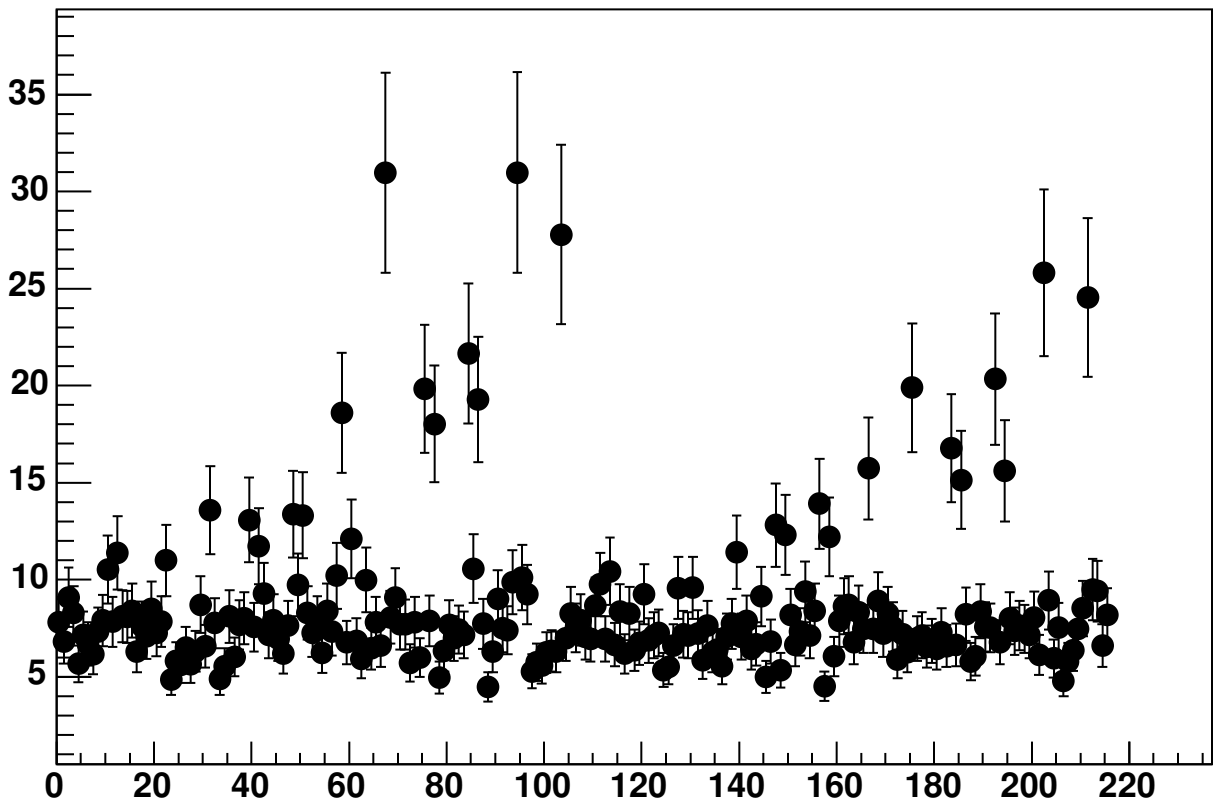
Enable 1, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



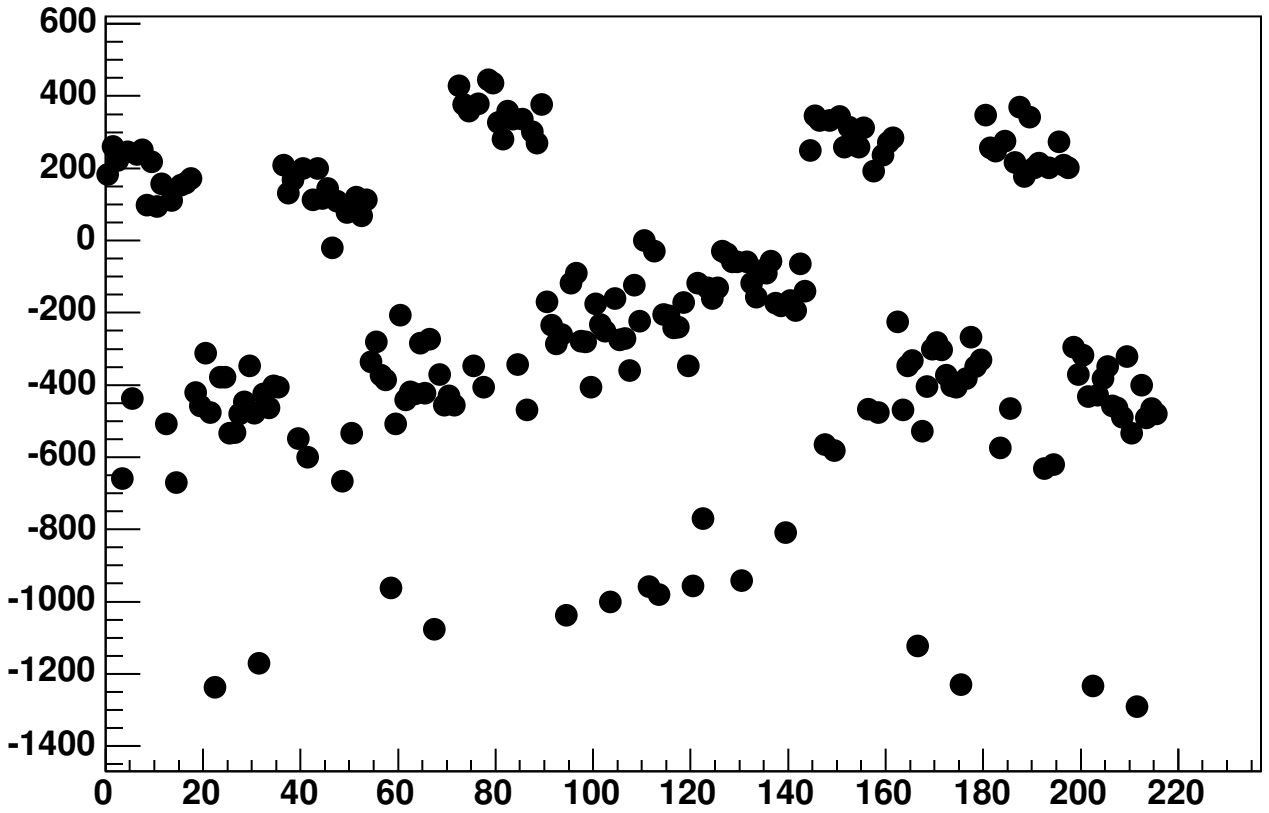
Enable 1, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



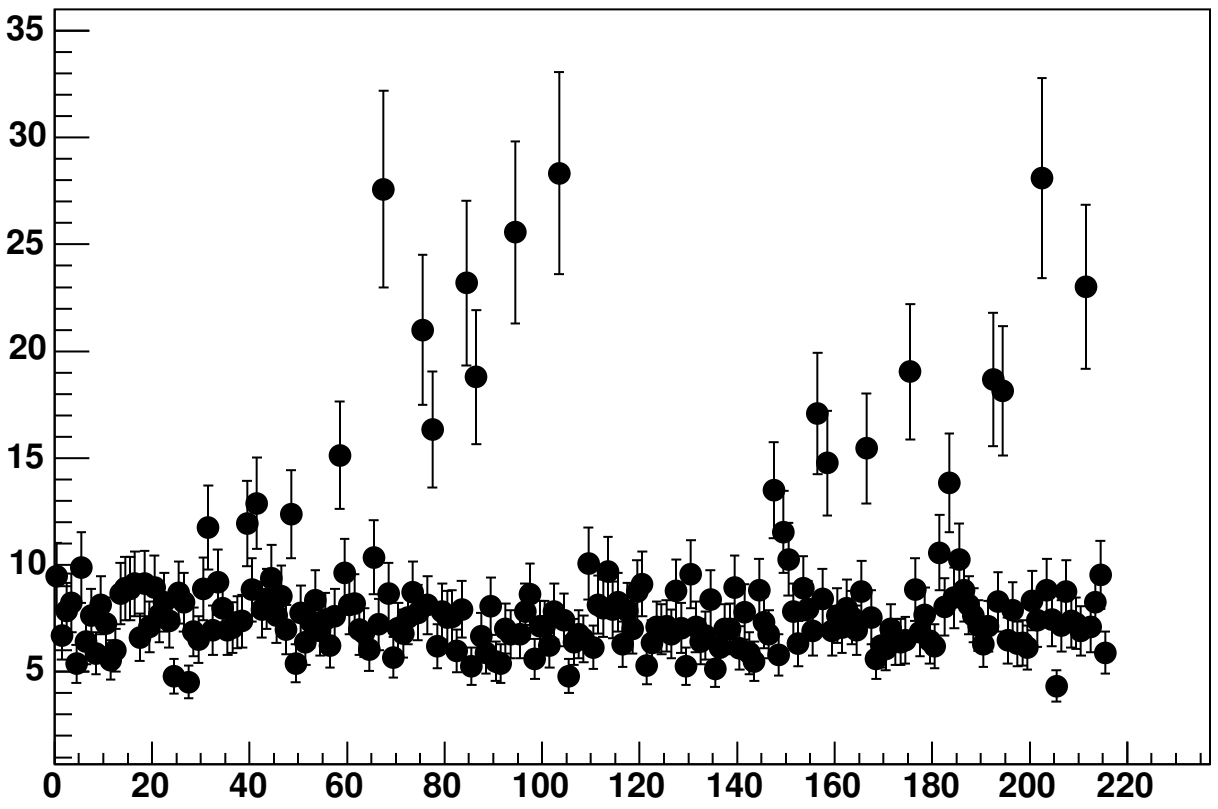
Enable 1, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



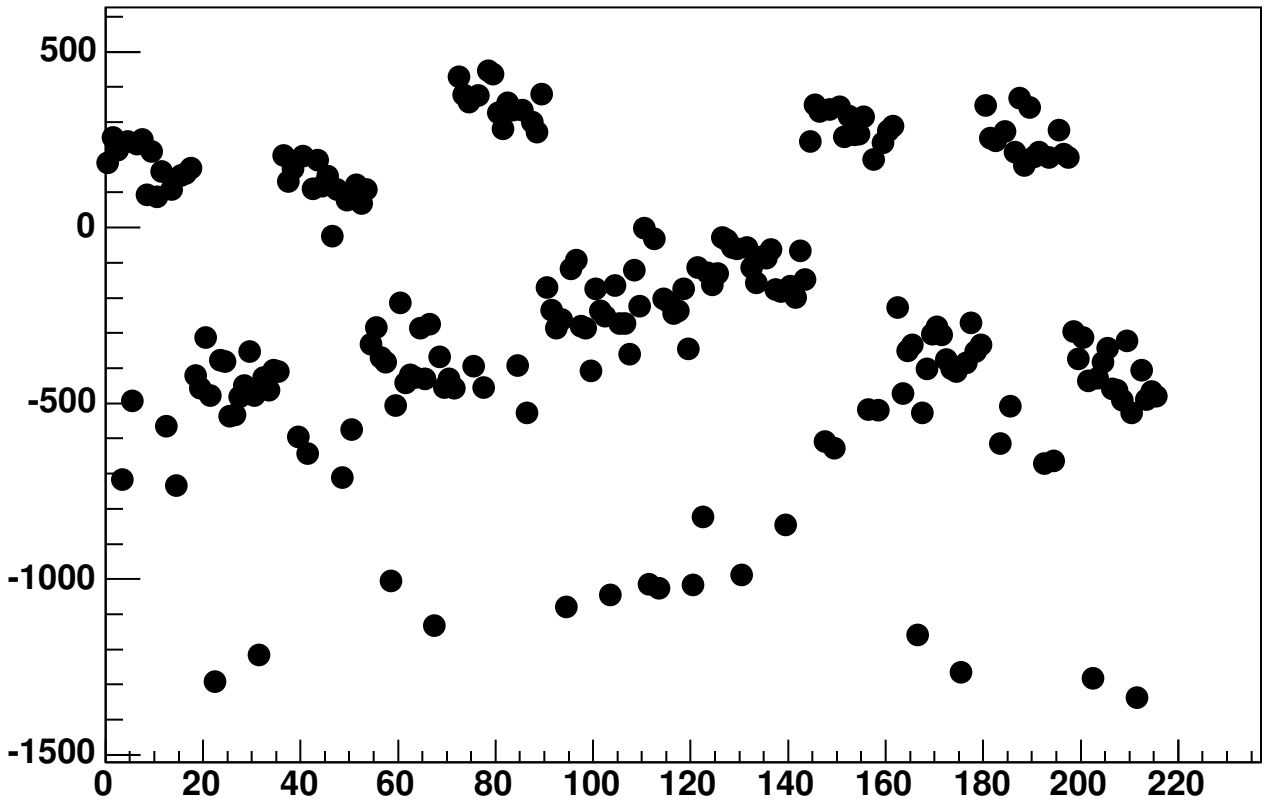
Enable 1, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



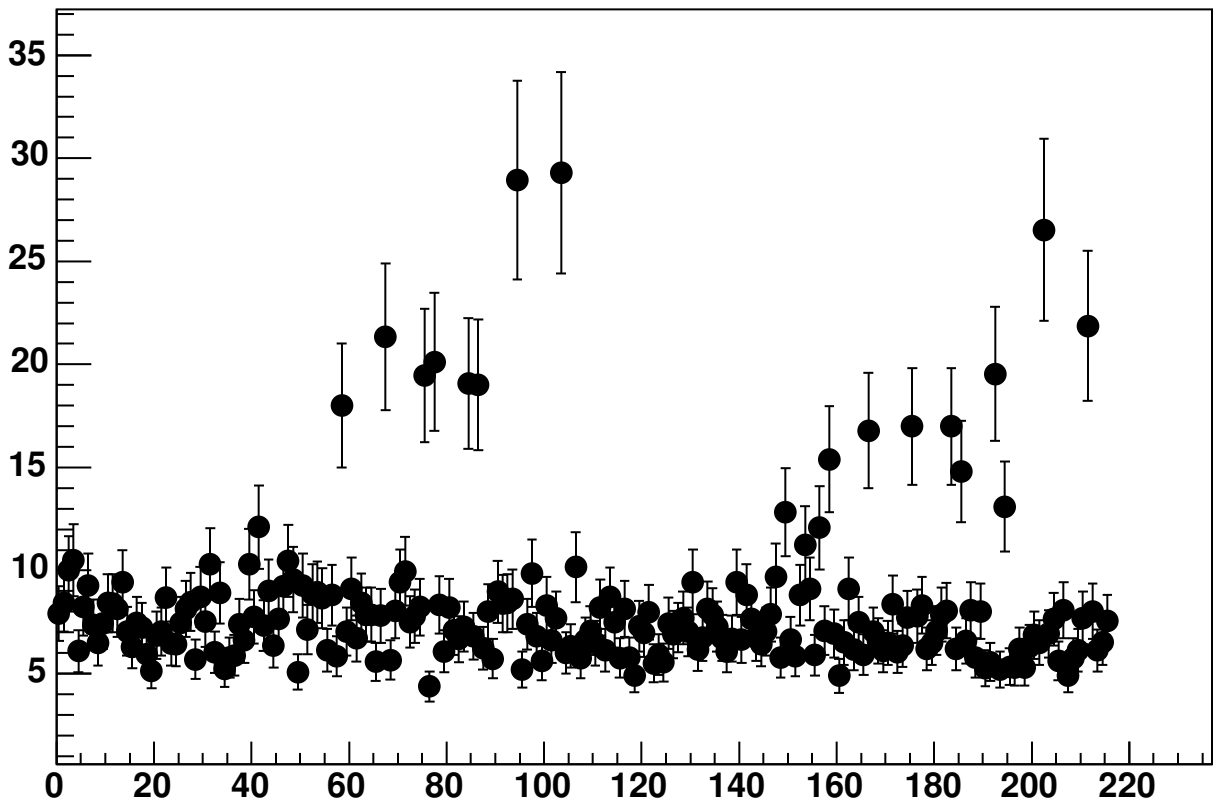
Enable 1, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



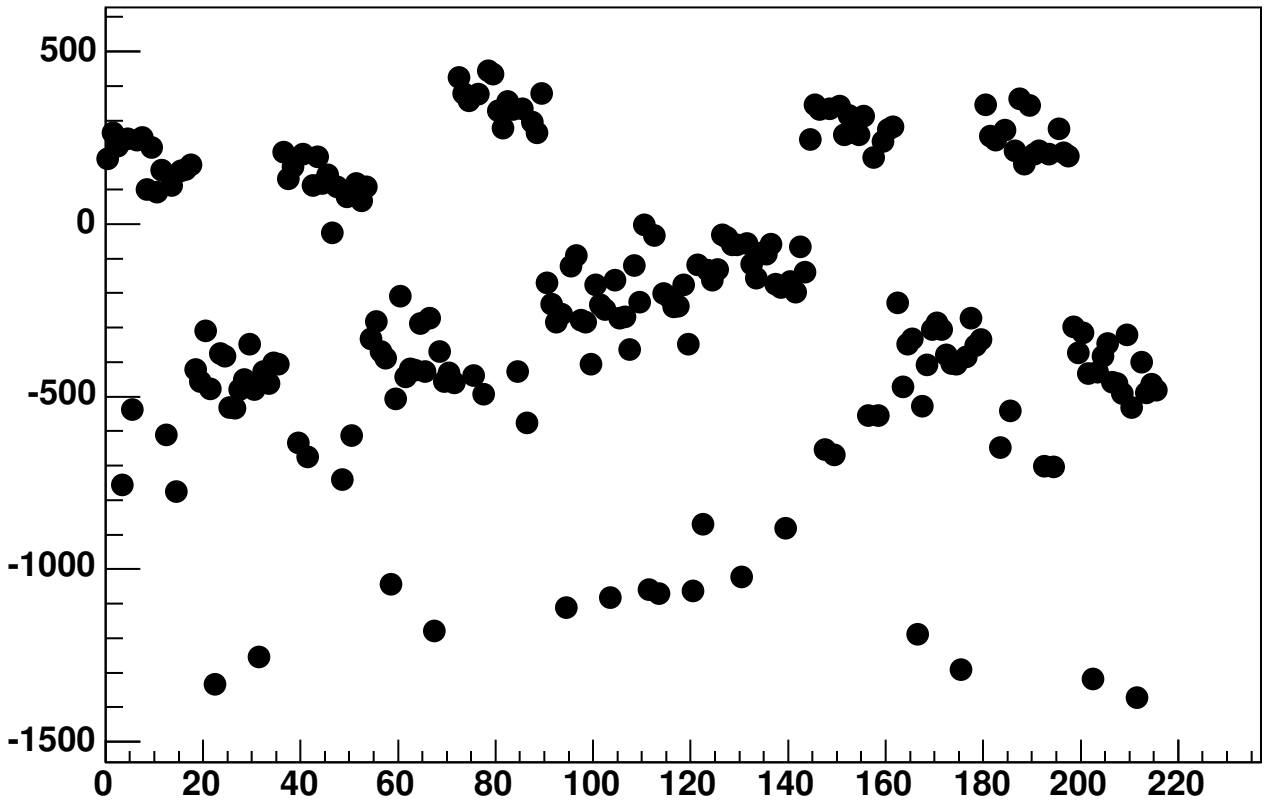
Enable 1, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



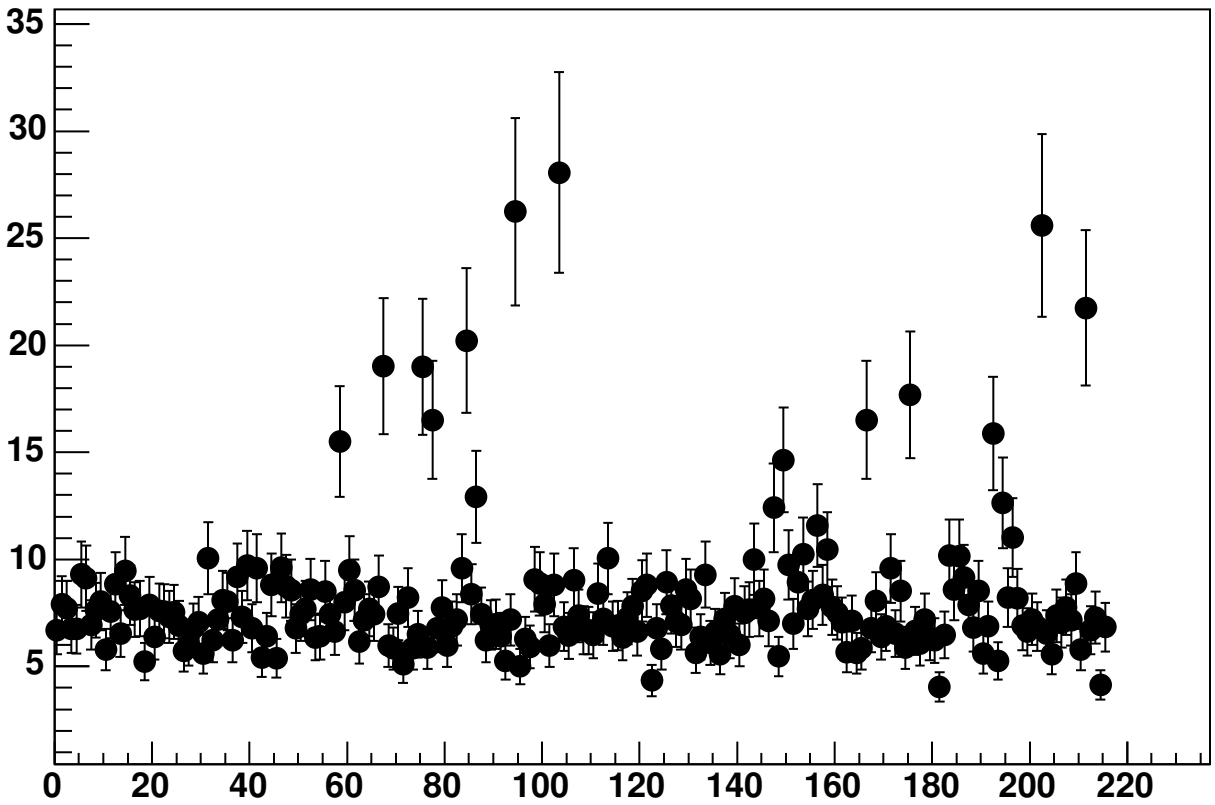
Enable 1, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



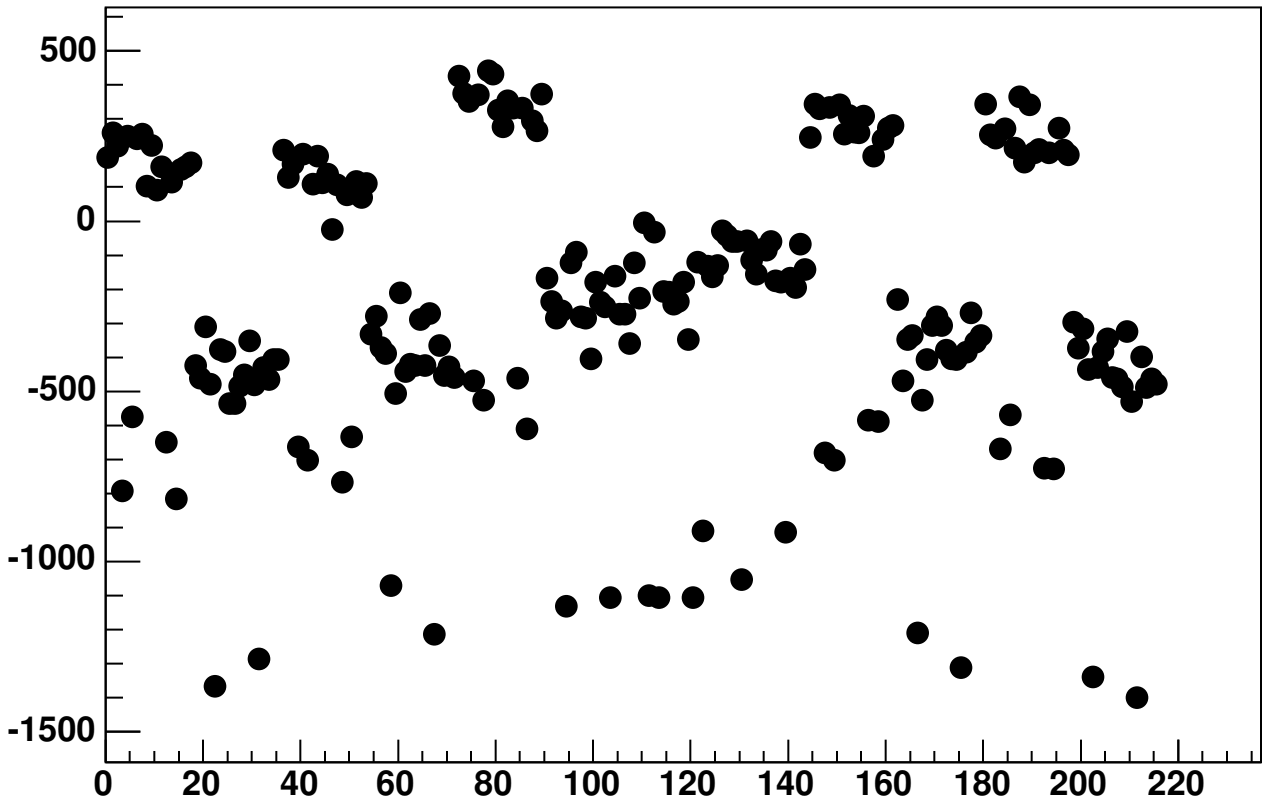
Enable 1, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



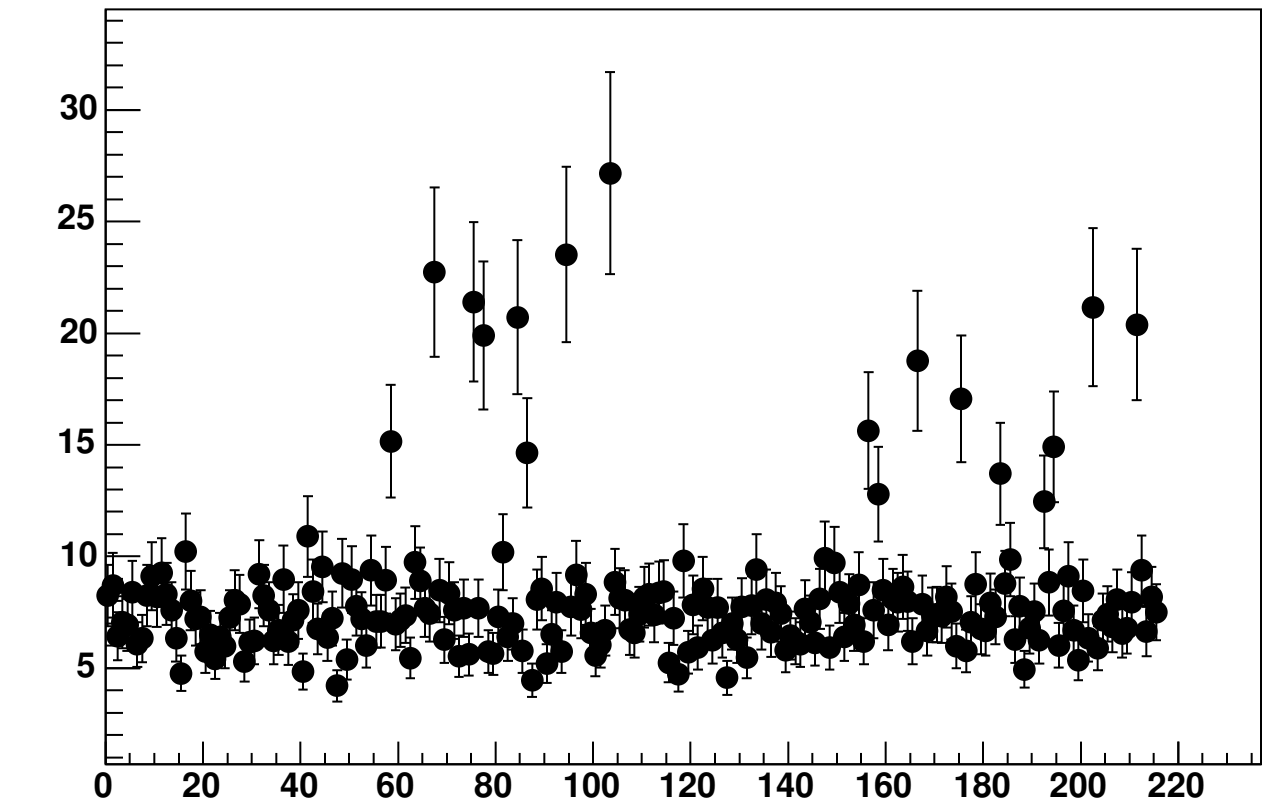
Enable 1, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



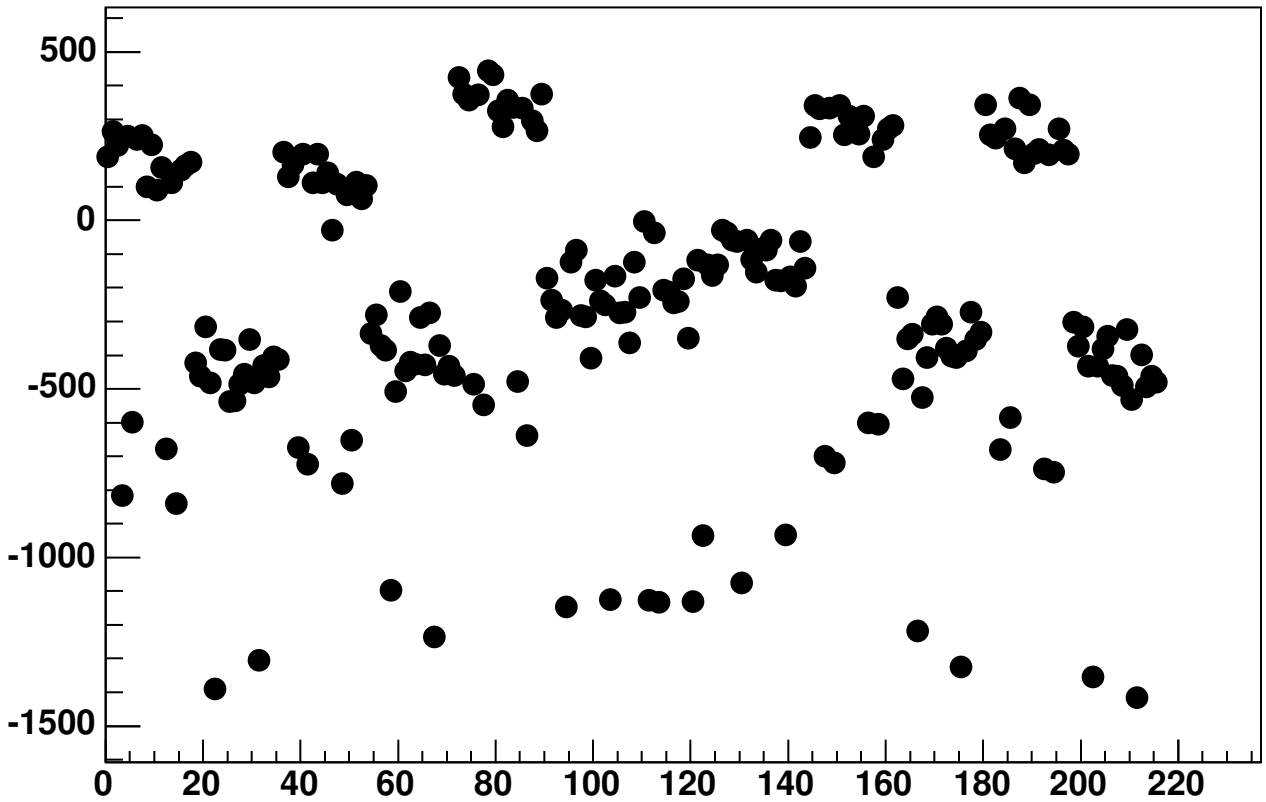
Enable 1, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



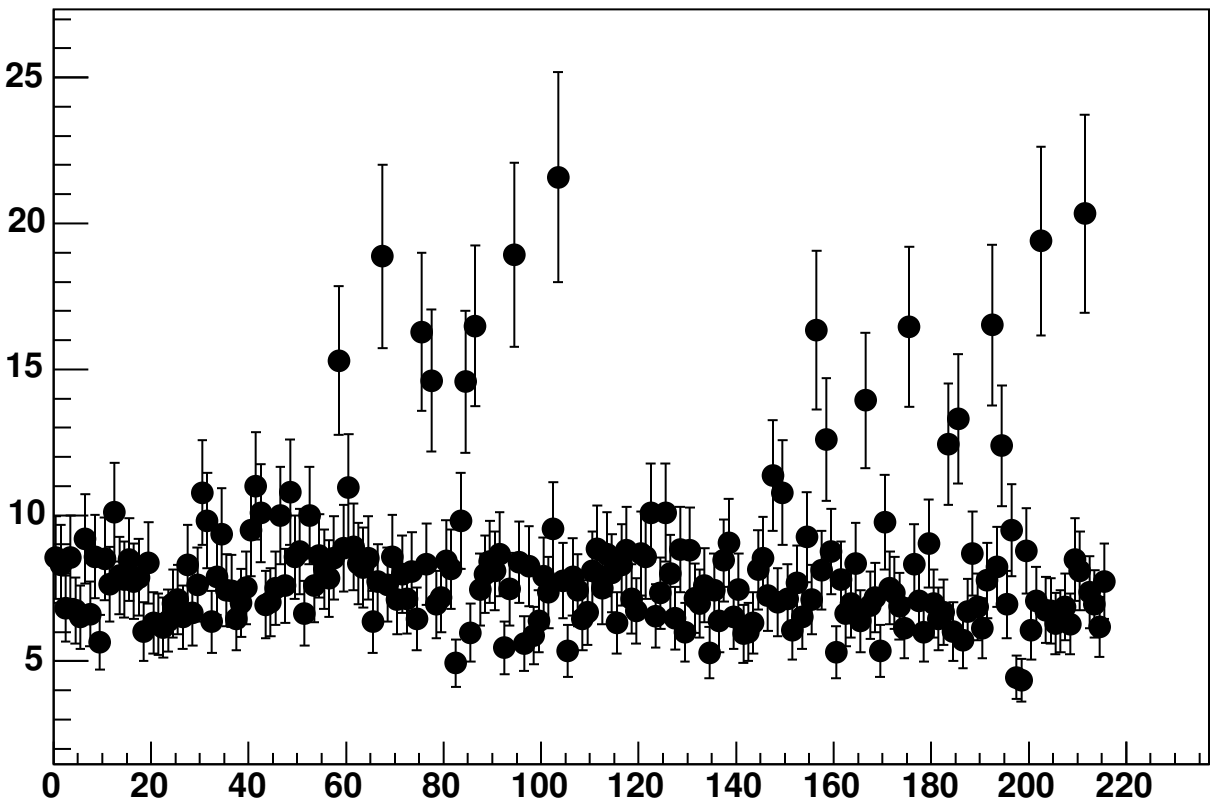
Enable 1, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



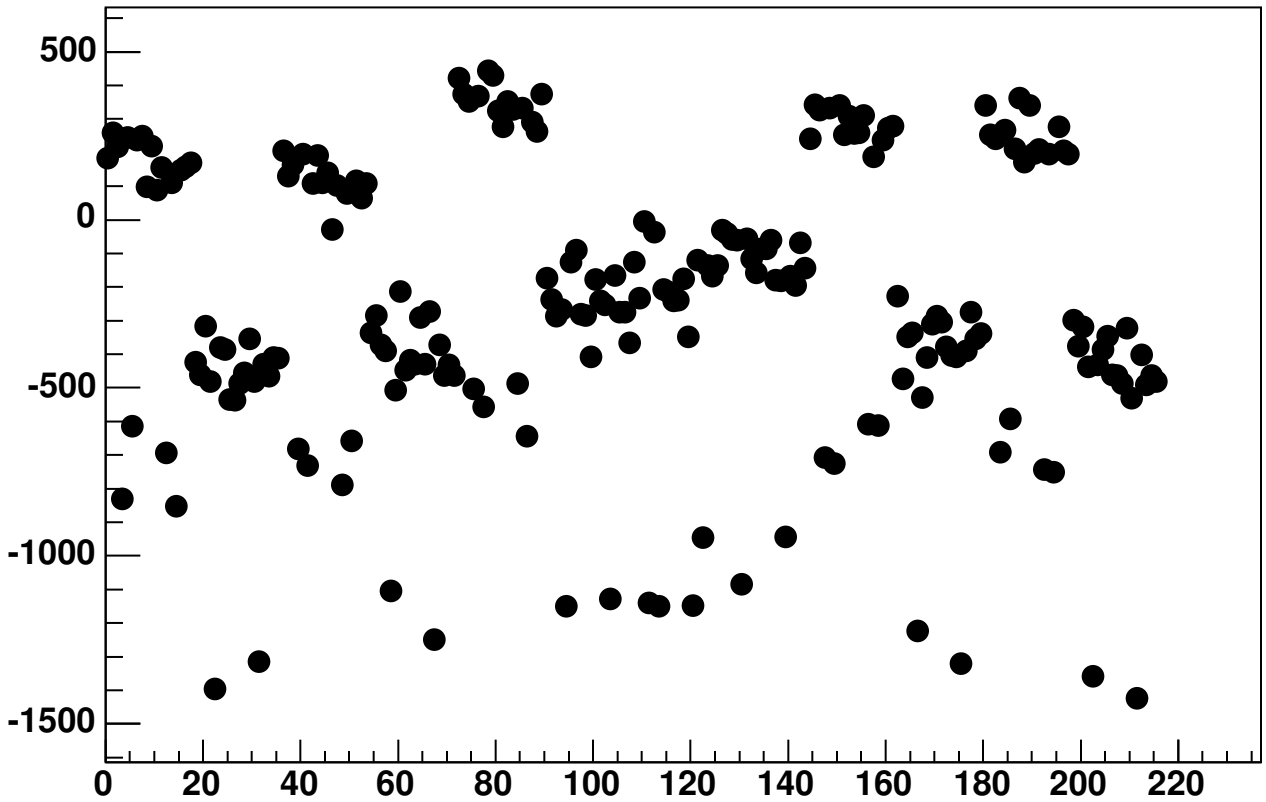
Enable 1, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



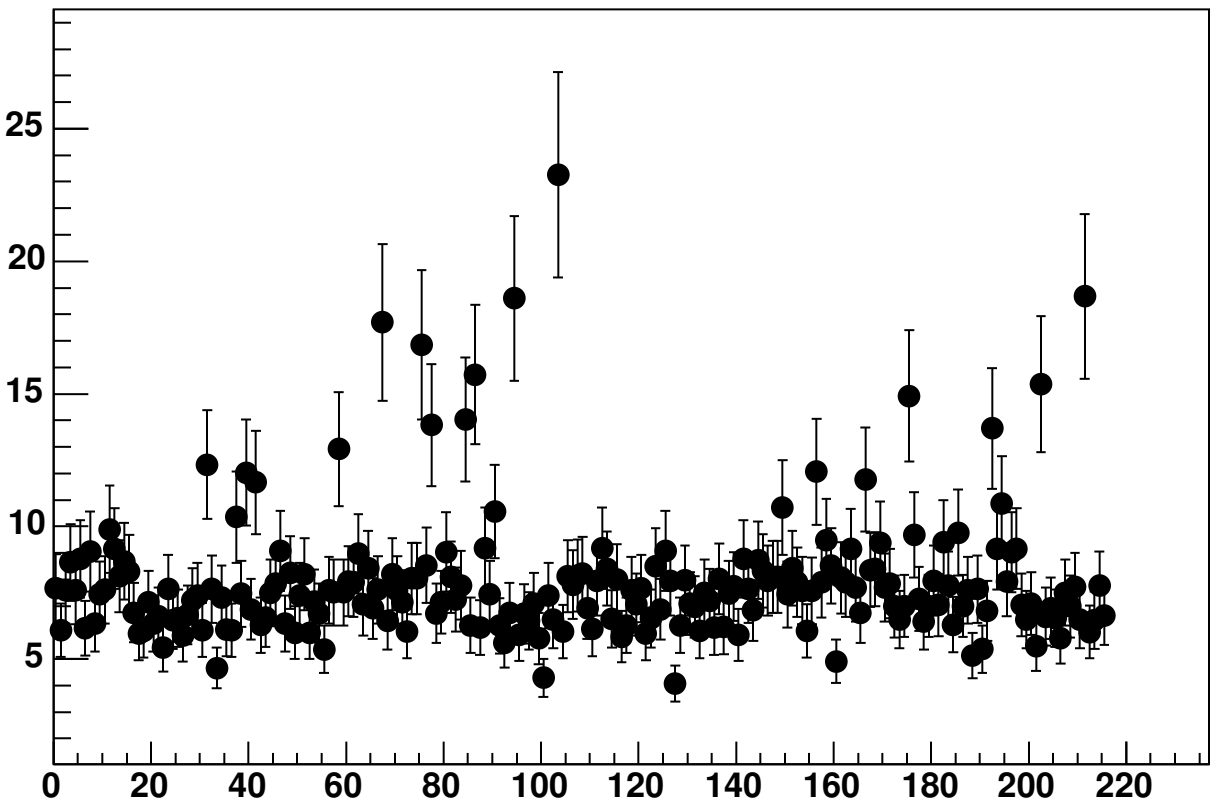
Enable 1, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



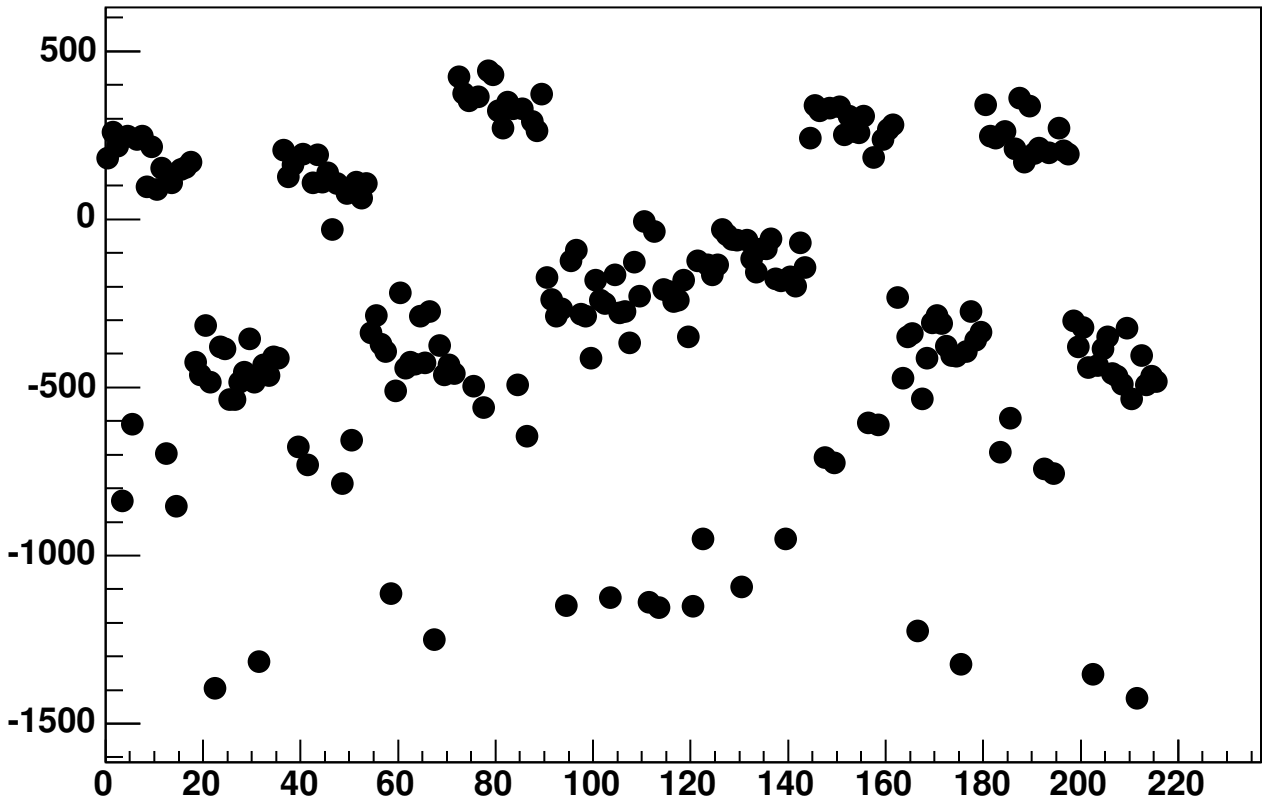
Enable 1, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



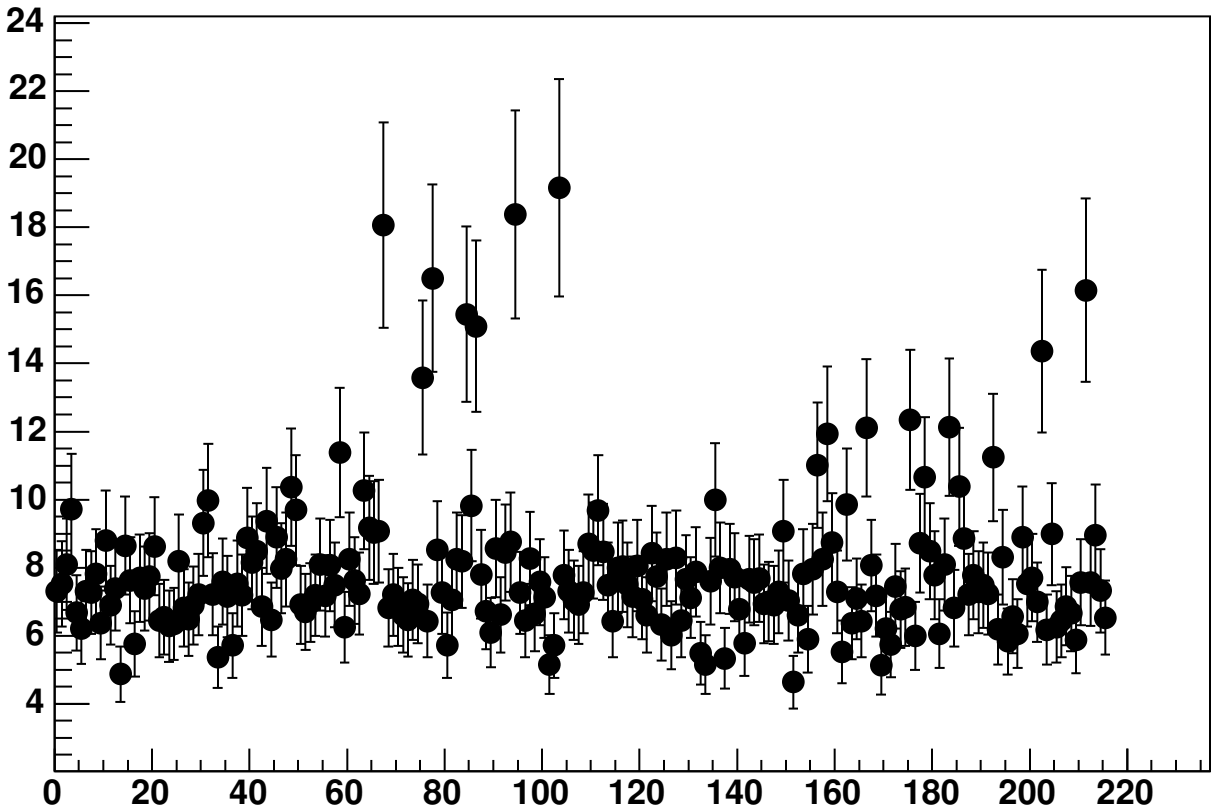
Enable 1, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



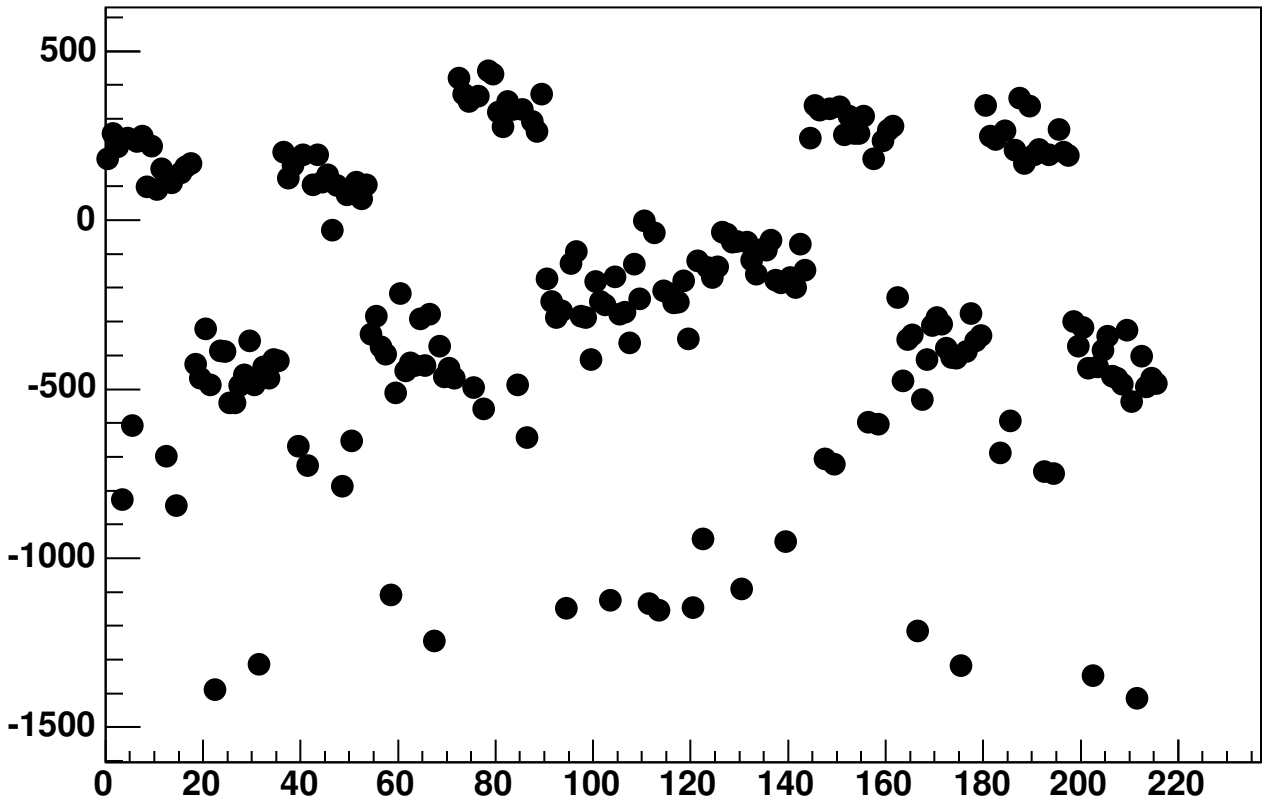
Enable 1, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



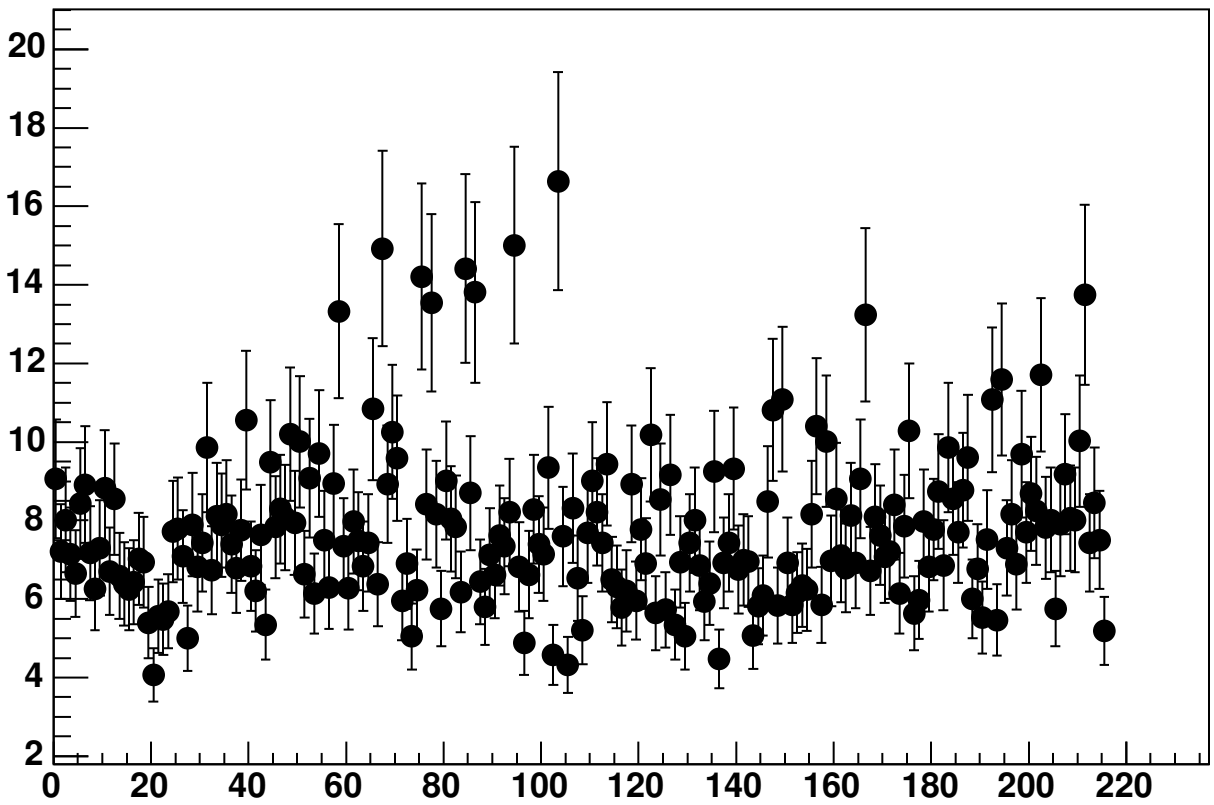
Enable 1, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



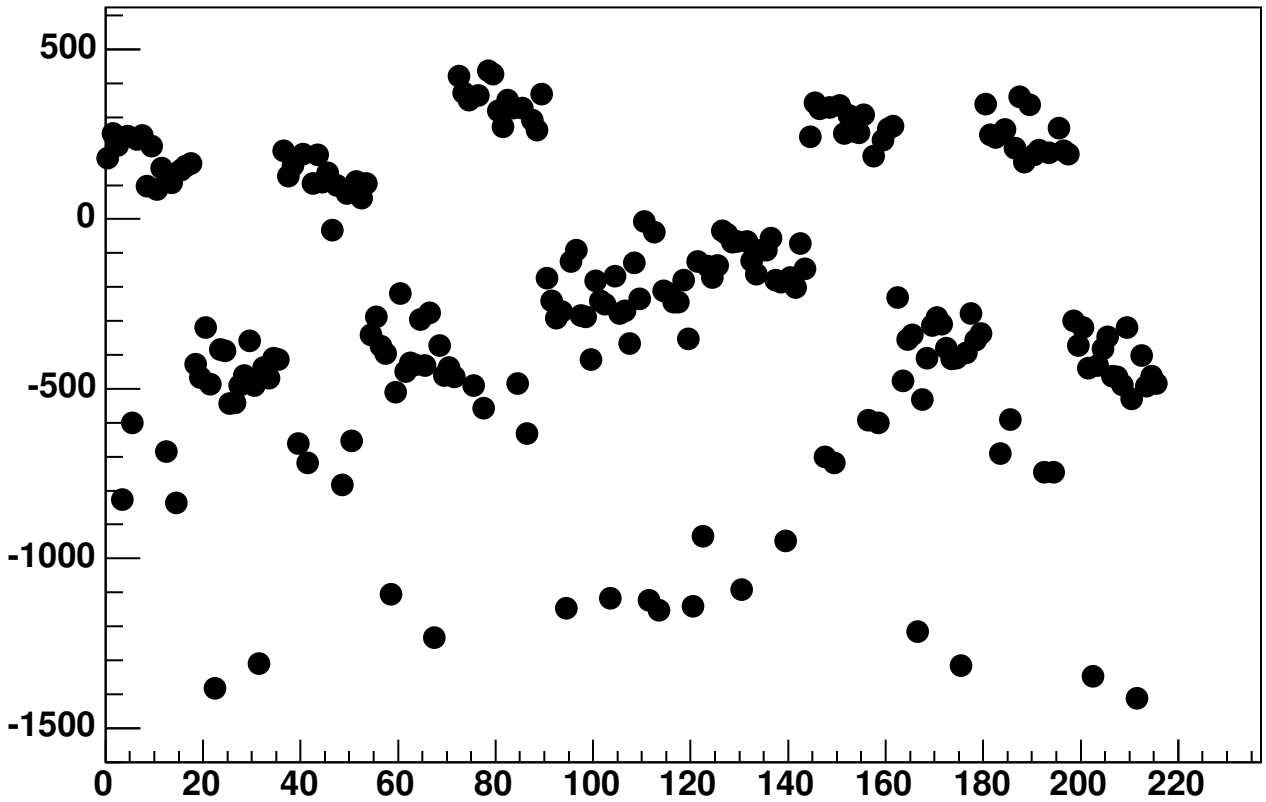
Enable 1, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



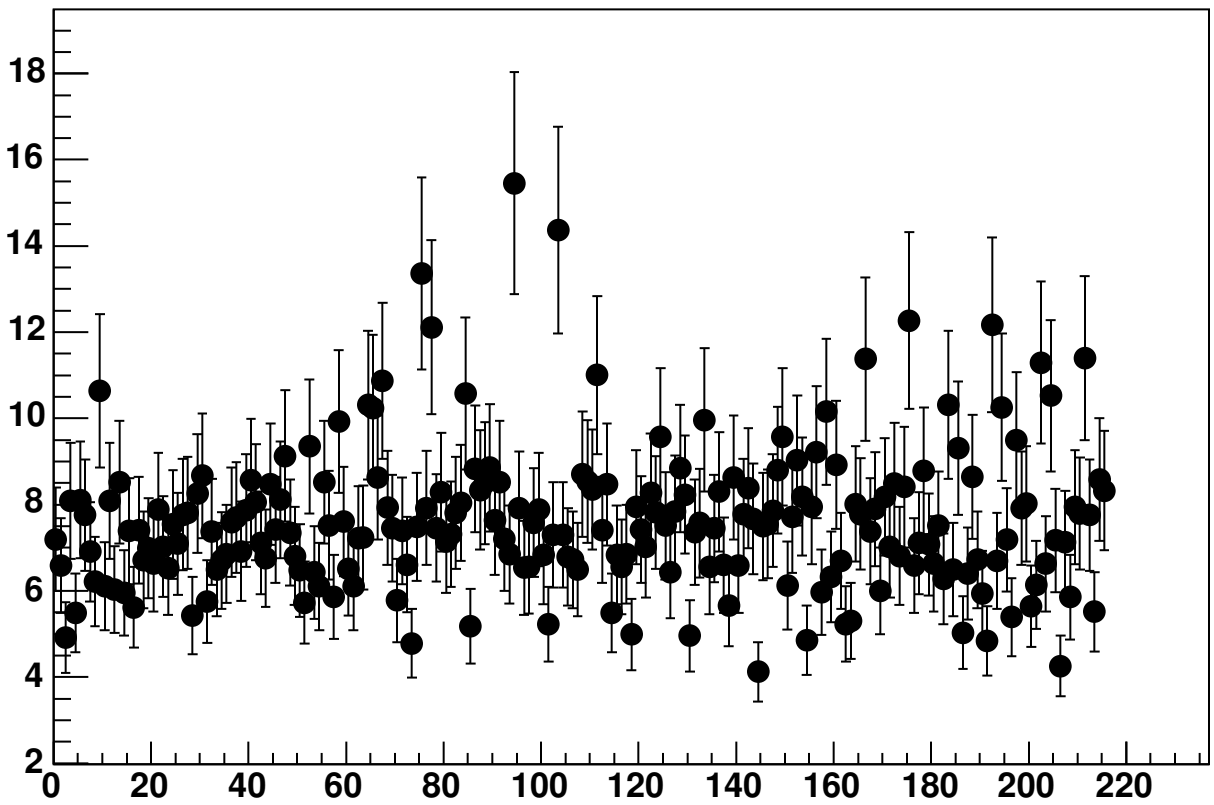
Enable 1, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



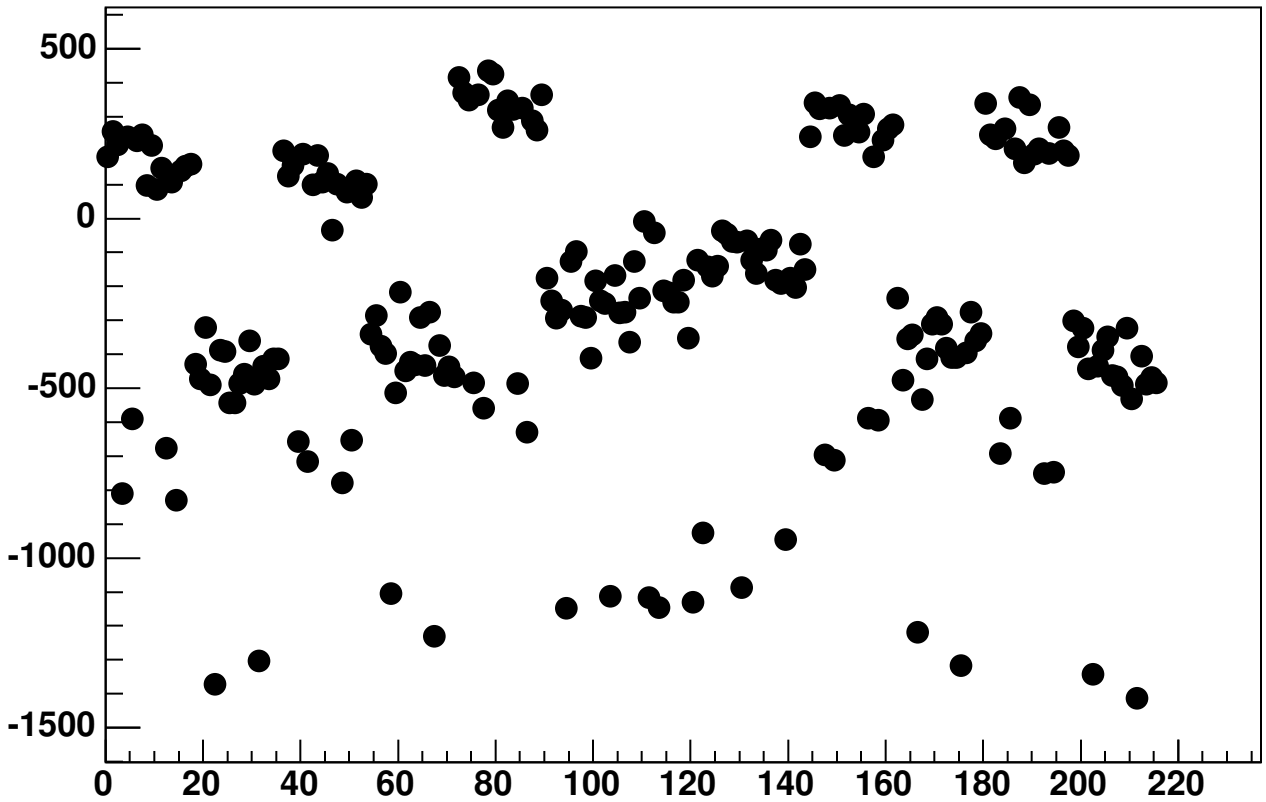
Enable 1, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



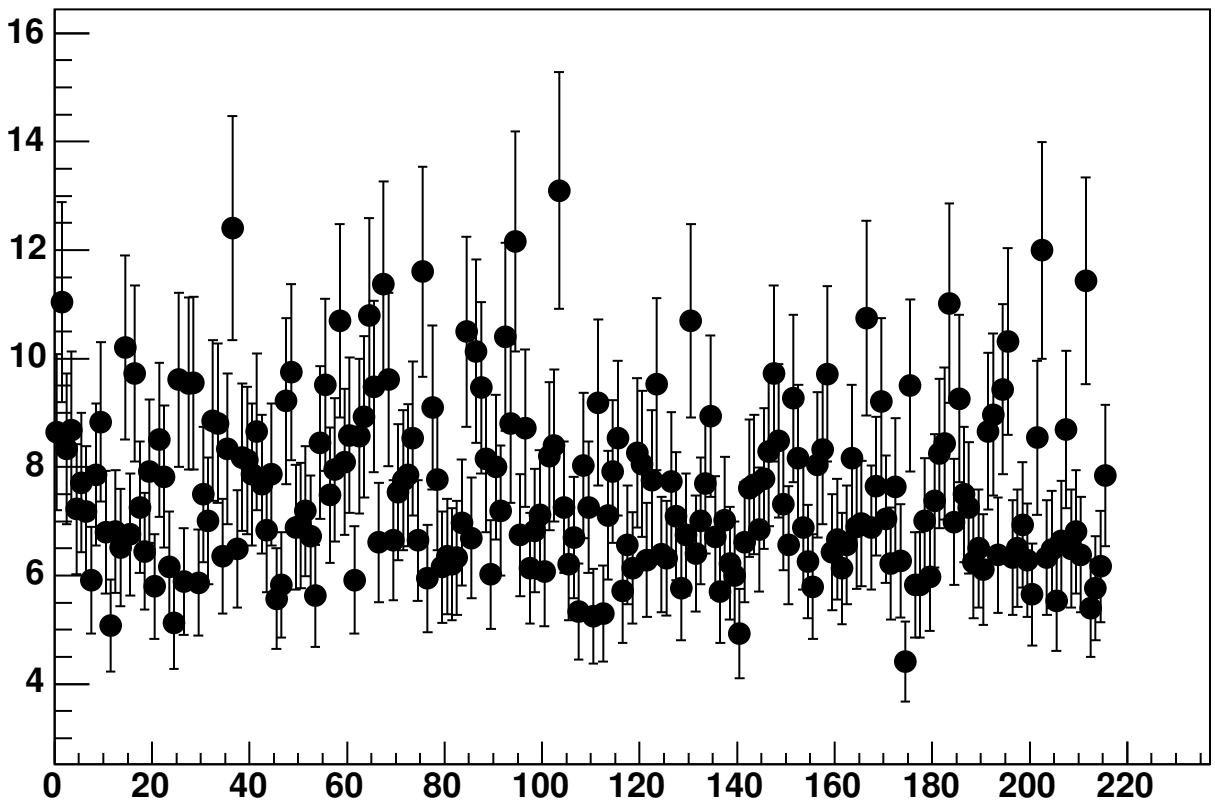
Enable 1, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan



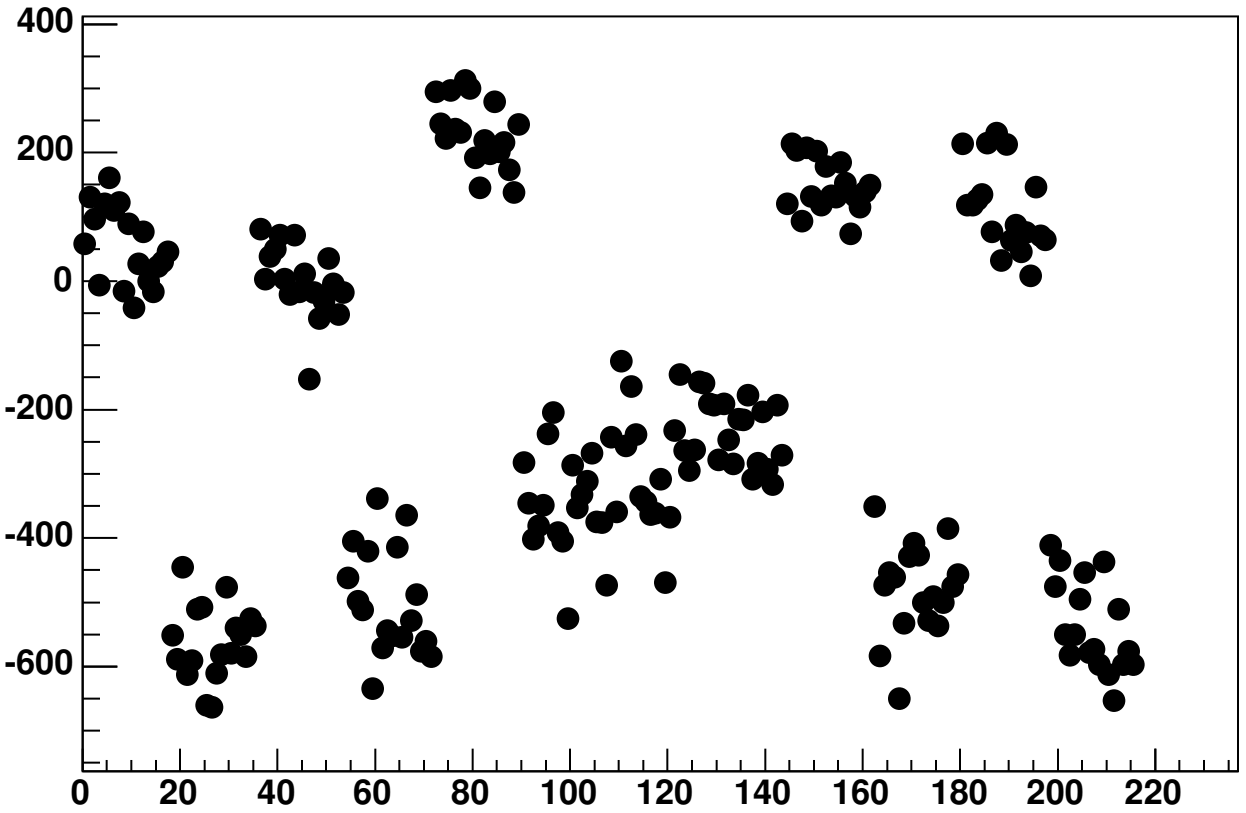
Enable 1, DAC=4000, Hold=245, ADC Mean vs 18*Chip+Chan



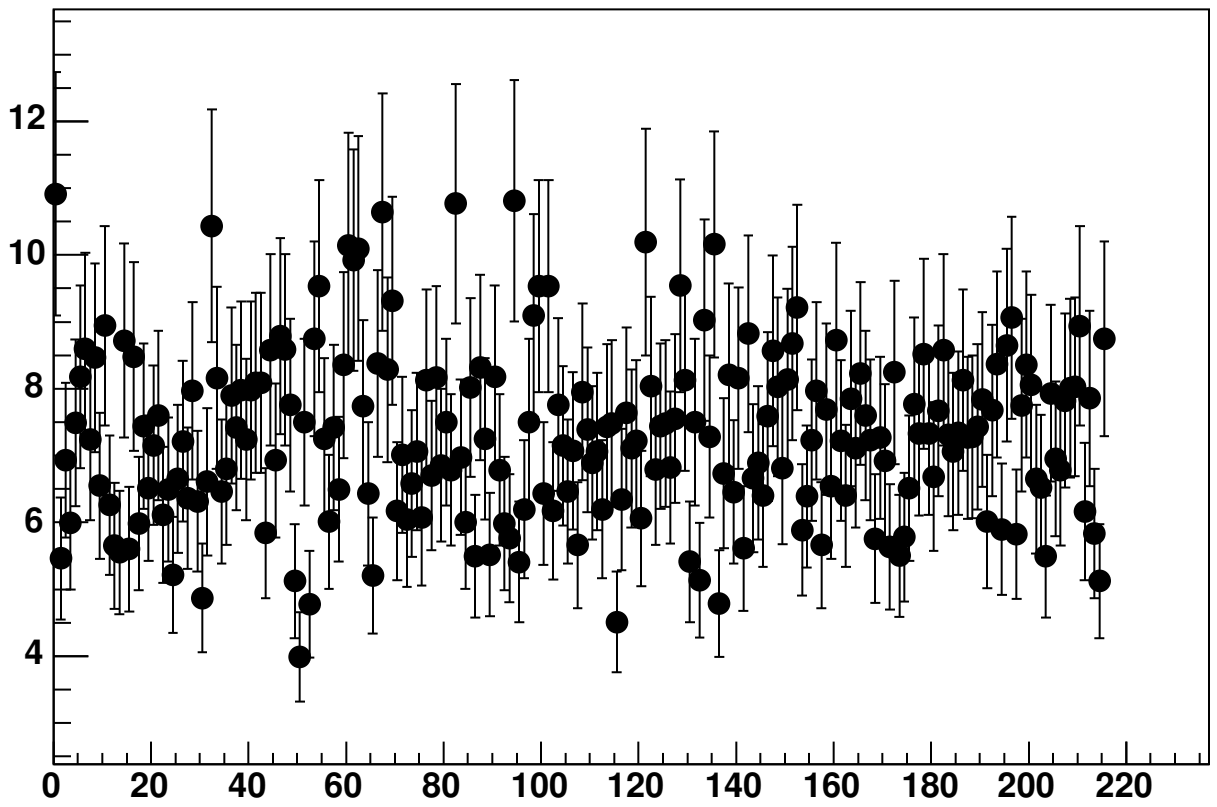
Enable 1, DAC=4000, Hold=245, ADC Noise vs 18*Chip+Chan



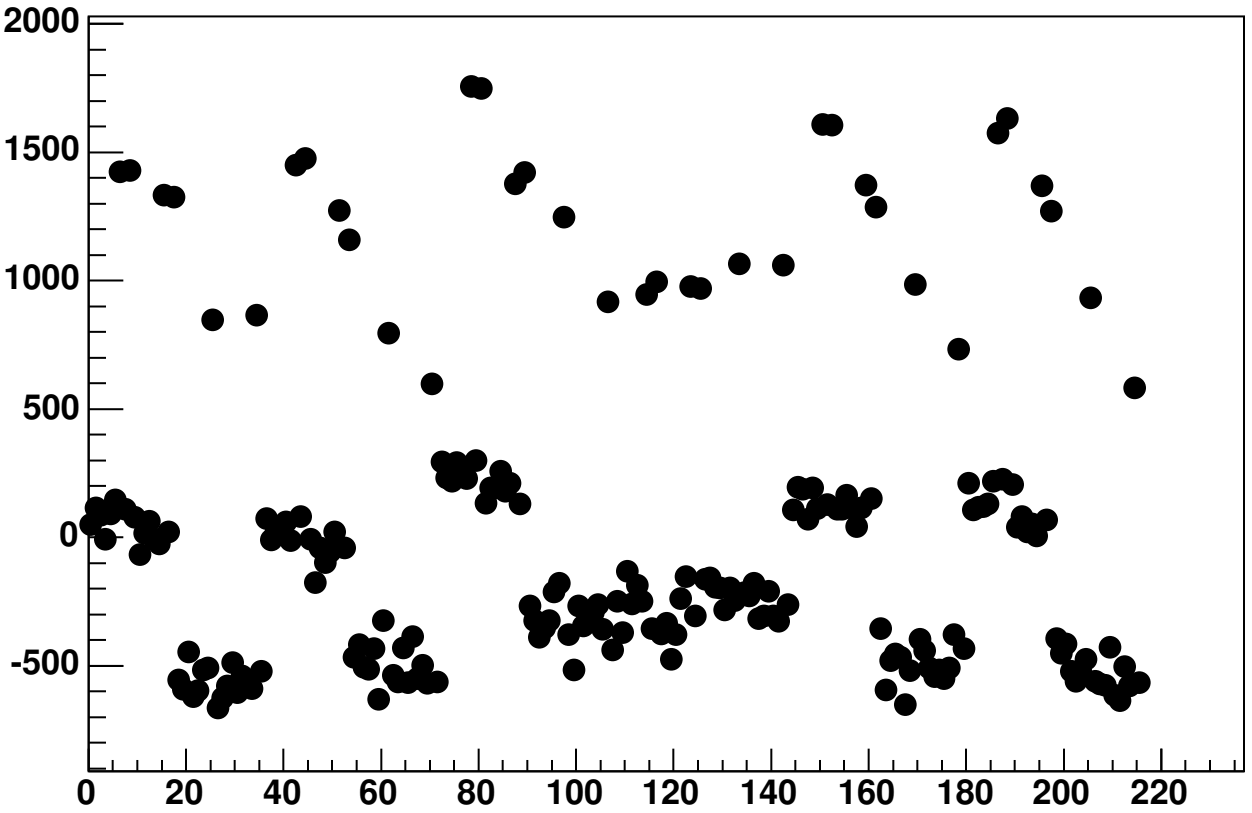
Enable 2, DAC=4000, Hold=0, ADC Mean vs 18*Chip+Chan



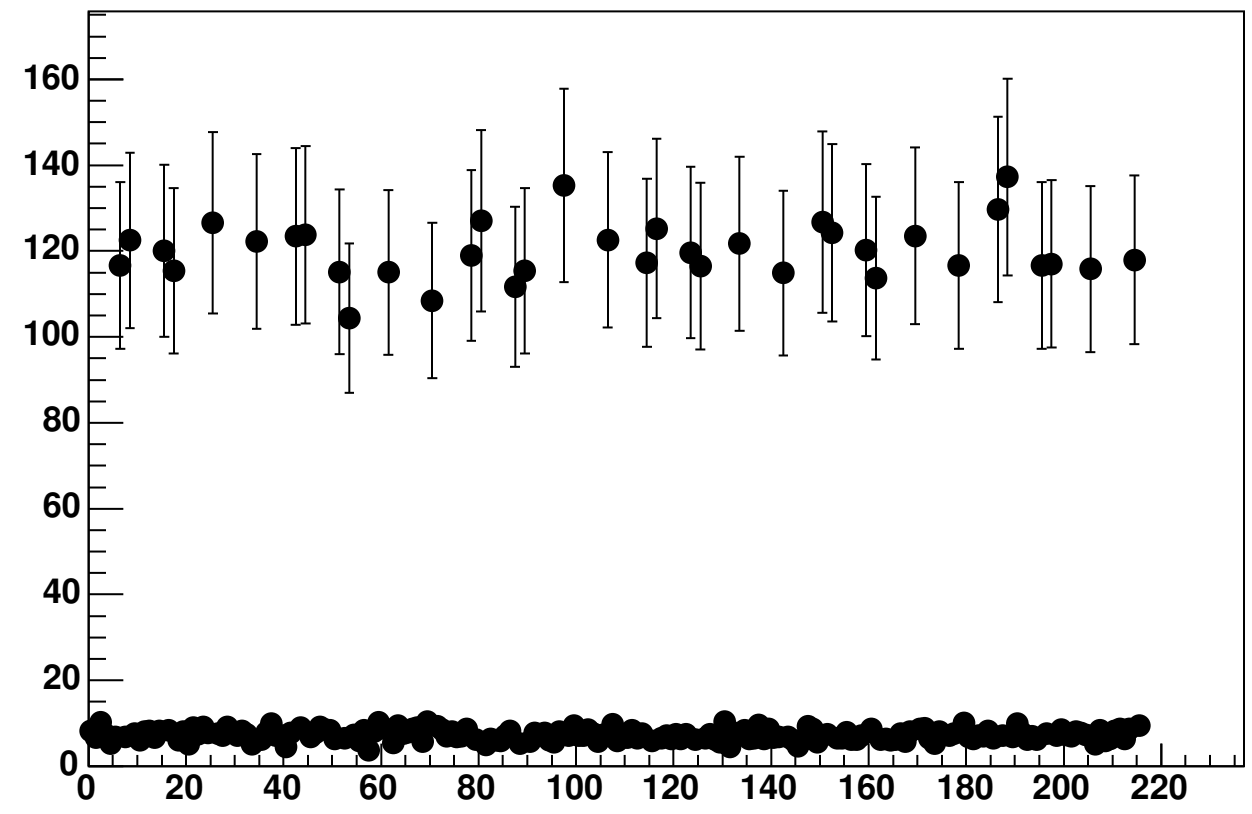
Enable 2, DAC=4000, Hold=0, ADC Noise vs 18*Chip+Chan



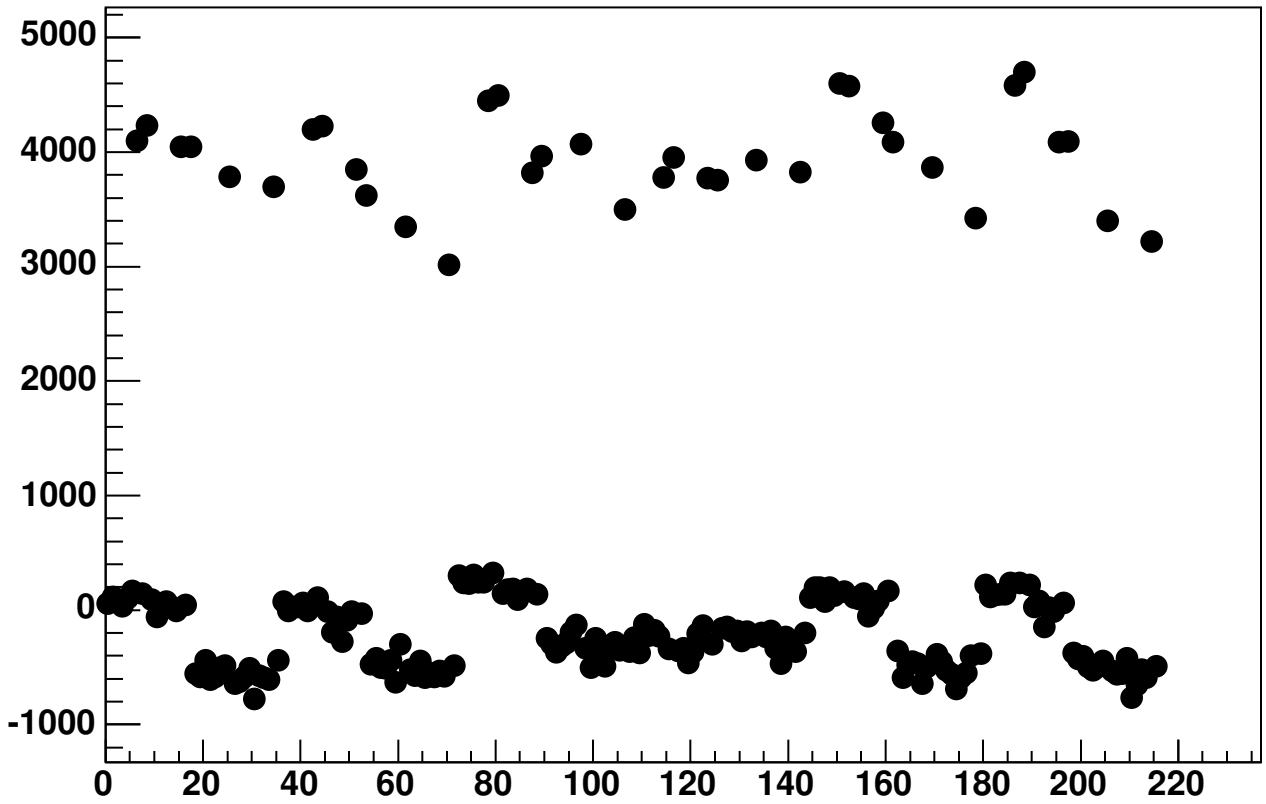
Enable 2, DAC=4000, Hold=5, ADC Mean vs 18*Chip+Chan



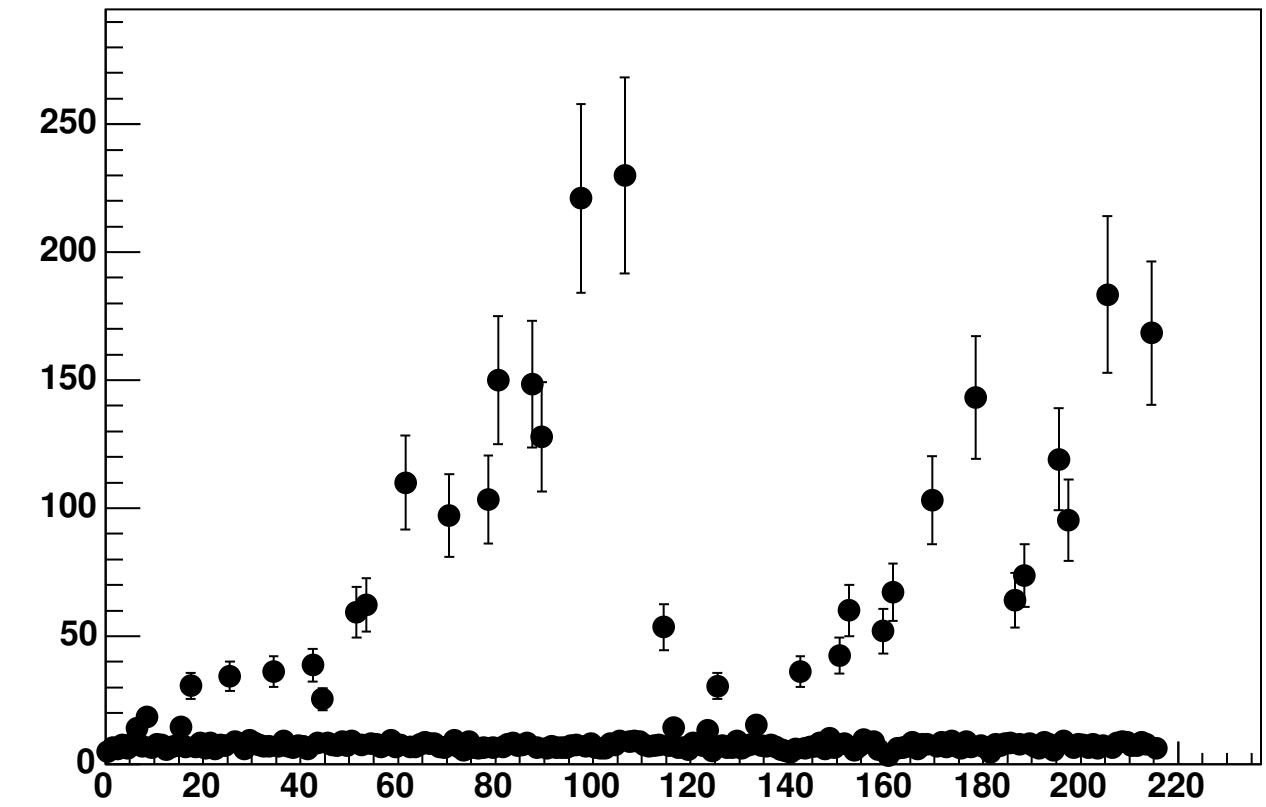
Enable 2, DAC=4000, Hold=5, ADC Noise vs 18*Chip+Chan



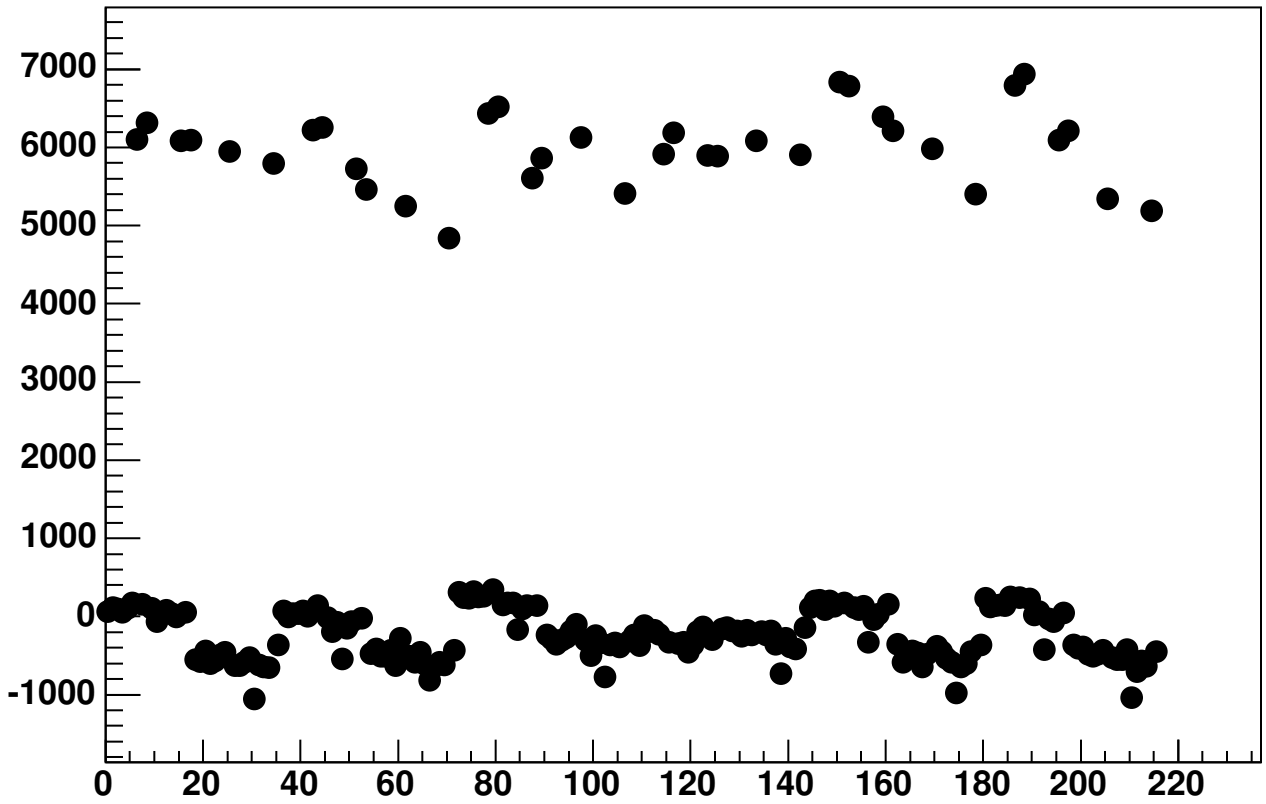
Enable 2, DAC=4000, Hold=10, ADC Mean vs 18*Chip+Chan



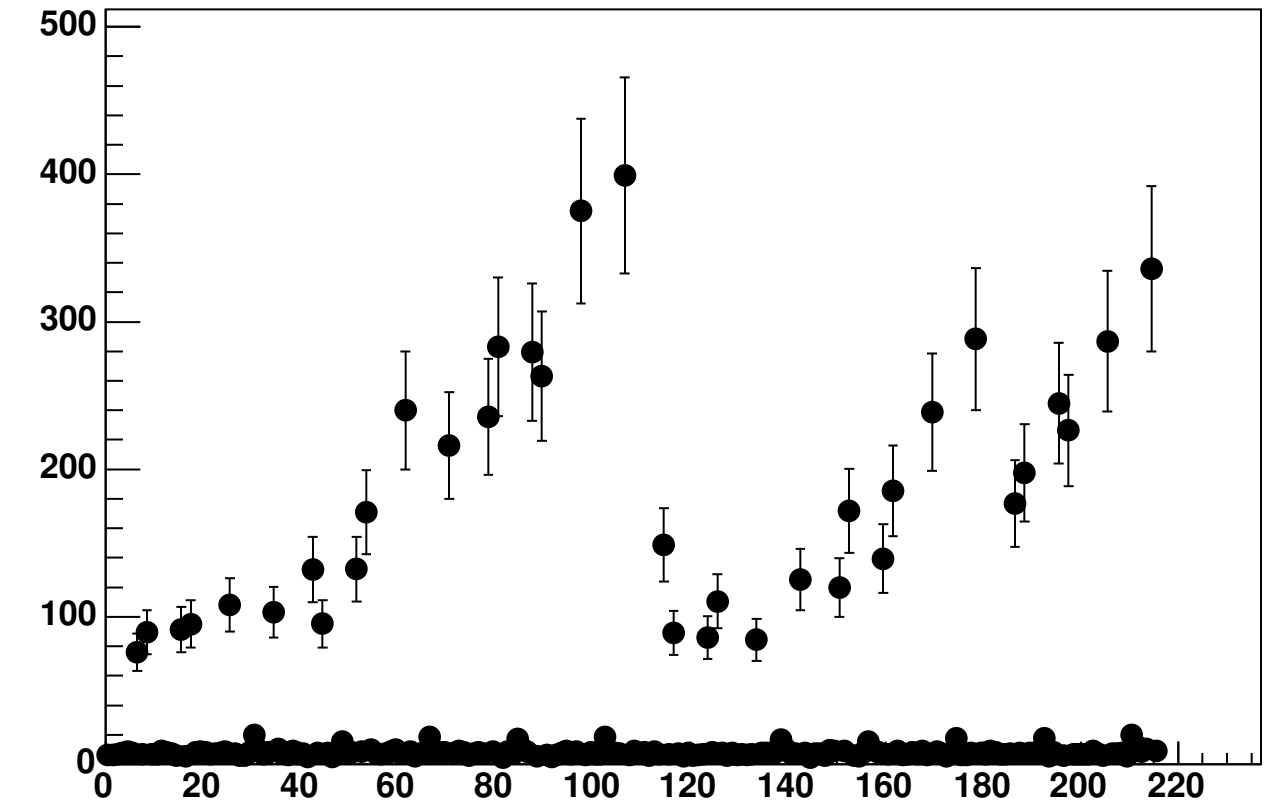
Enable 2, DAC=4000, Hold=10, ADC Noise vs 18*Chip+Chan



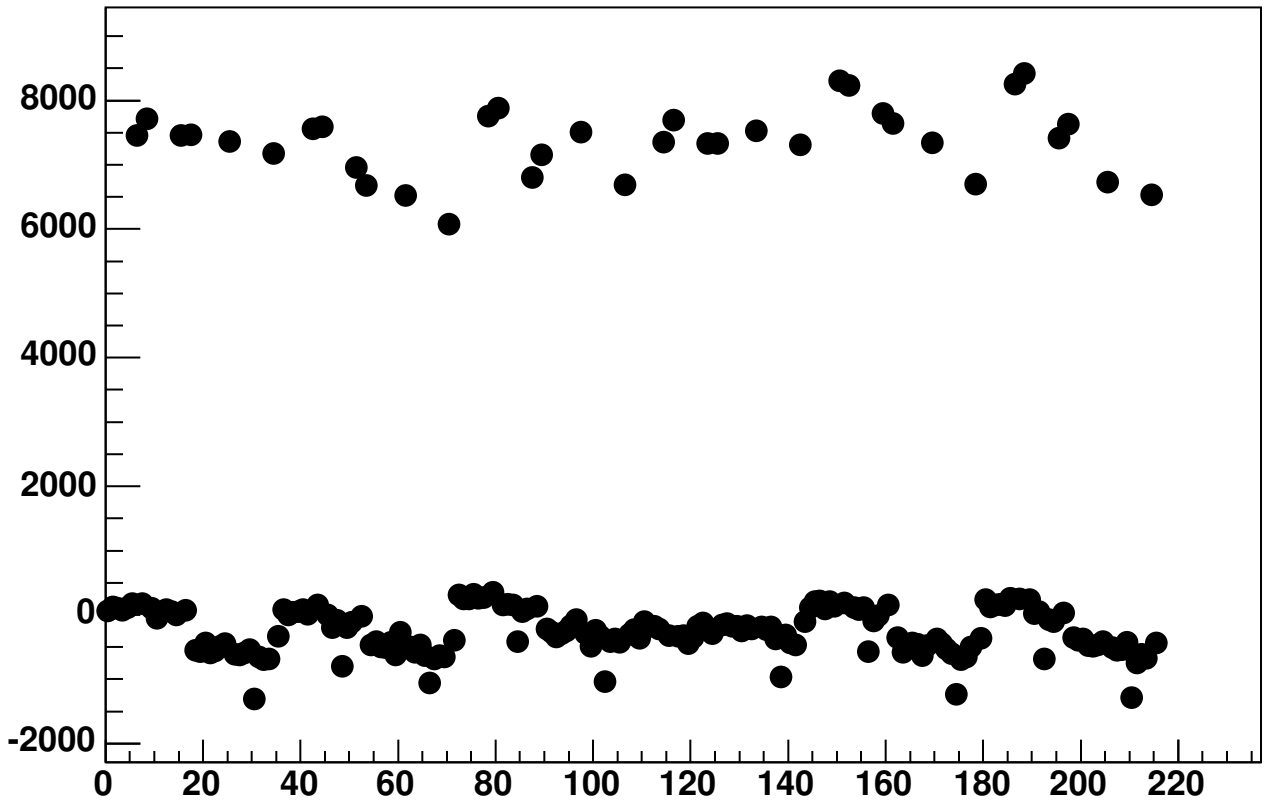
Enable 2, DAC=4000, Hold=15, ADC Mean vs 18*Chip+Chan



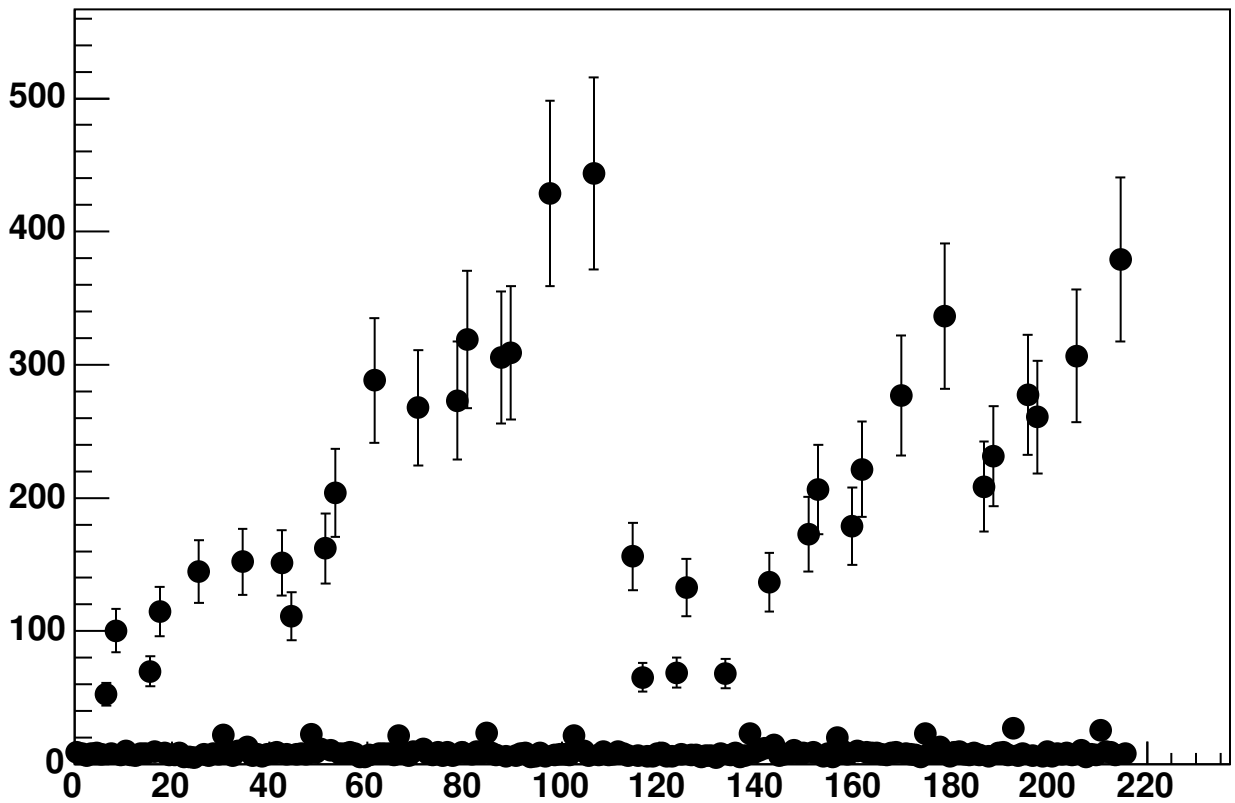
Enable 2, DAC=4000, Hold=15, ADC Noise vs 18*Chip+Chan



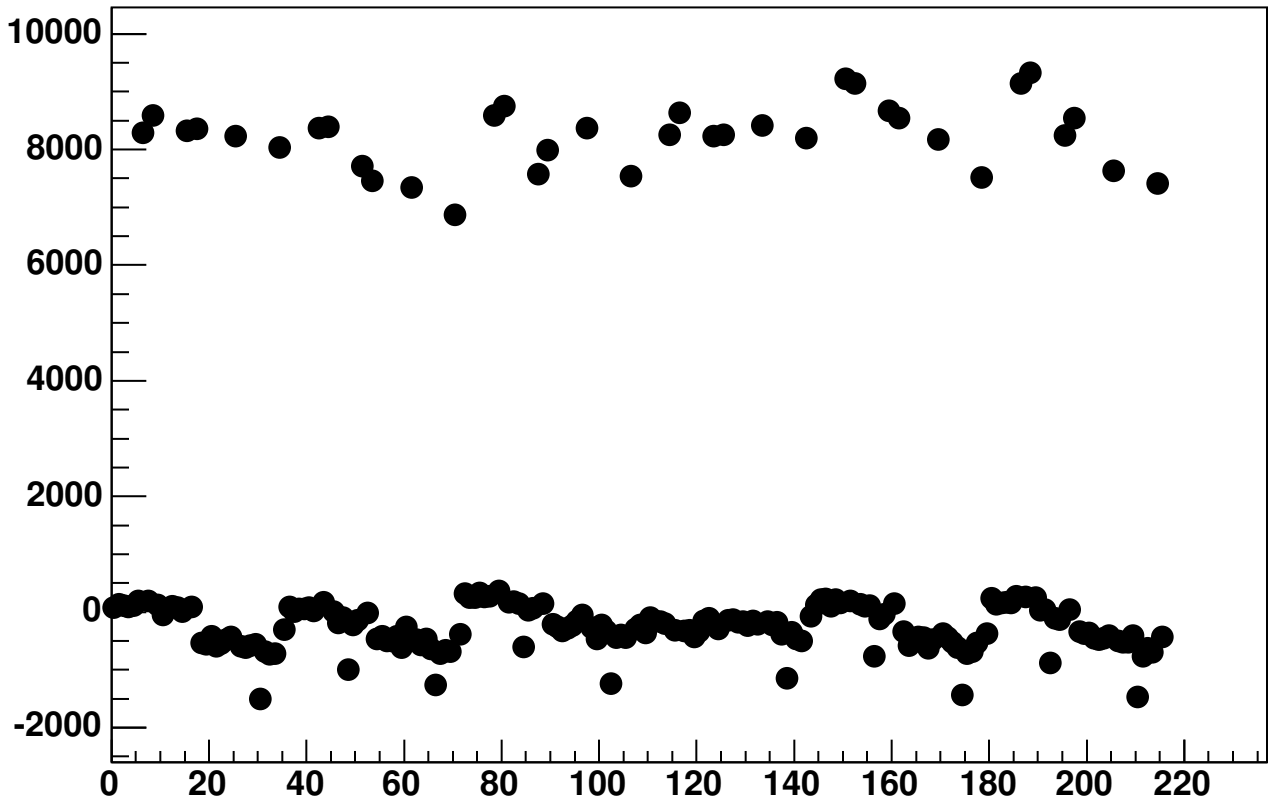
Enable 2, DAC=4000, Hold=20, ADC Mean vs 18*Chip+Chan



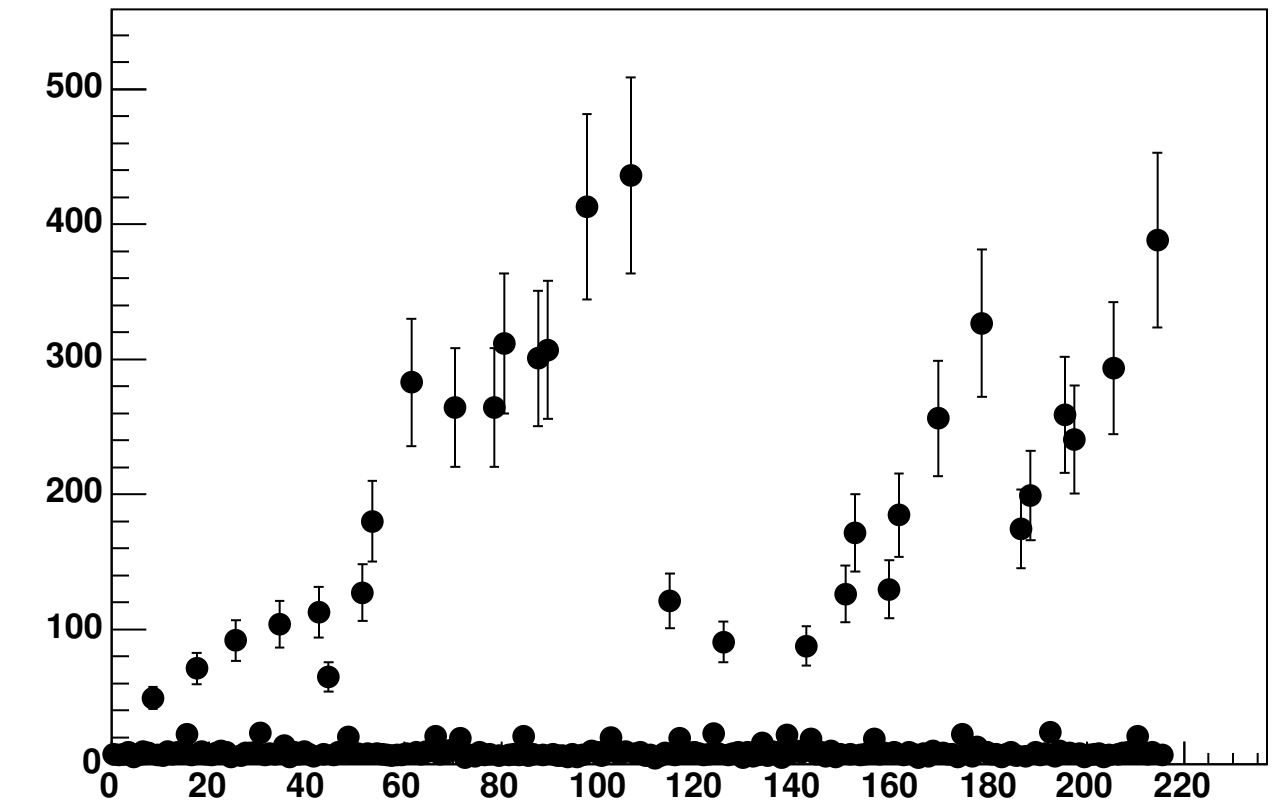
Enable 2, DAC=4000, Hold=20, ADC Noise vs 18*Chip+Chan



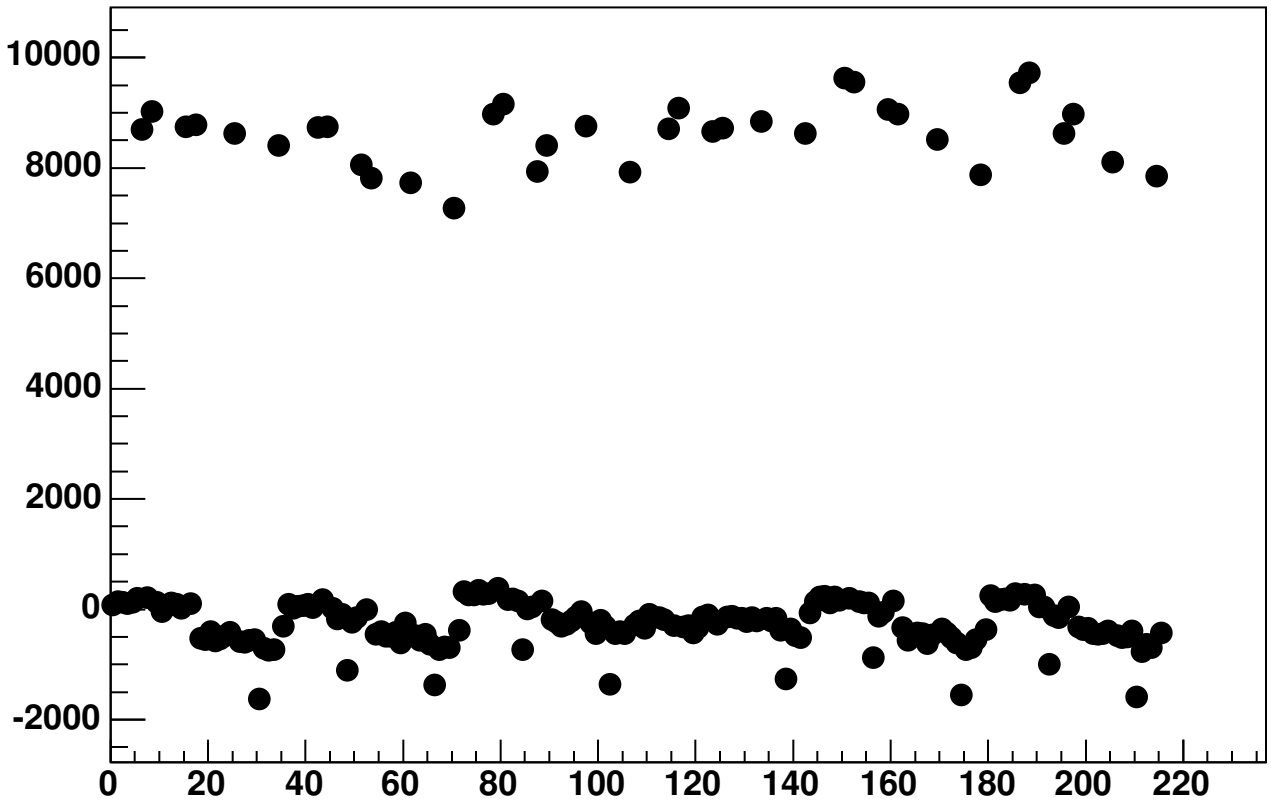
Enable 2, DAC=4000, Hold=25, ADC Mean vs 18*Chip+Chan



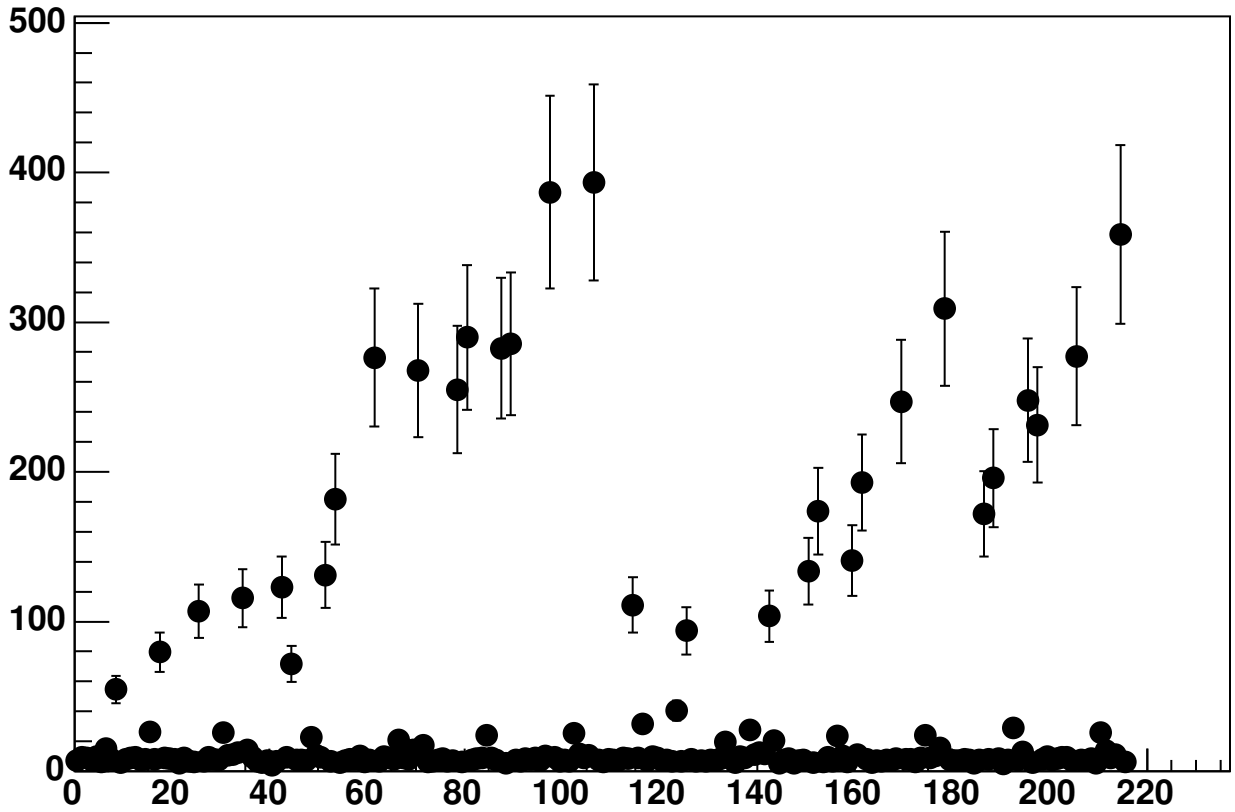
Enable 2, DAC=4000, Hold=25, ADC Noise vs 18*Chip+Chan



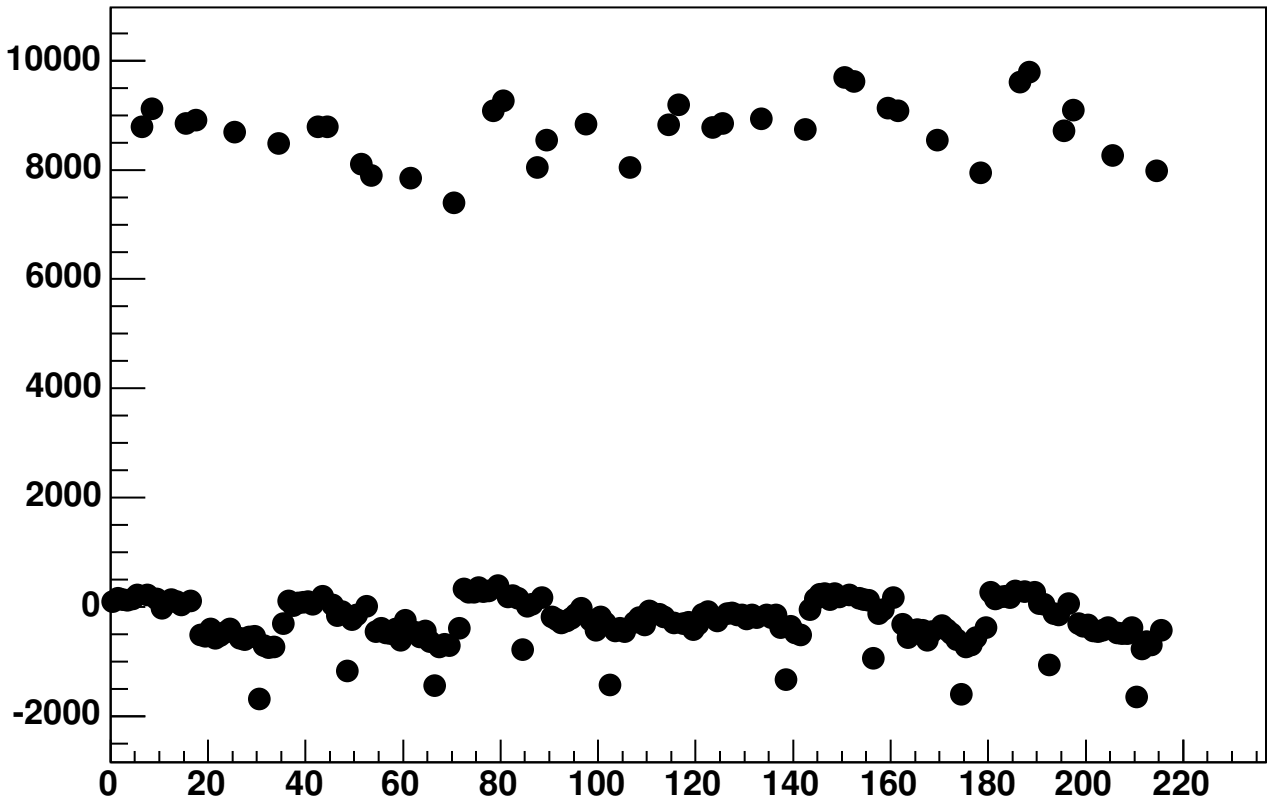
Enable 2, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



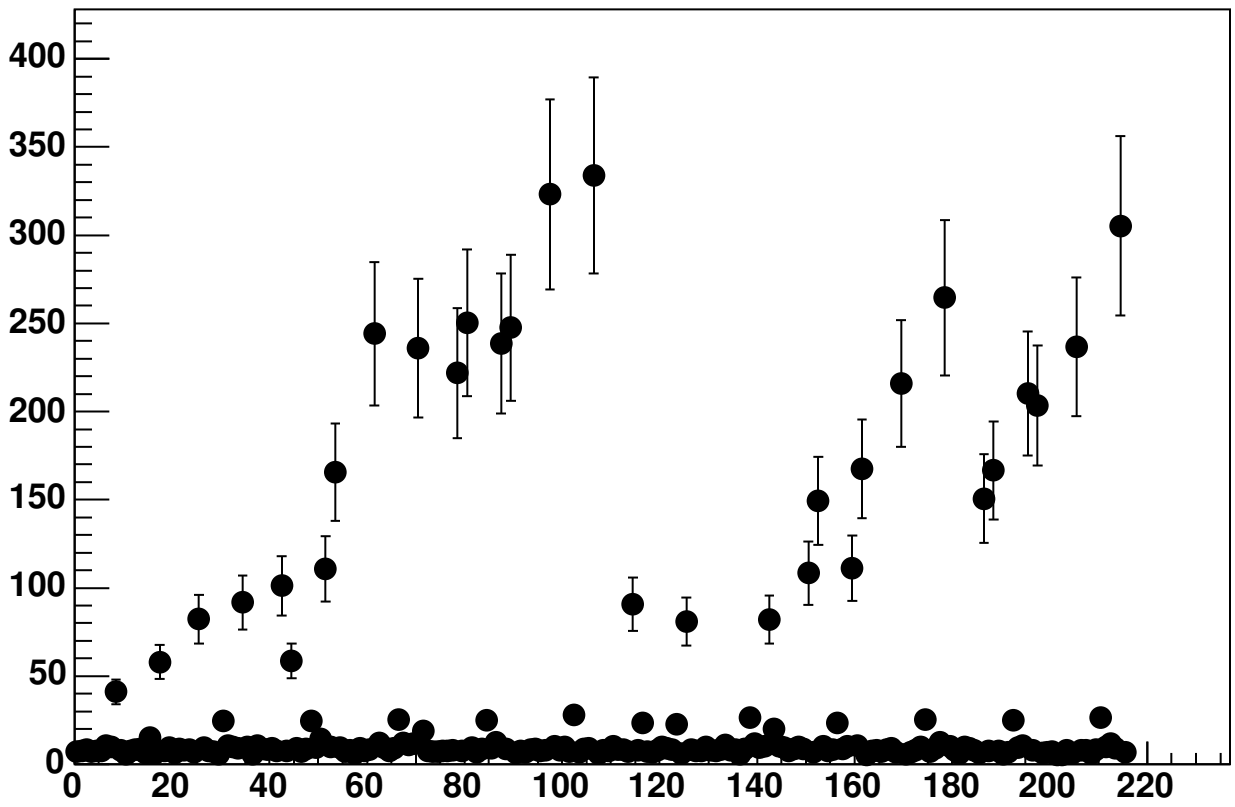
Enable 2, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



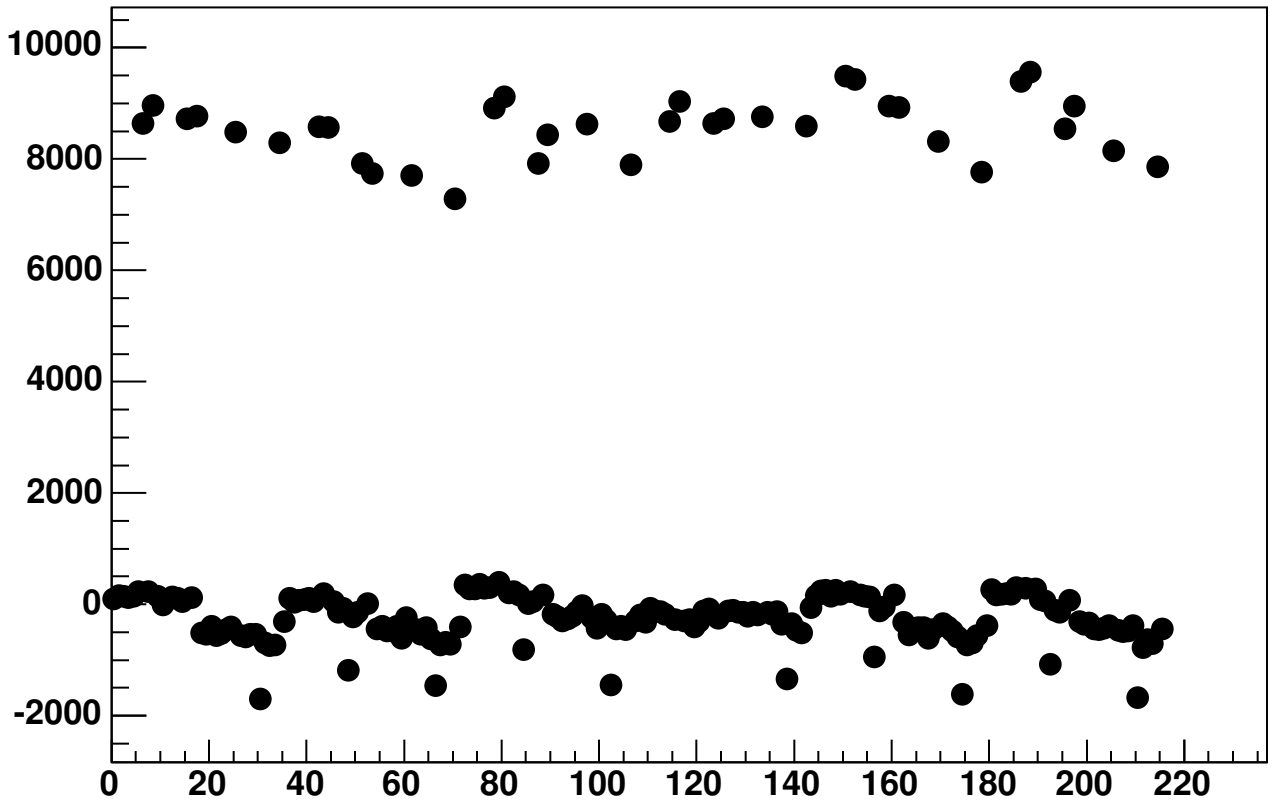
Enable 2, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



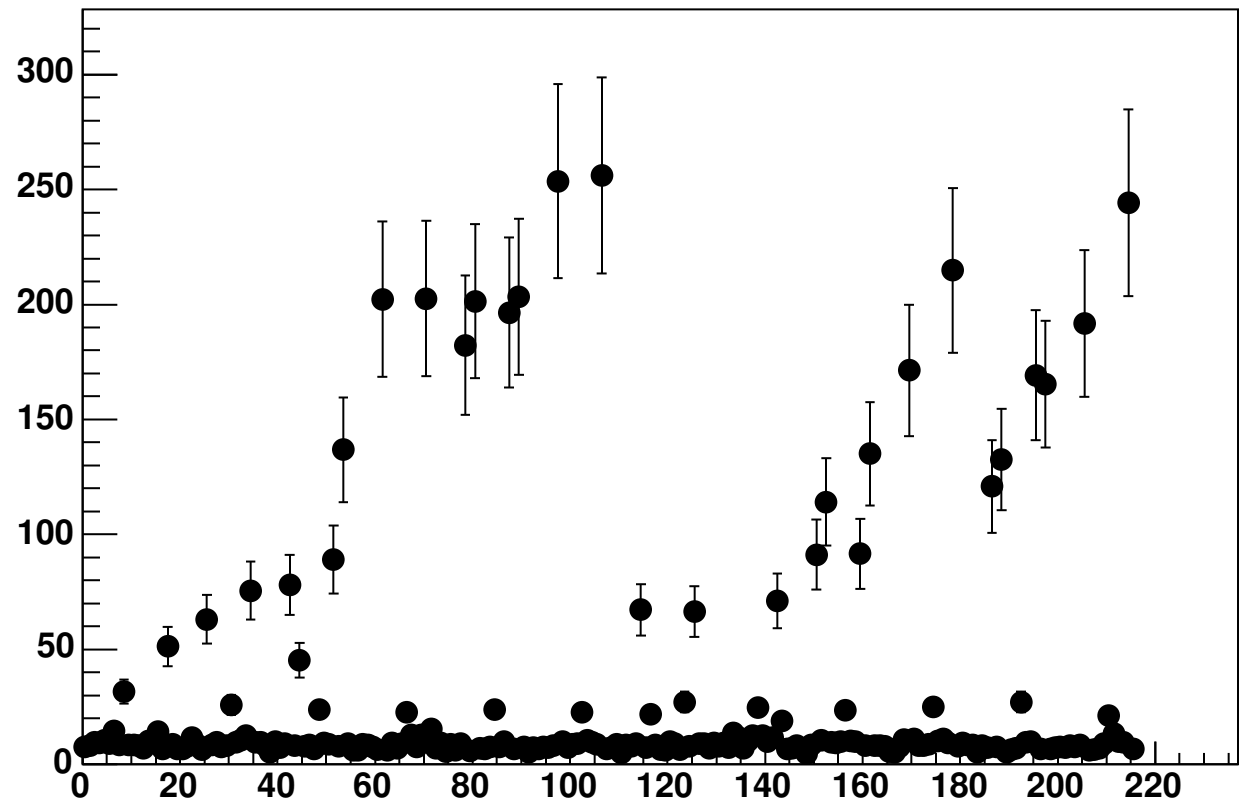
Enable 2, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



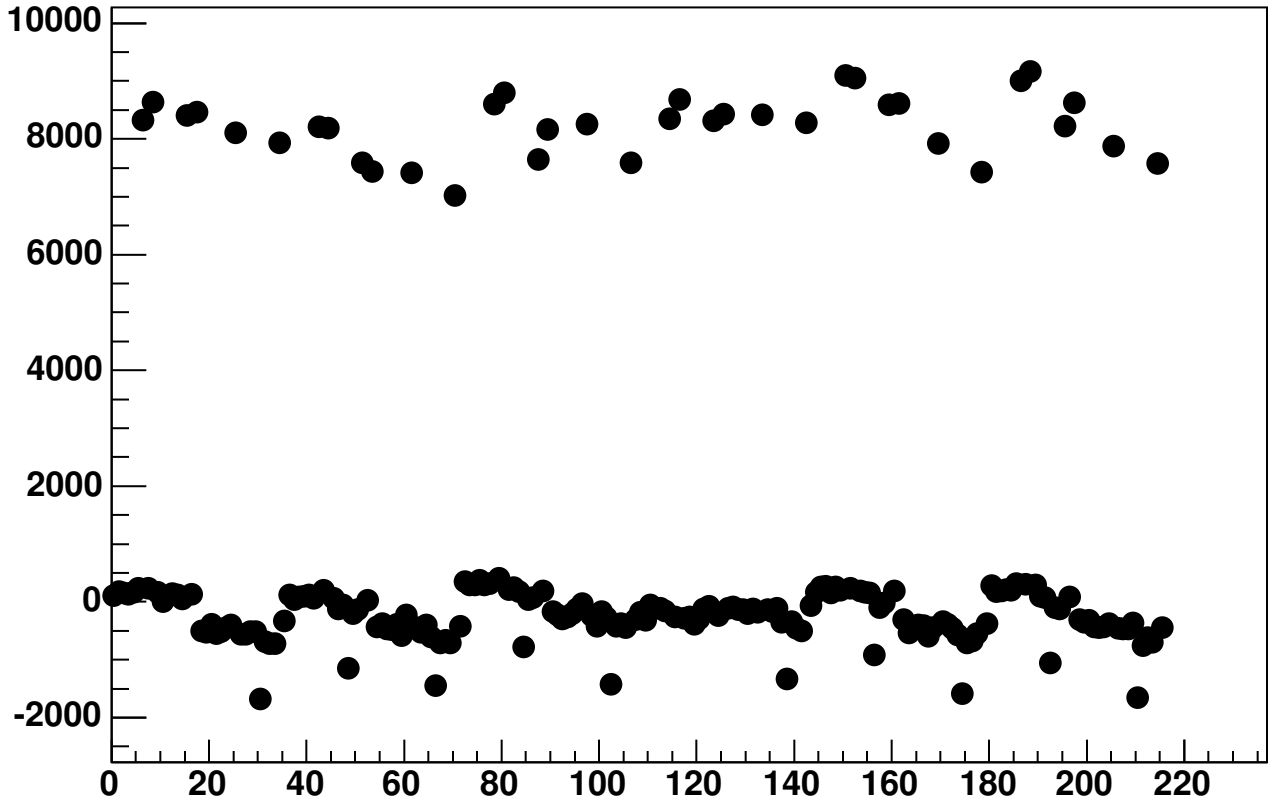
Enable 2, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



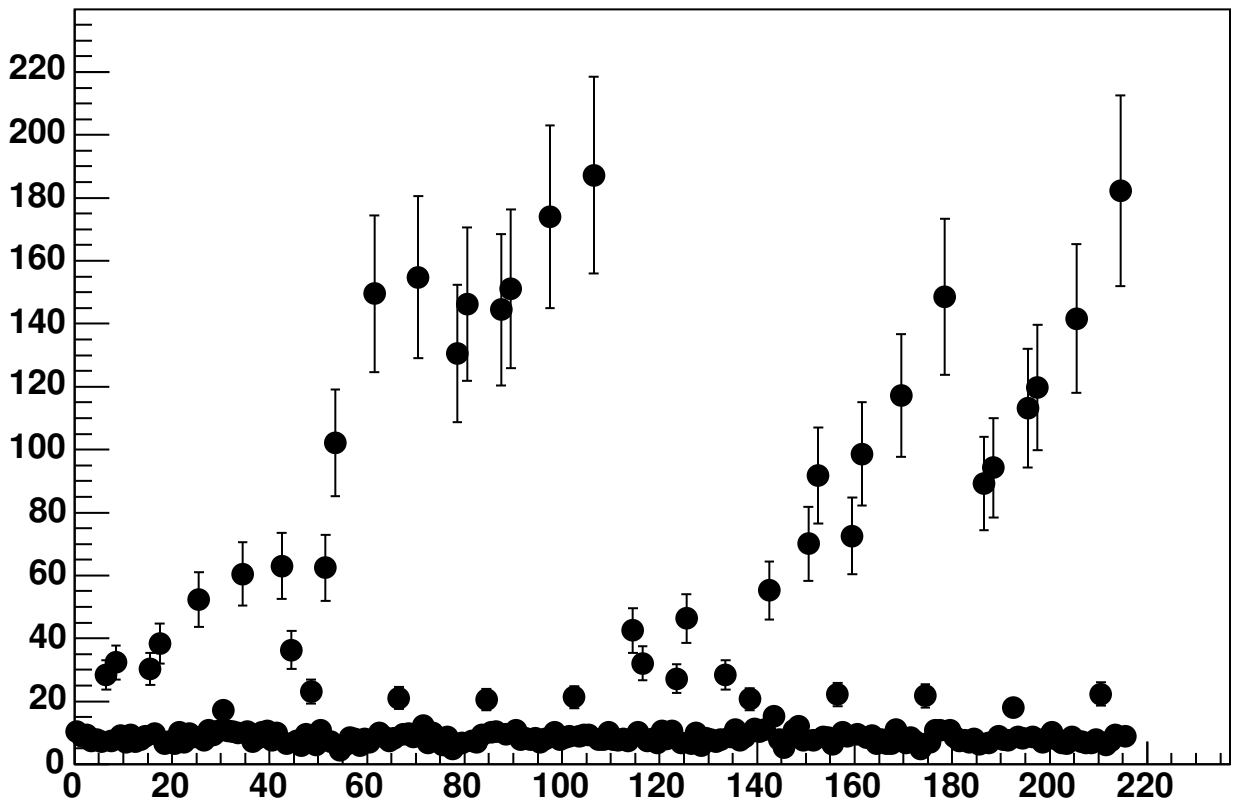
Enable 2, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



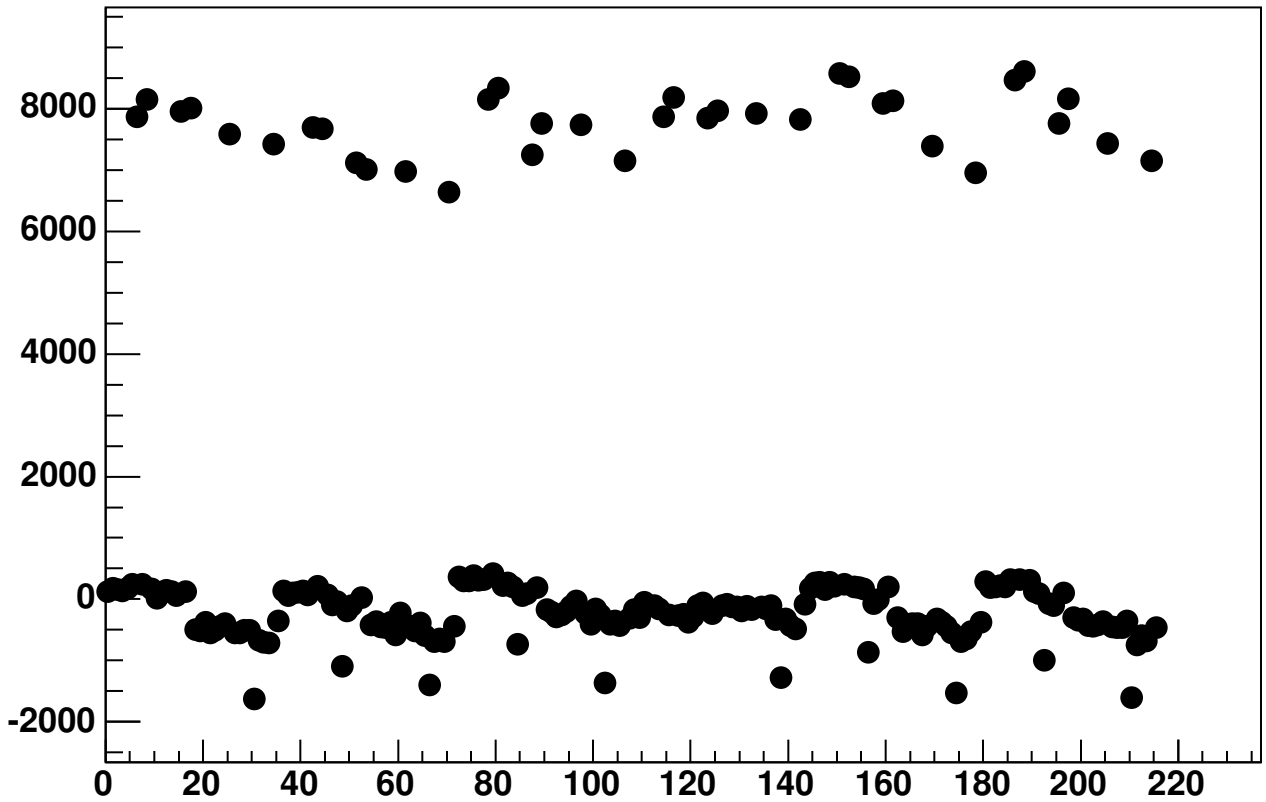
Enable 2, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



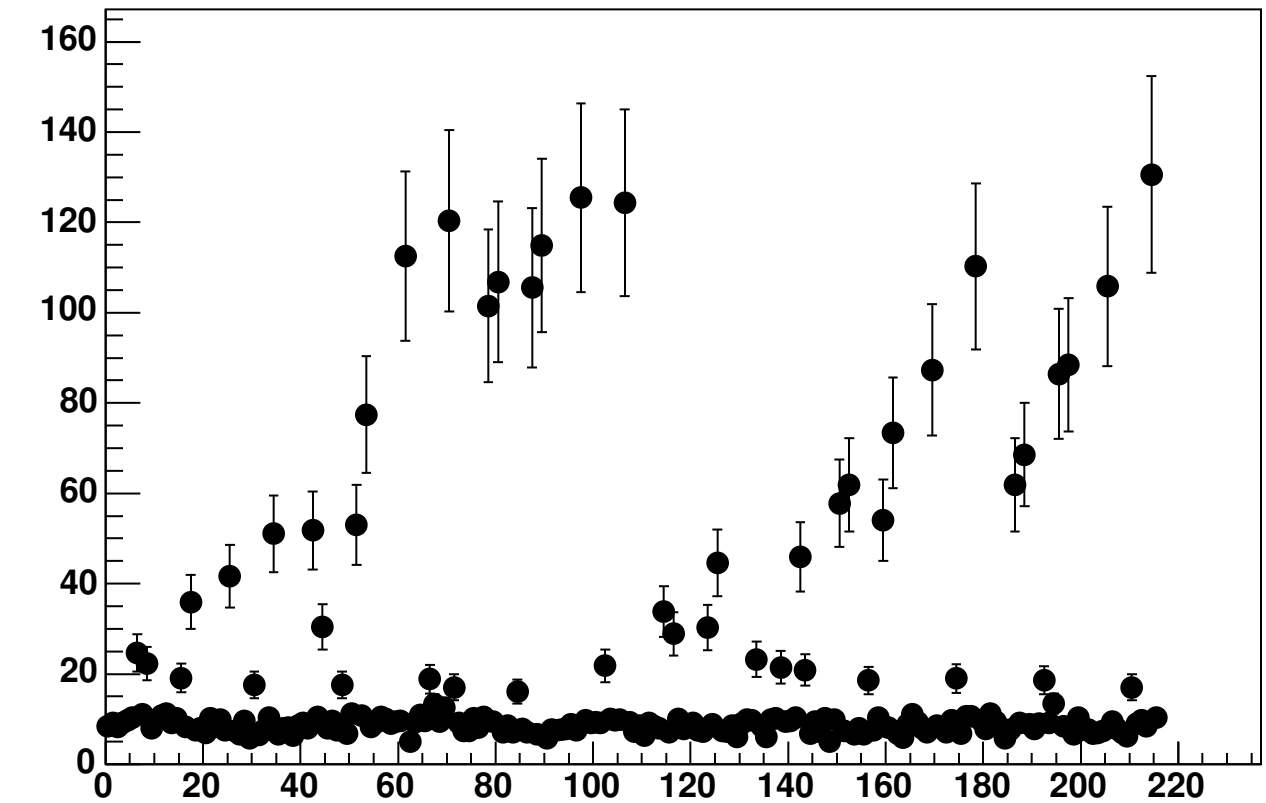
Enable 2, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



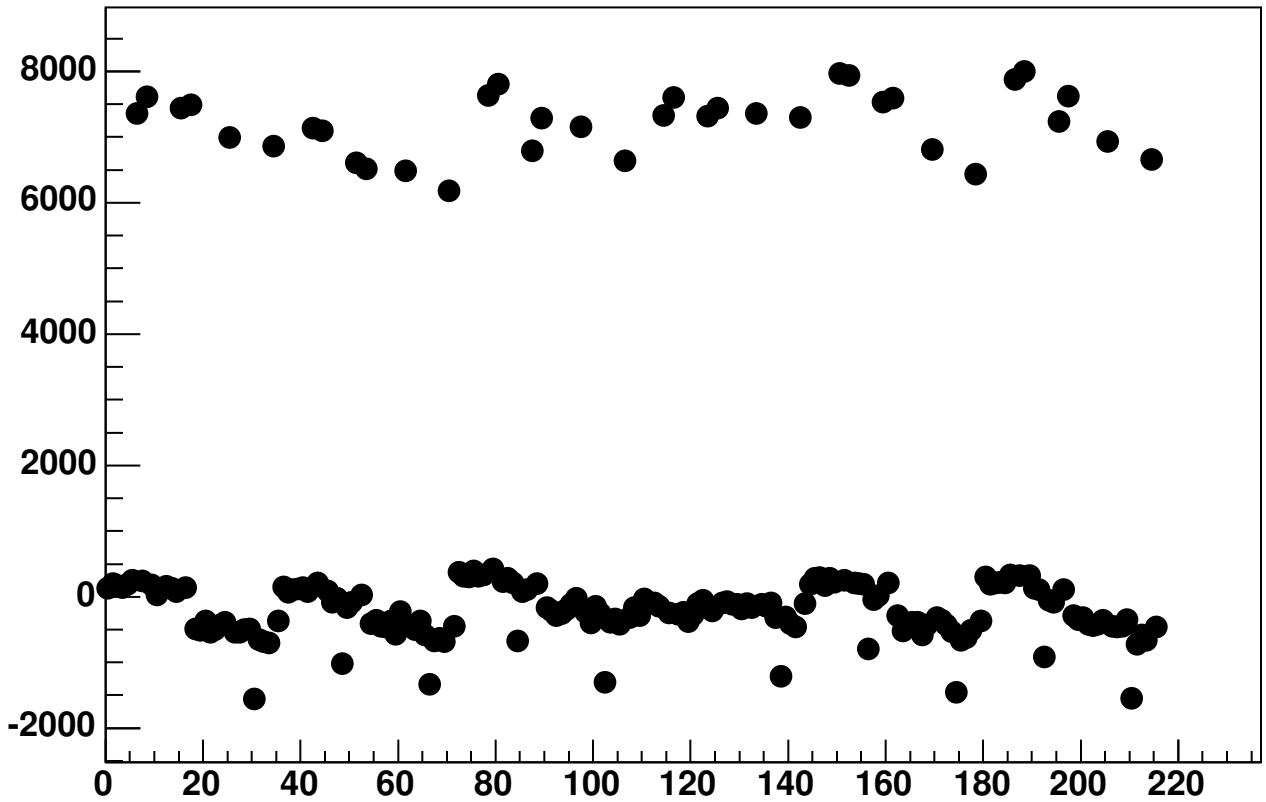
Enable 2, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



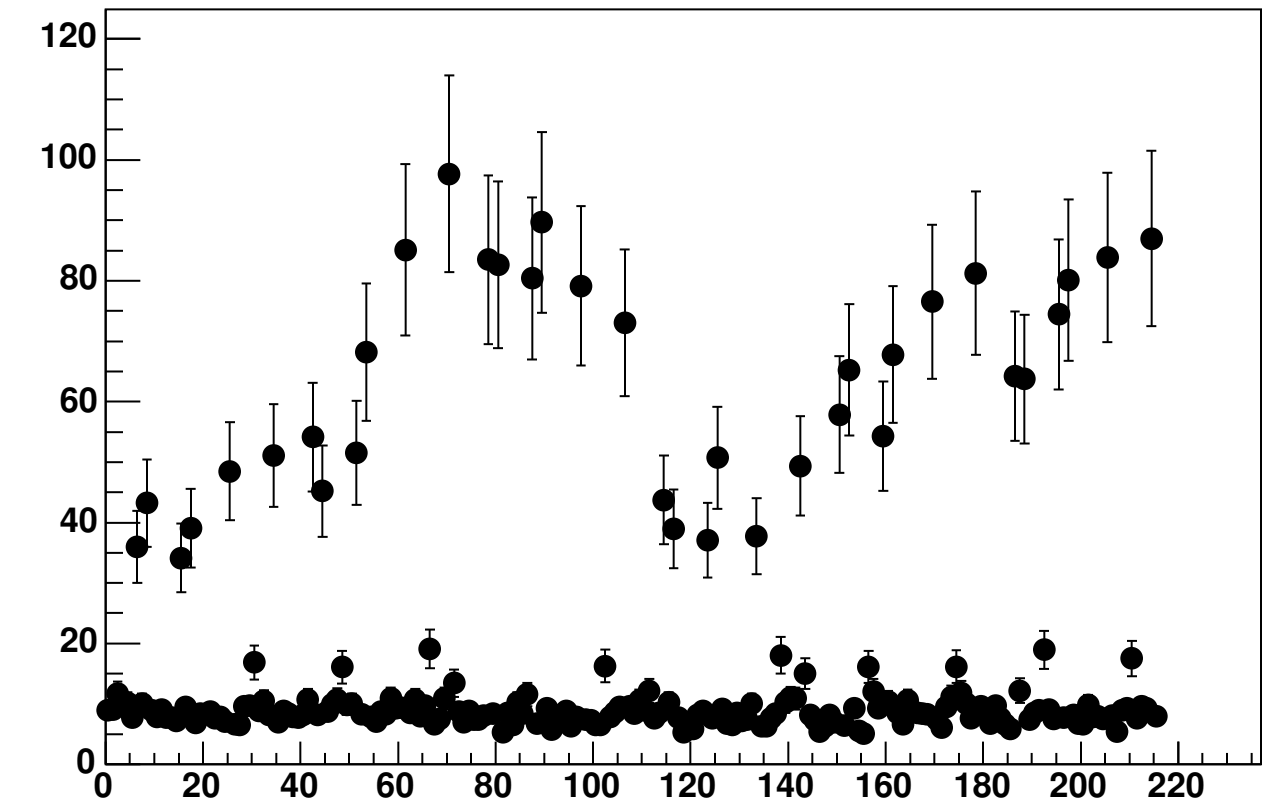
Enable 2, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



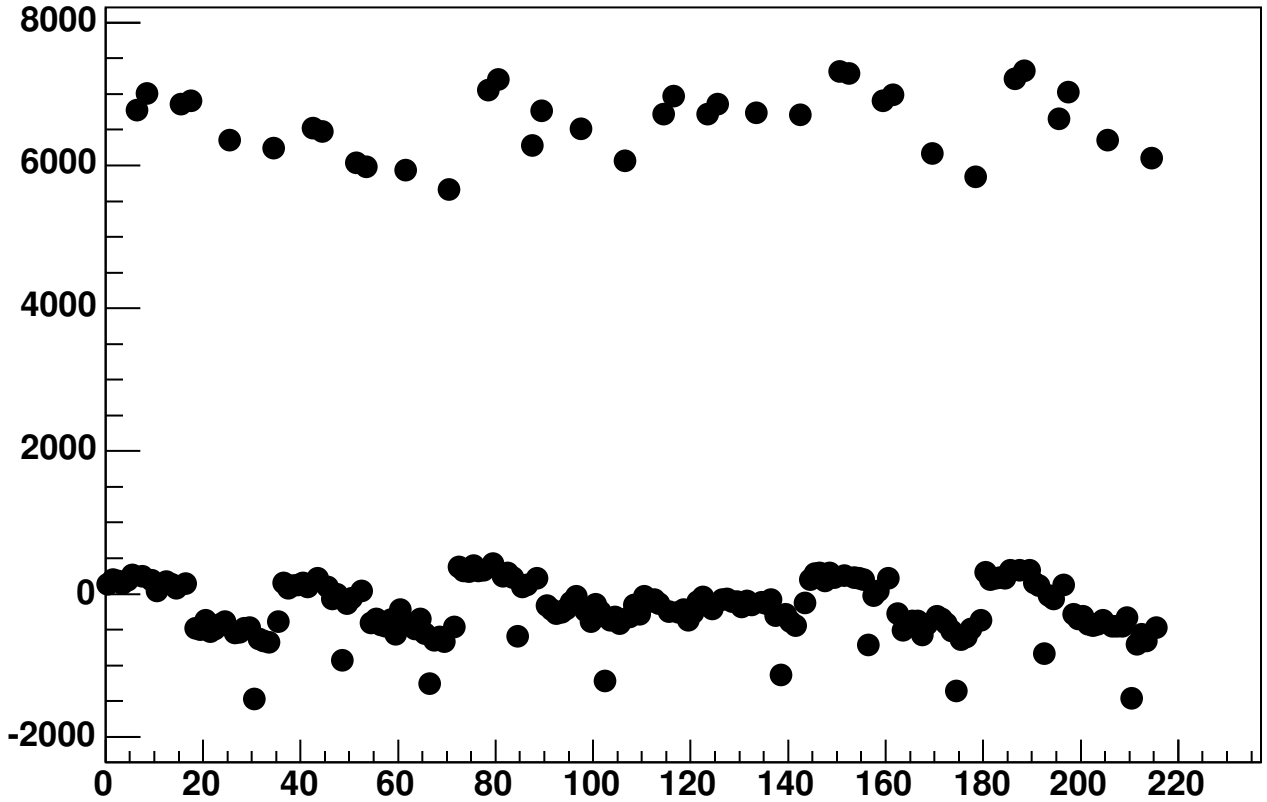
Enable 2, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



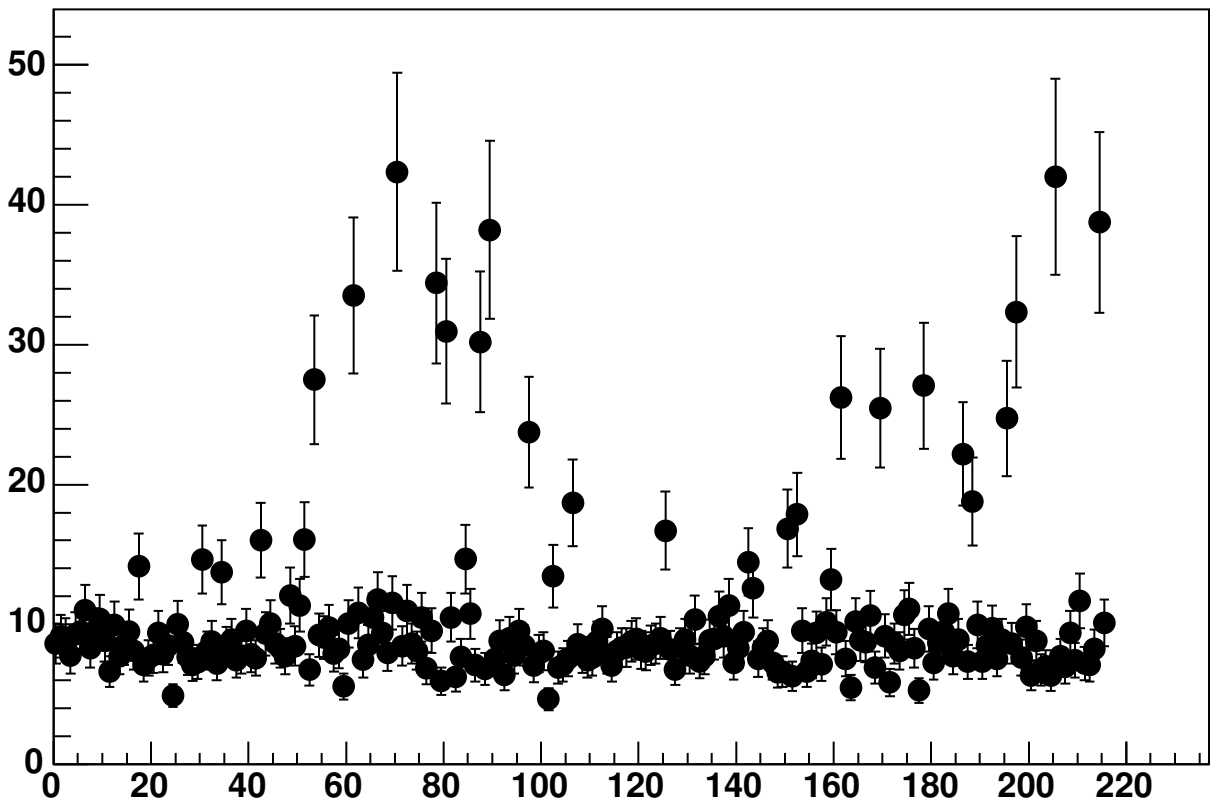
Enable 2, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



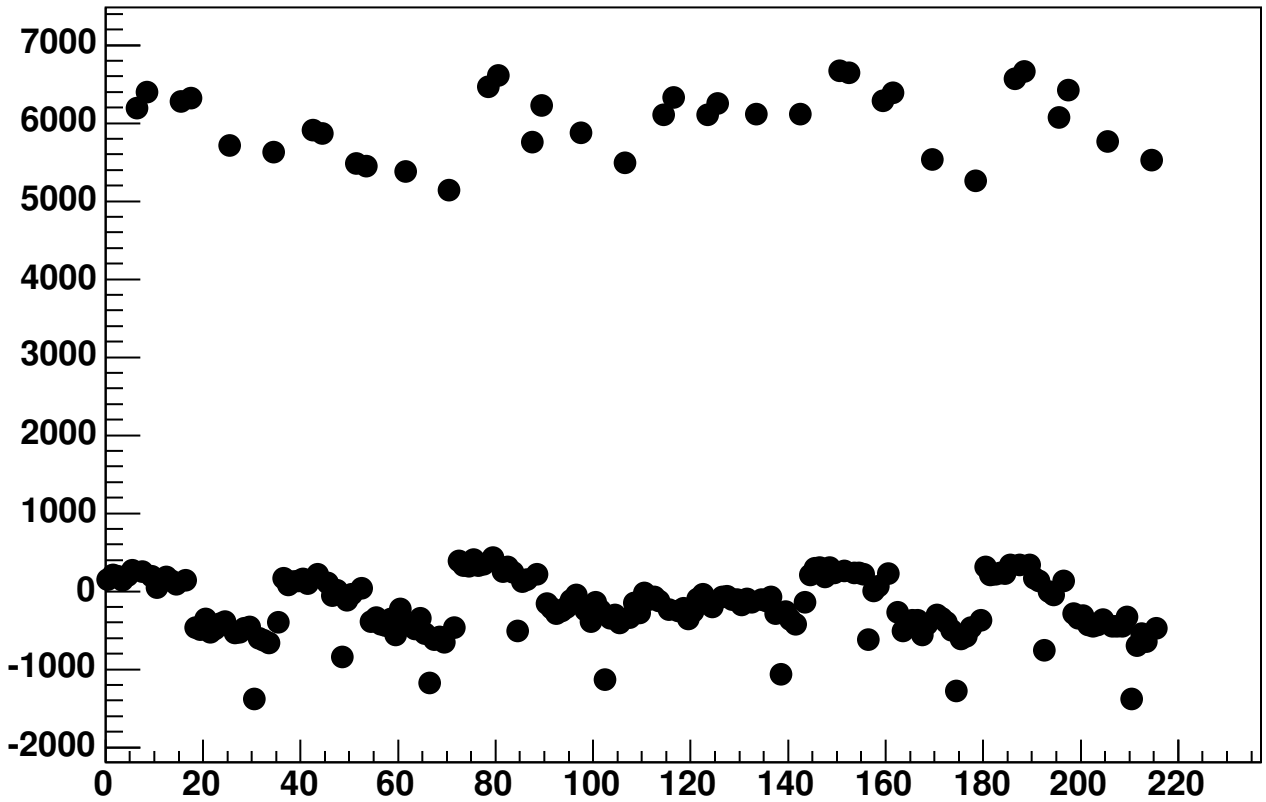
Enable 2, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



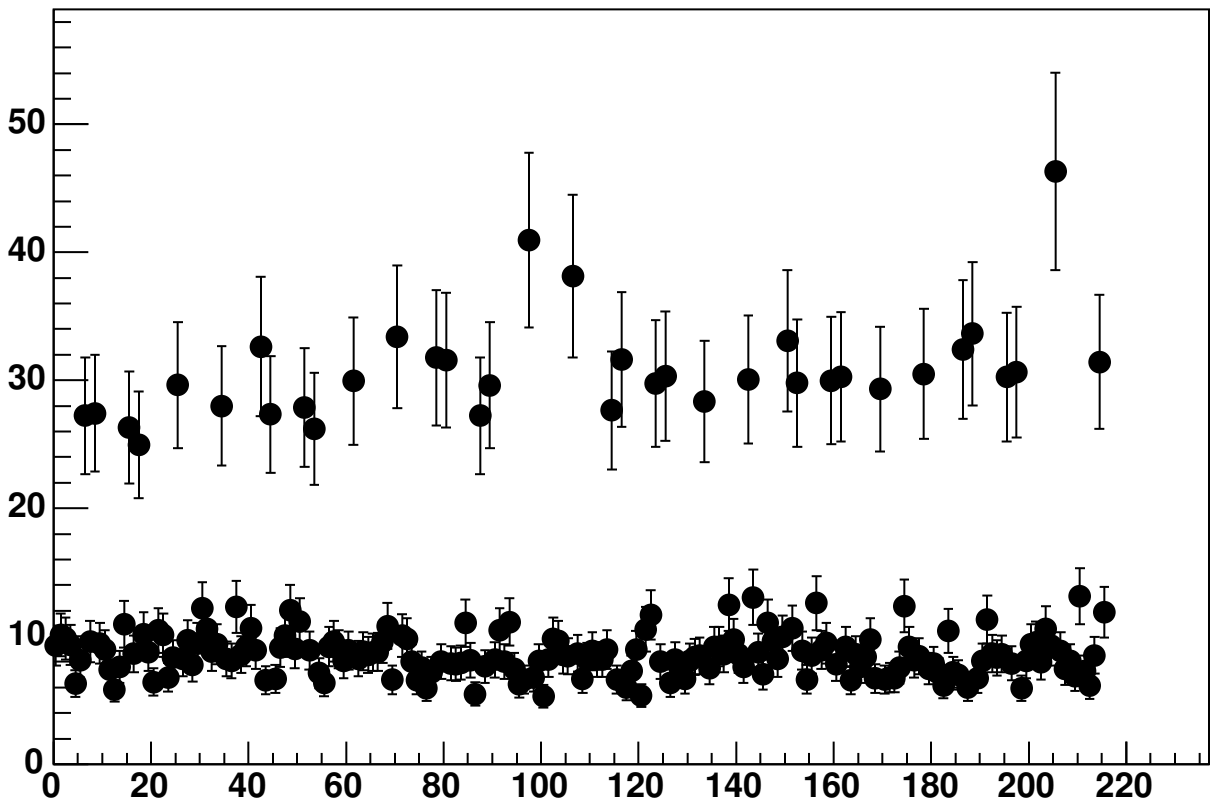
Enable 2, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



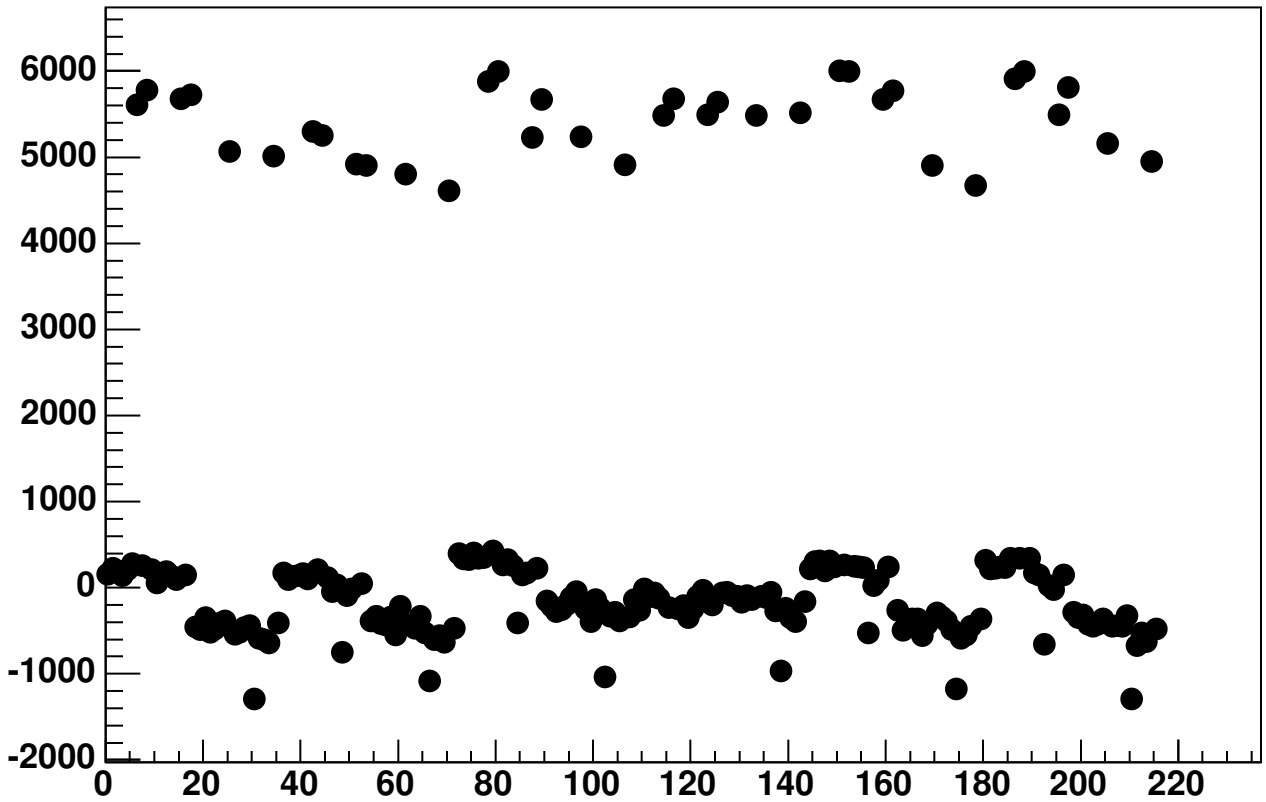
Enable 2, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



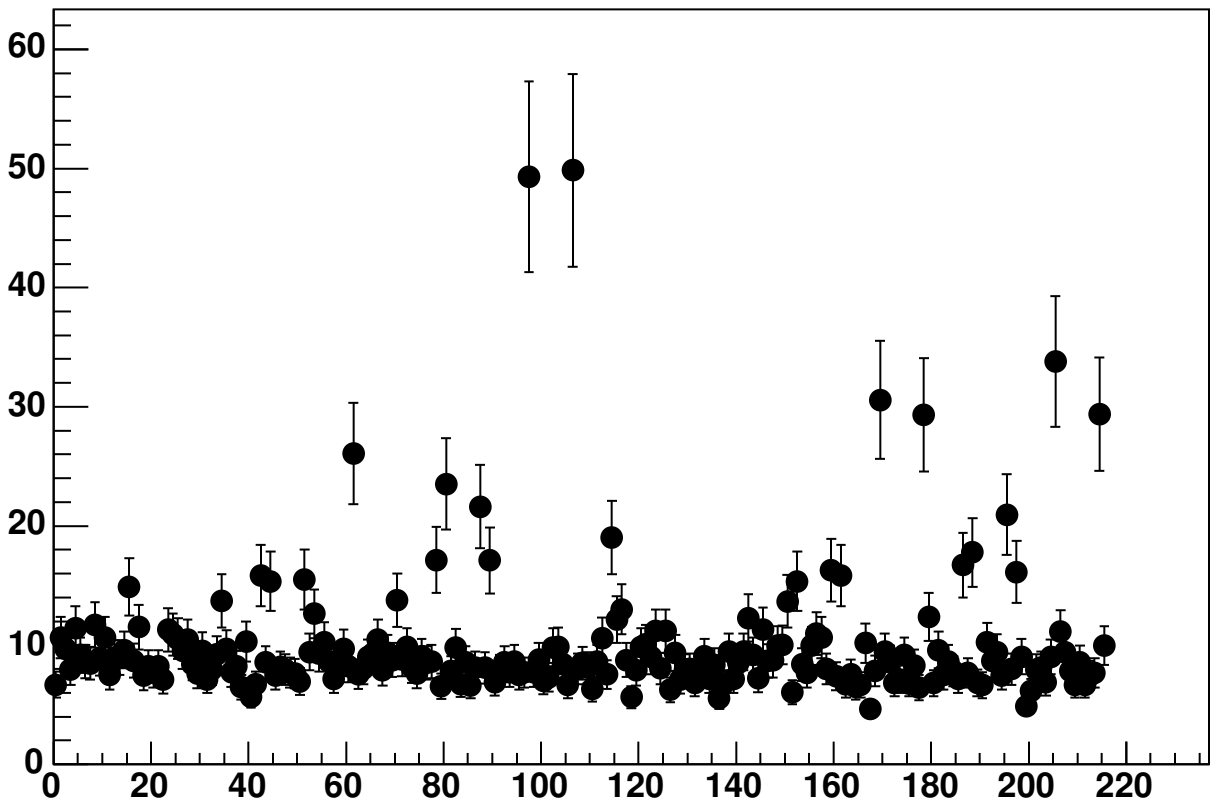
Enable 2, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



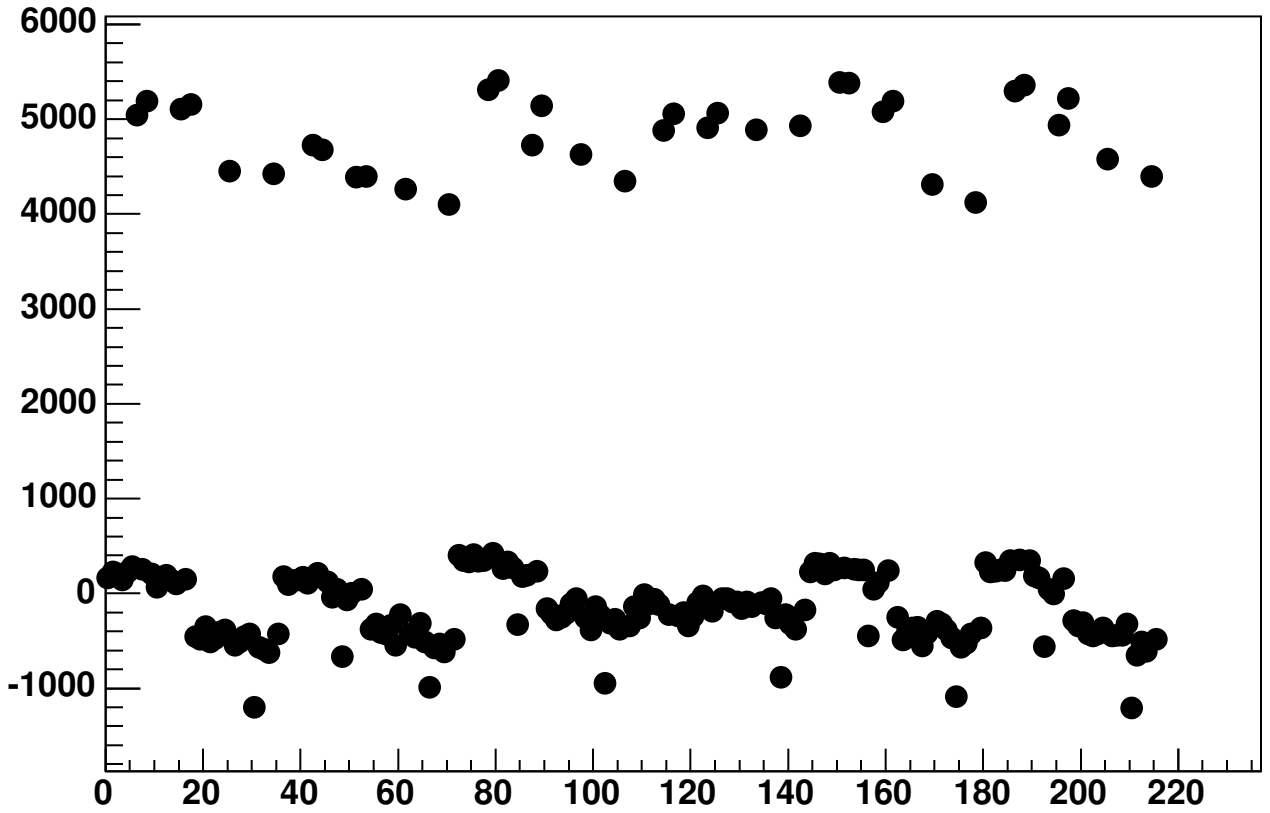
Enable 2, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



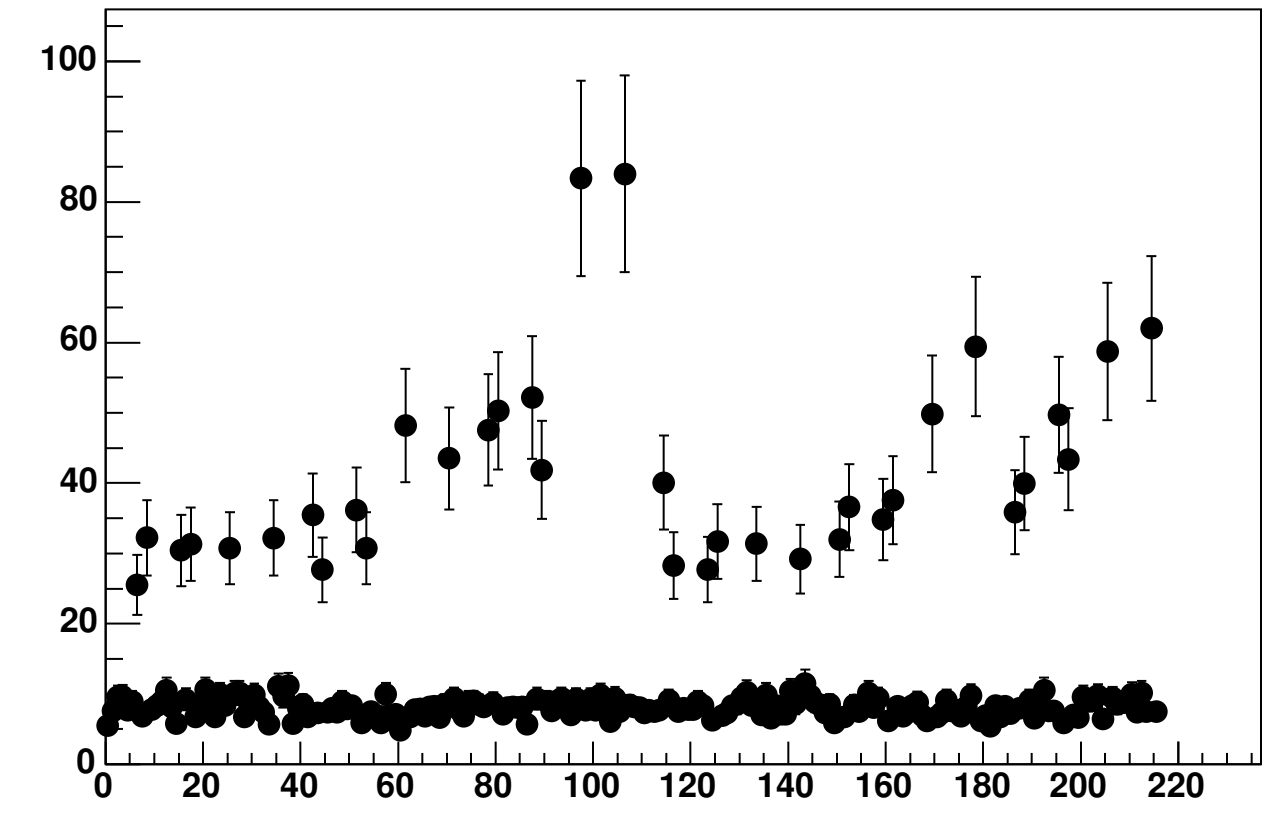
Enable 2, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



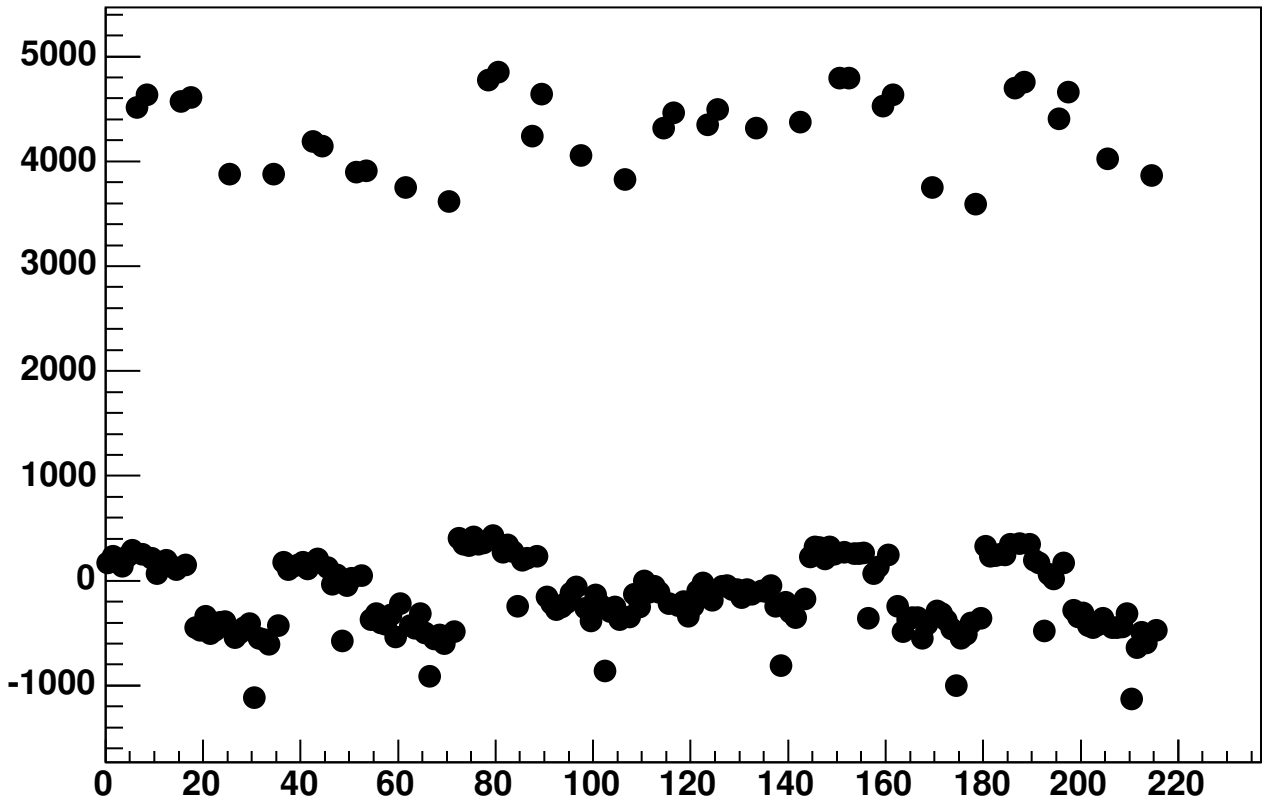
Enable 2, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



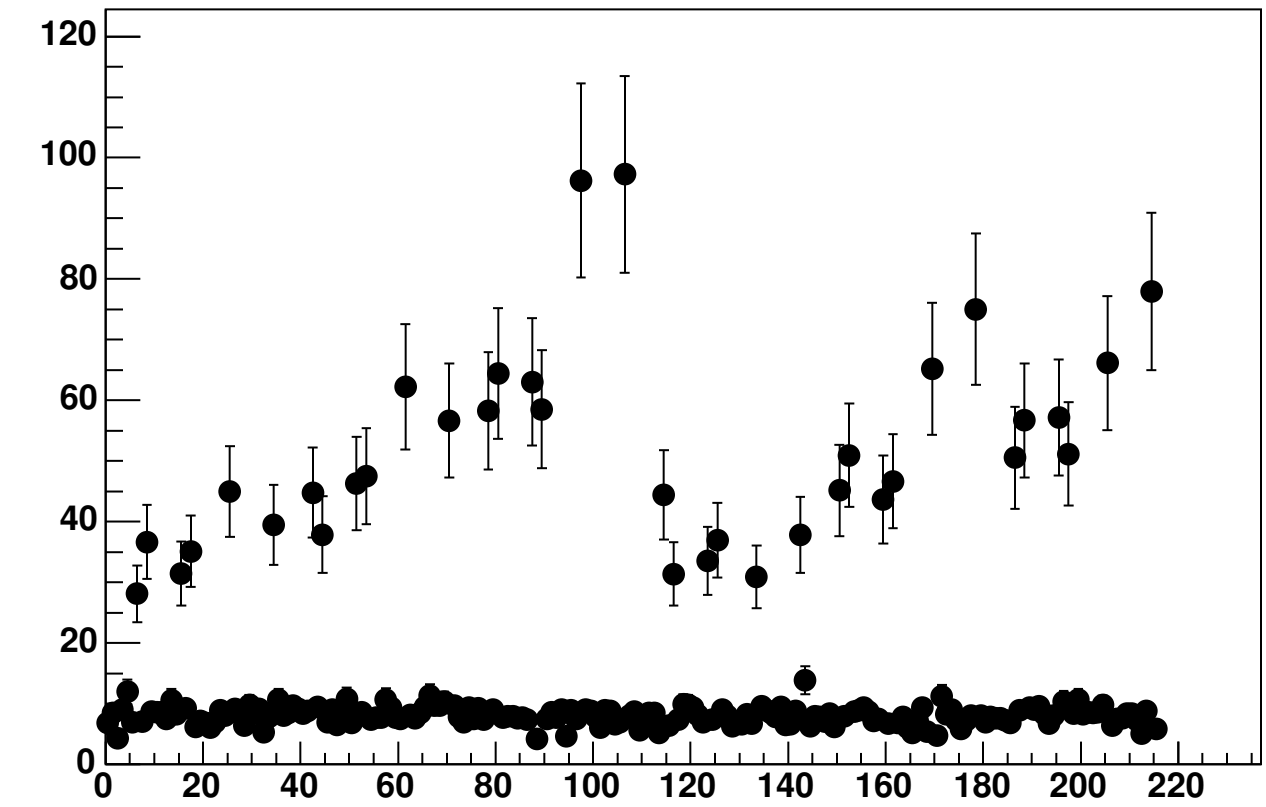
Enable 2, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



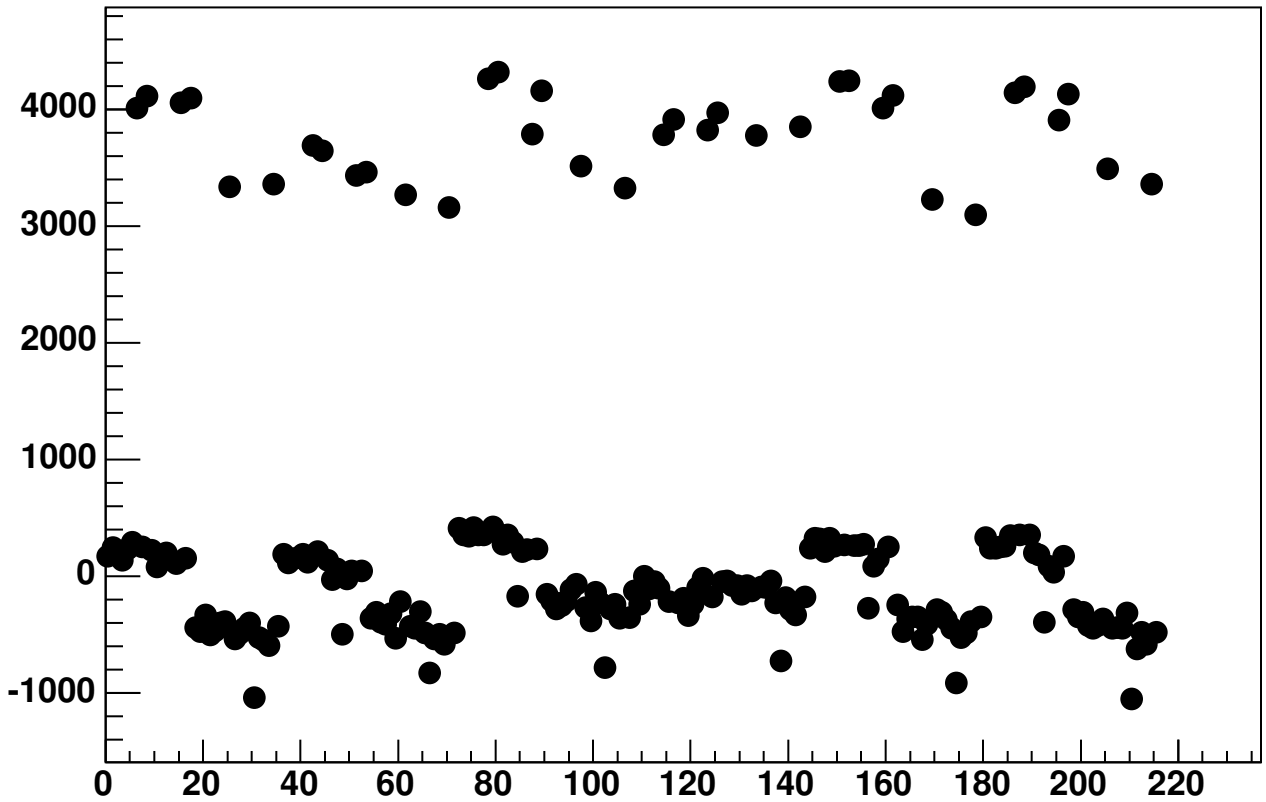
Enable 2, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



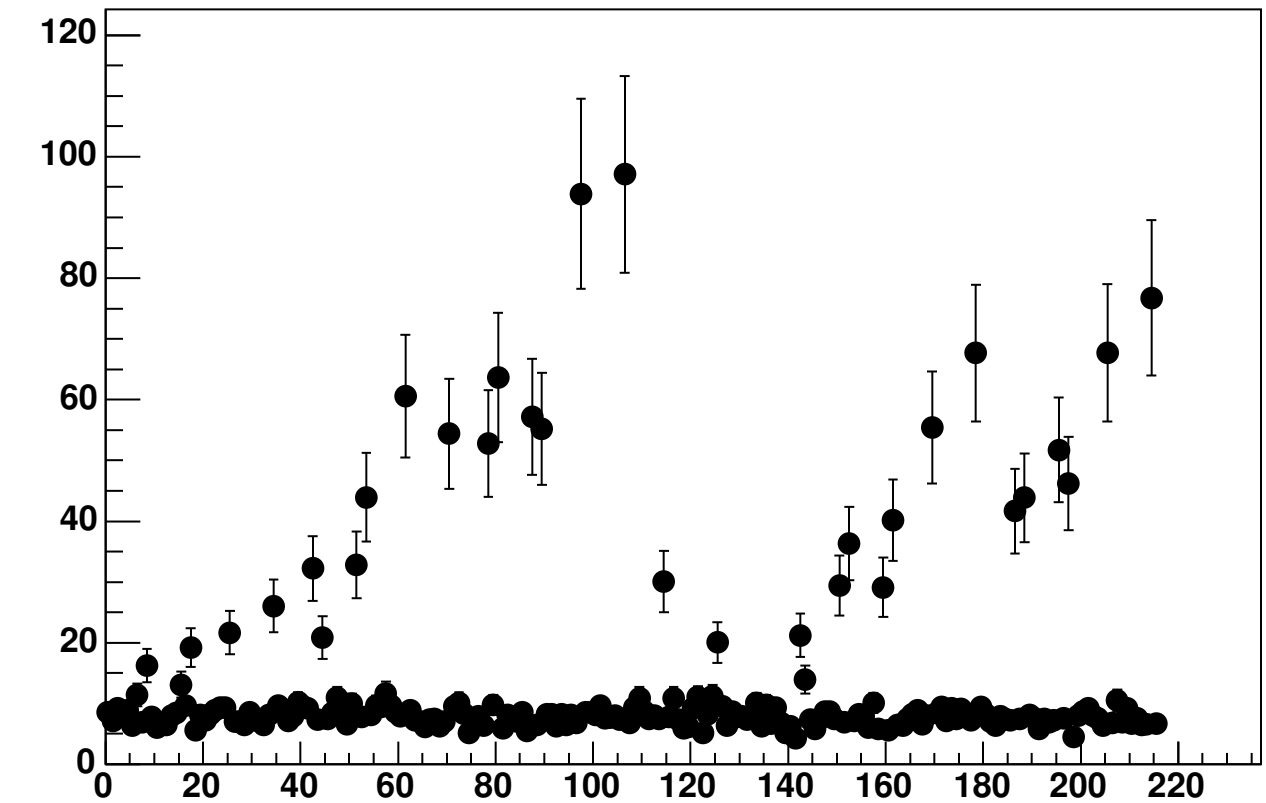
Enable 2, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



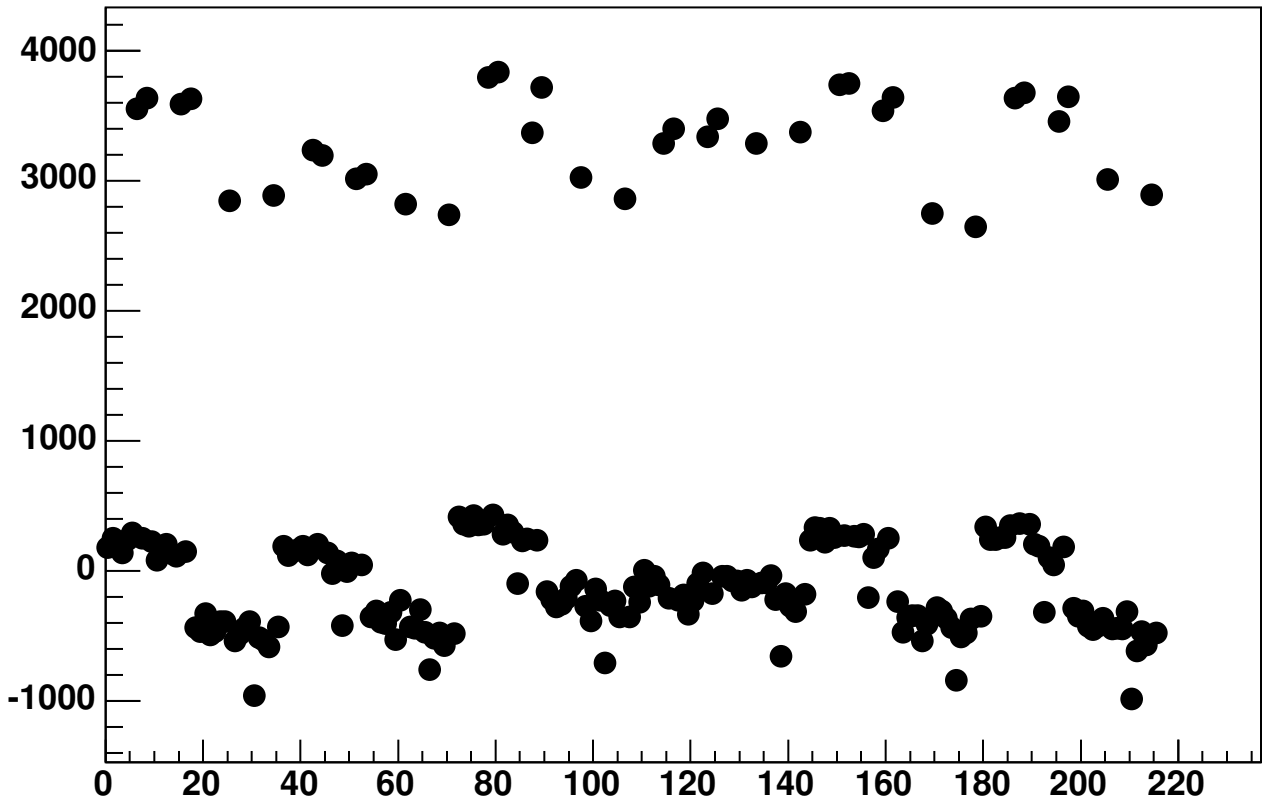
Enable 2, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



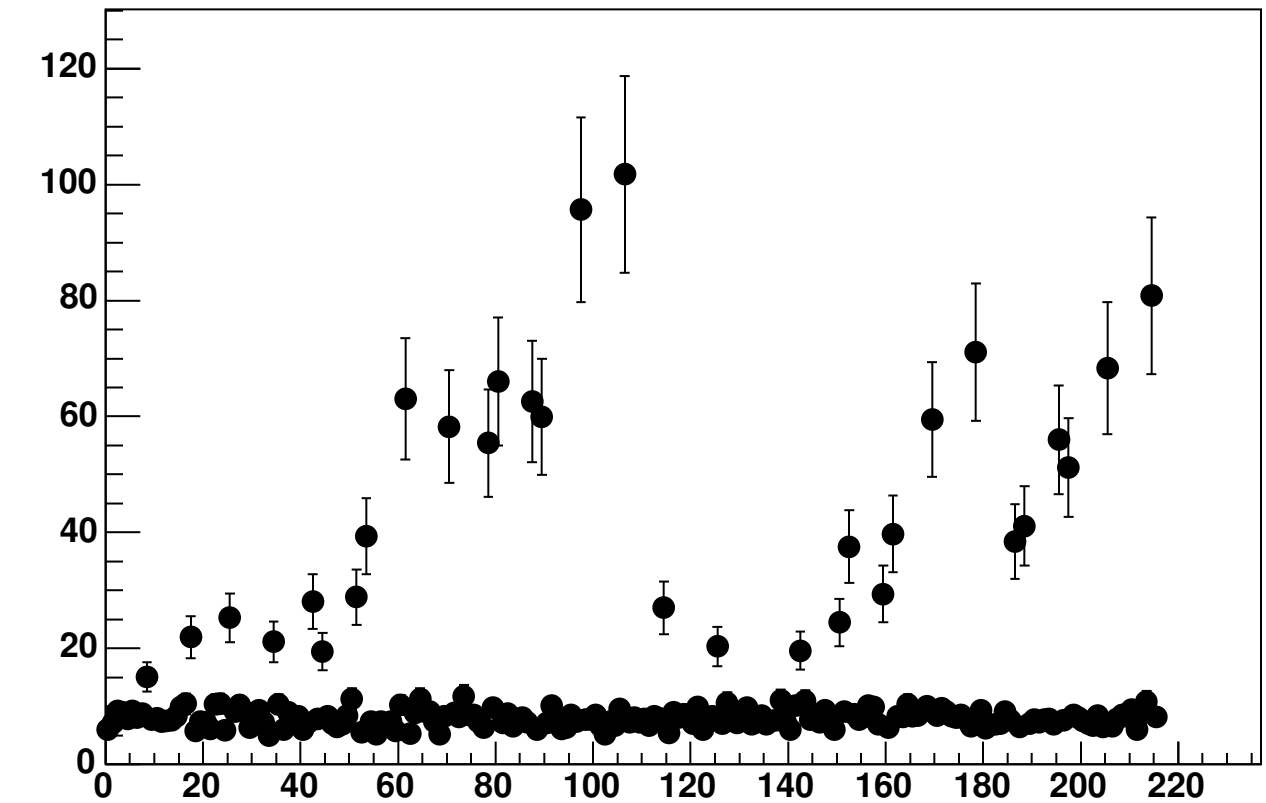
Enable 2, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



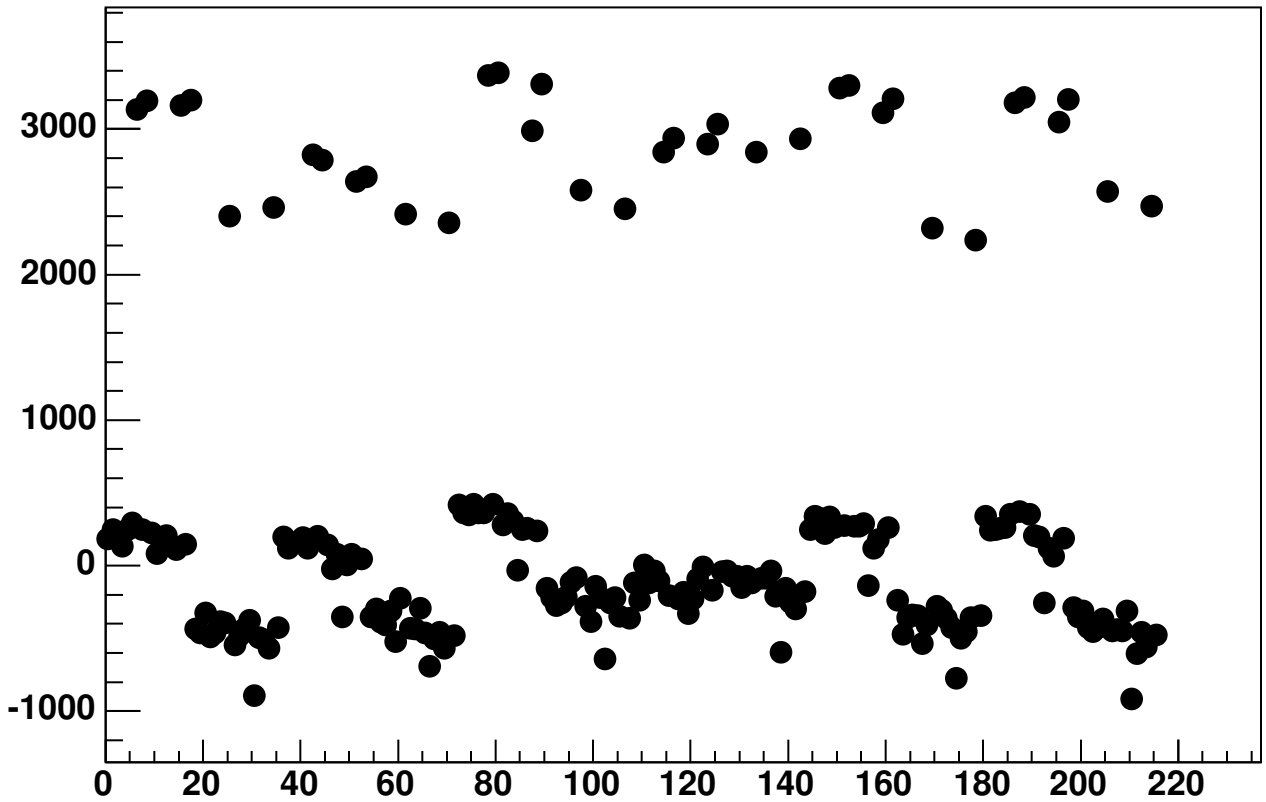
Enable 2, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



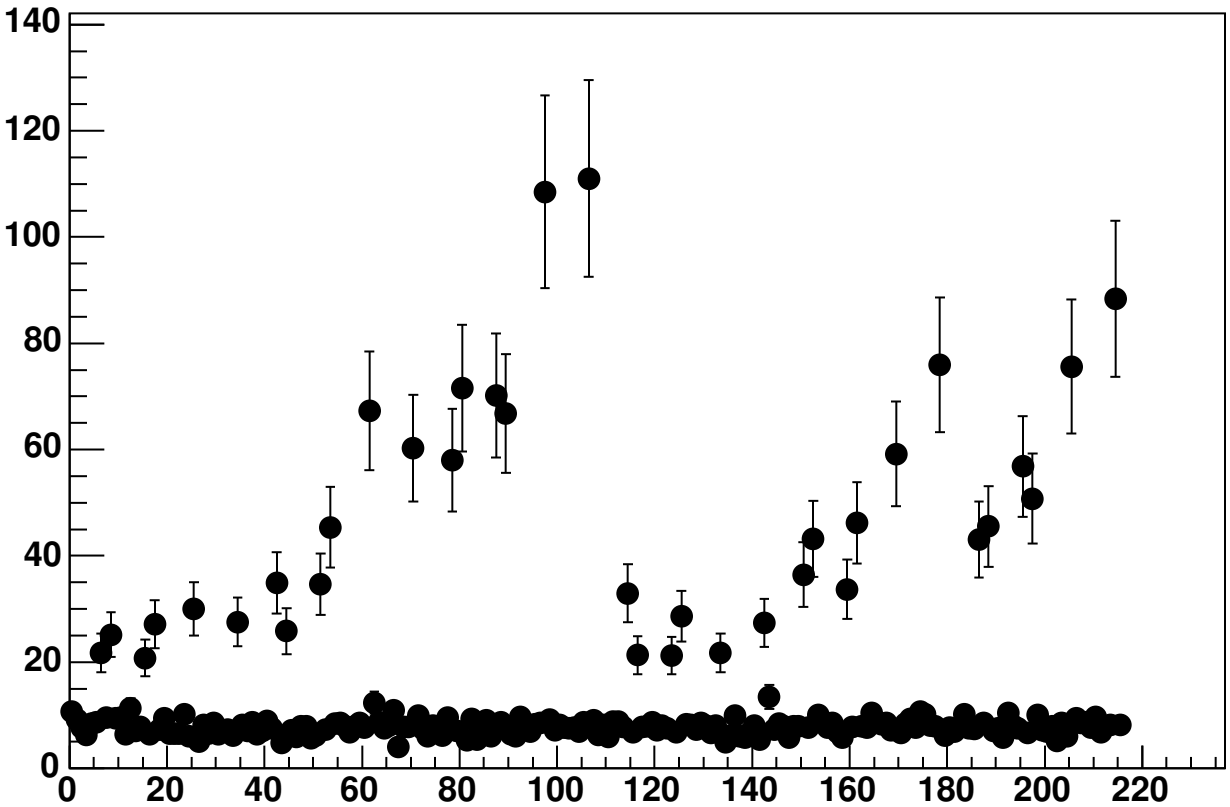
Enable 2, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



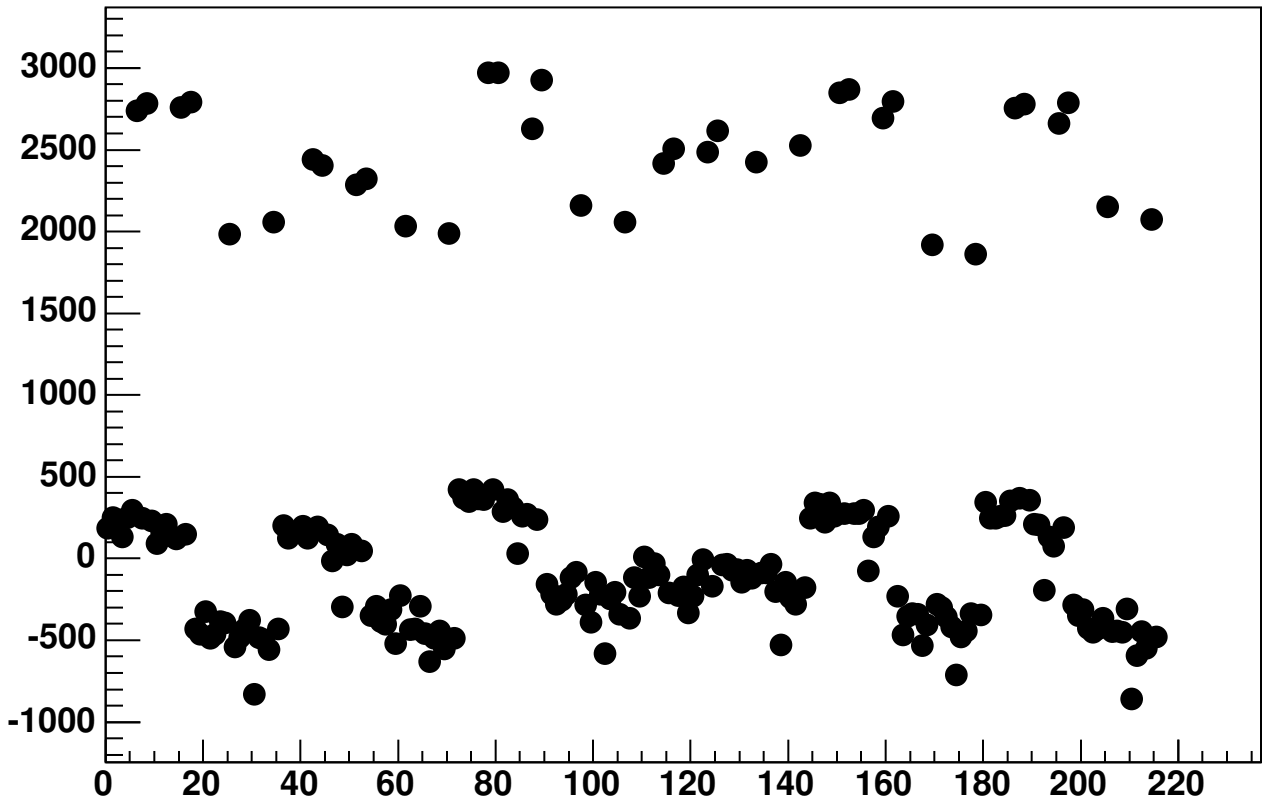
Enable 2, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



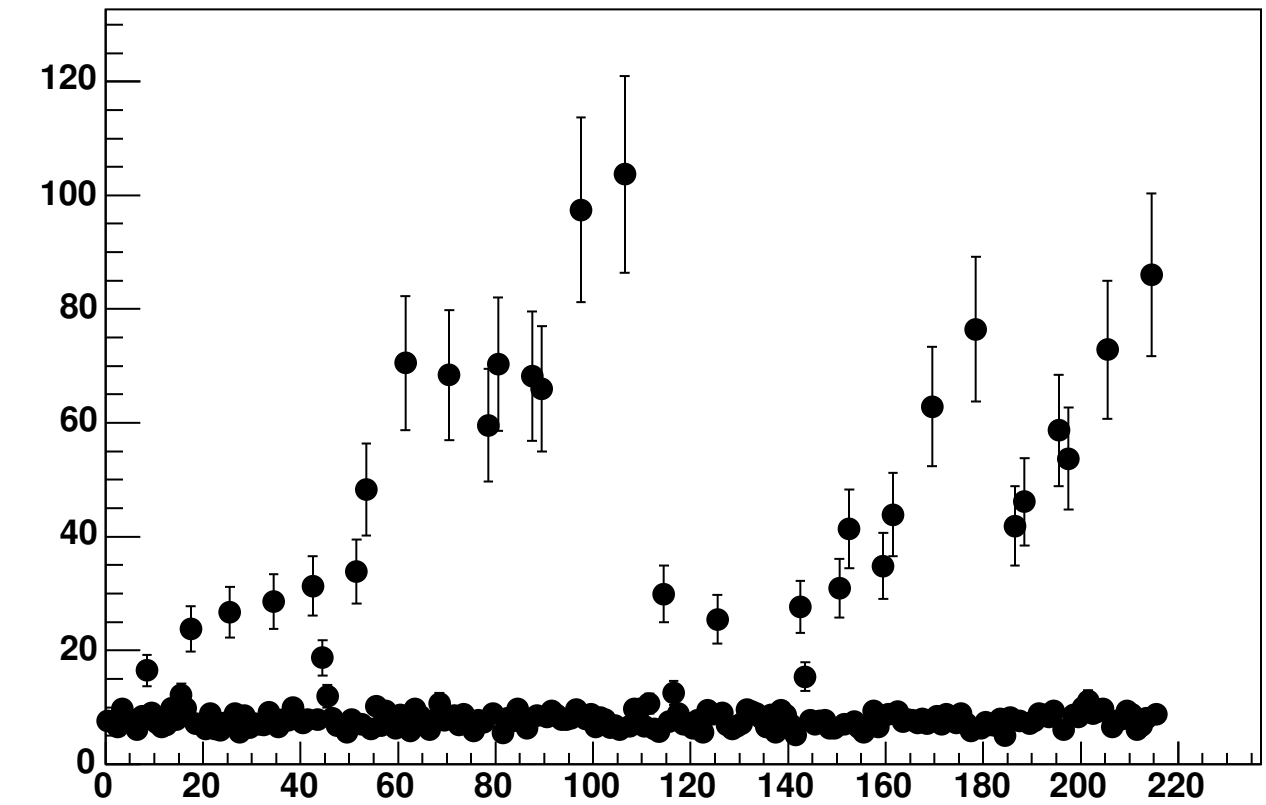
Enable 2, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



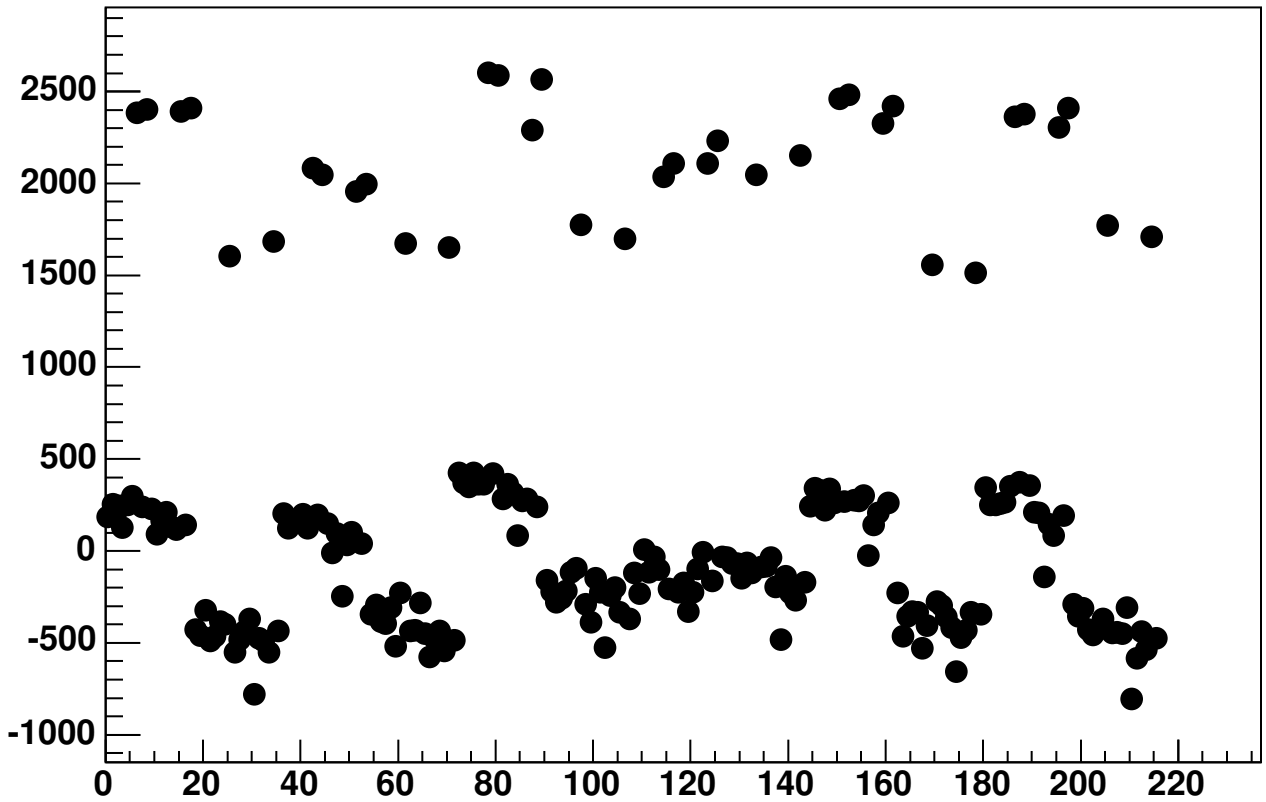
Enable 2, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



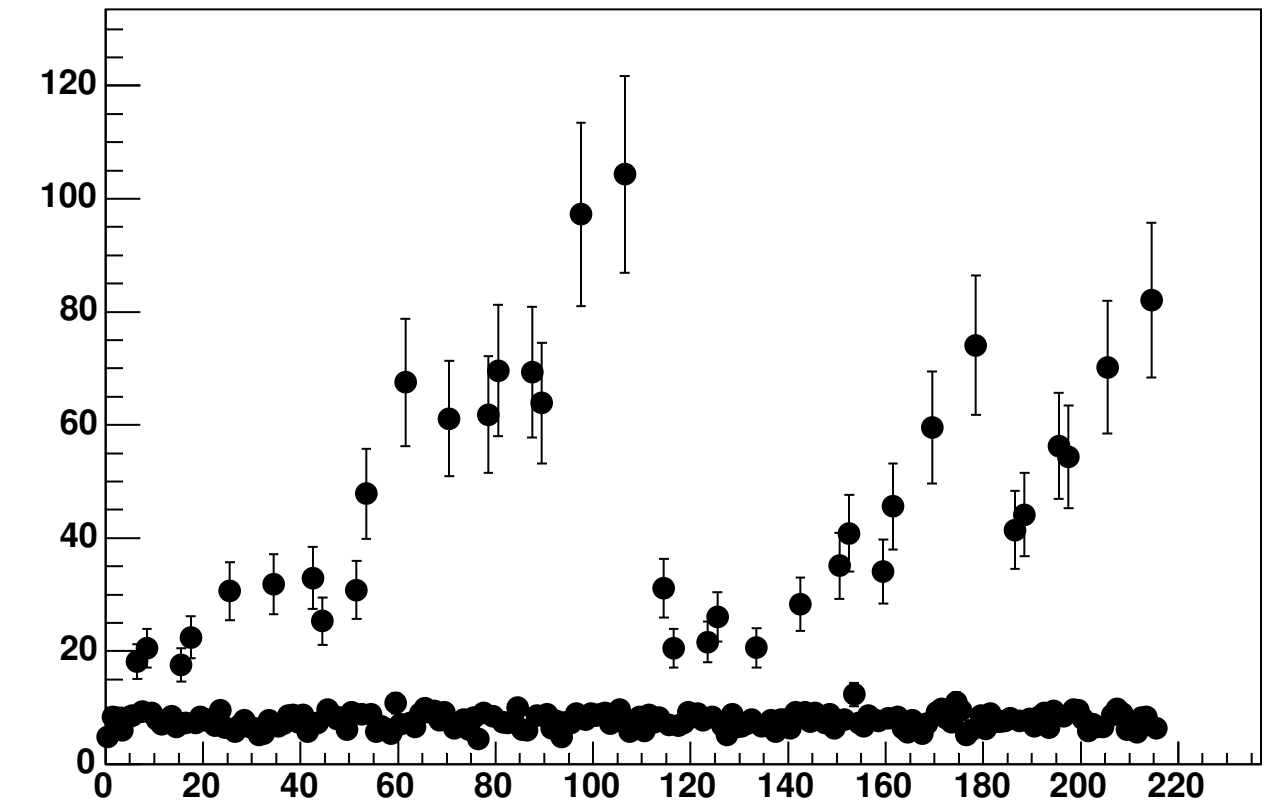
Enable 2, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



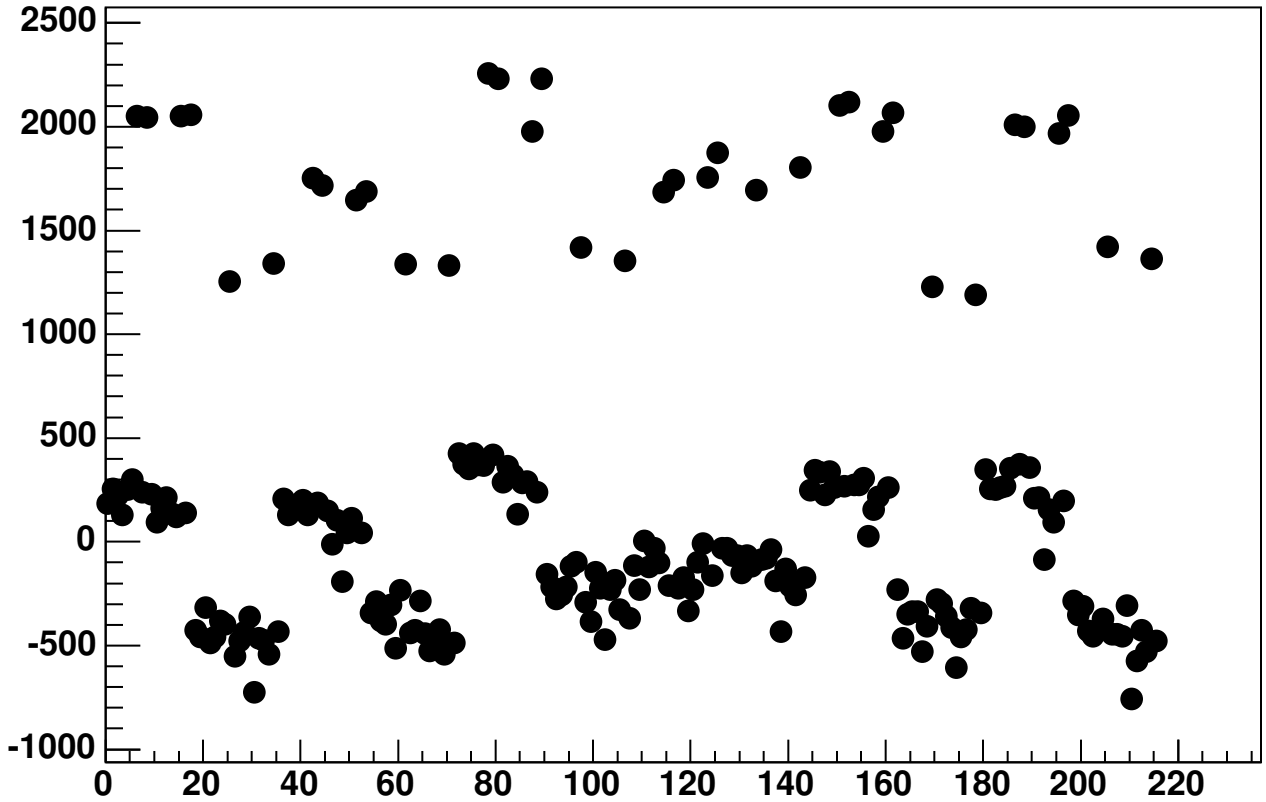
Enable 2, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



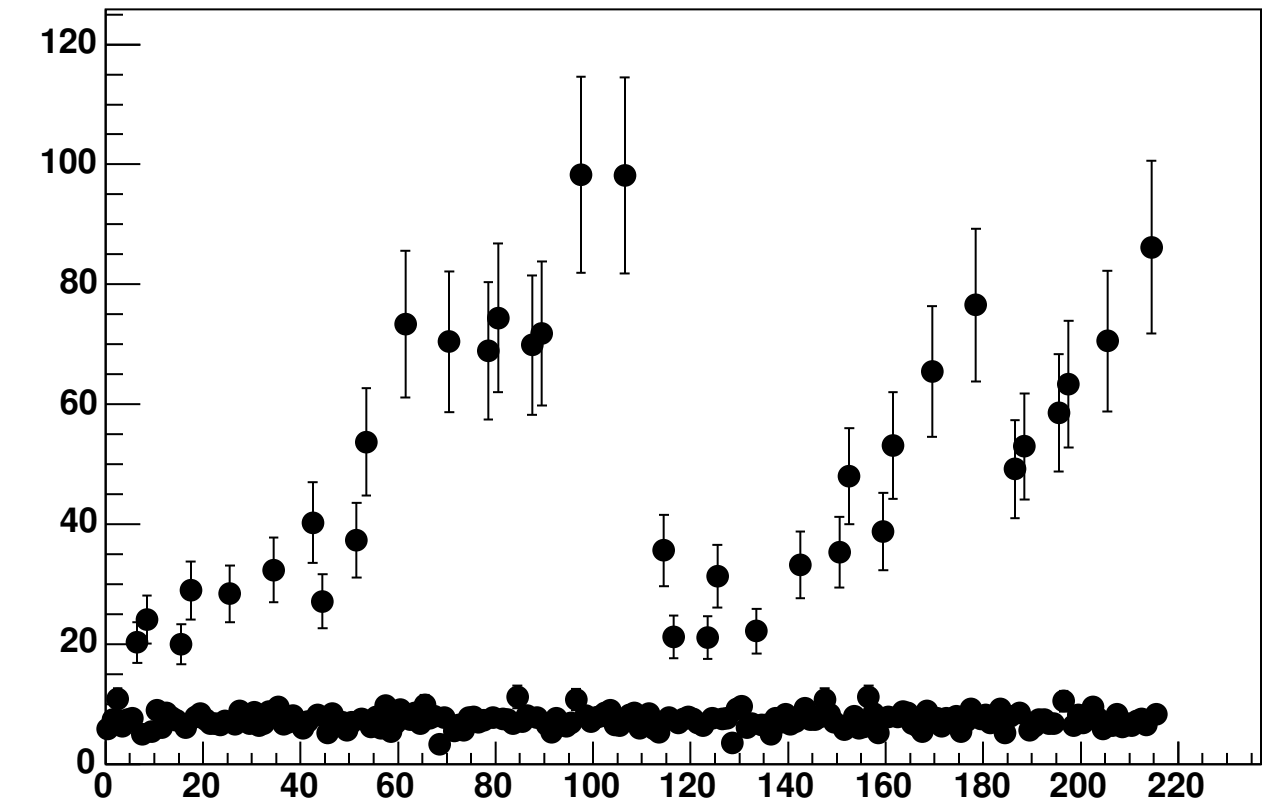
Enable 2, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



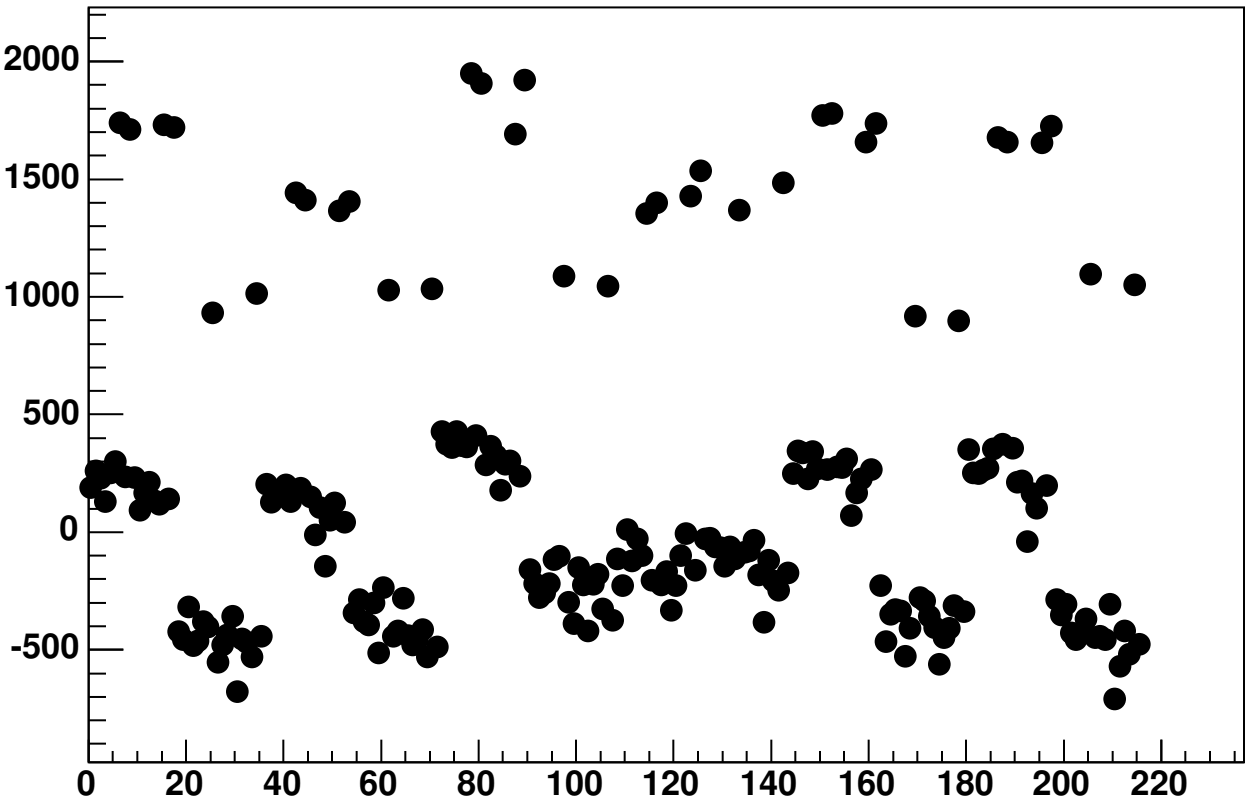
Enable 2, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



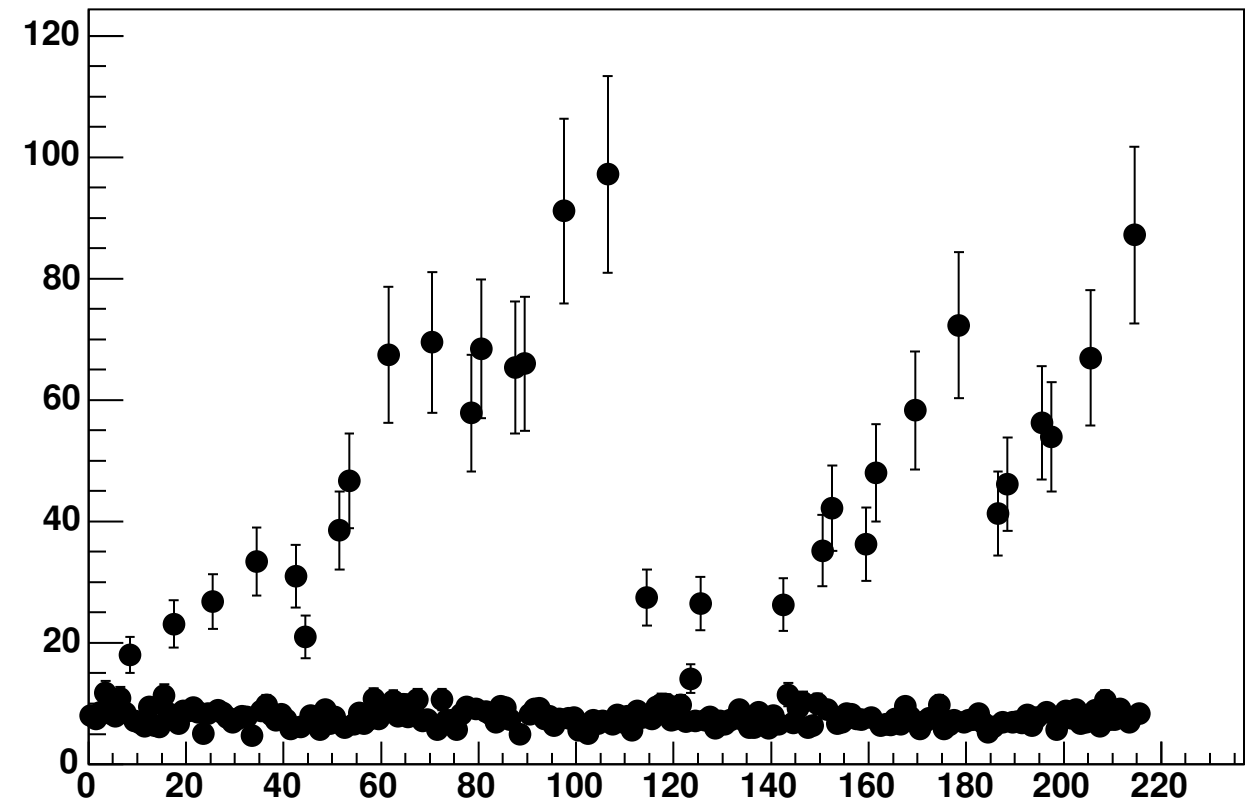
Enable 2, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



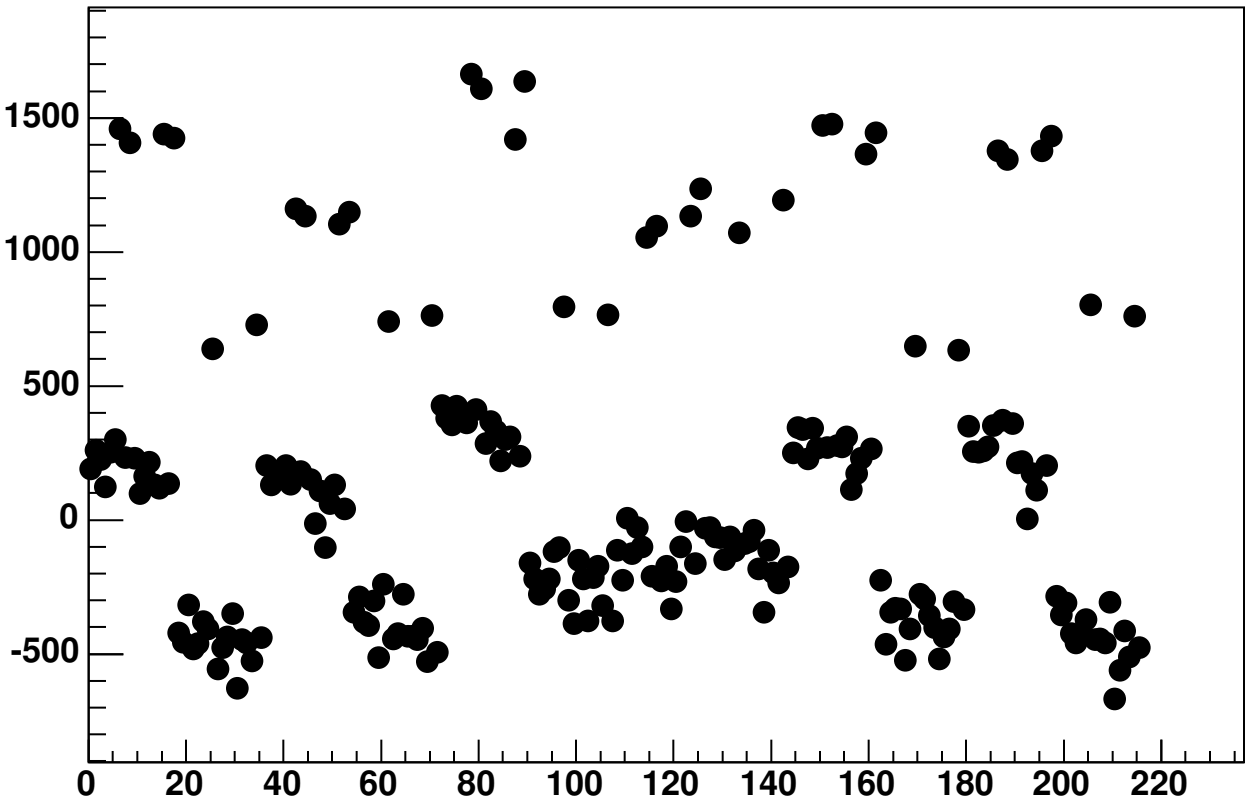
Enable 2, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



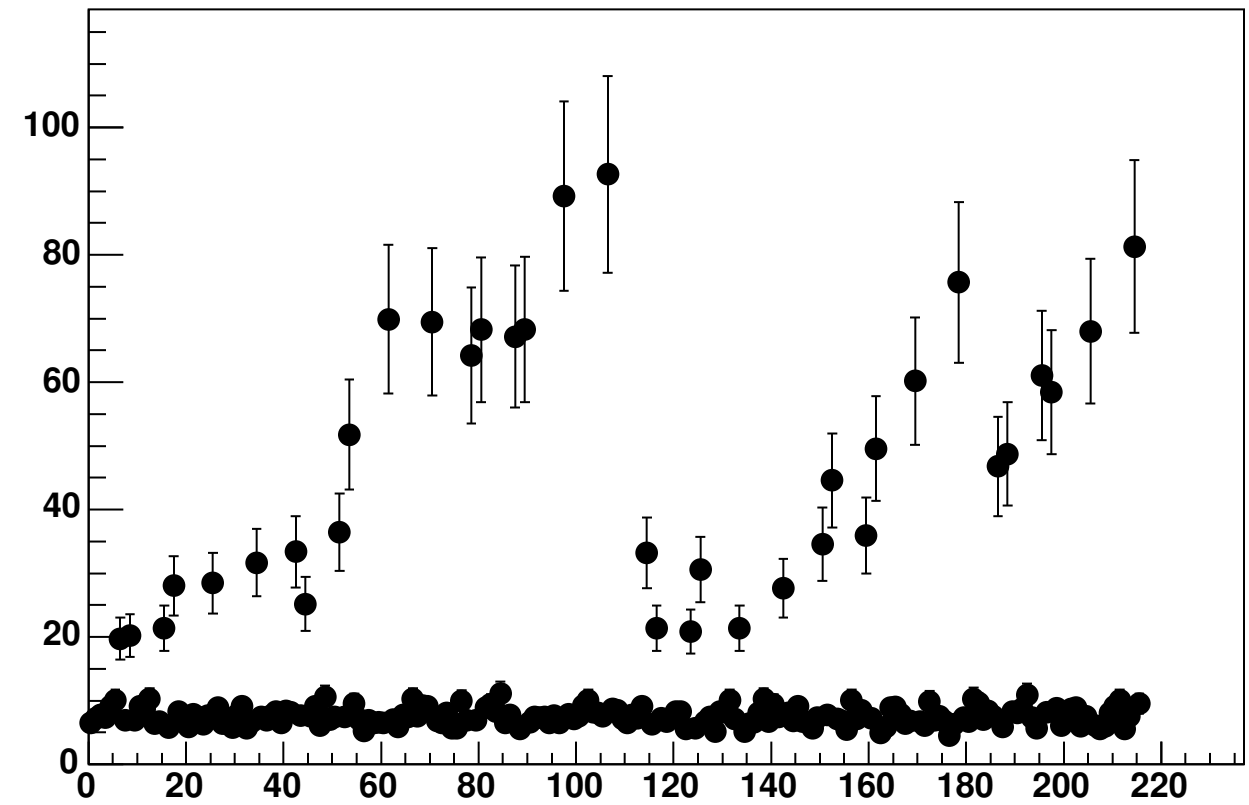
Enable 2, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



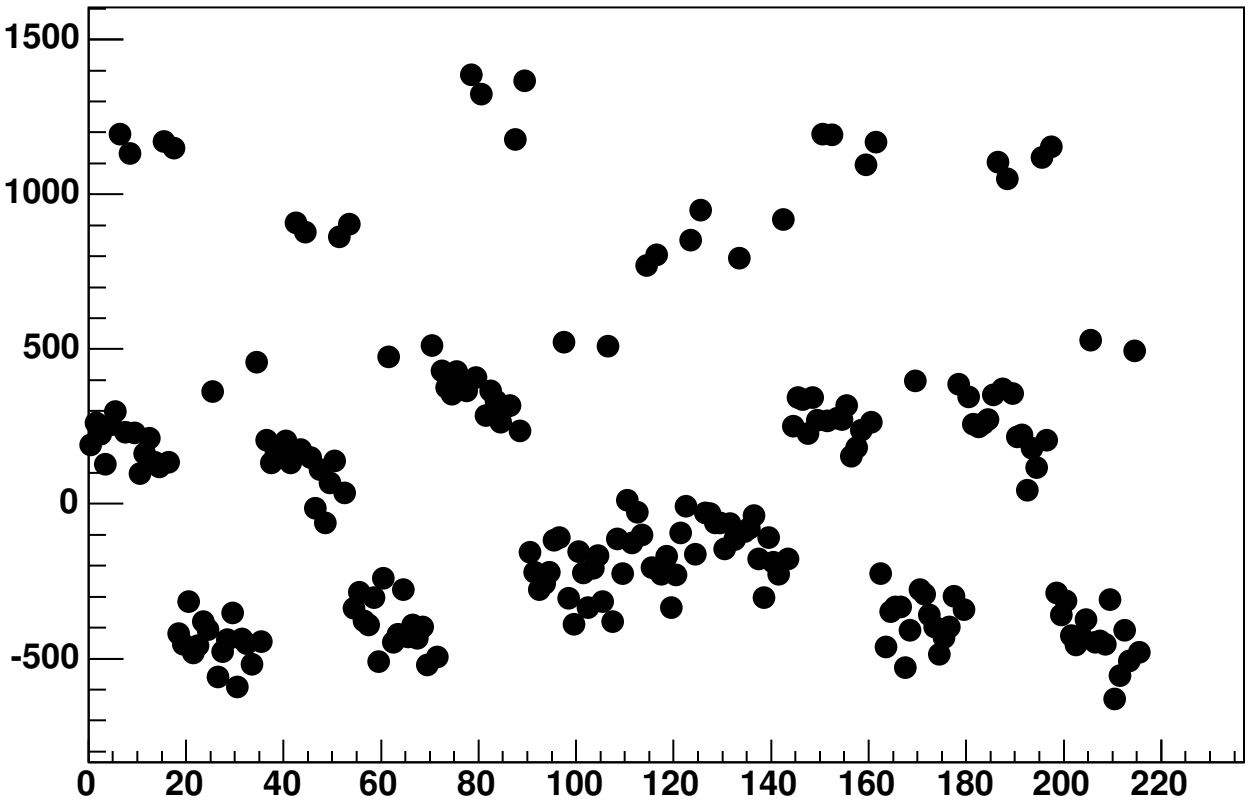
Enable 2, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



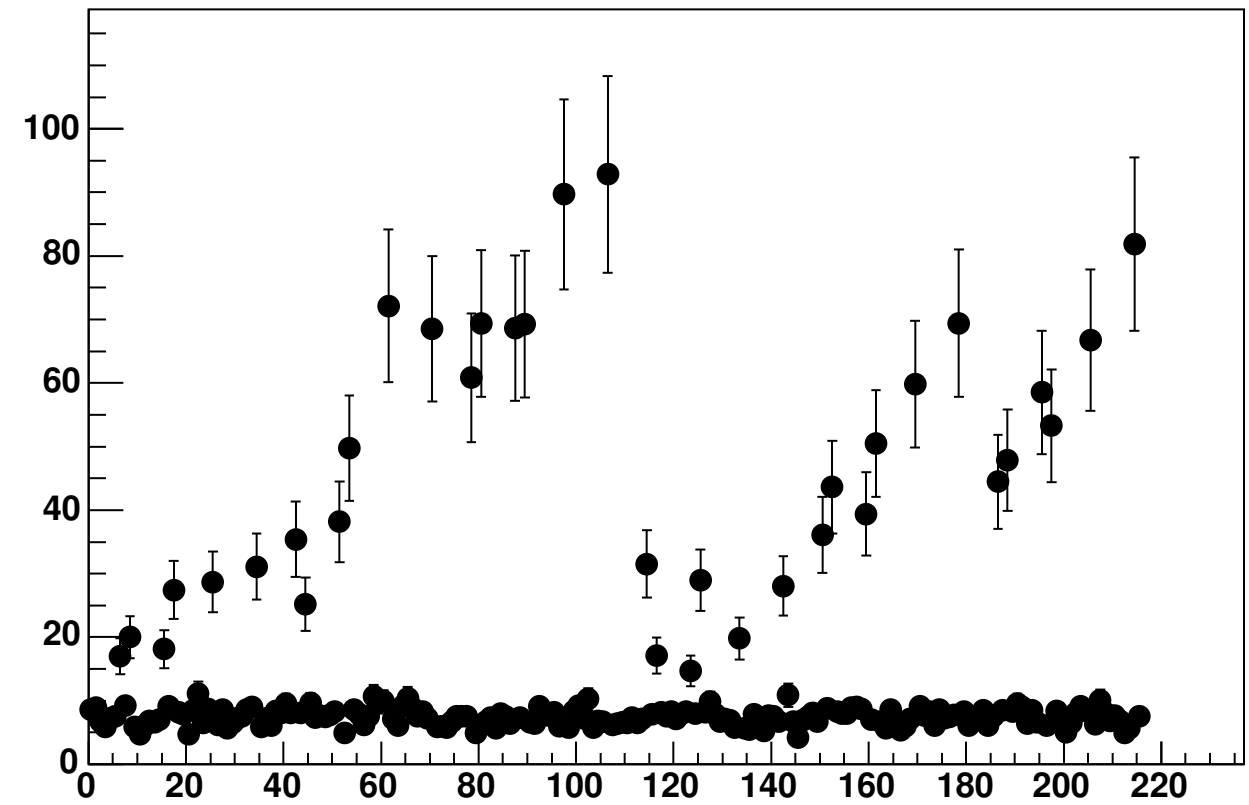
Enable 2, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



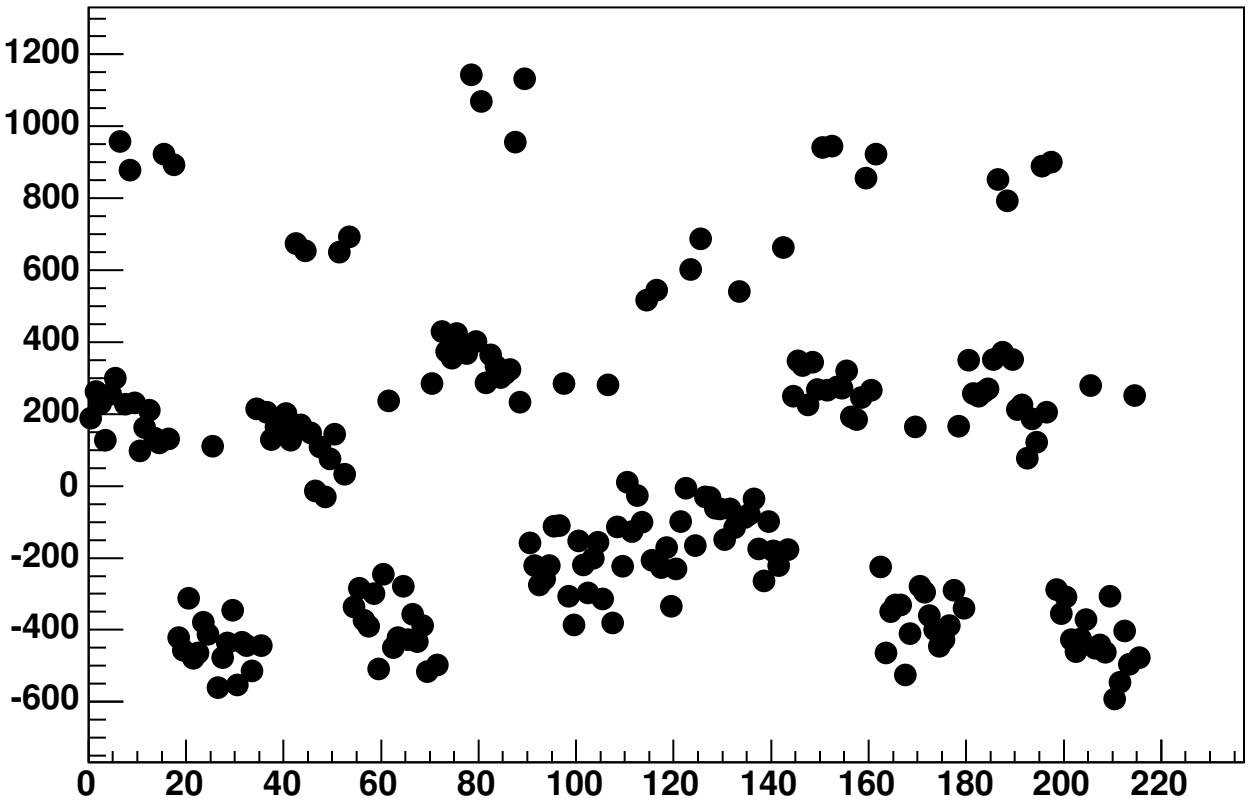
Enable 2, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



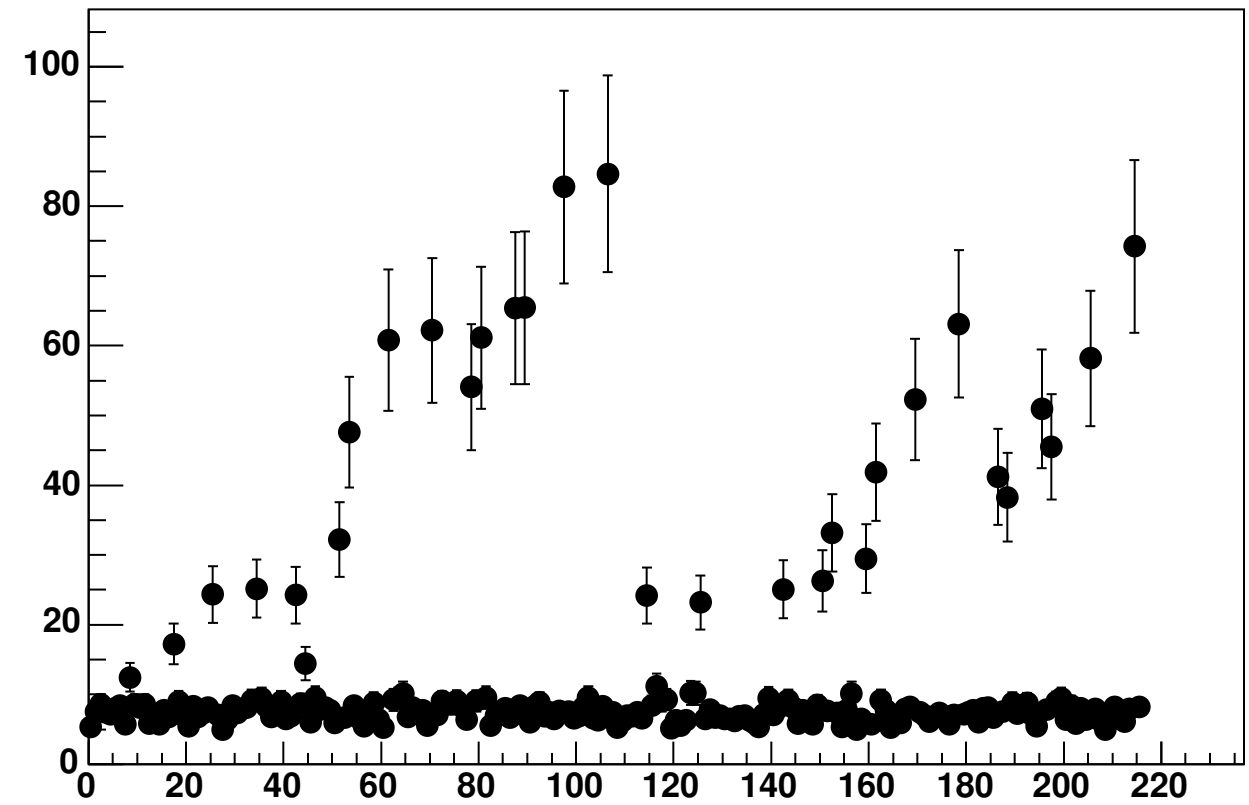
Enable 2, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



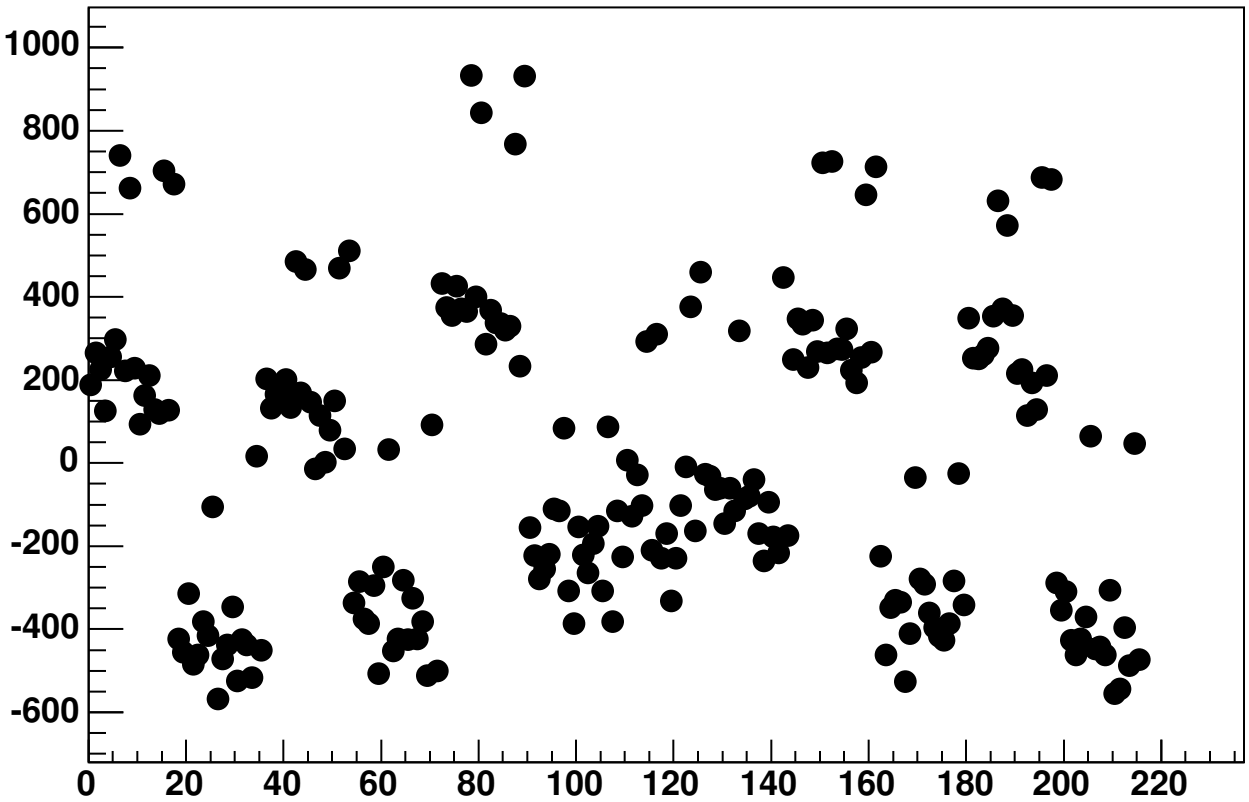
Enable 2, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



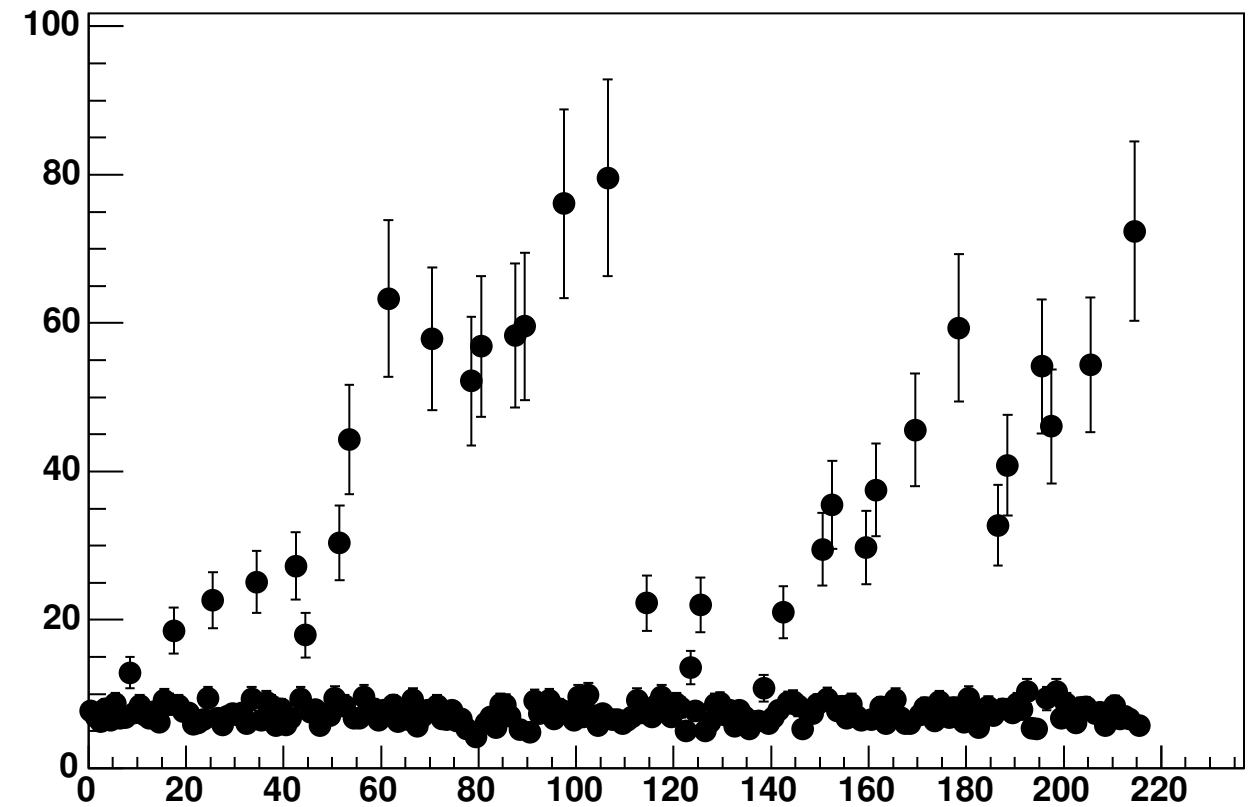
Enable 2, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



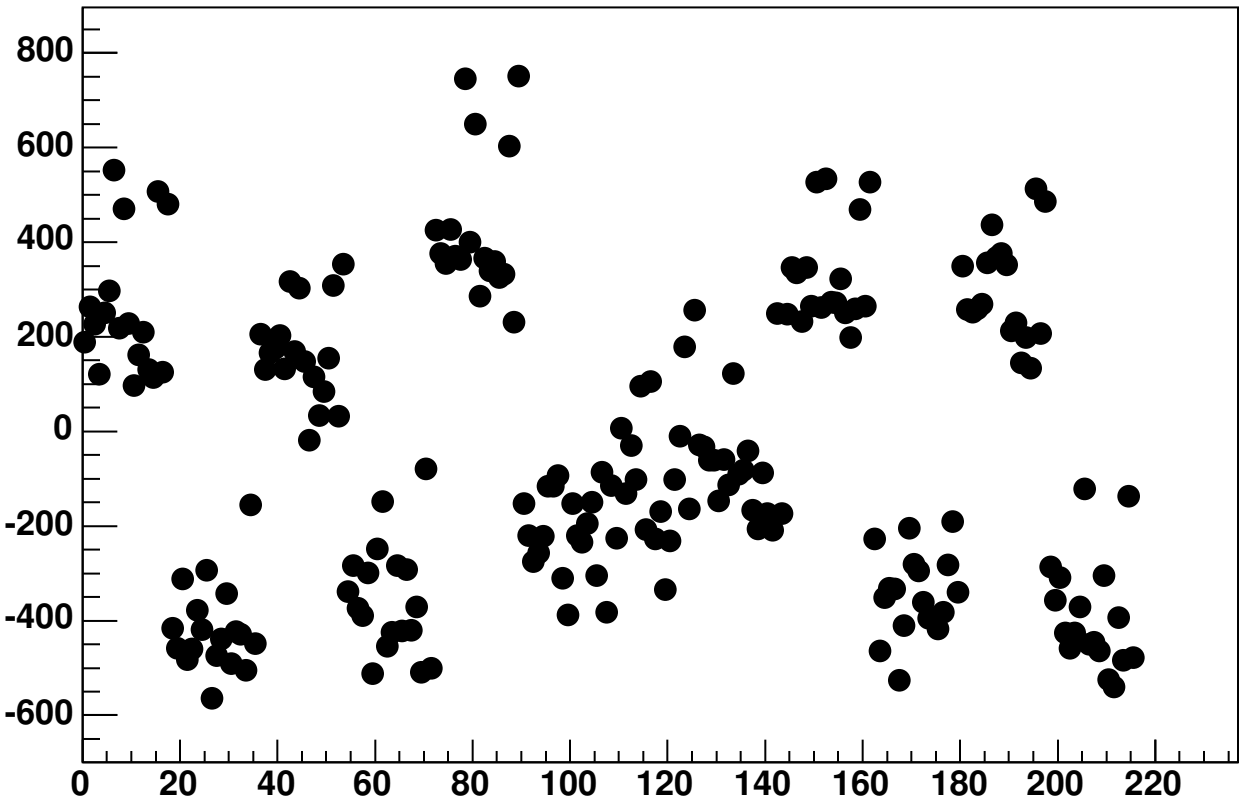
Enable 2, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



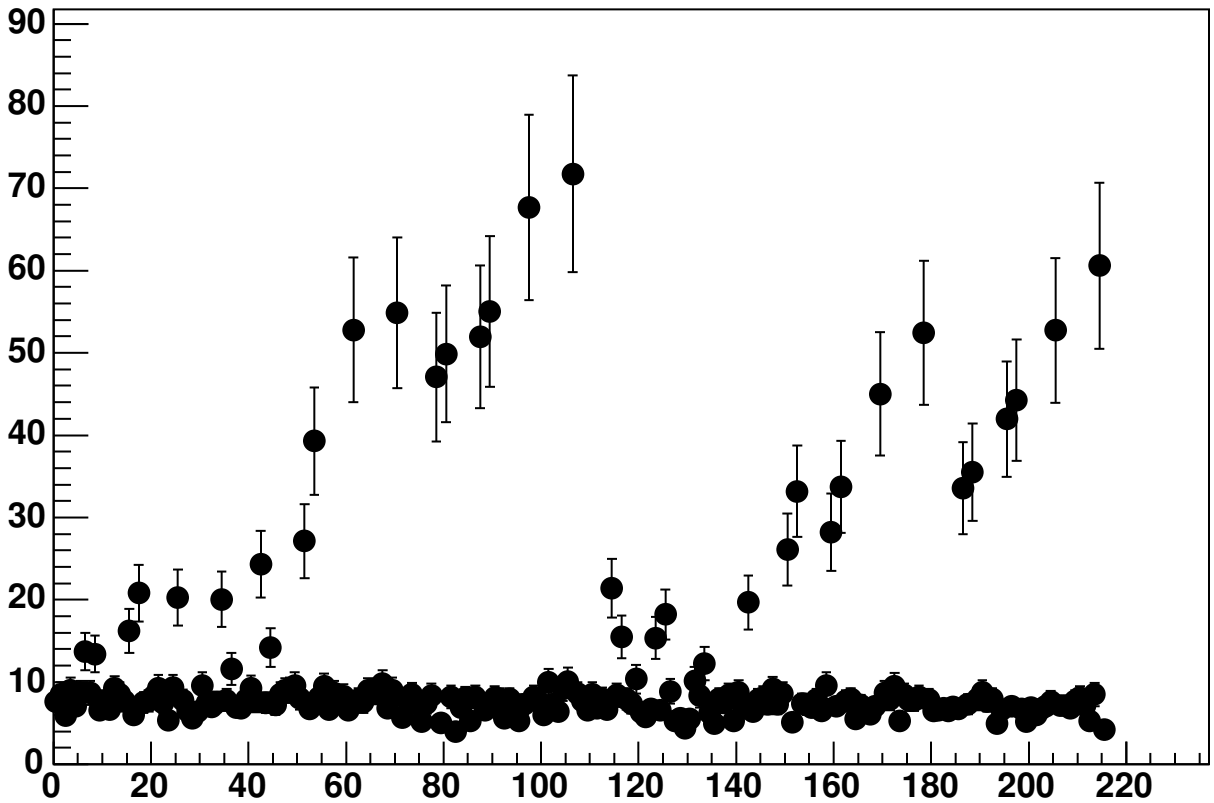
Enable 2, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



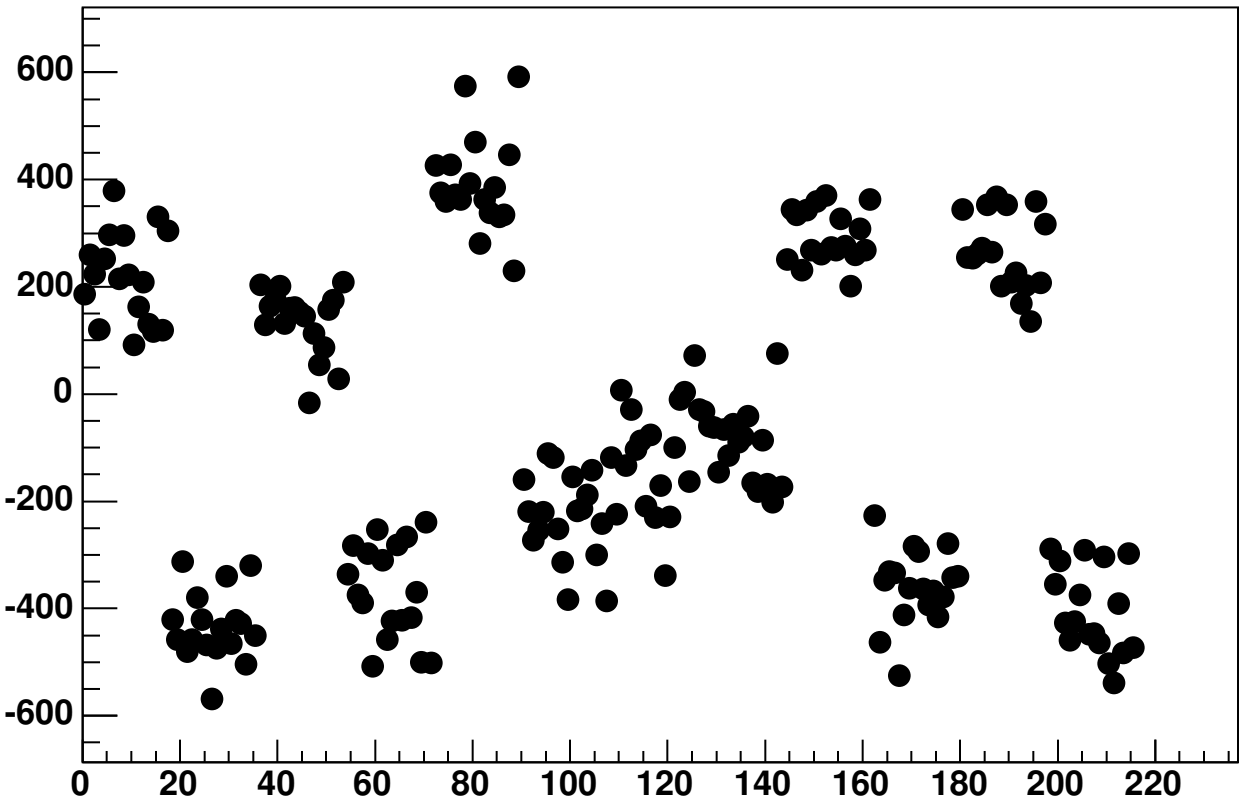
Enable 2, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



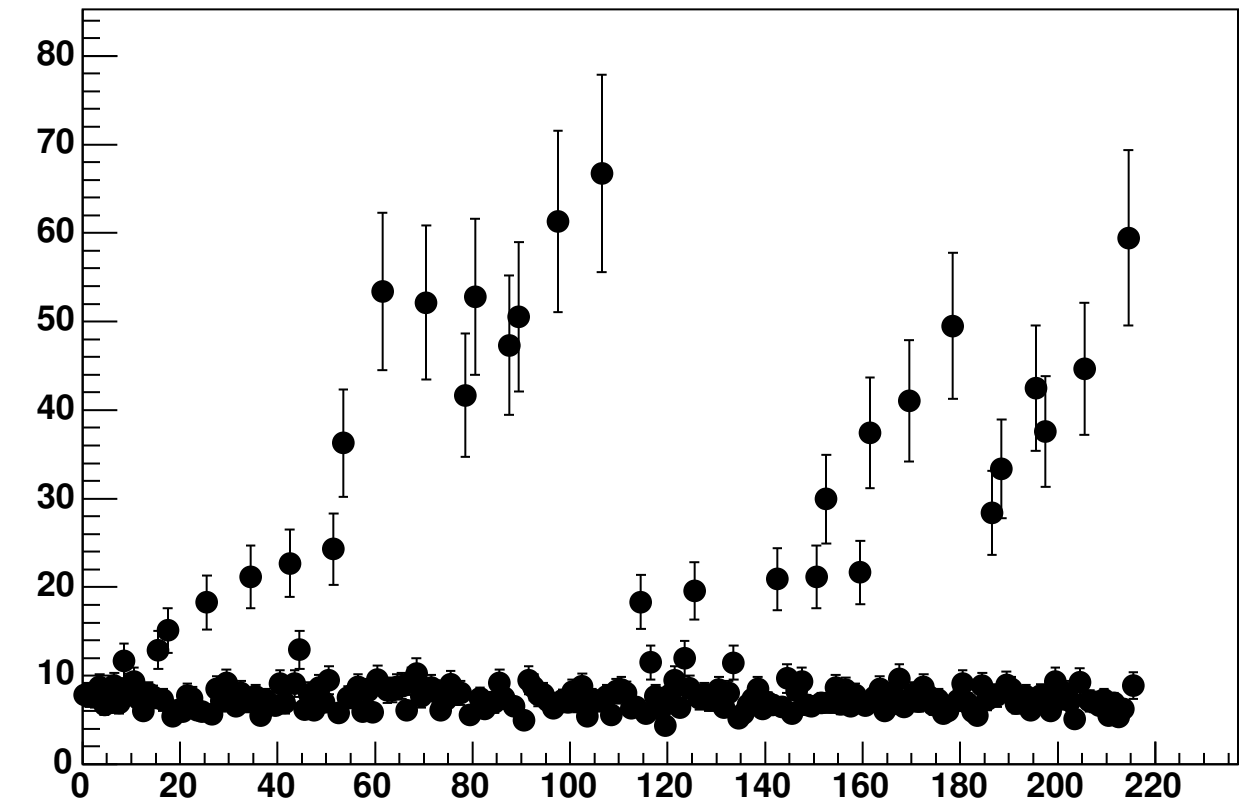
Enable 2, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



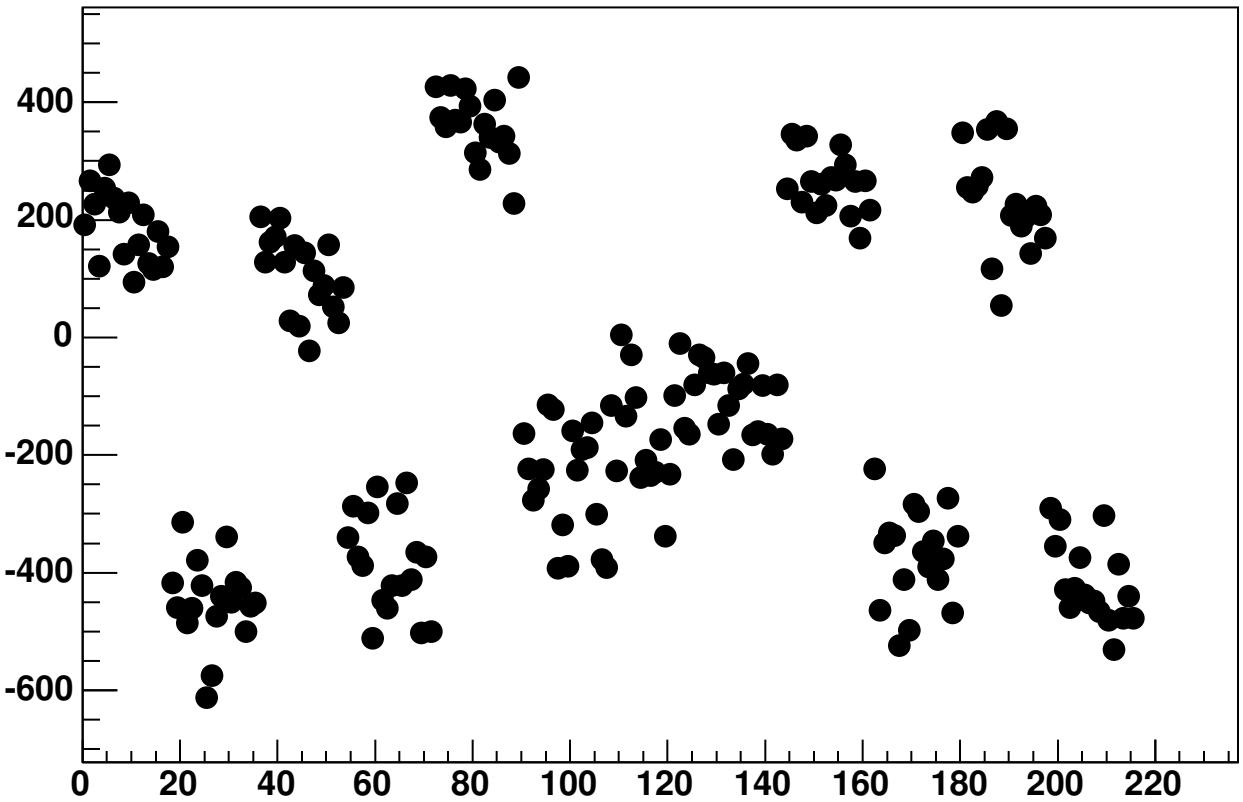
Enable 2, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



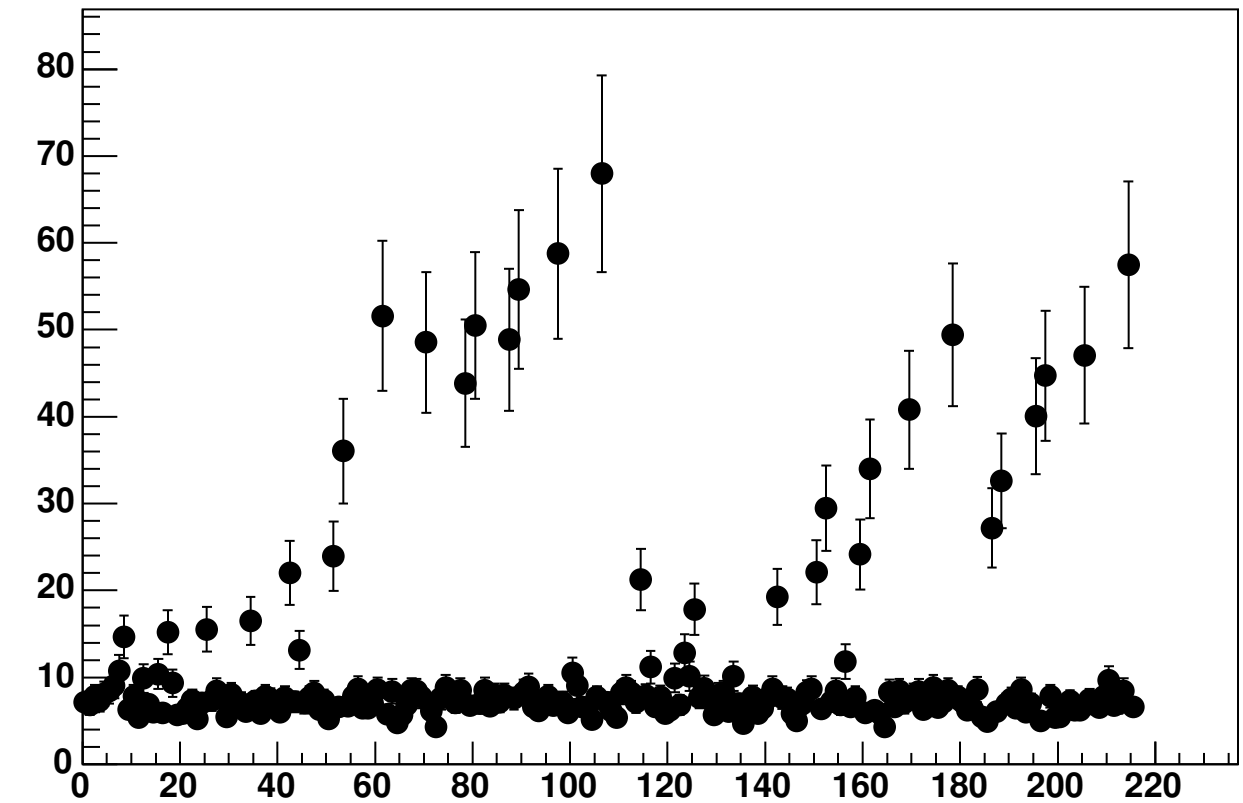
Enable 2, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



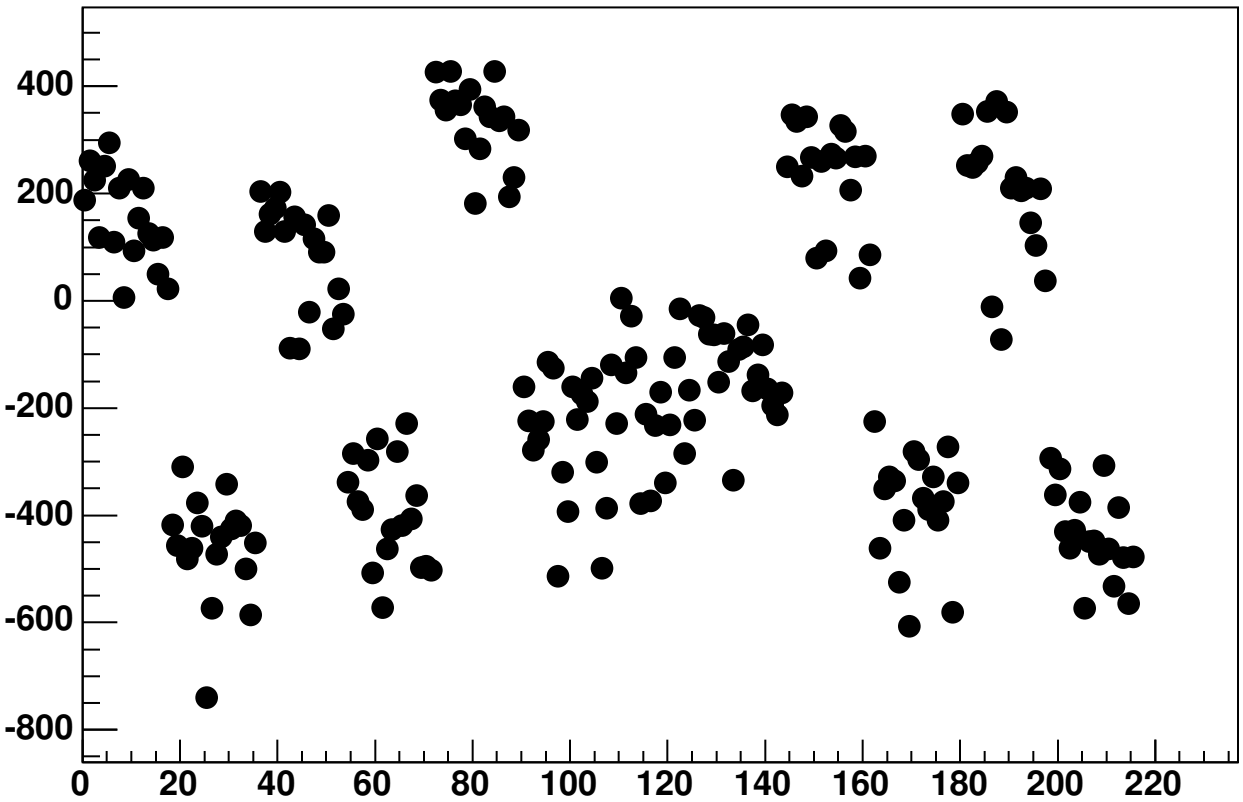
Enable 2, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



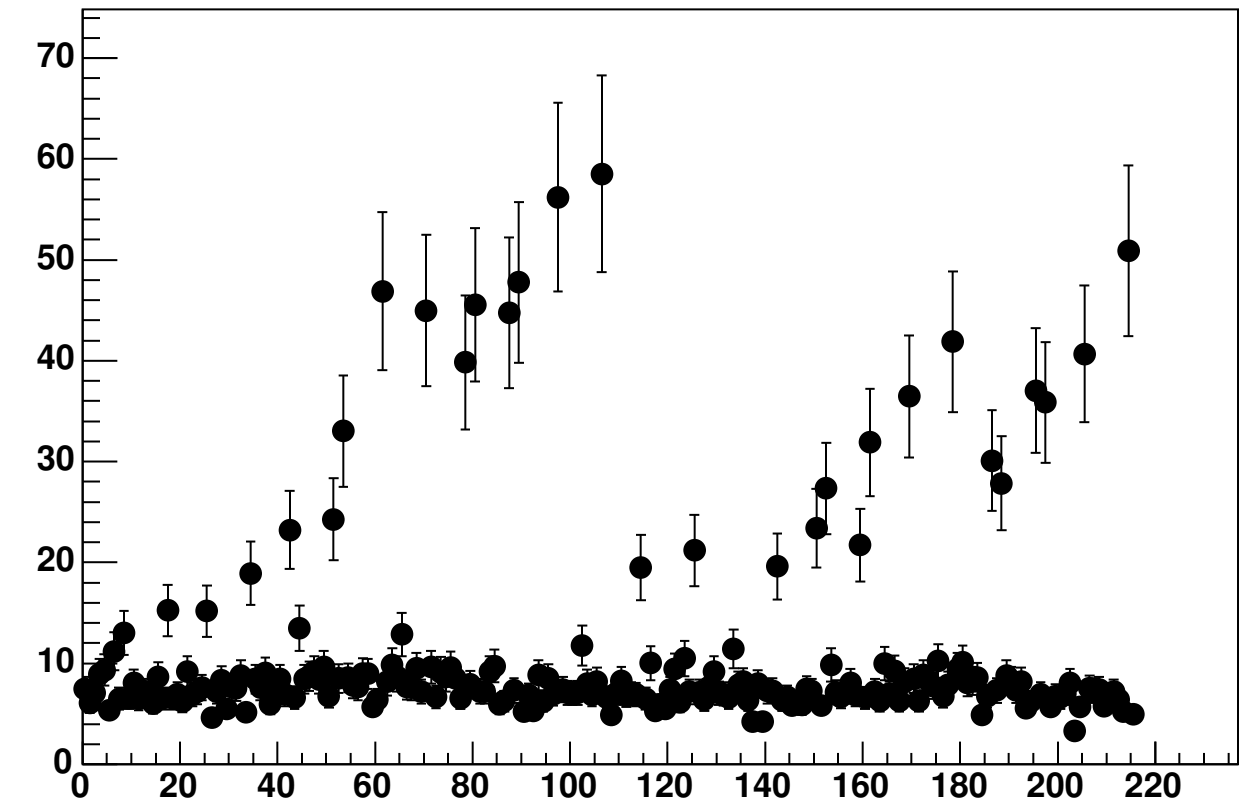
Enable 2, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



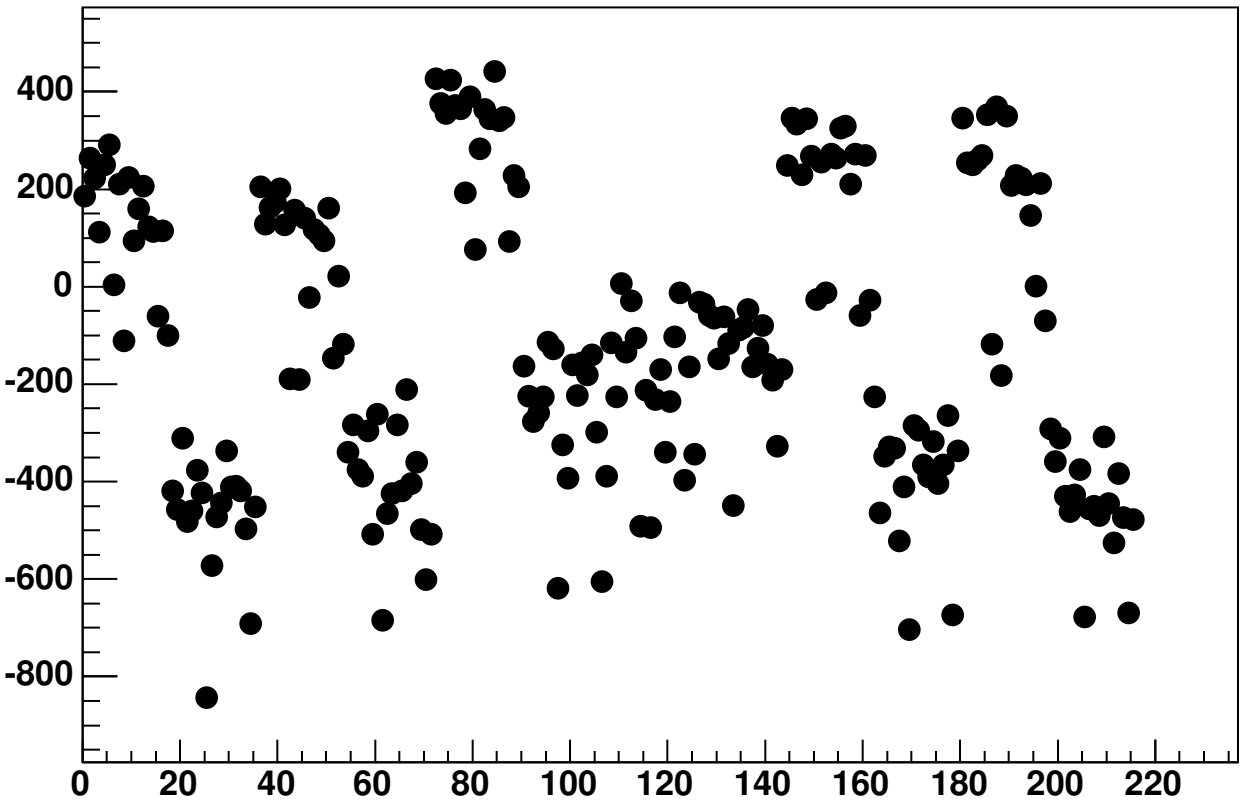
Enable 2, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



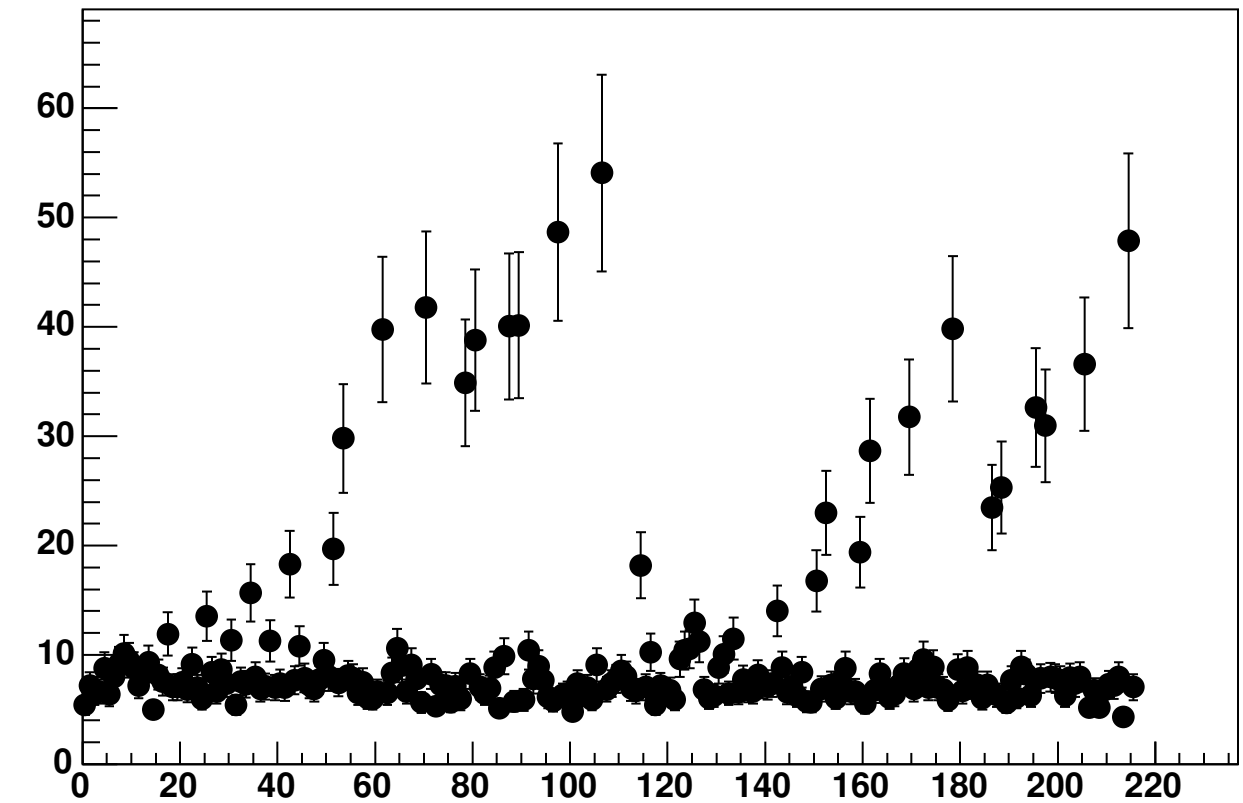
Enable 2, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



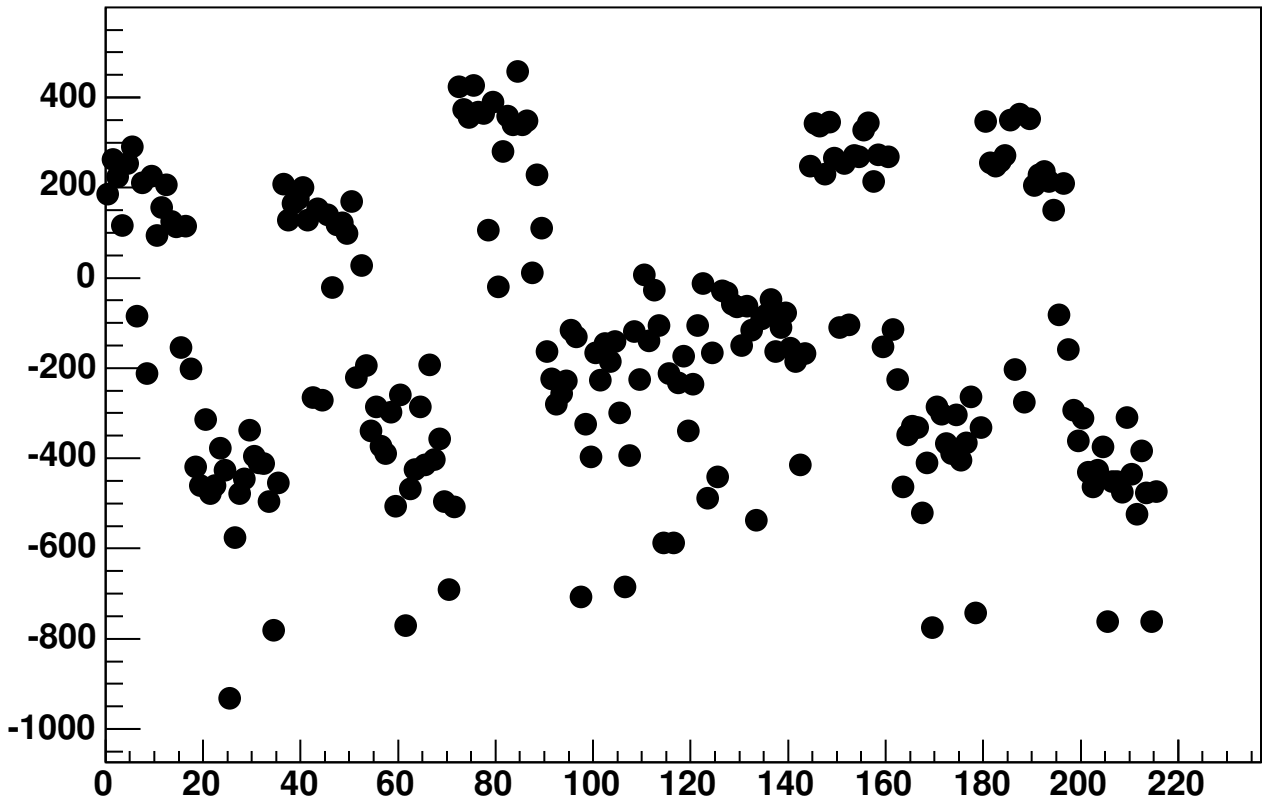
Enable 2, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



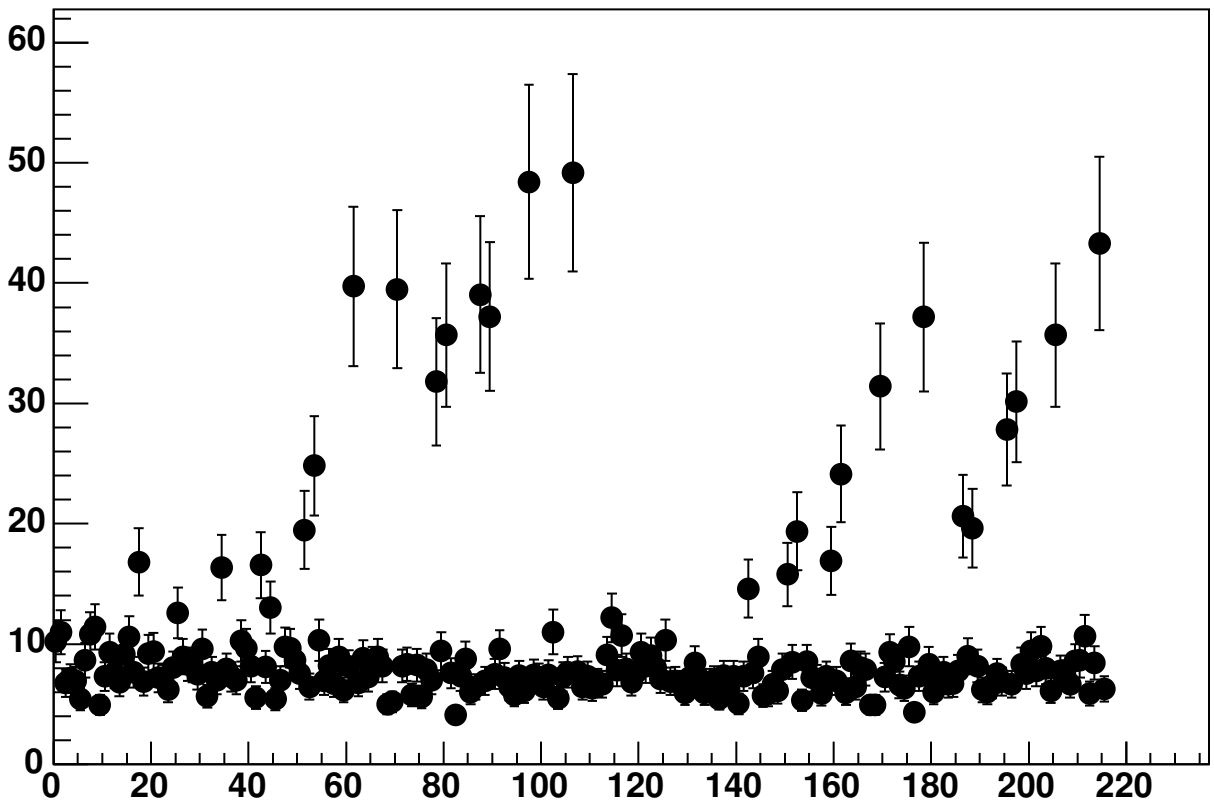
Enable 2, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



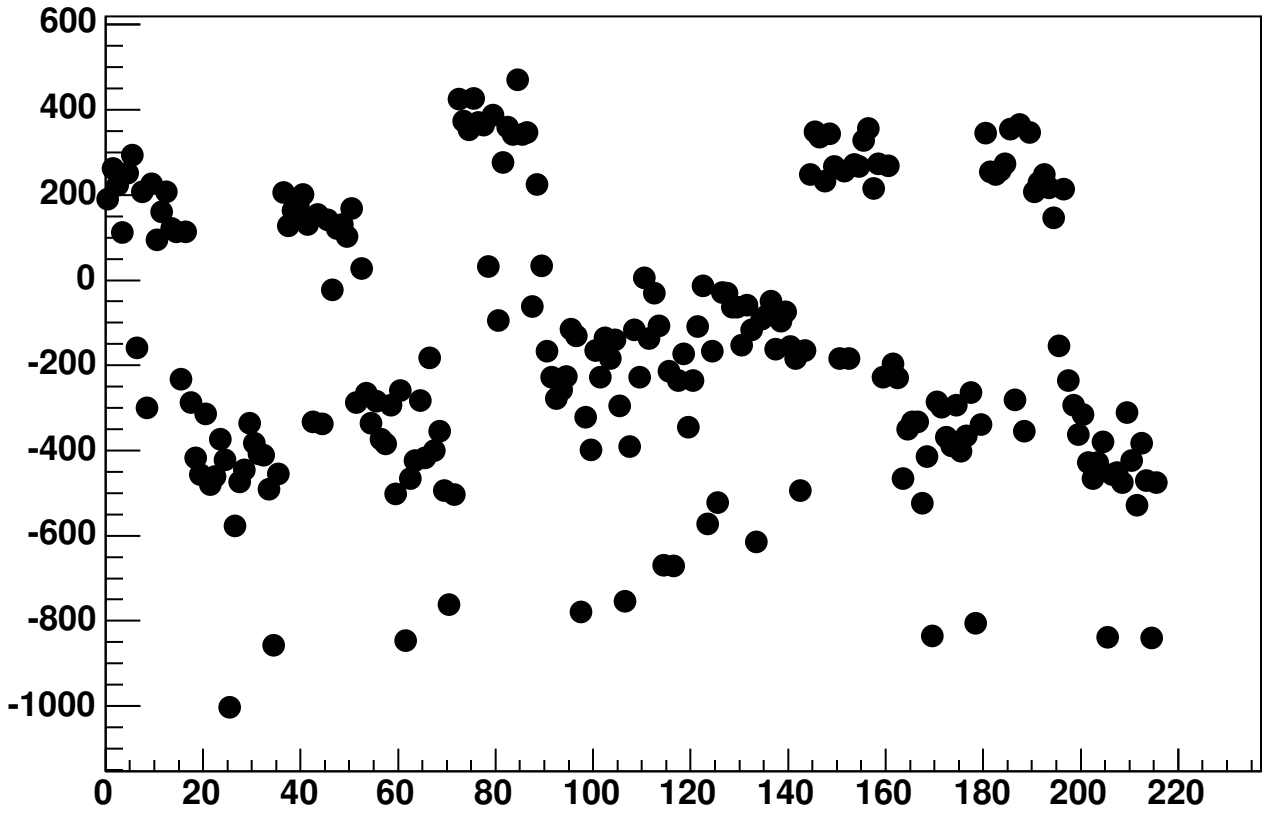
Enable 2, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



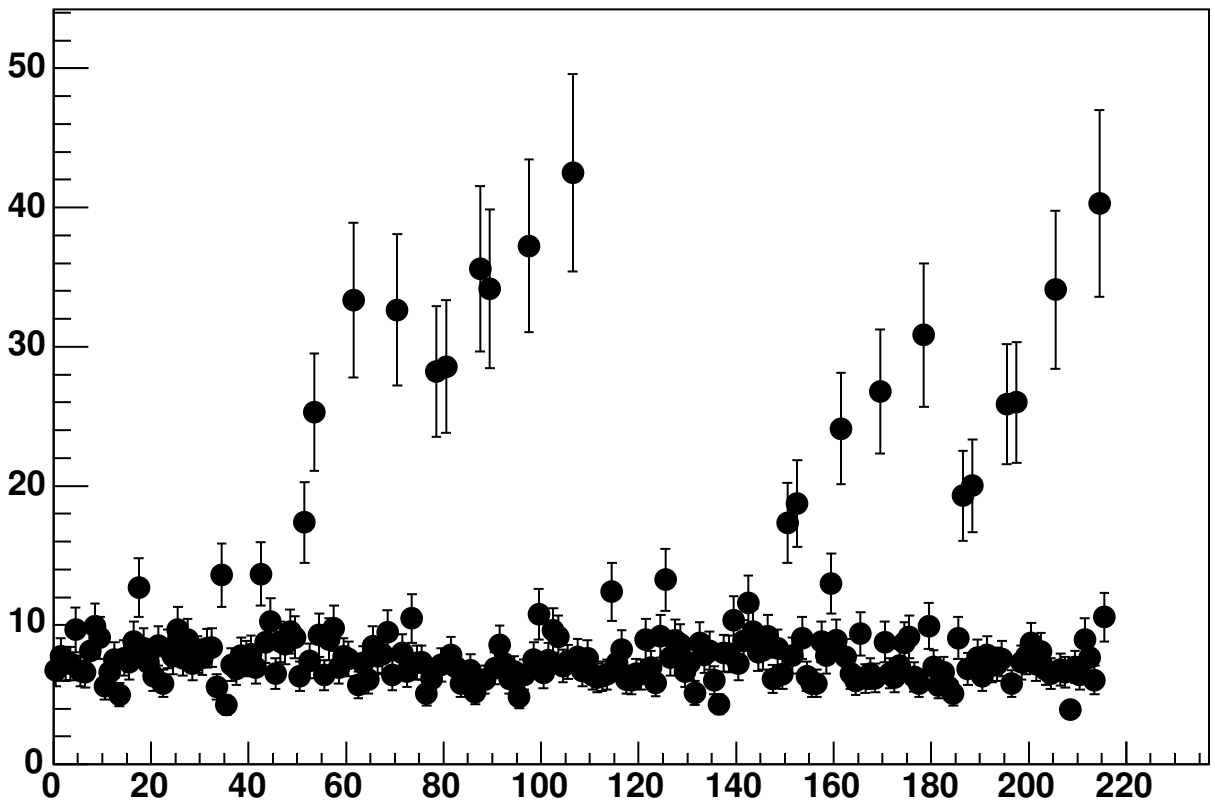
Enable 2, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



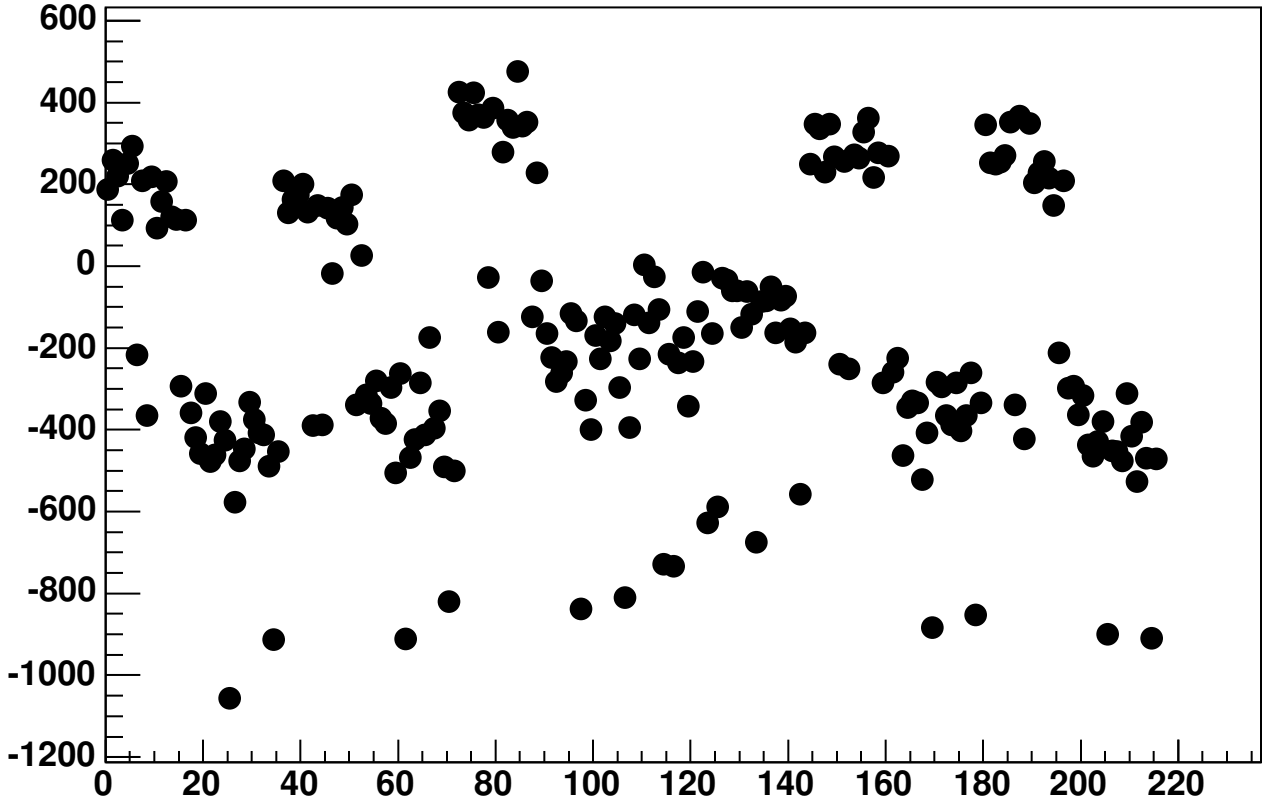
Enable 2, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



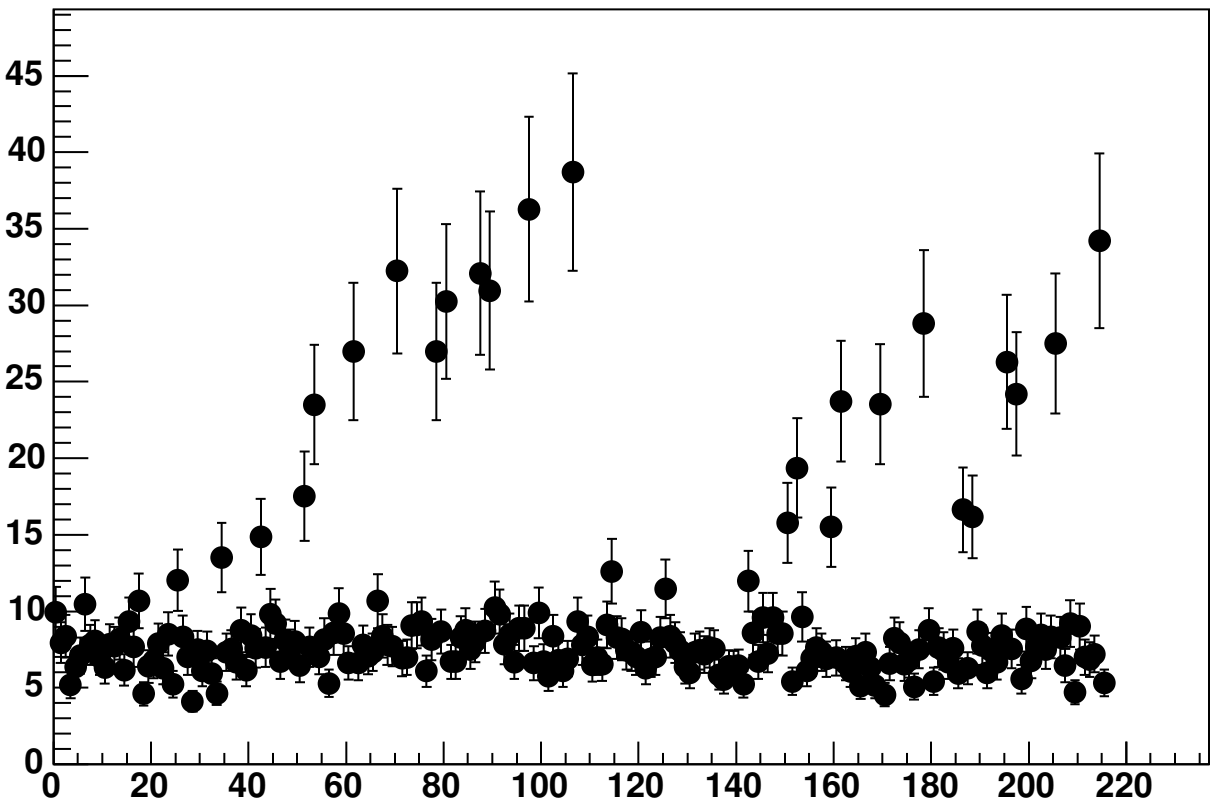
Enable 2, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



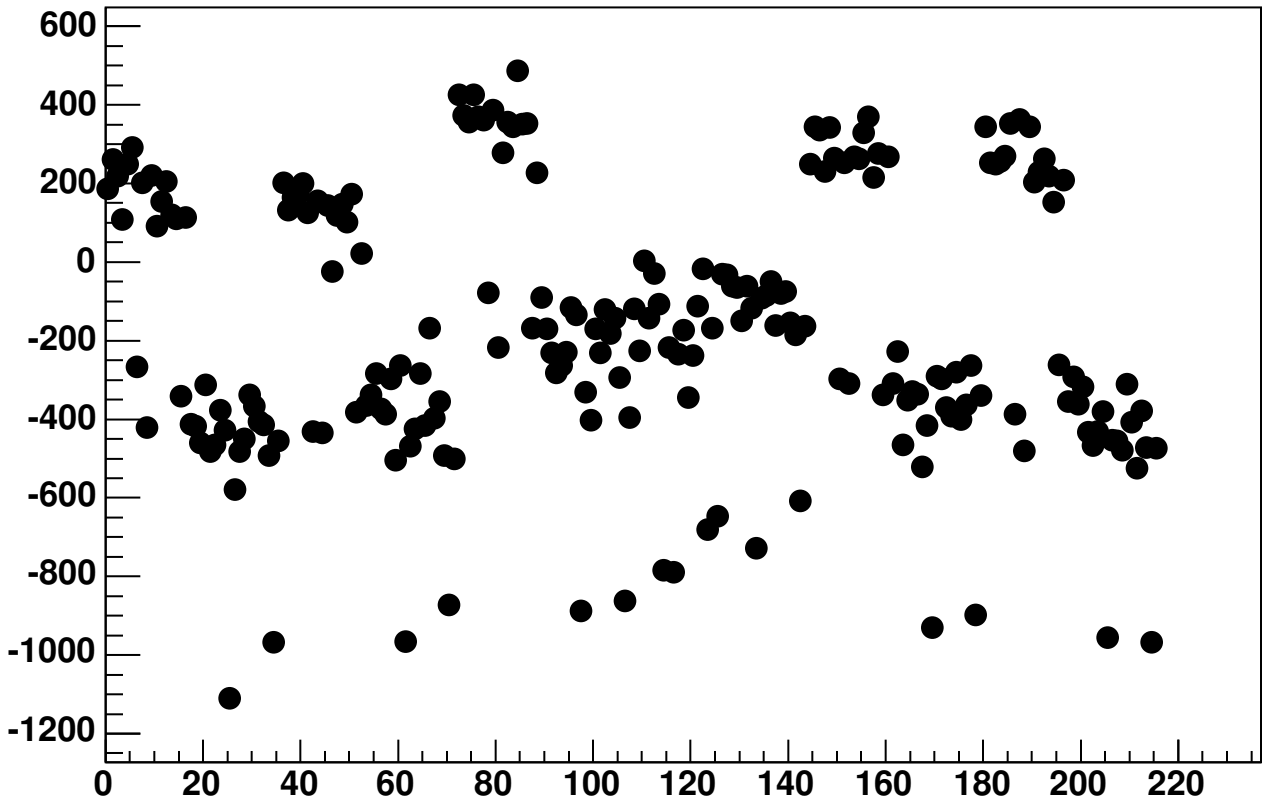
Enable 2, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



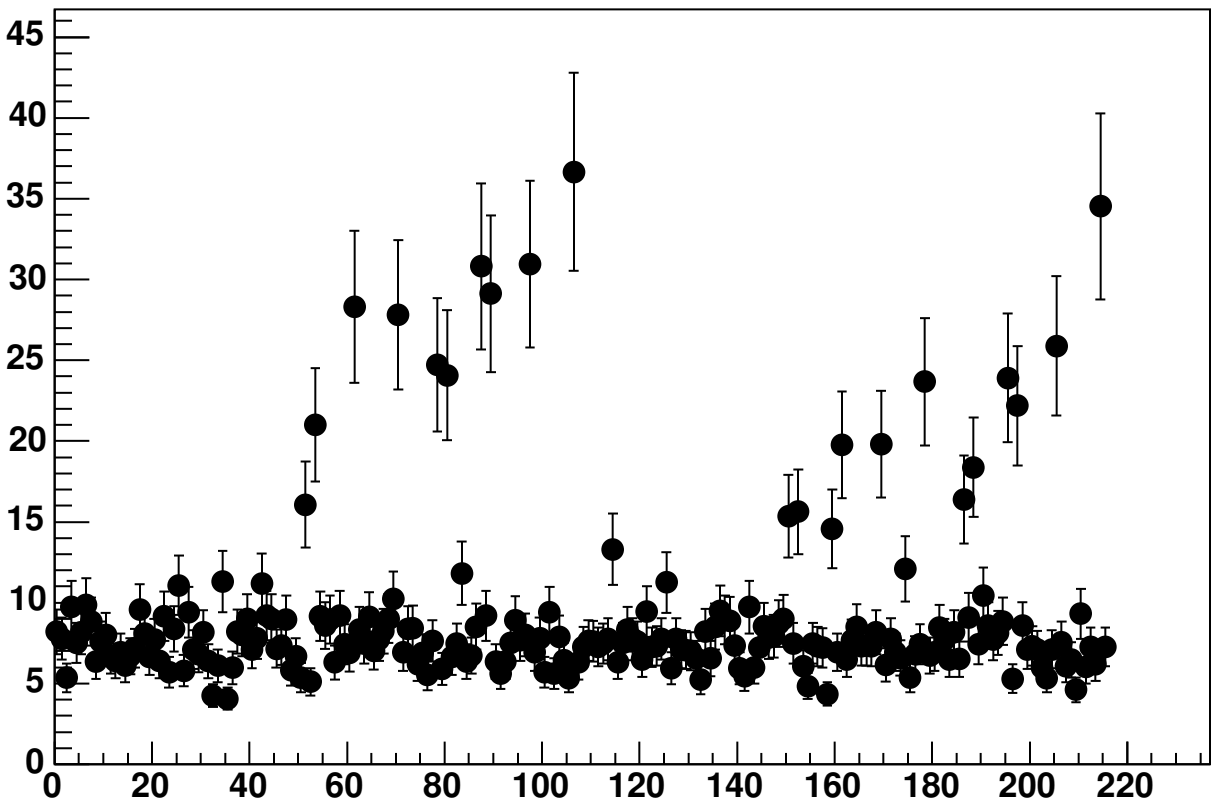
Enable 2, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



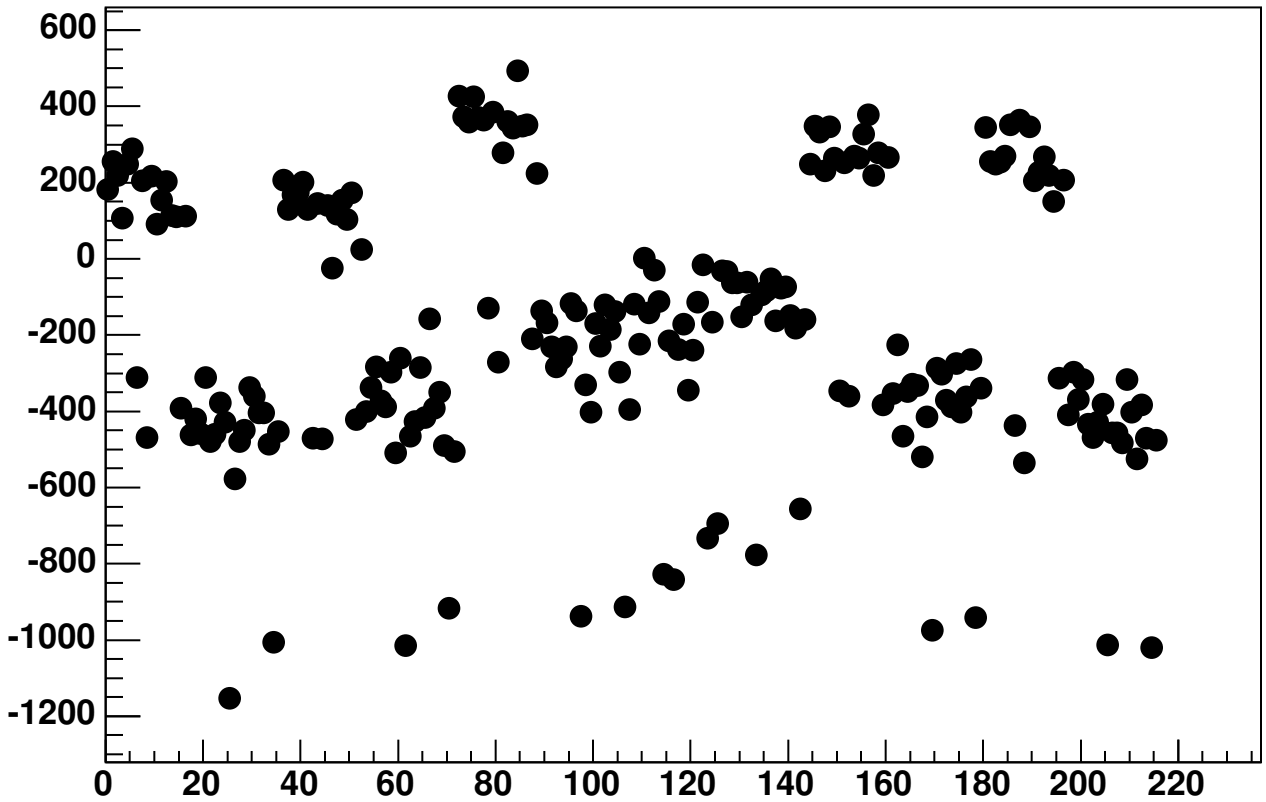
Enable 2, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



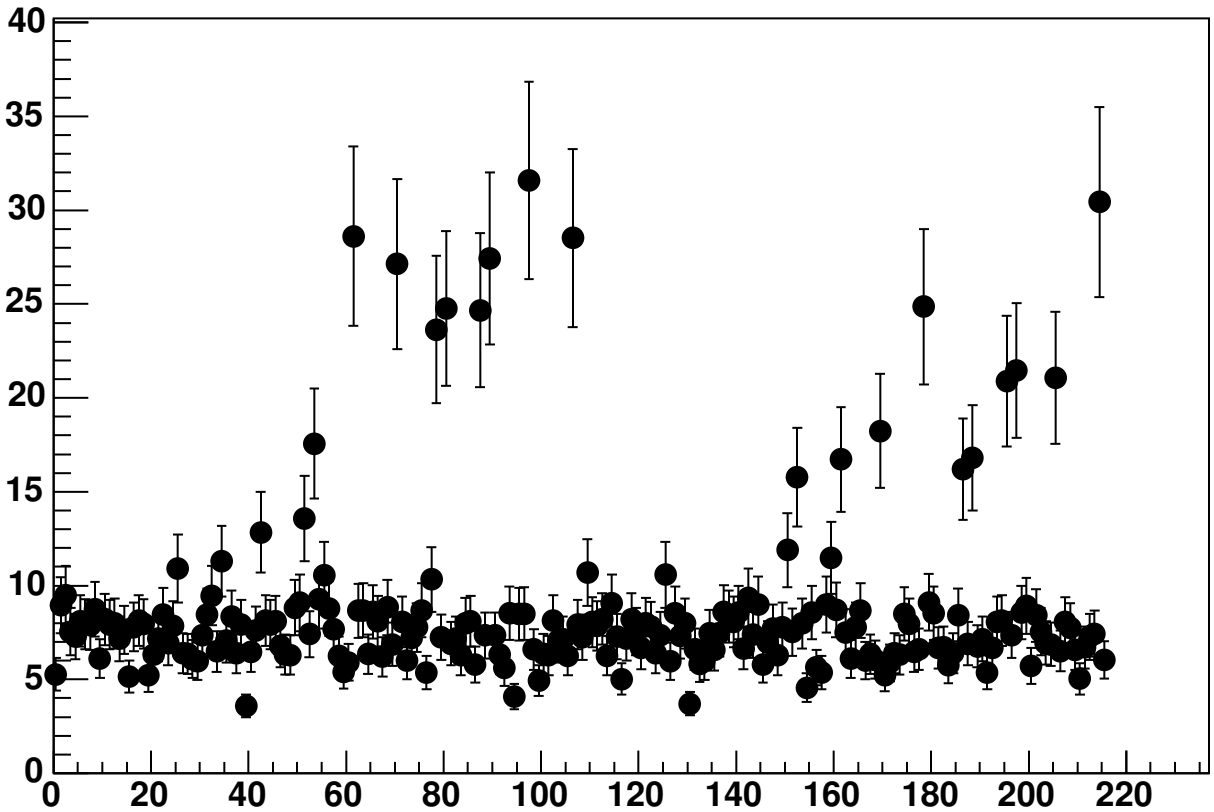
Enable 2, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



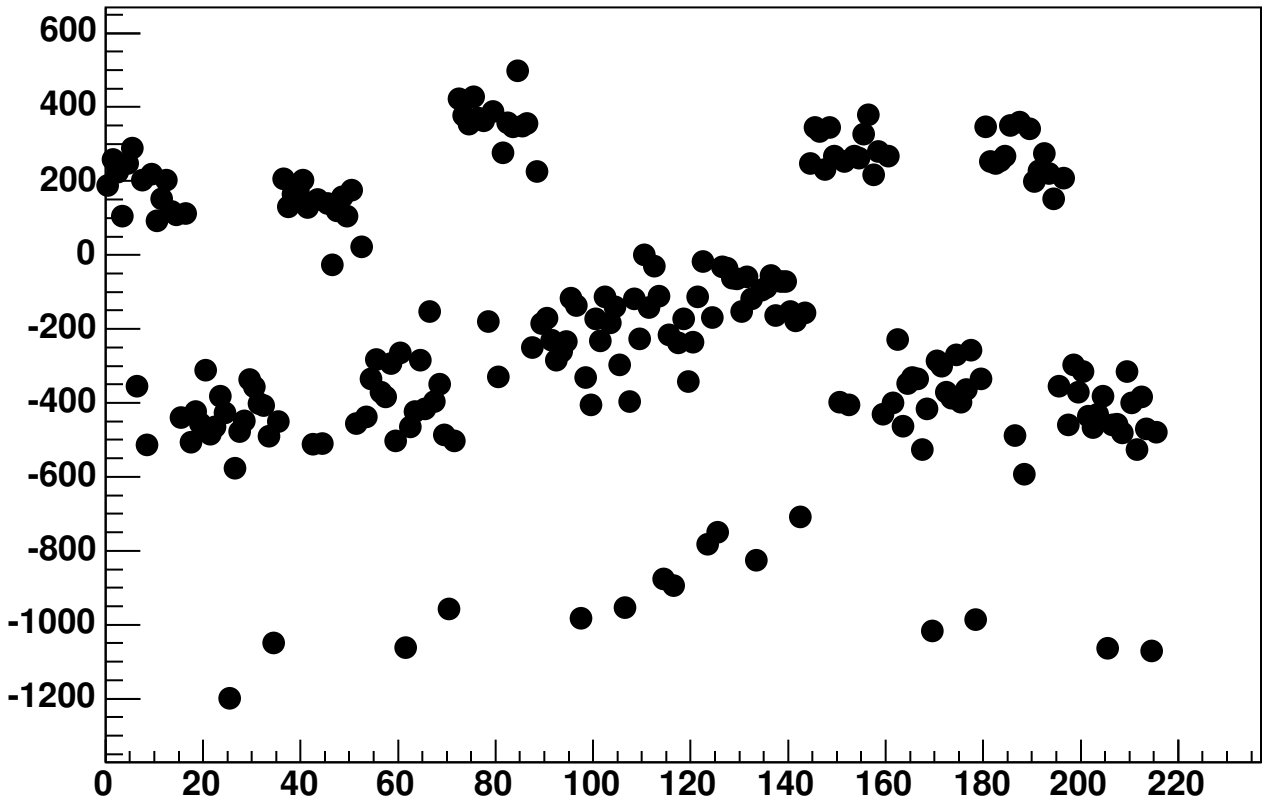
Enable 2, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



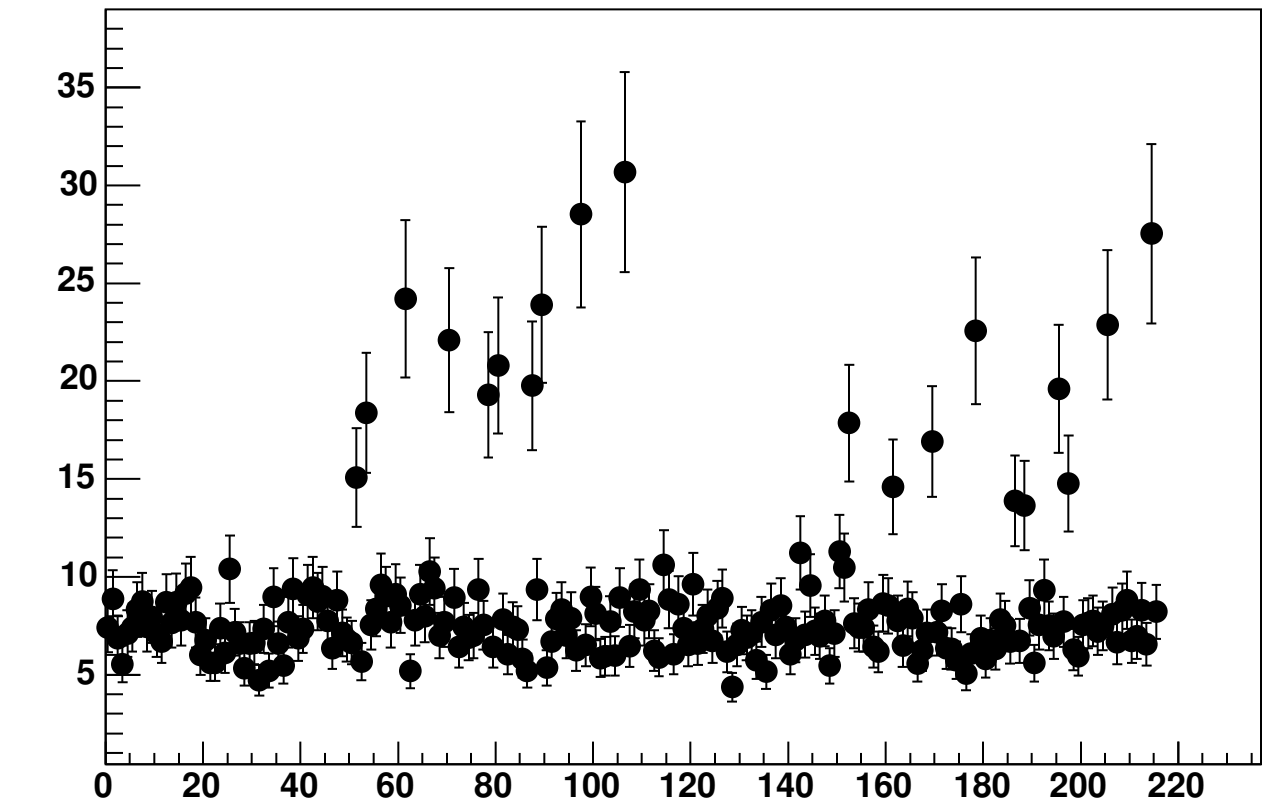
Enable 2, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



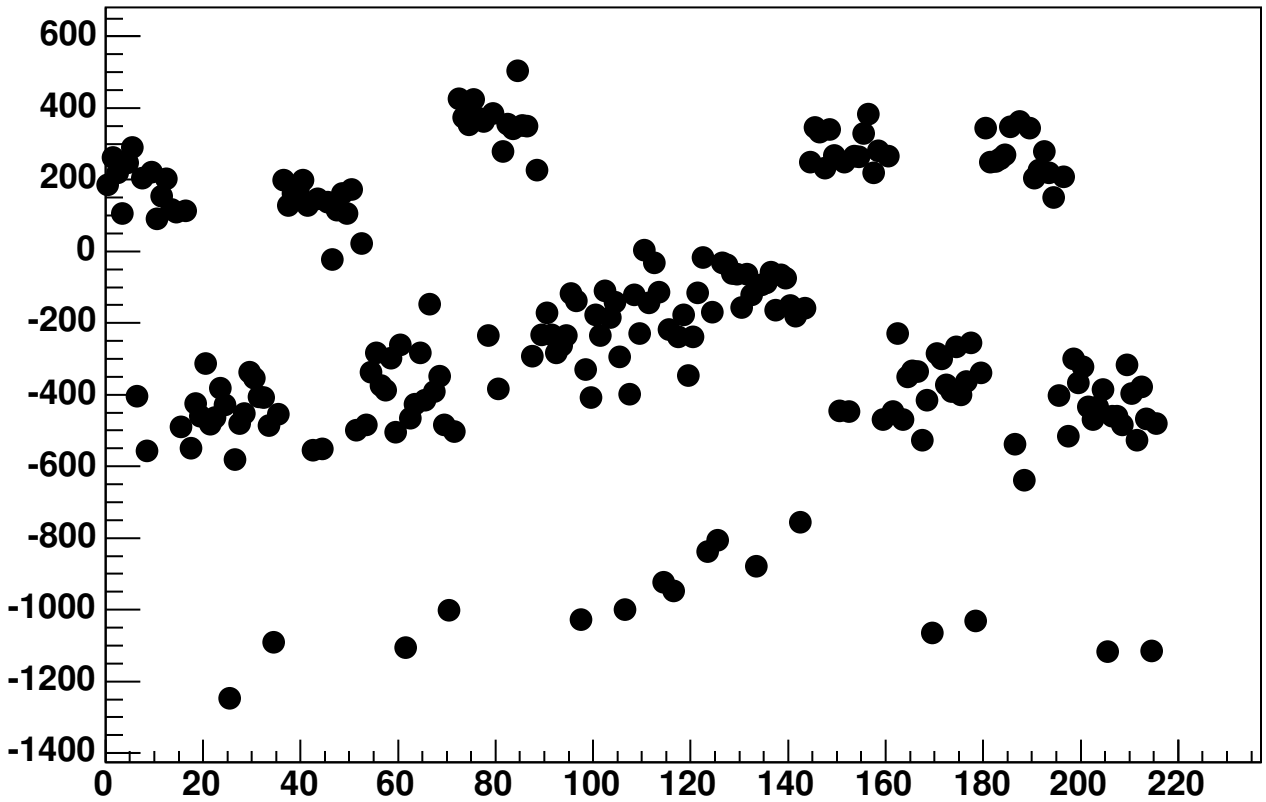
Enable 2, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



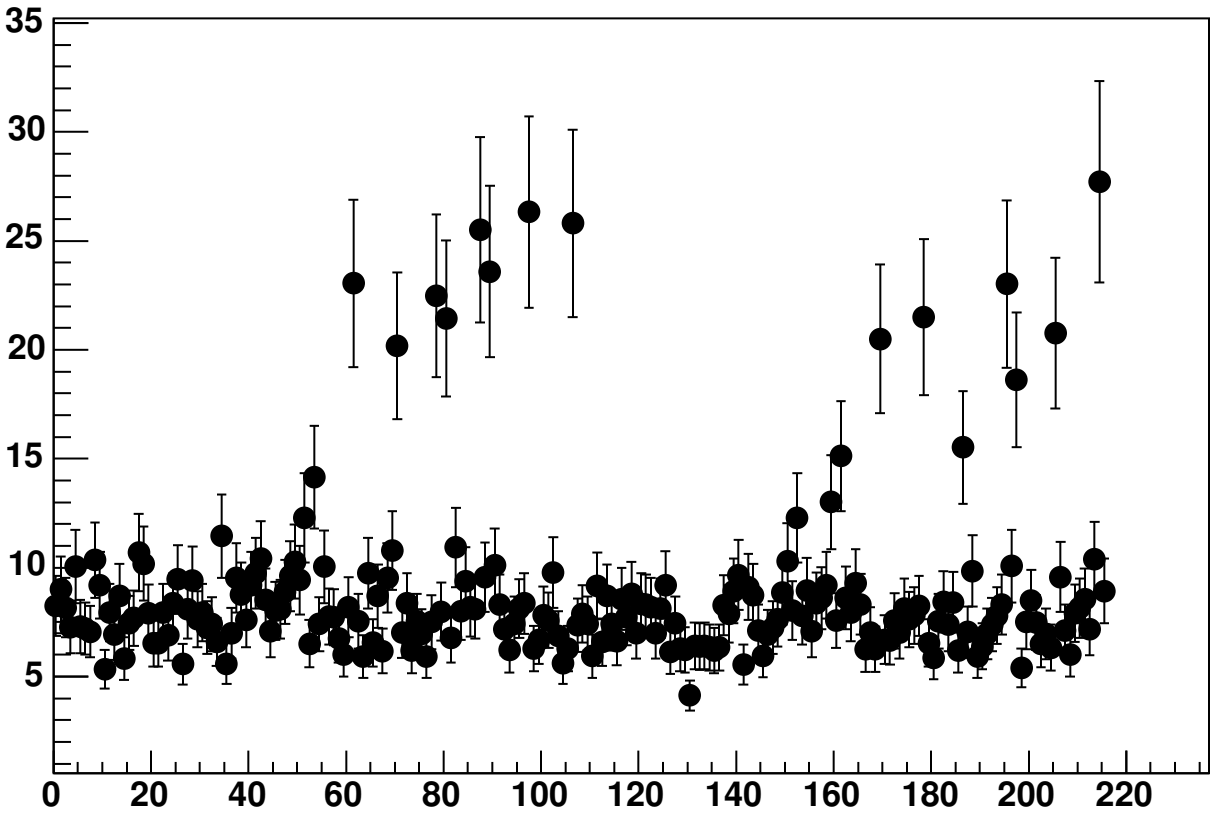
Enable 2, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



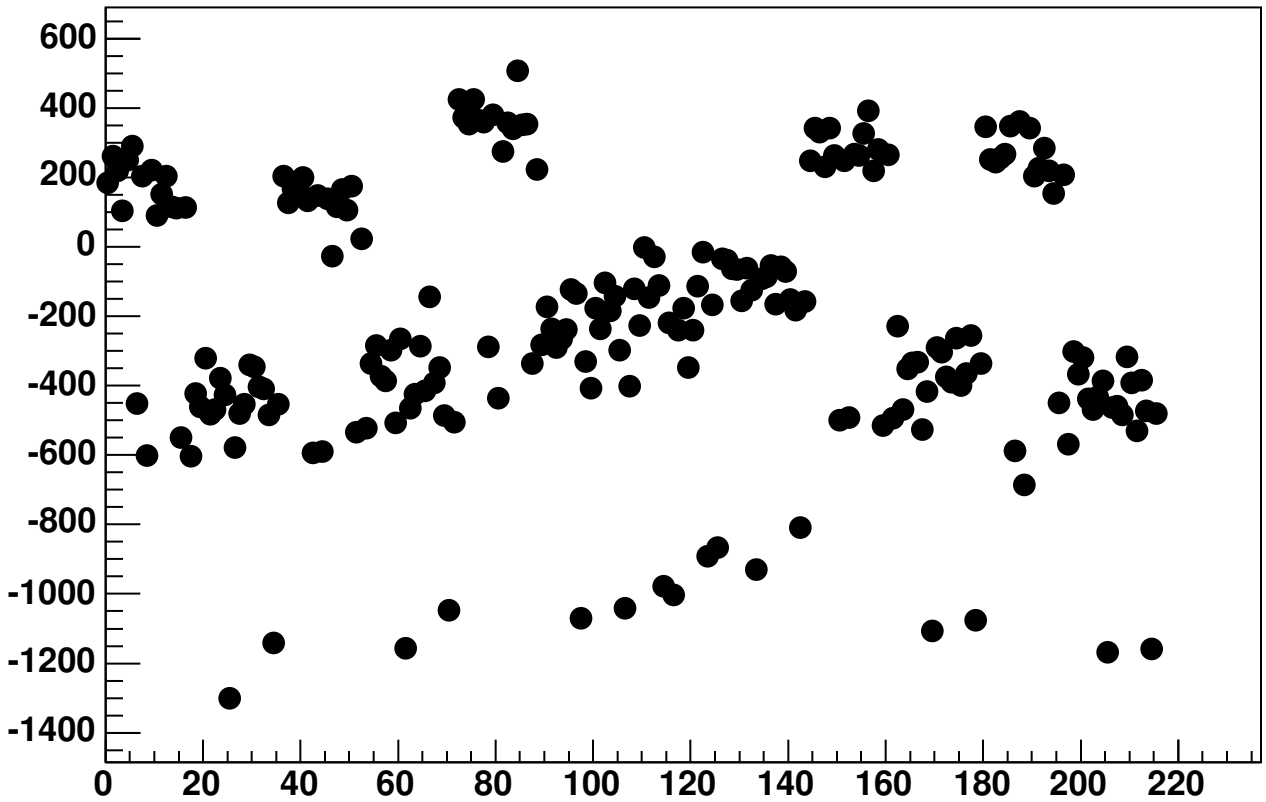
Enable 2, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



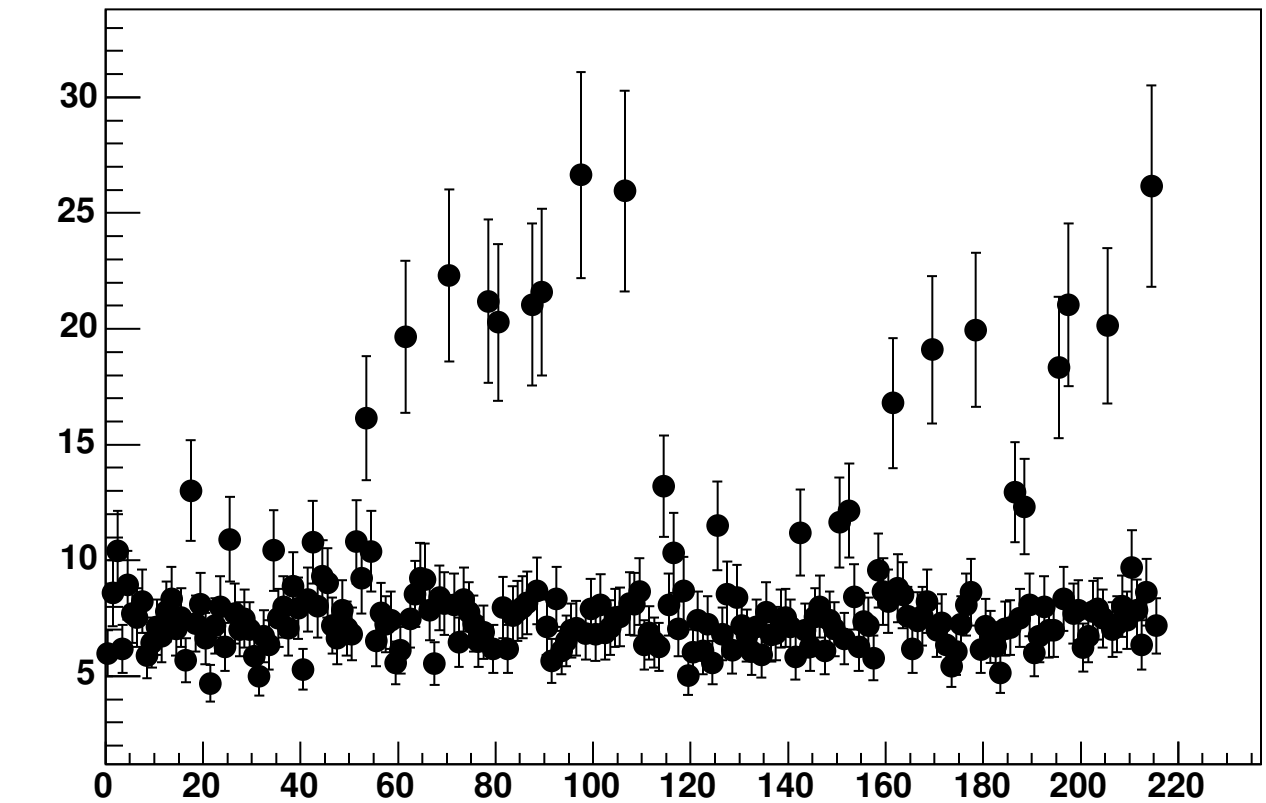
Enable 2, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



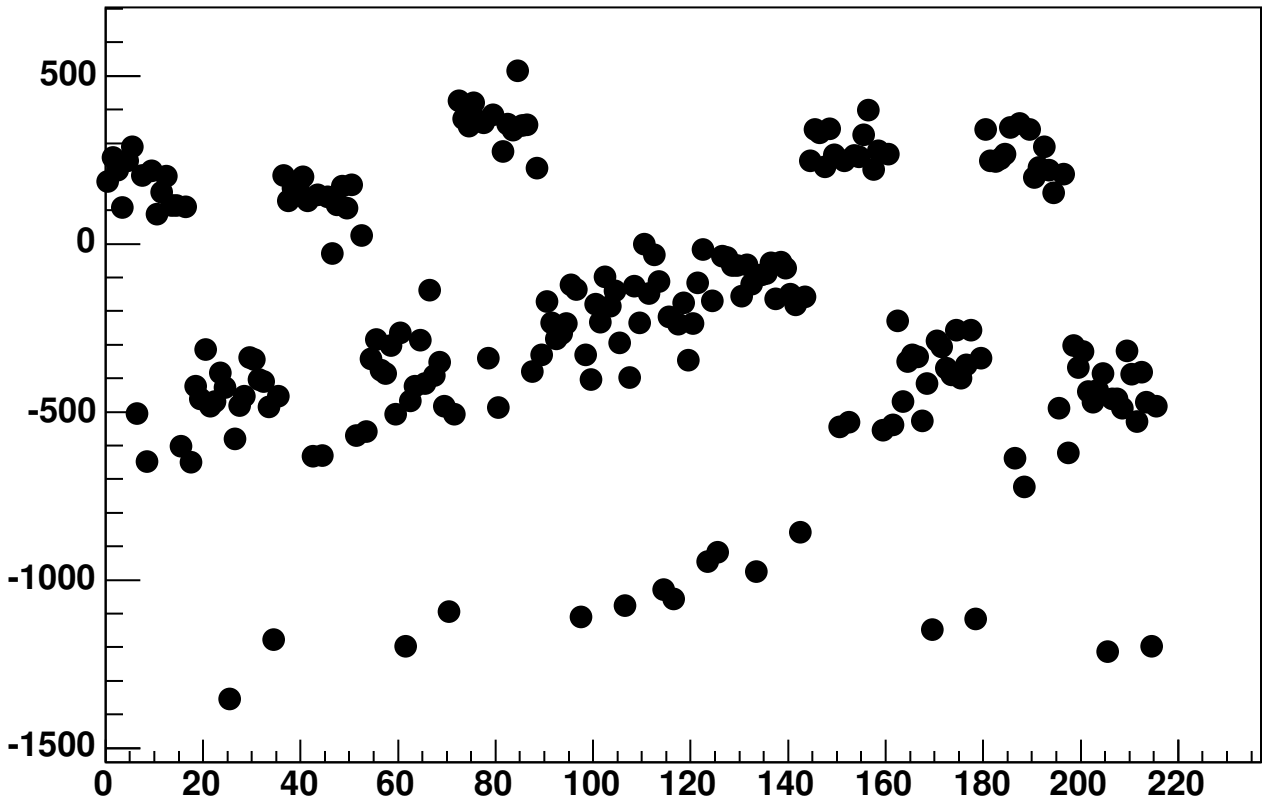
Enable 2, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



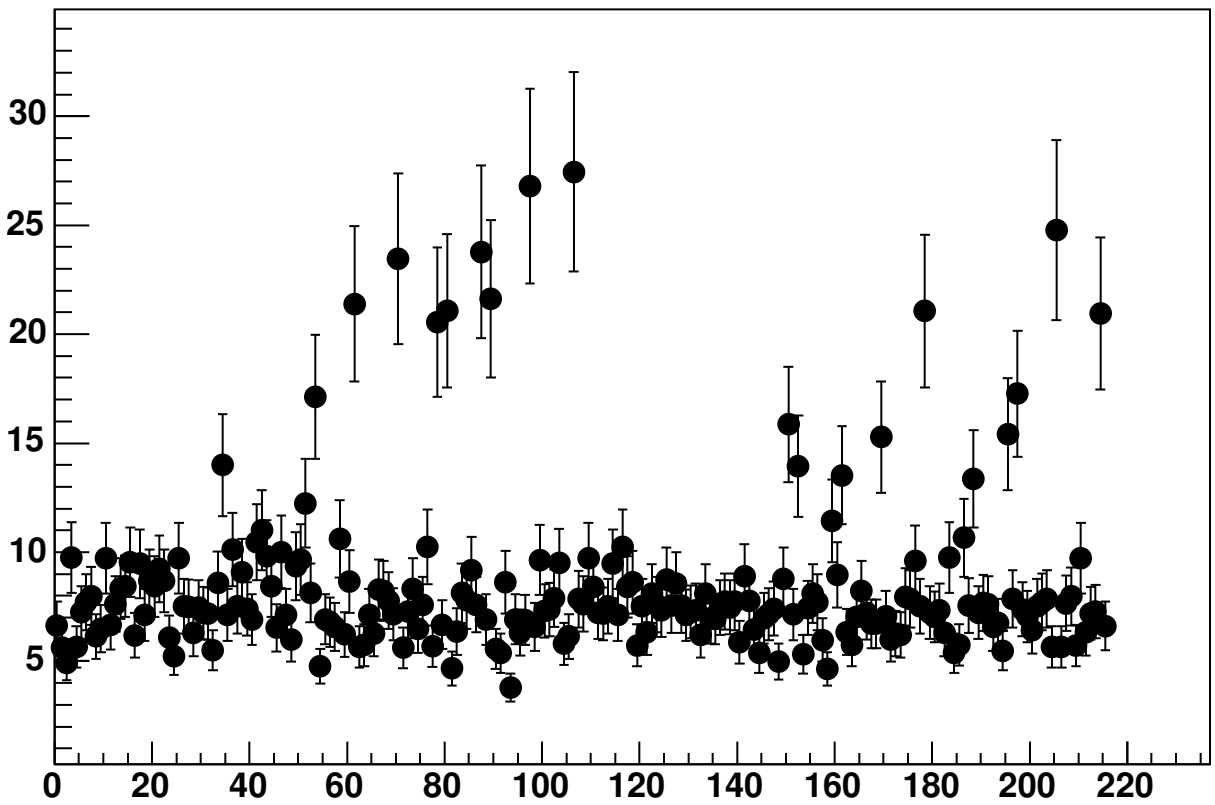
Enable 2, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



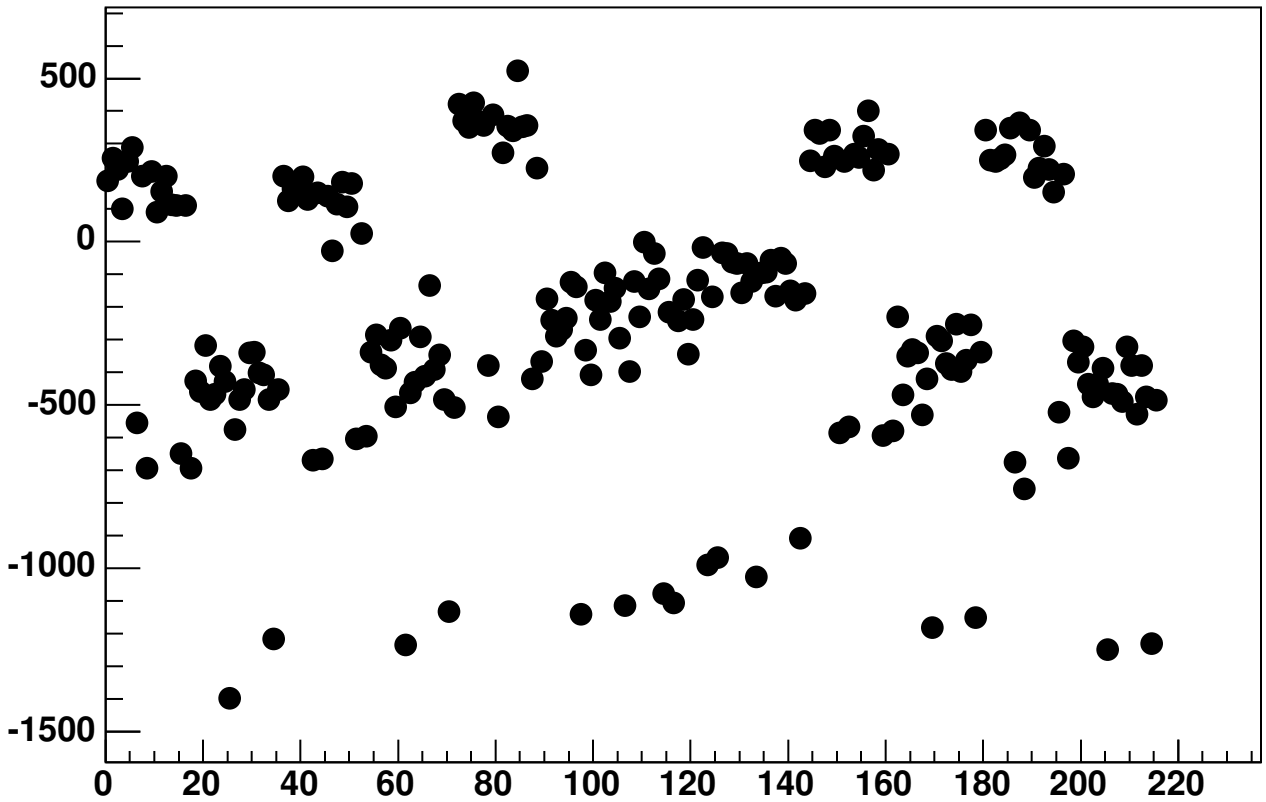
Enable 2, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



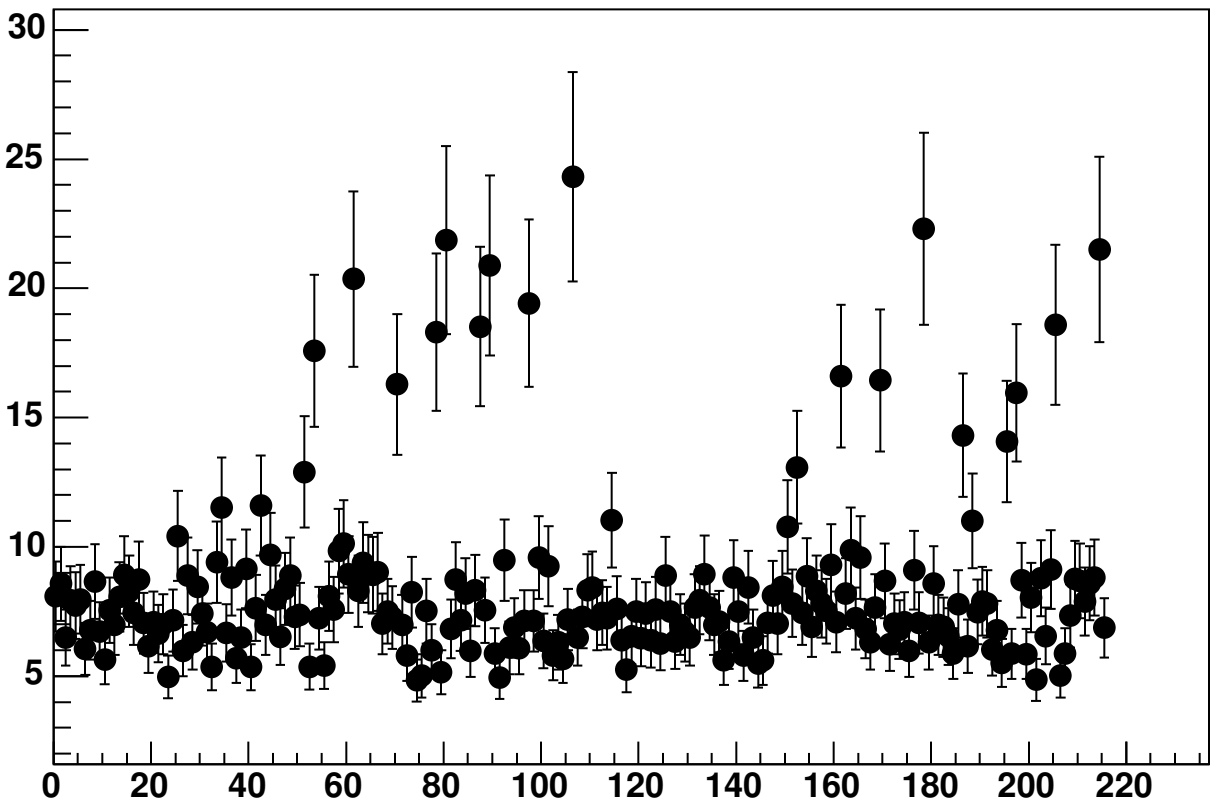
Enable 2, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



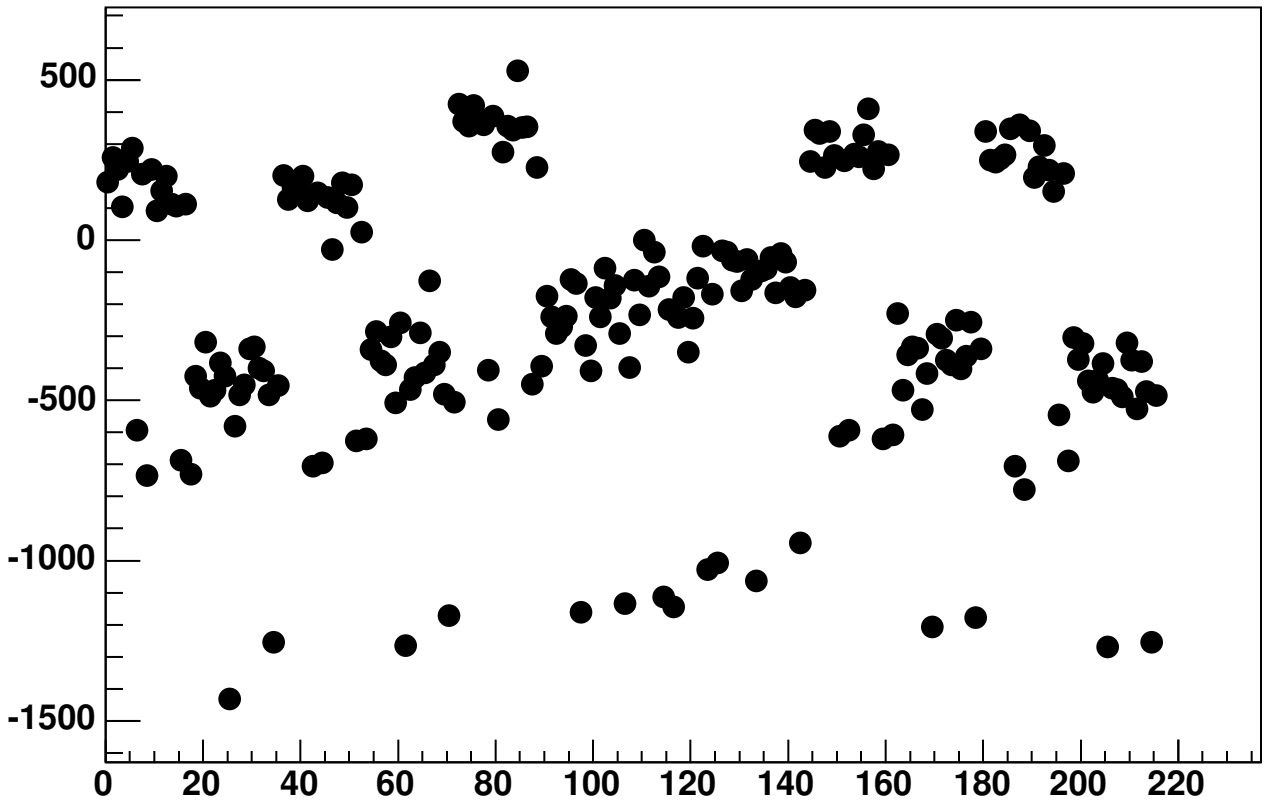
Enable 2, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



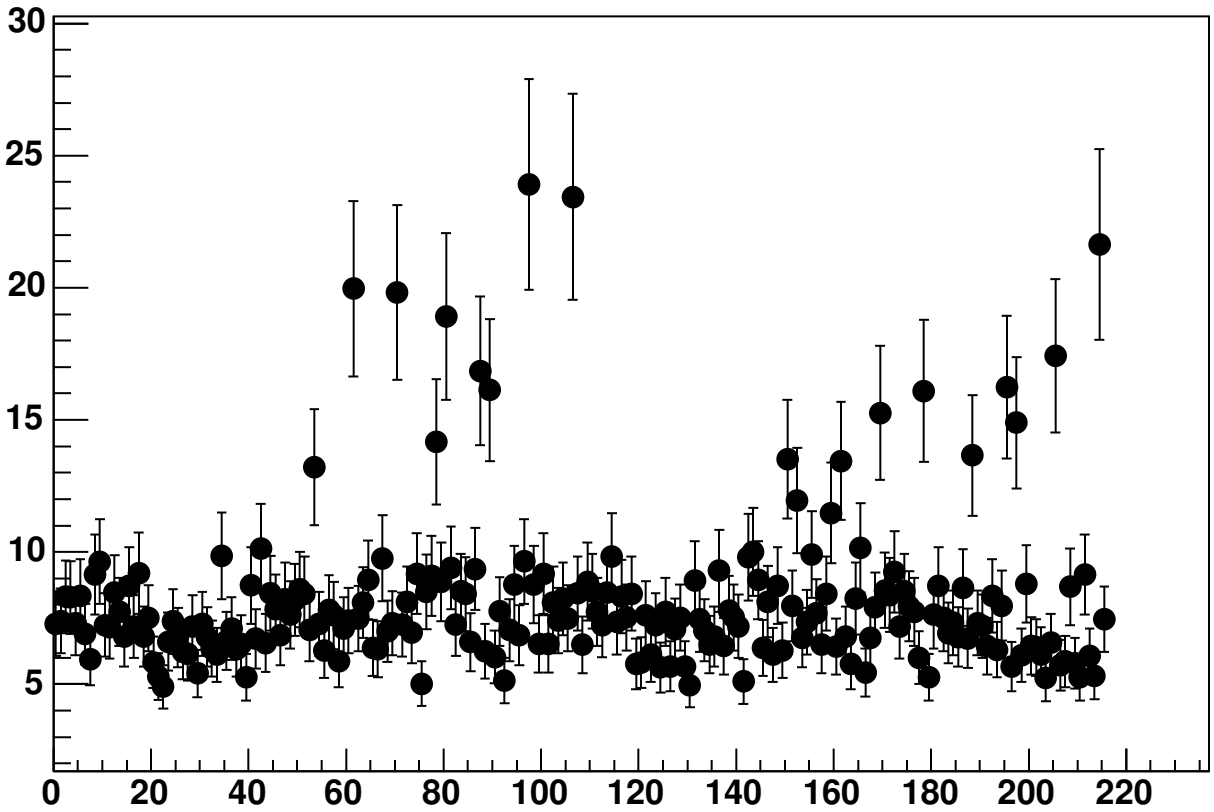
Enable 2, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



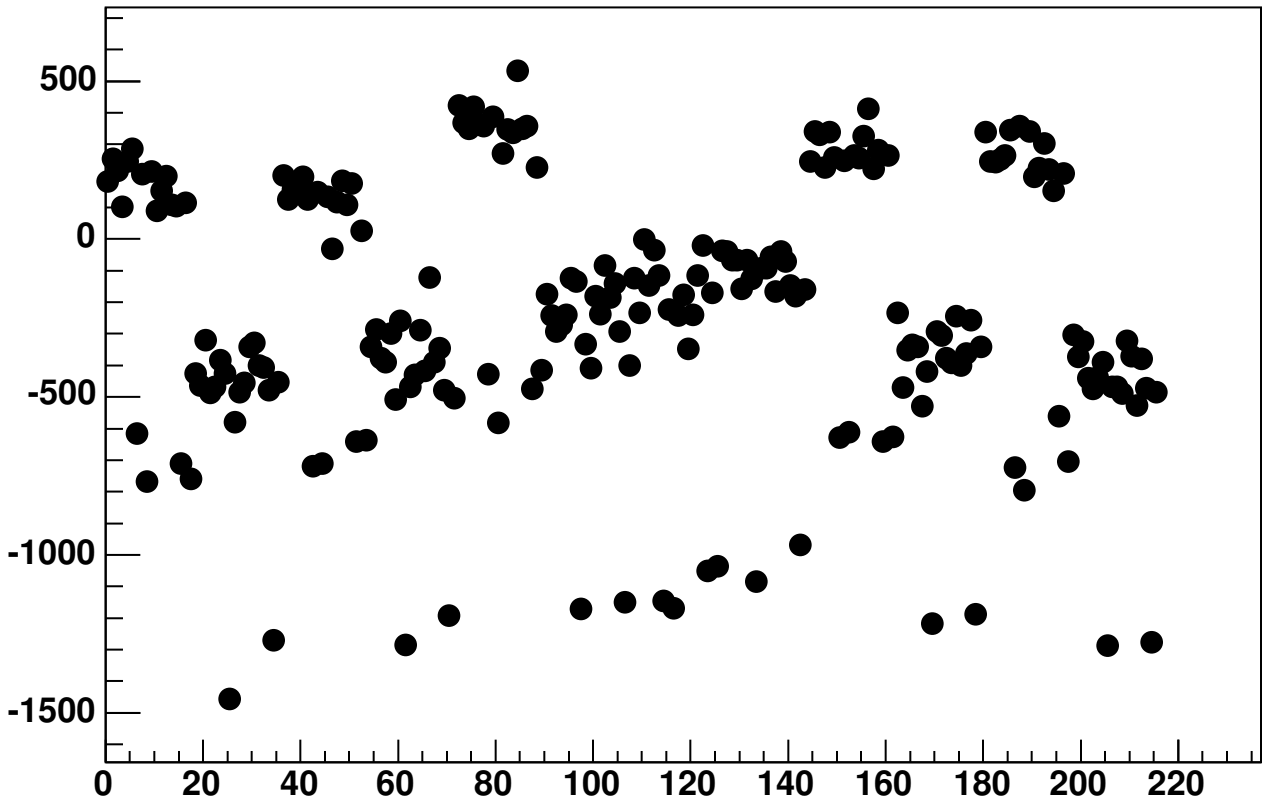
Enable 2, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



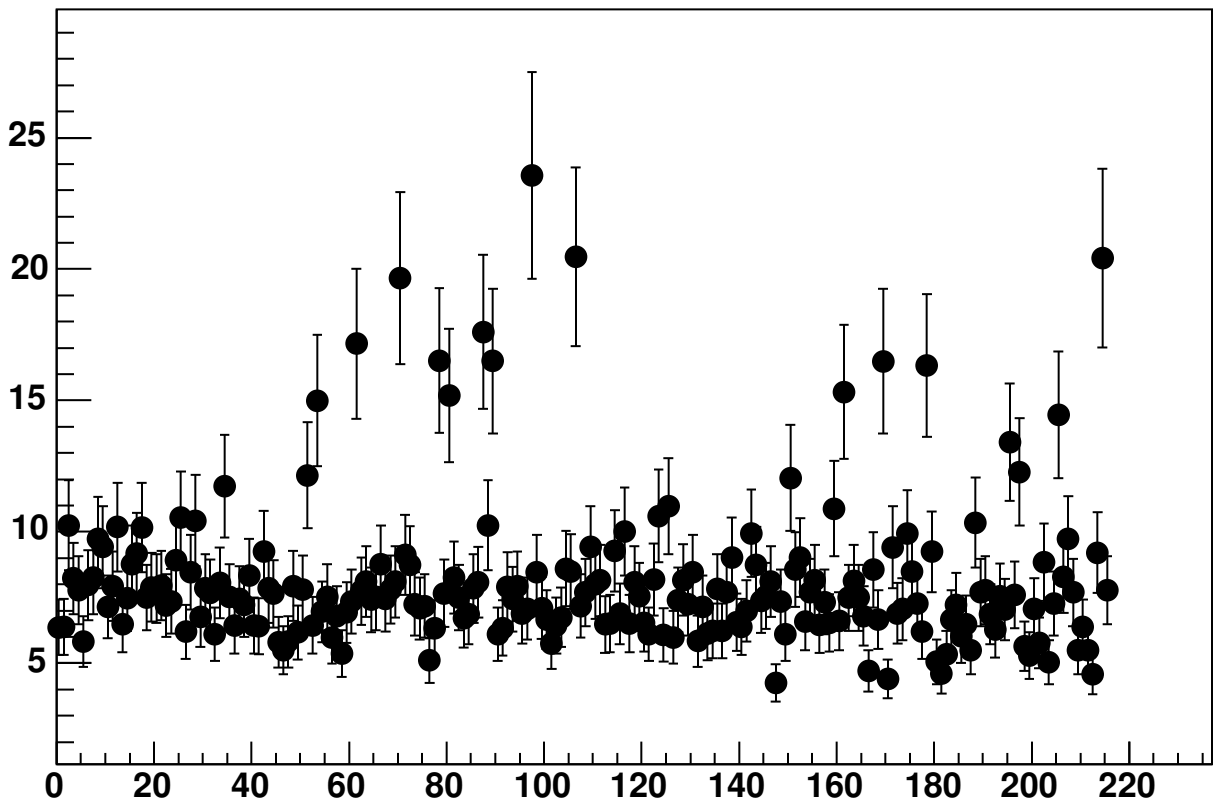
Enable 2, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



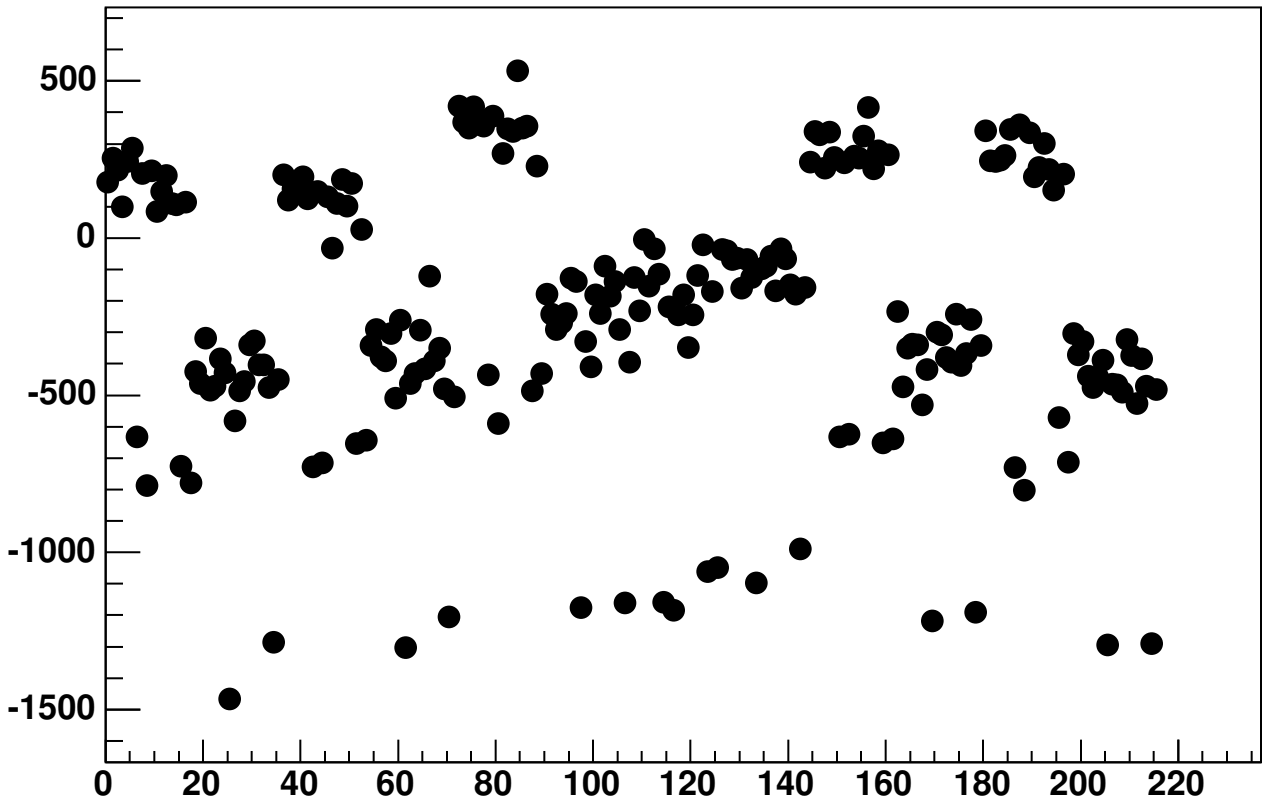
Enable 2, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



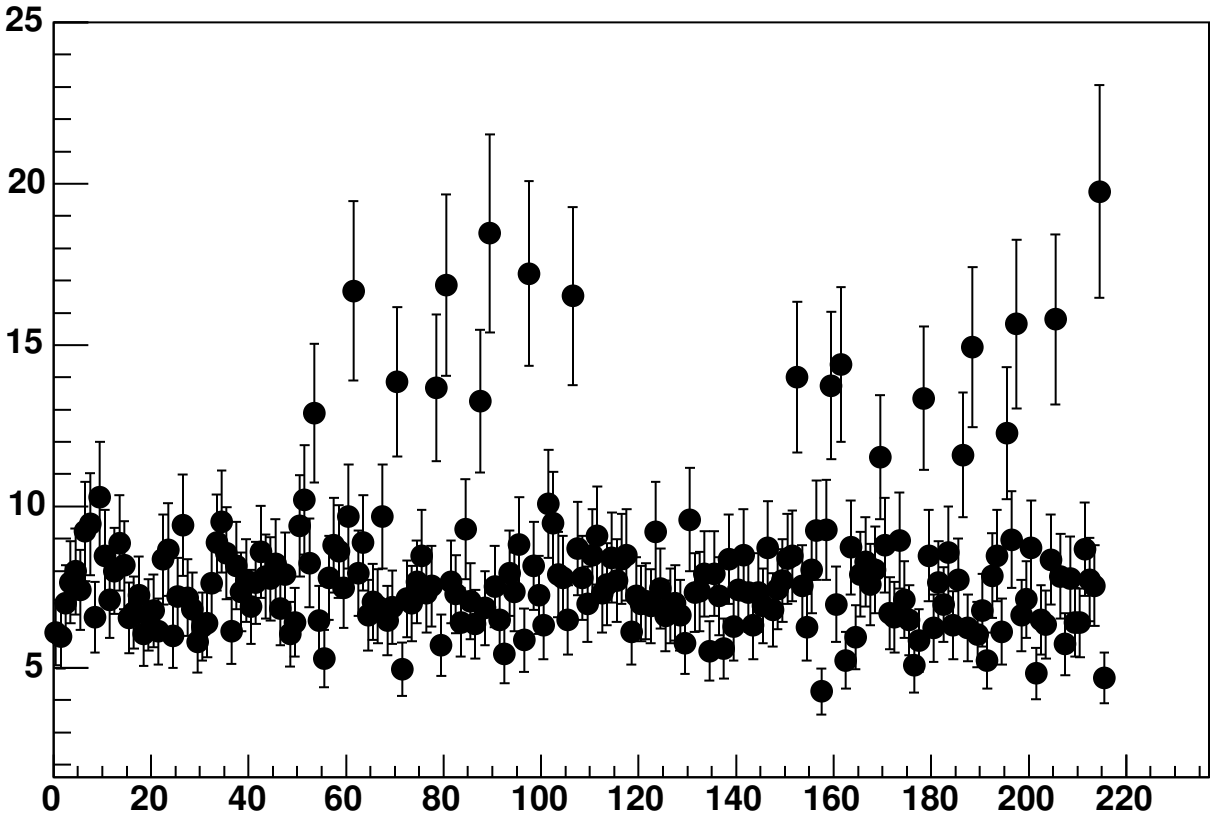
Enable 2, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



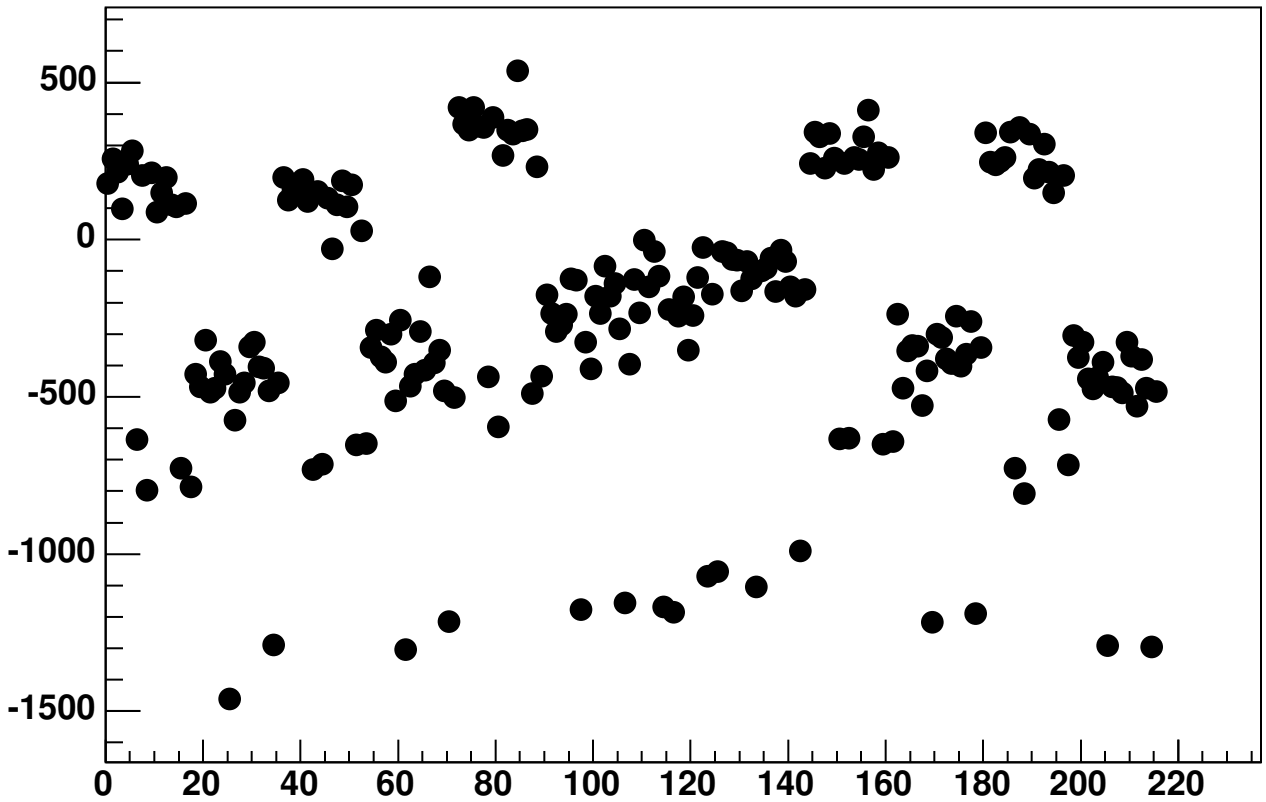
Enable 2, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



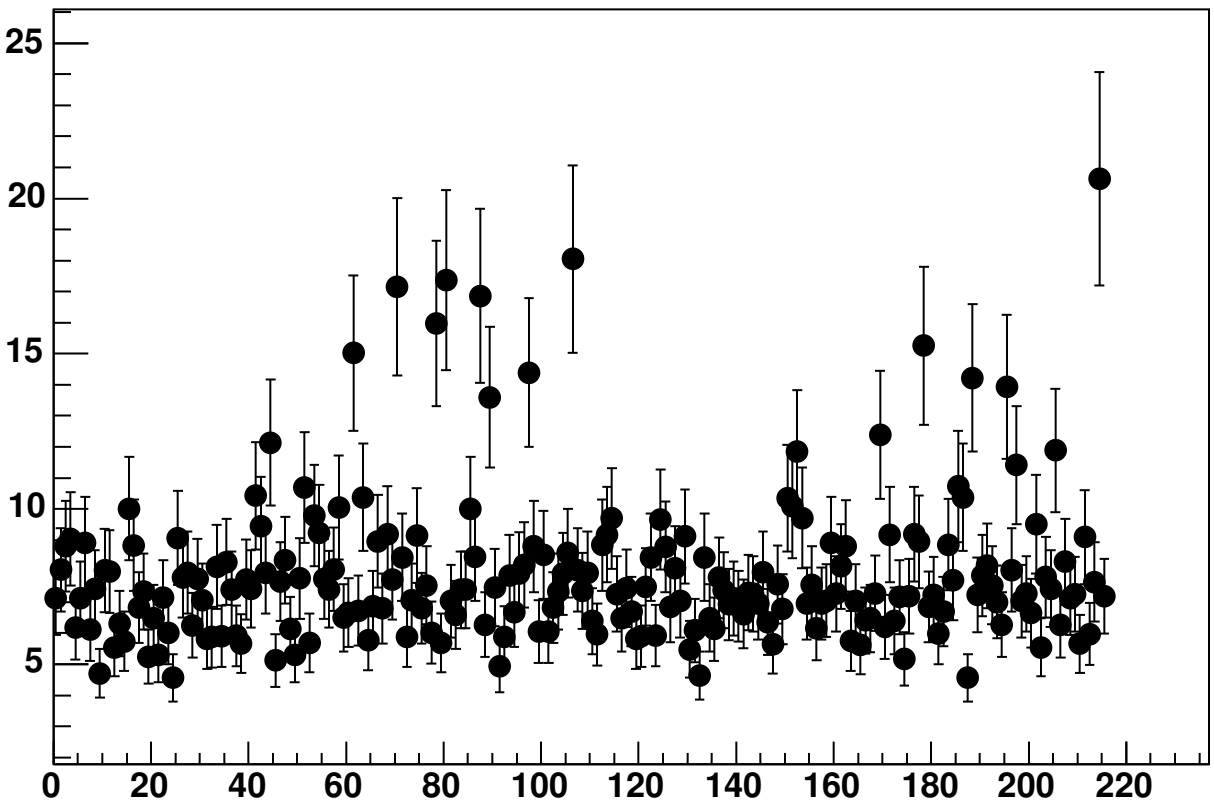
Enable 2, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



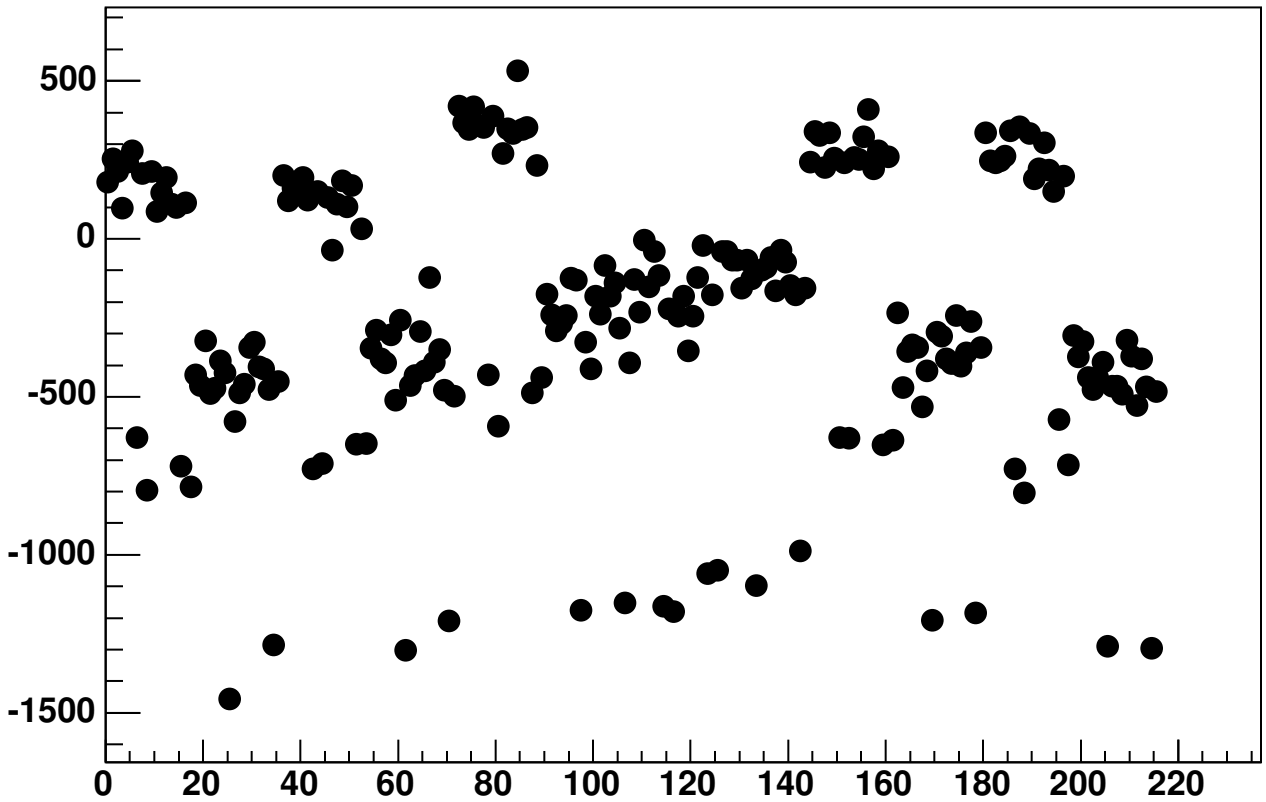
Enable 2, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



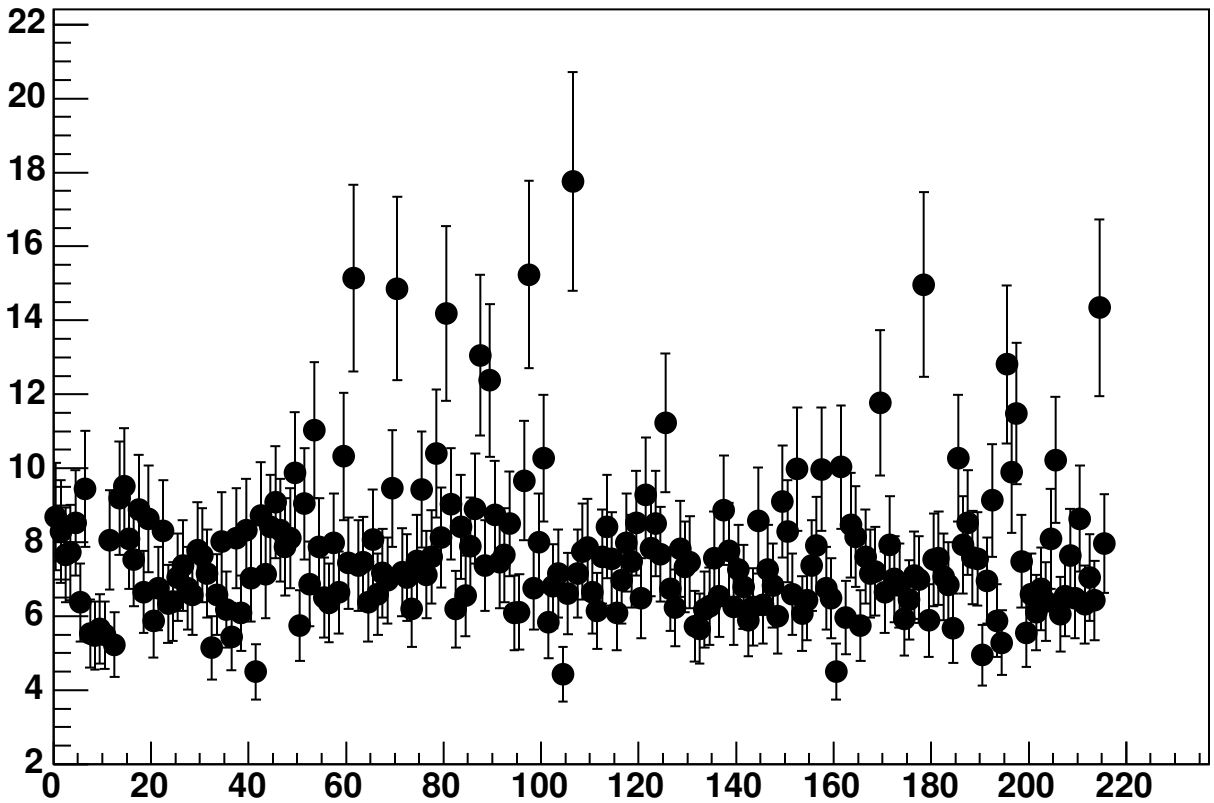
Enable 2, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



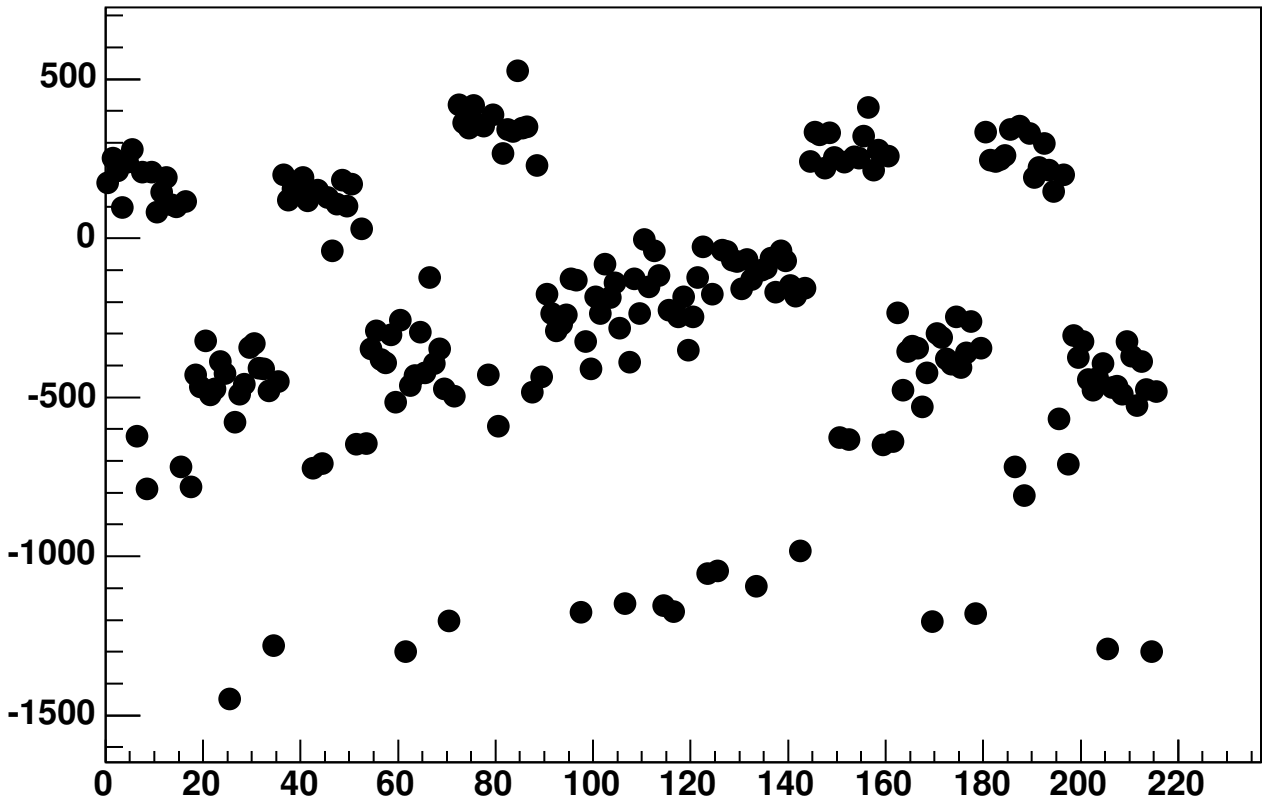
Enable 2, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



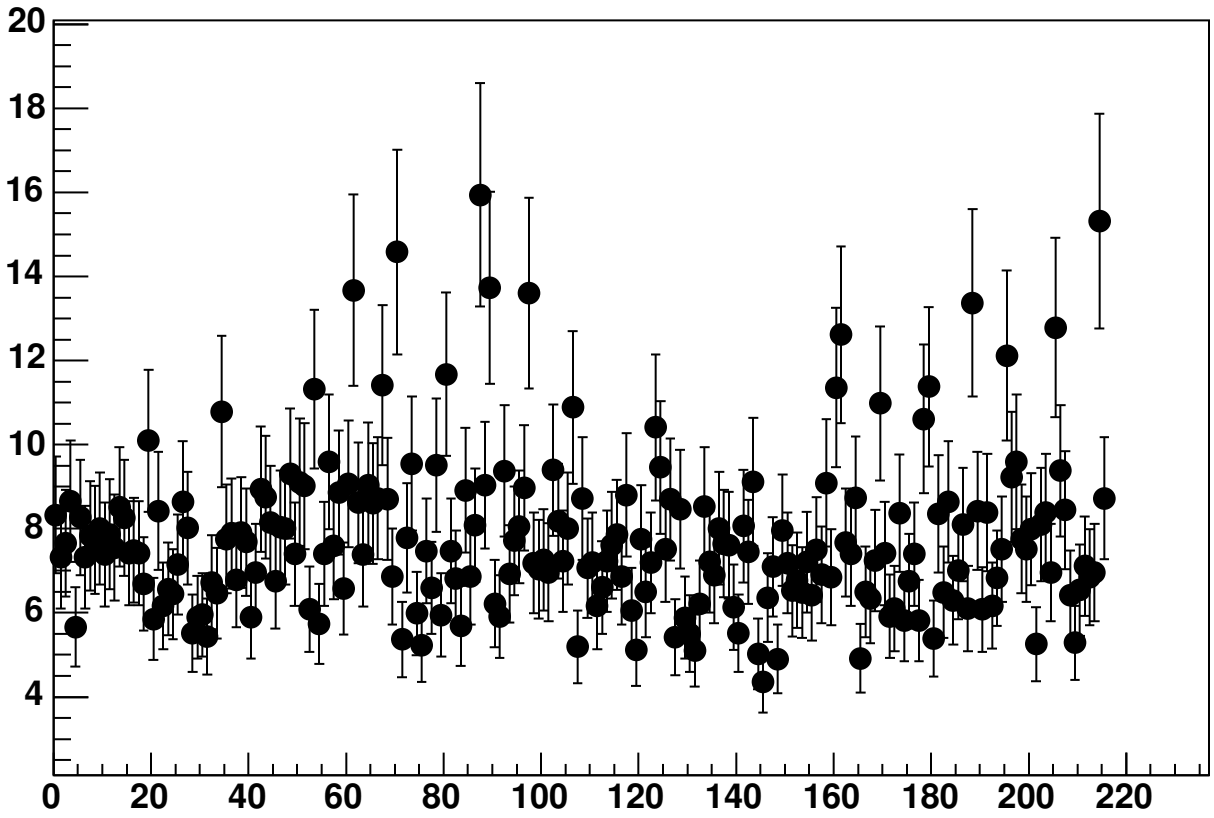
Enable 2, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



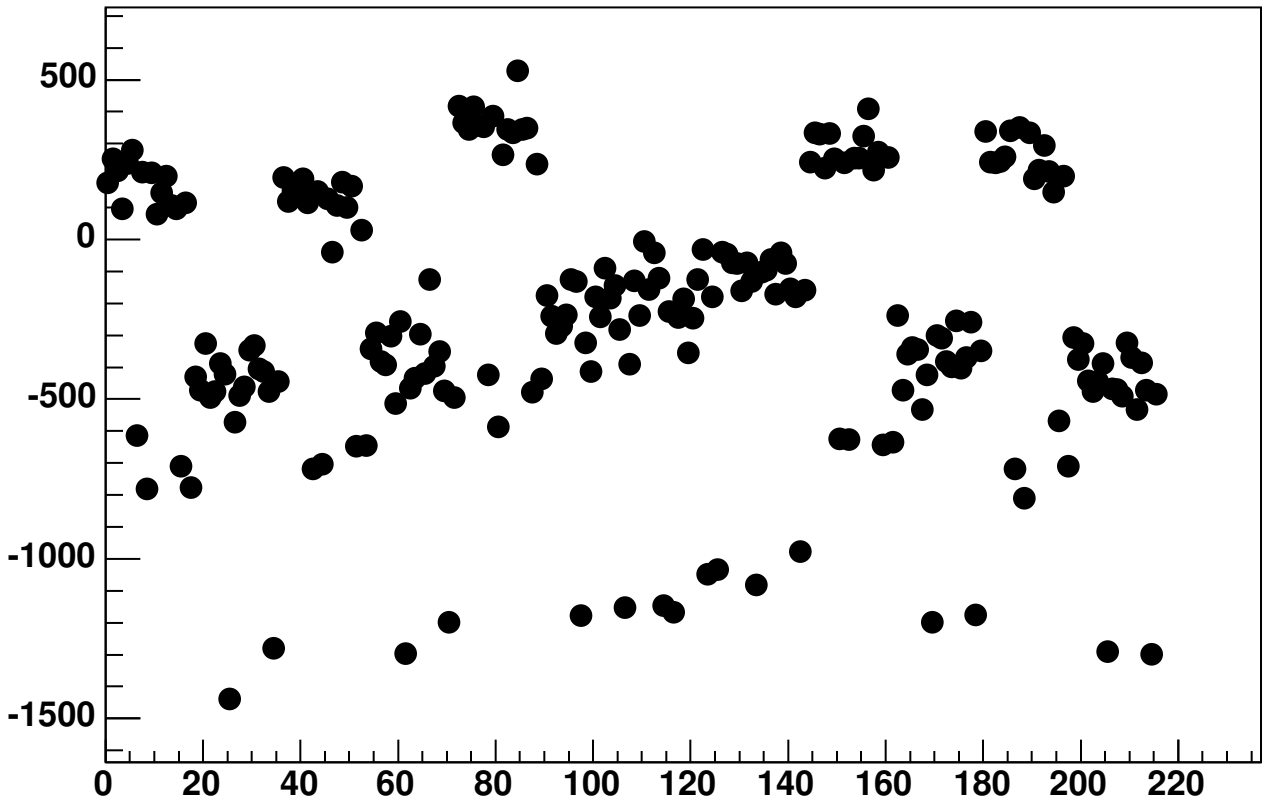
Enable 2, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



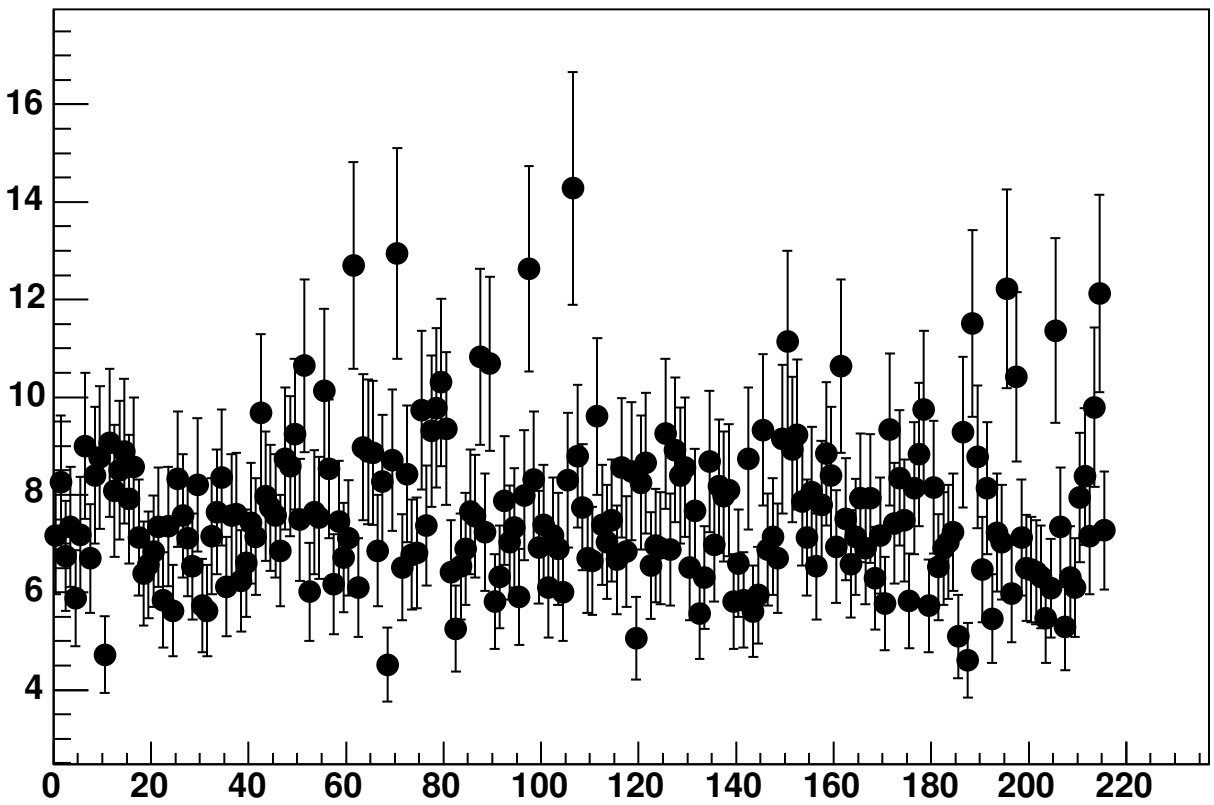
Enable 2, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan



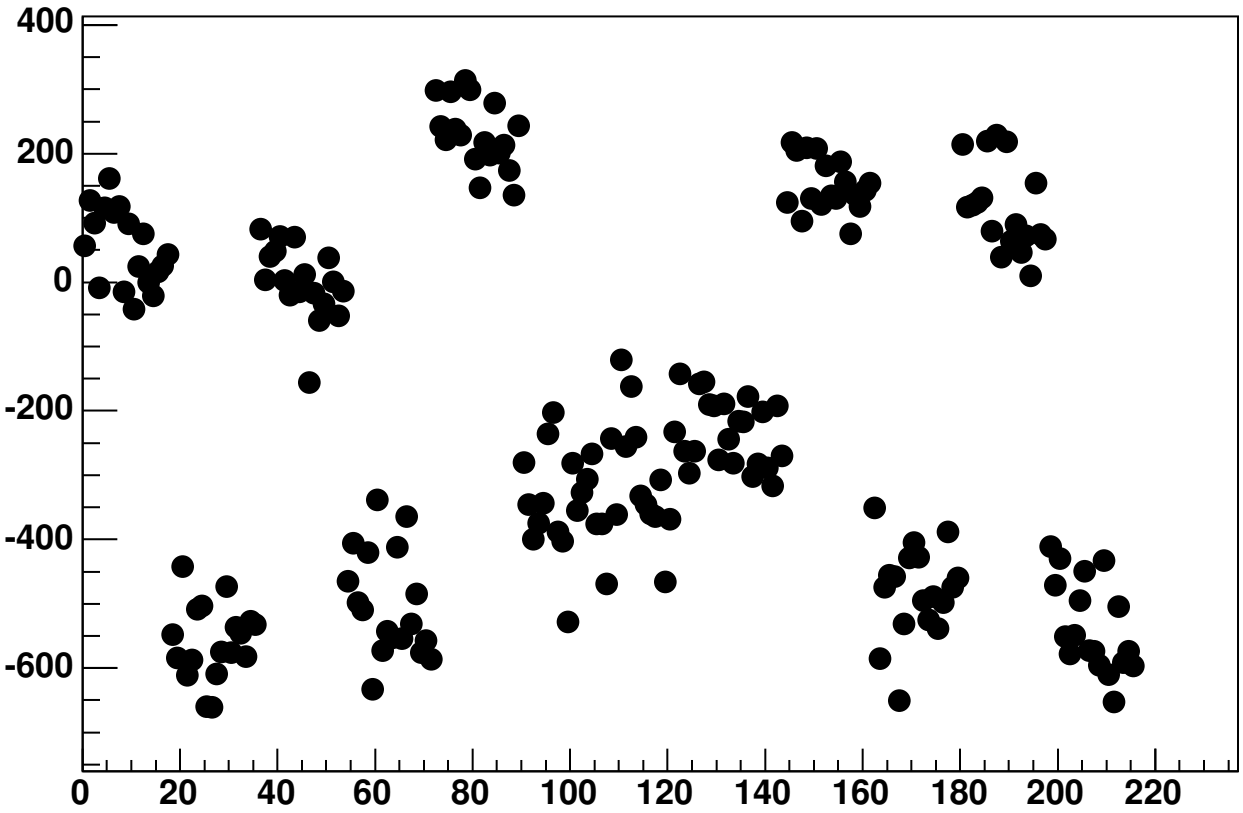
Enable 2, DAC=4000, Hold=245, ADC Mean vs 18*Chip+Chan



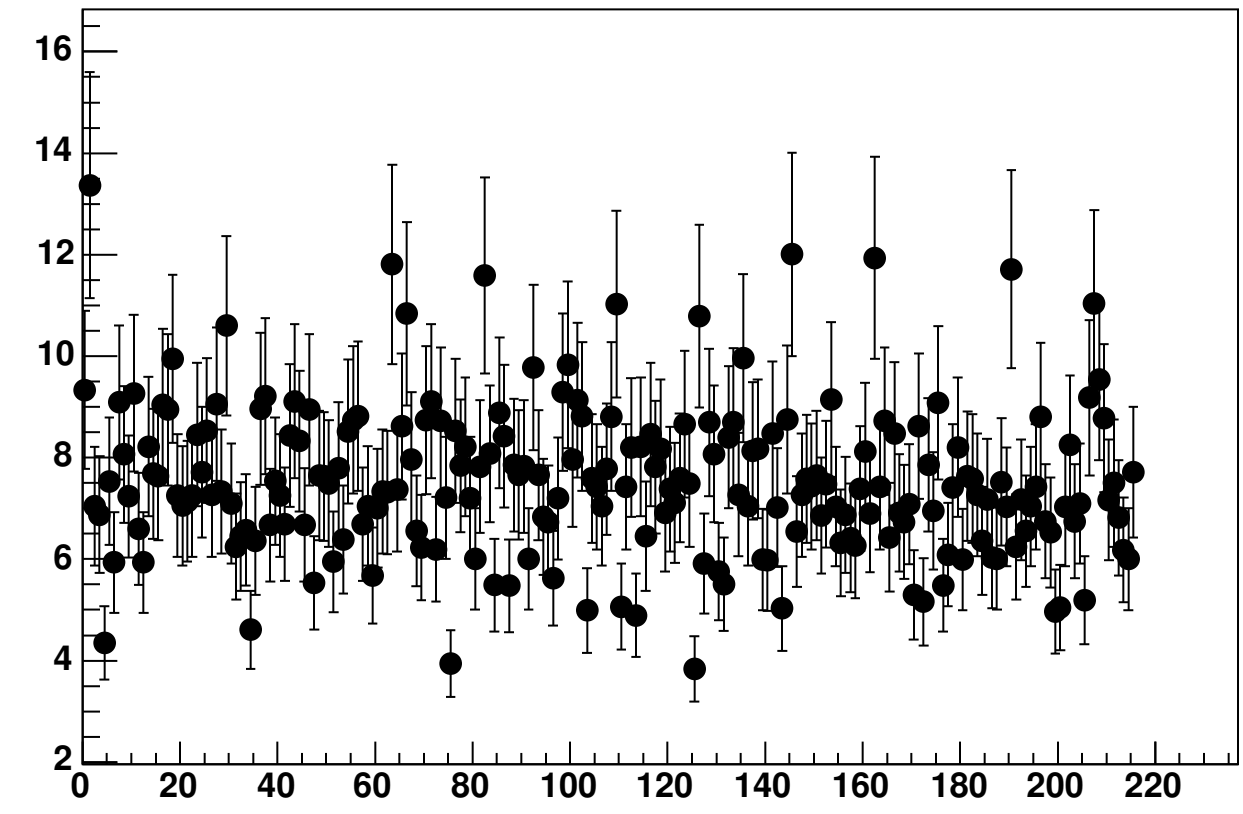
Enable 2, DAC=4000, Hold=245, ADC Noise vs 18*Chip+Chan



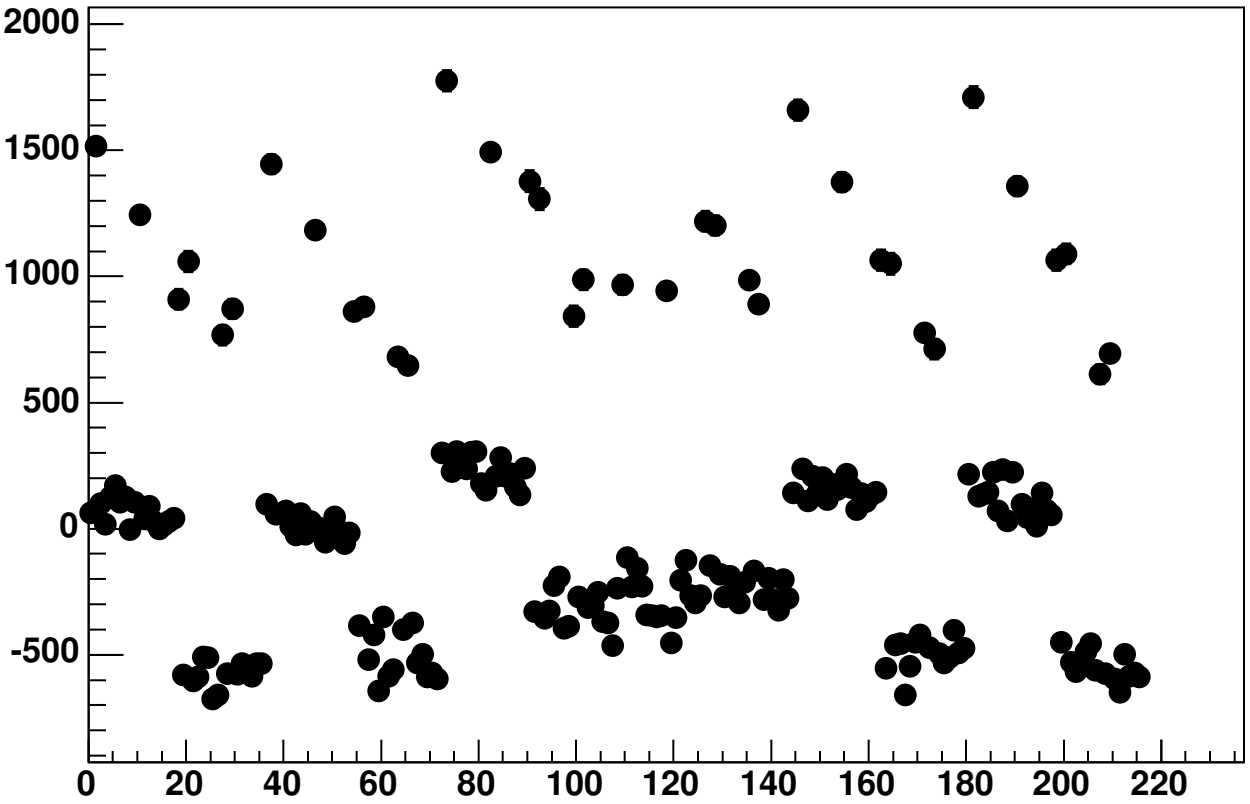
Enable 3, DAC=4000, Hold=0, ADC Mean vs 18*Chip+Chan



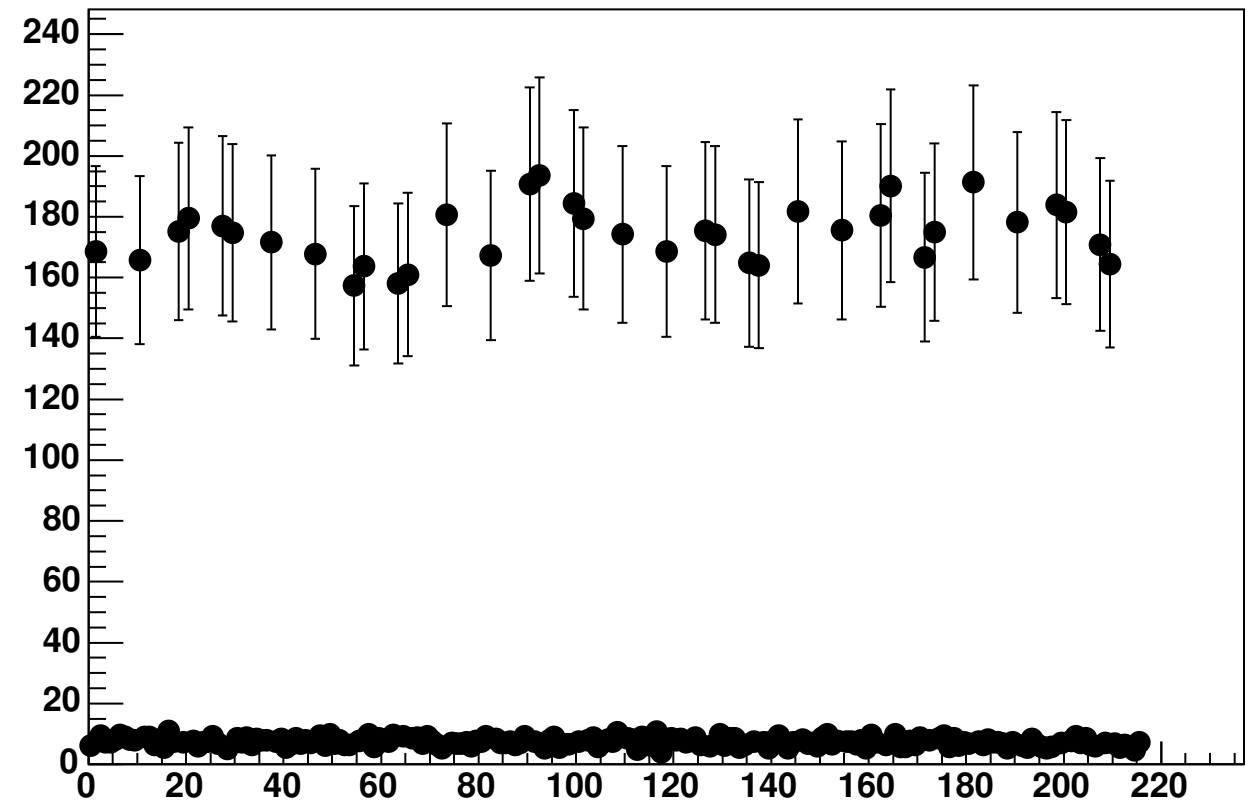
Enable 3, DAC=4000, Hold=0, ADC Noise vs 18*Chip+Chan



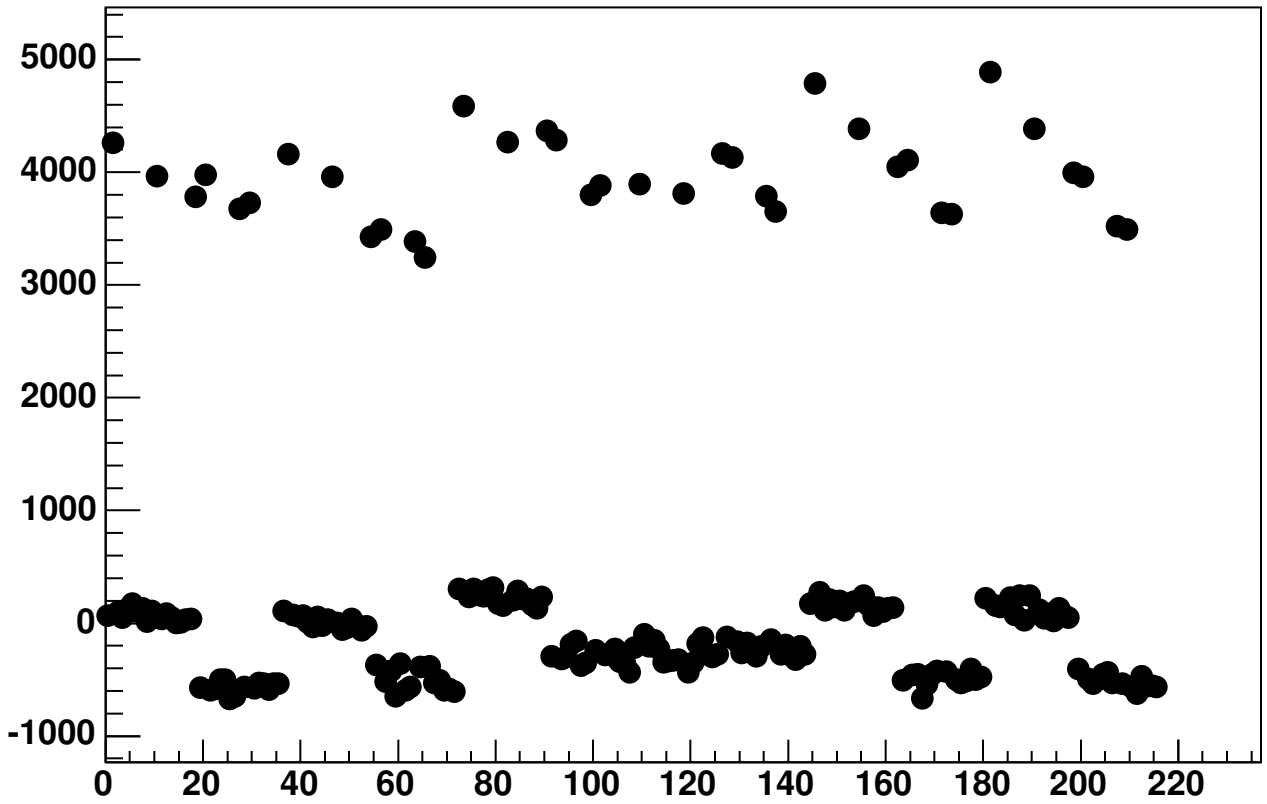
Enable 3, DAC=4000, Hold=5, ADC Mean vs 18*Chip+Chan



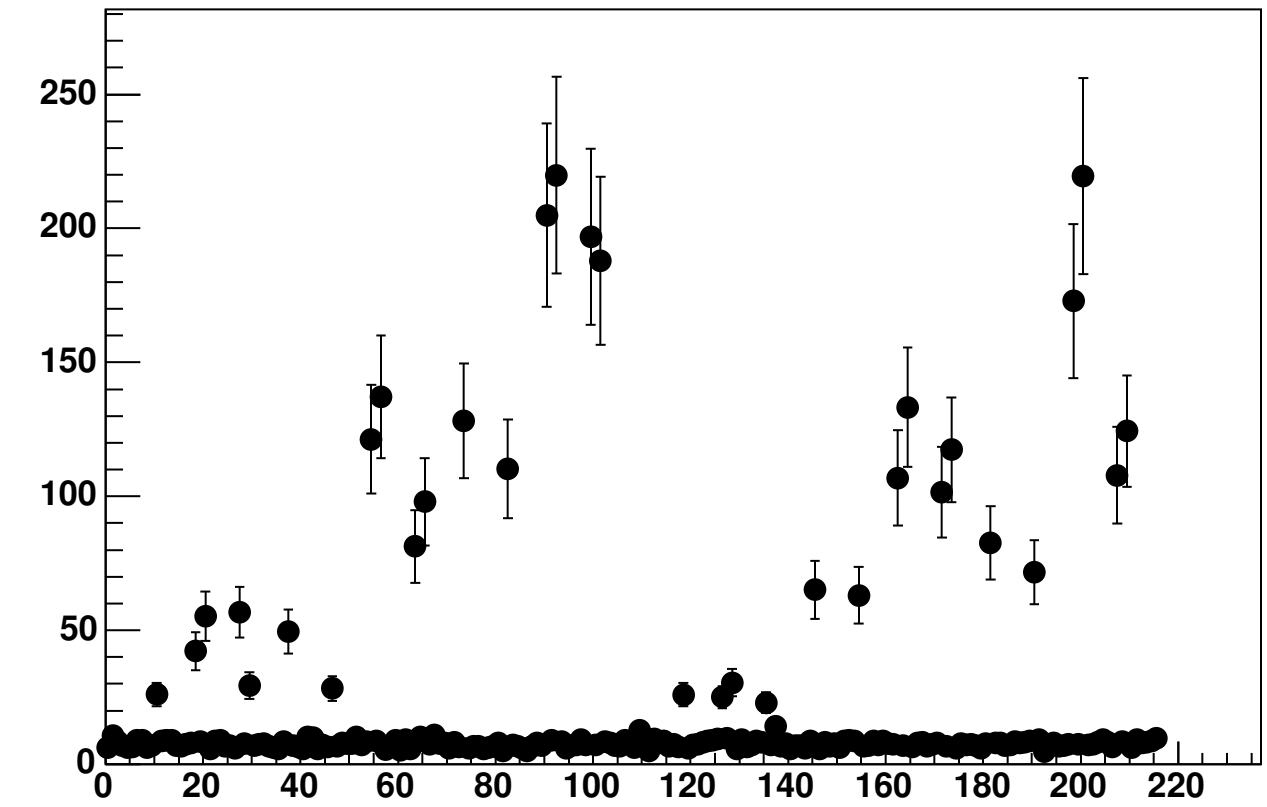
Enable 3, DAC=4000, Hold=5, ADC Noise vs 18*Chip+Chan



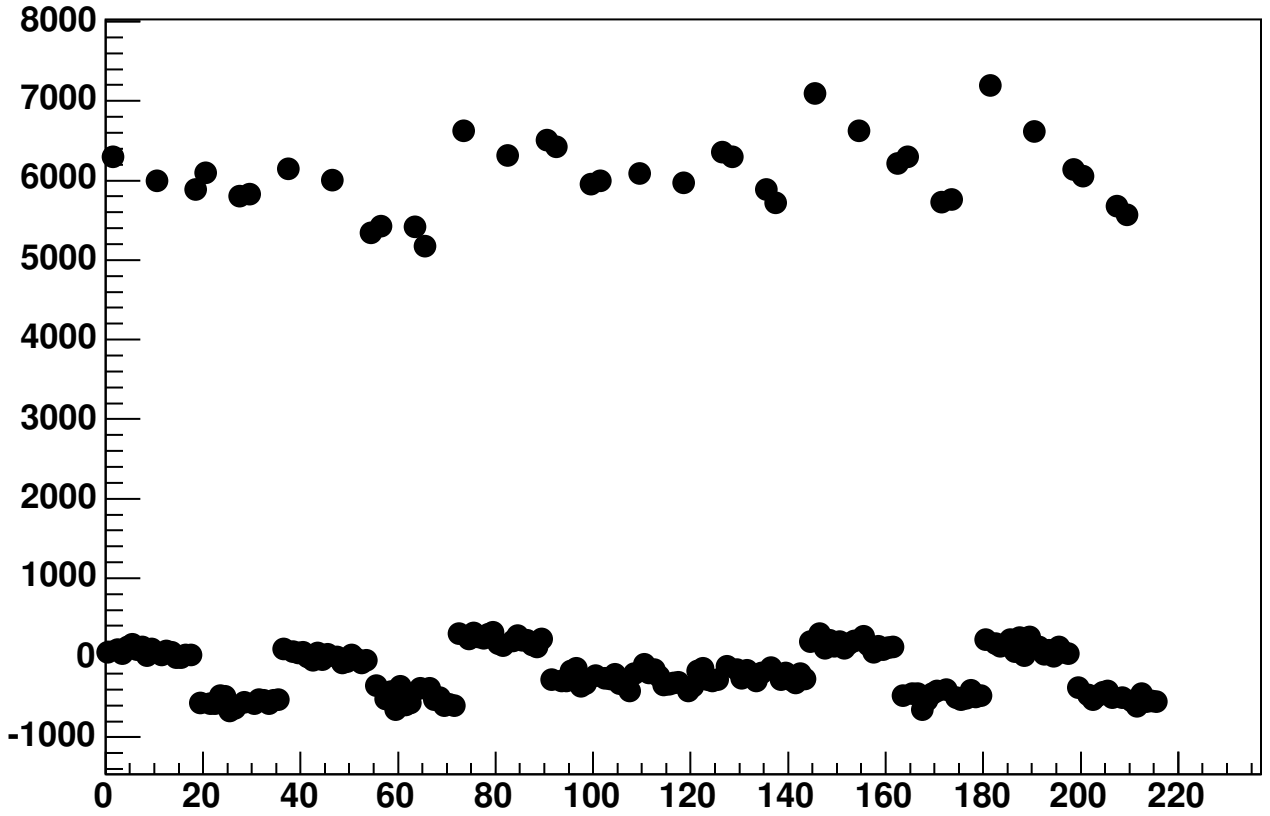
Enable 3, DAC=4000, Hold=10, ADC Mean vs 18*Chip+Chan



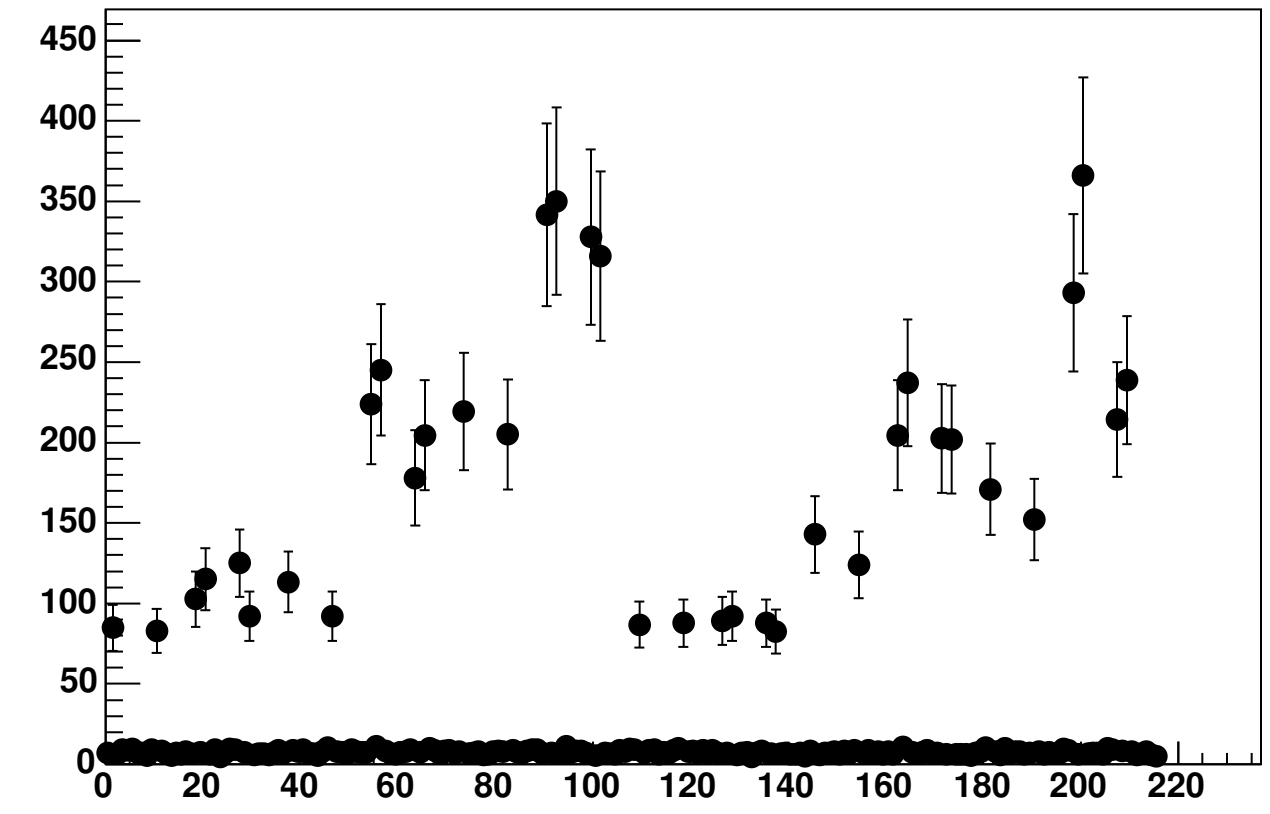
Enable 3, DAC=4000, Hold=10, ADC Noise vs 18*Chip+Chan



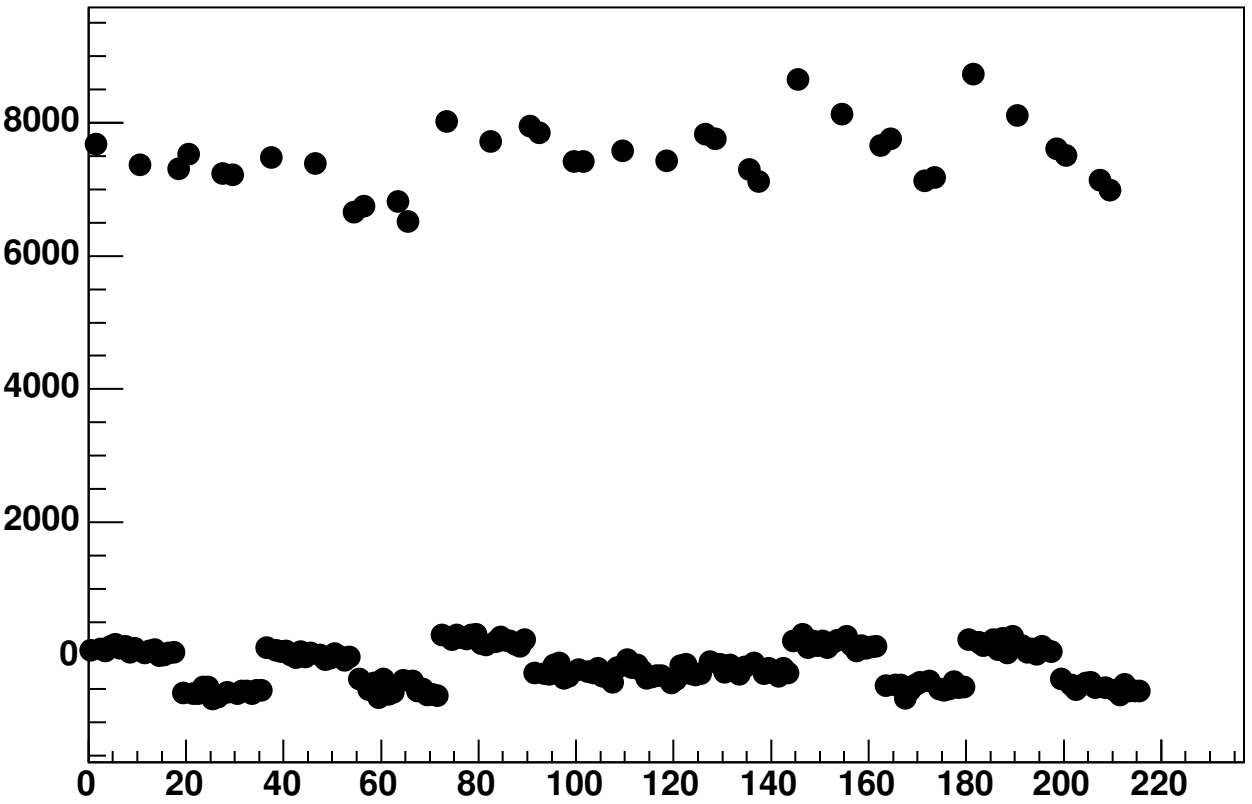
Enable 3, DAC=4000, Hold=15, ADC Mean vs 18*Chip+Chan



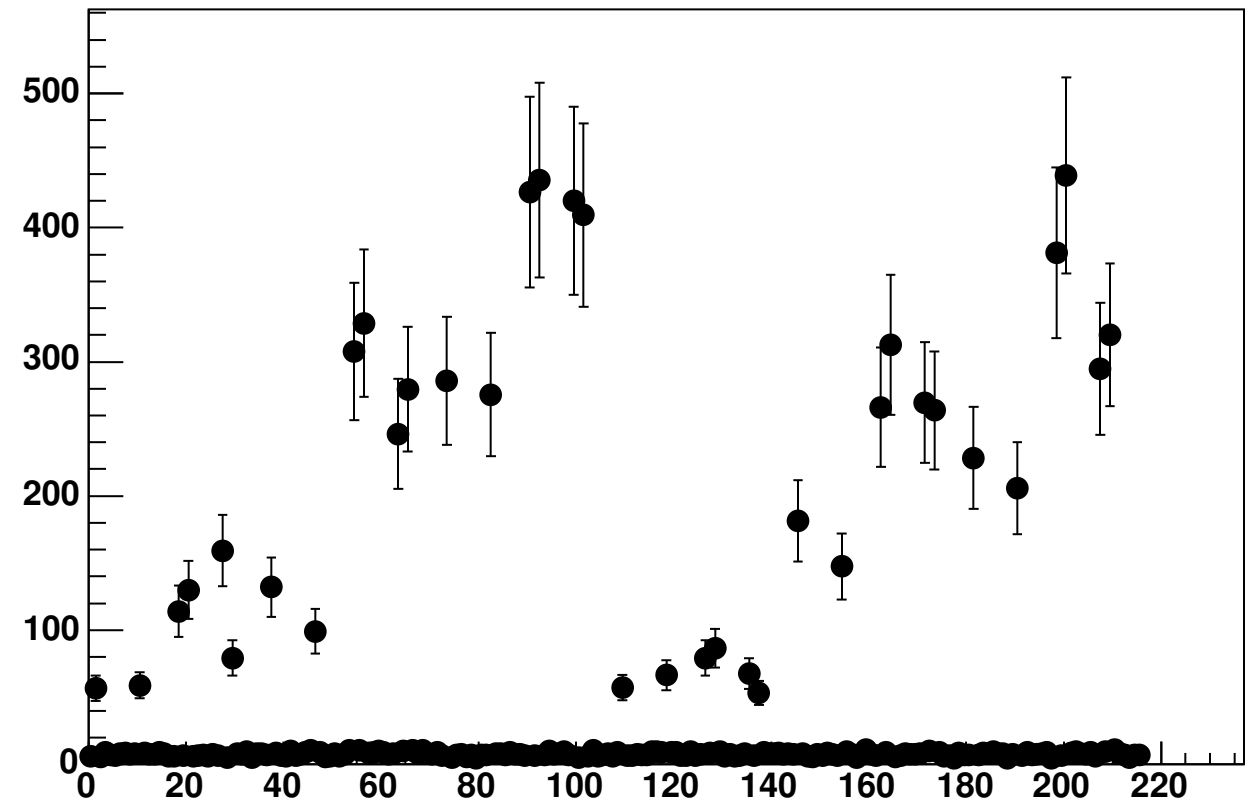
Enable 3, DAC=4000, Hold=15, ADC Noise vs 18*Chip+Chan



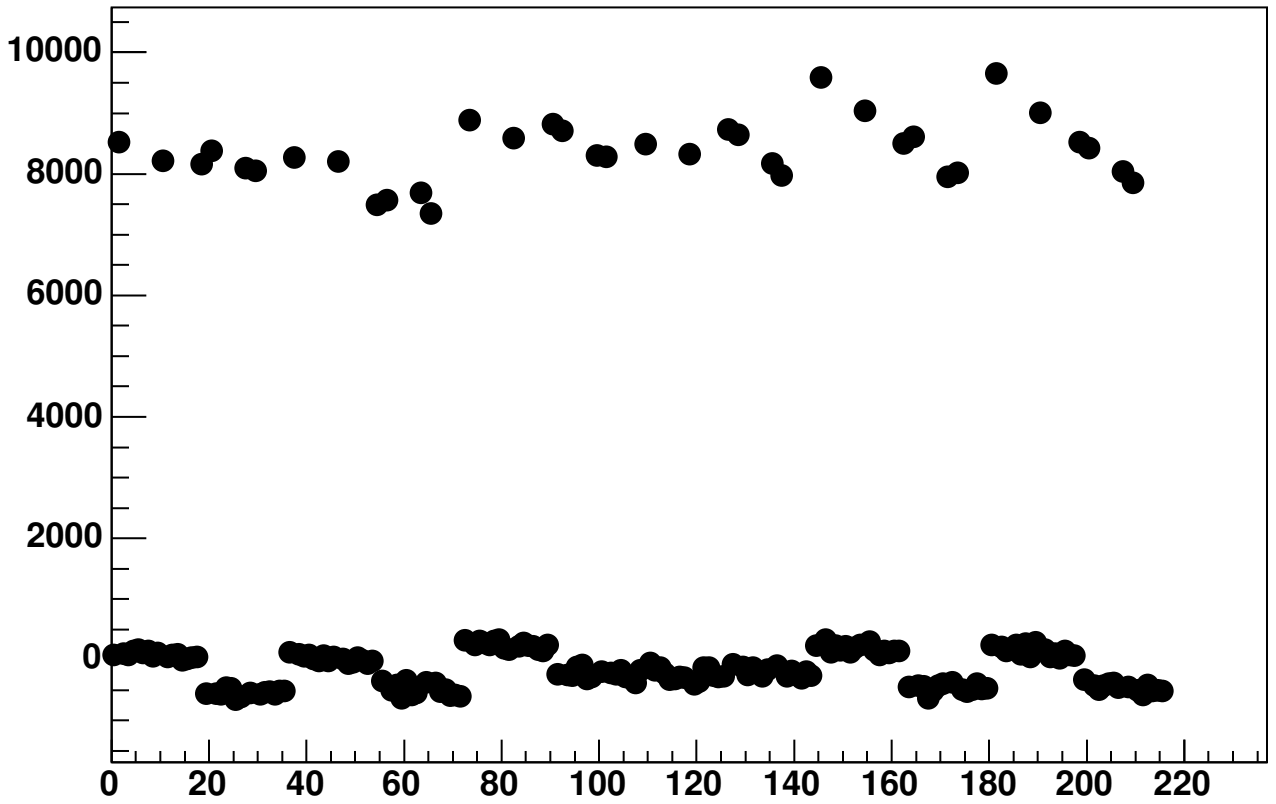
Enable 3, DAC=4000, Hold=20, ADC Mean vs 18*Chip+Chan



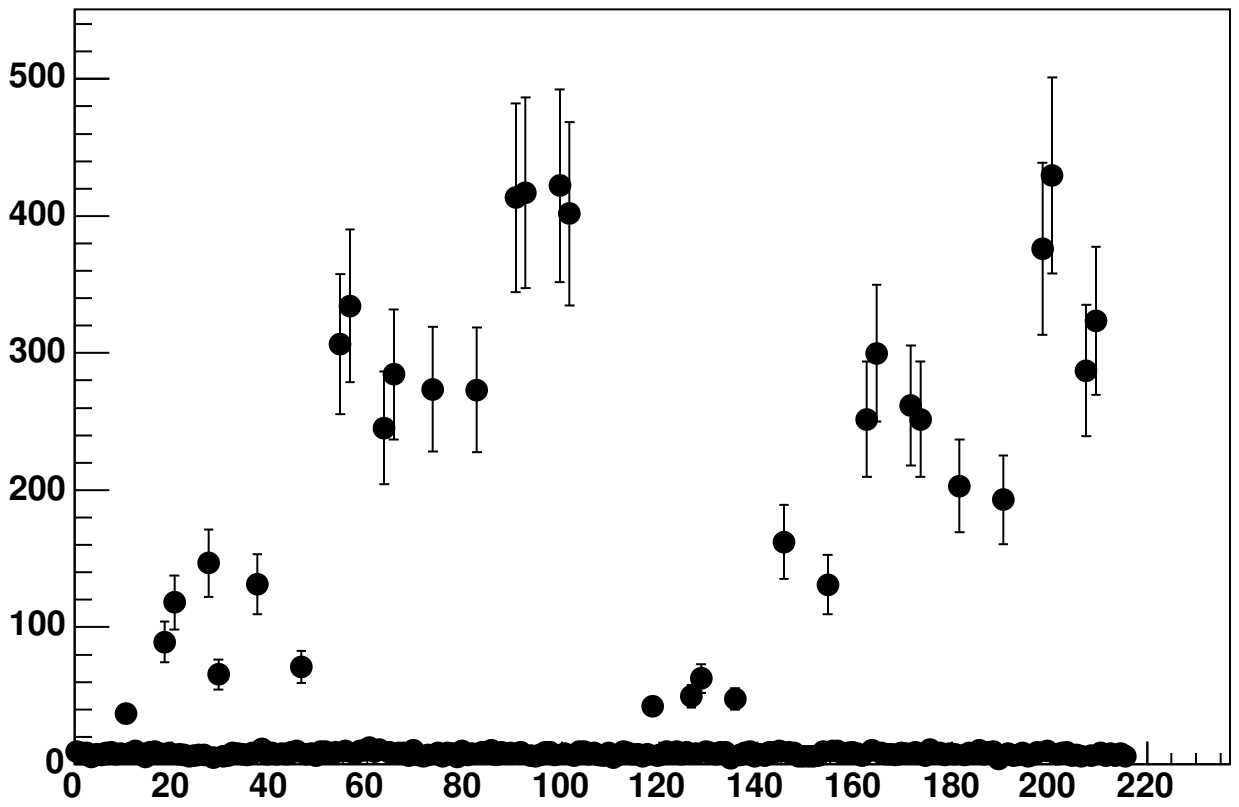
Enable 3, DAC=4000, Hold=20, ADC Noise vs 18*Chip+Chan



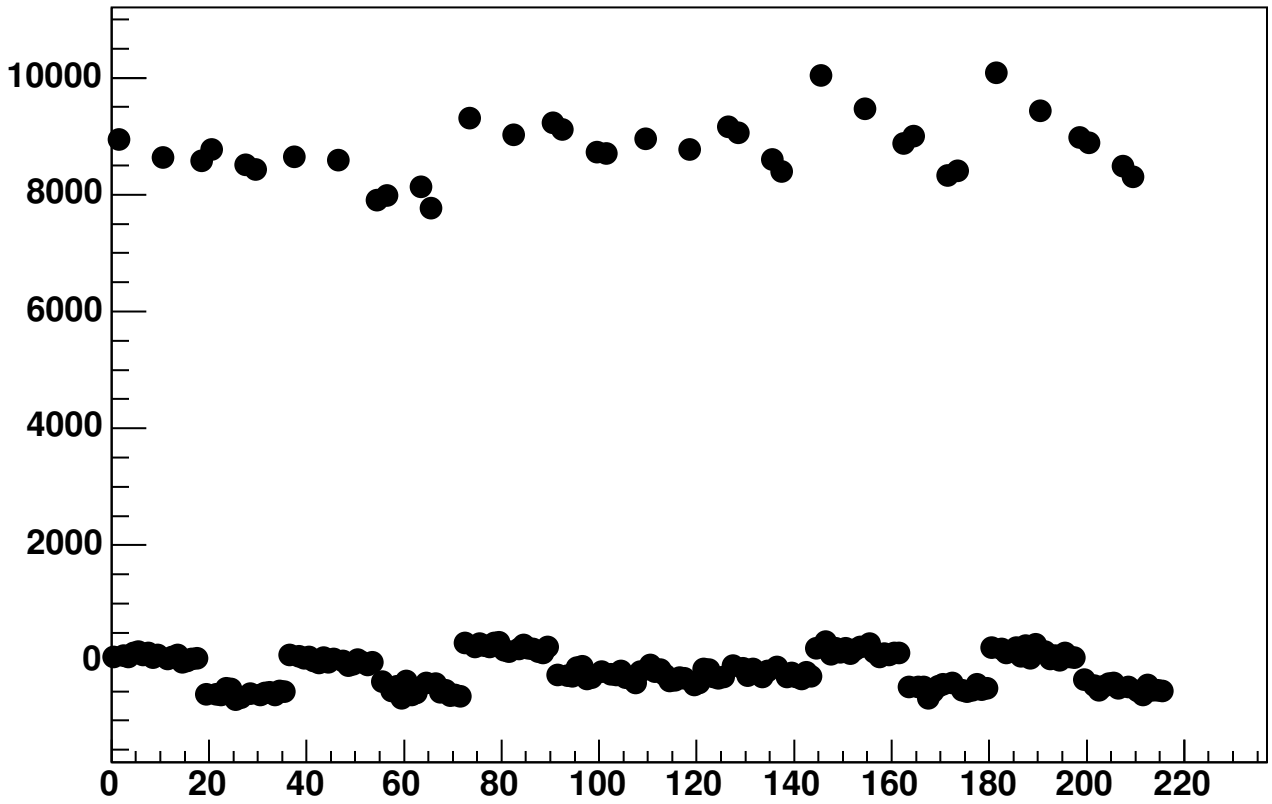
Enable 3, DAC=4000, Hold=25, ADC Mean vs 18*Chip+Chan



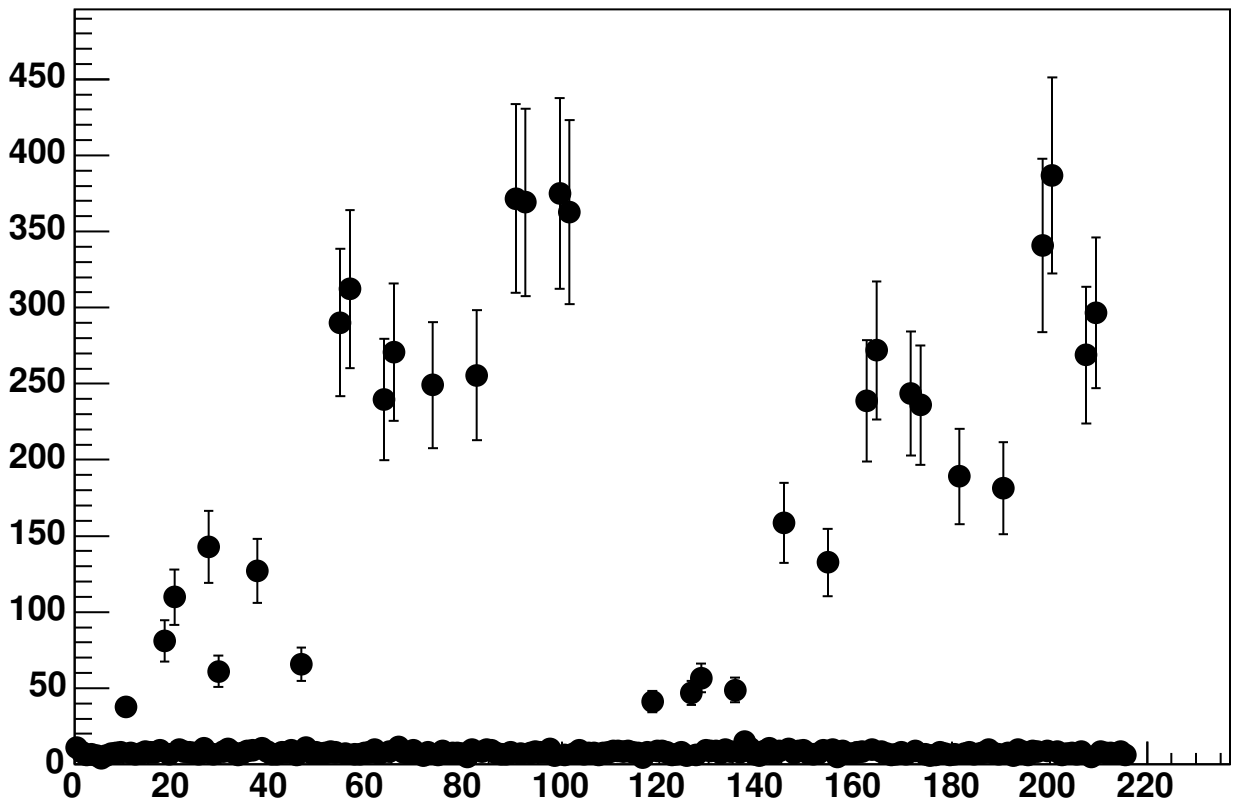
Enable 3, DAC=4000, Hold=25, ADC Noise vs 18*Chip+Chan



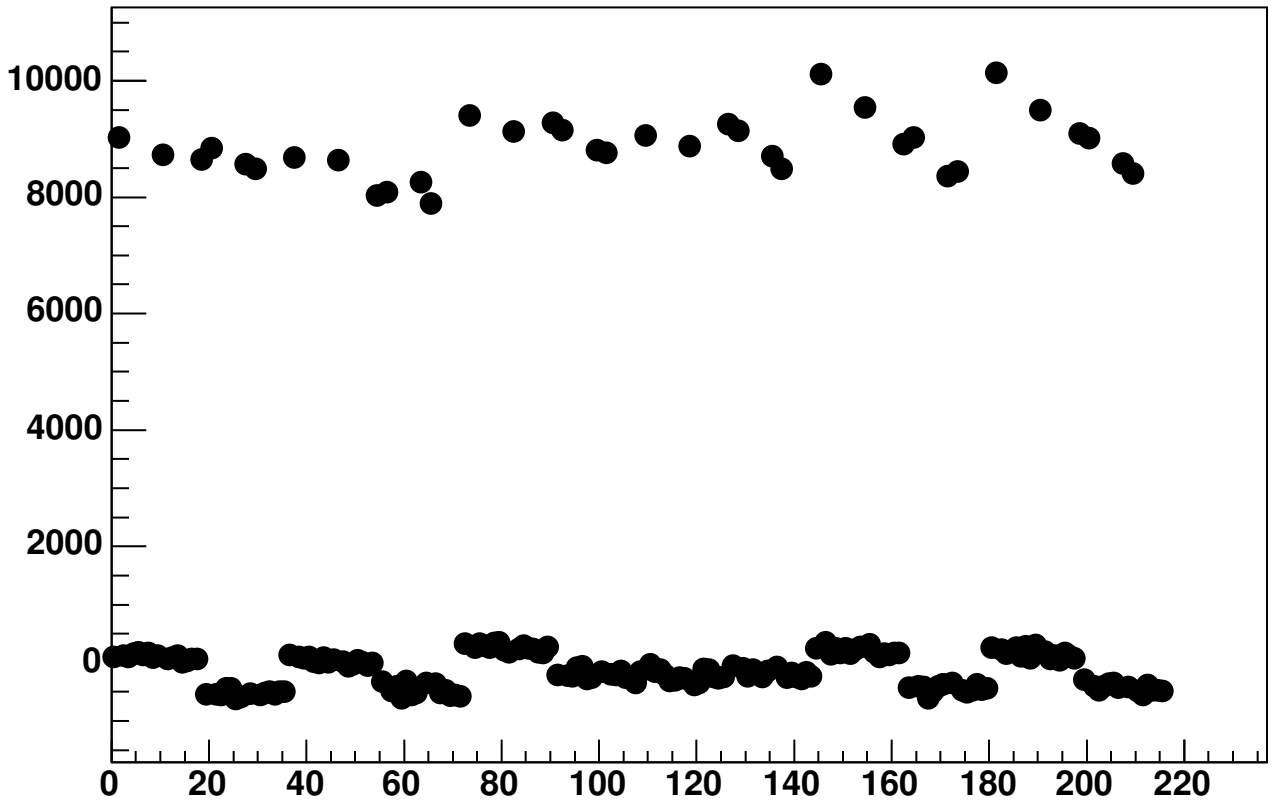
Enable 3, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



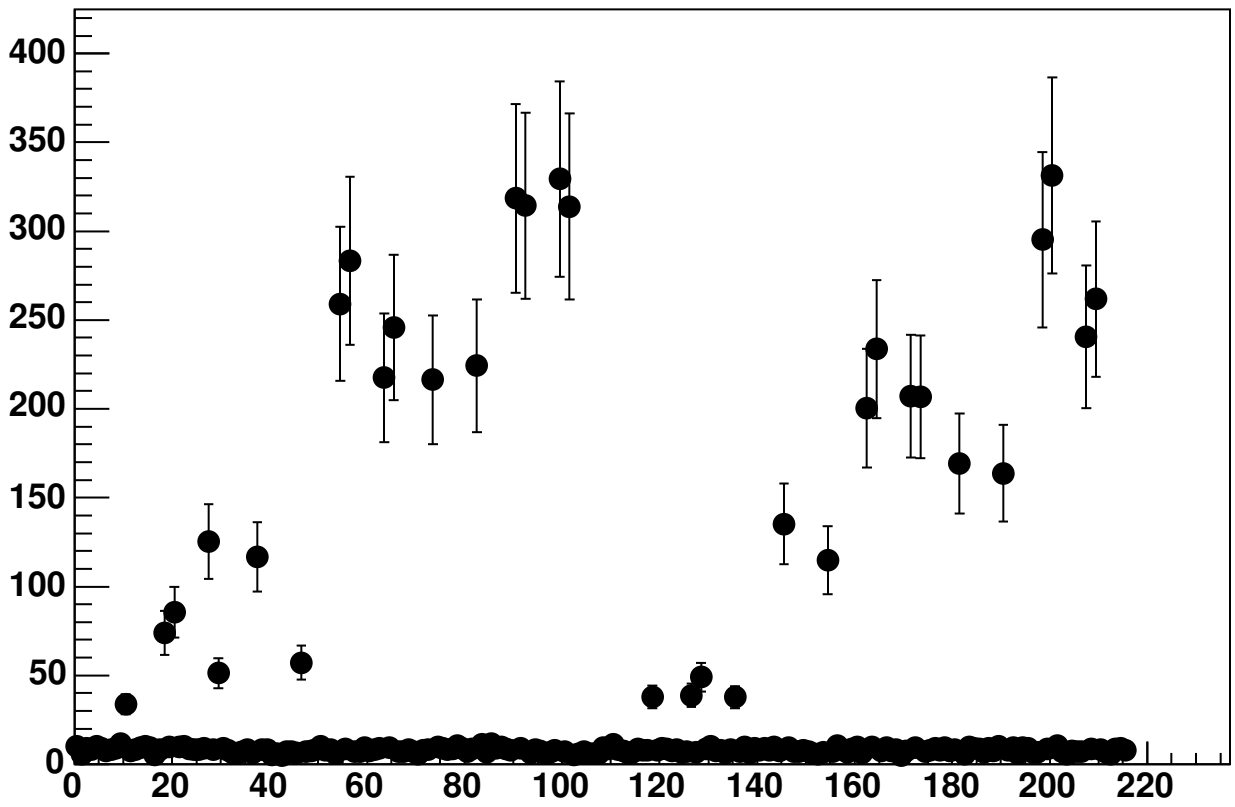
Enable 3, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



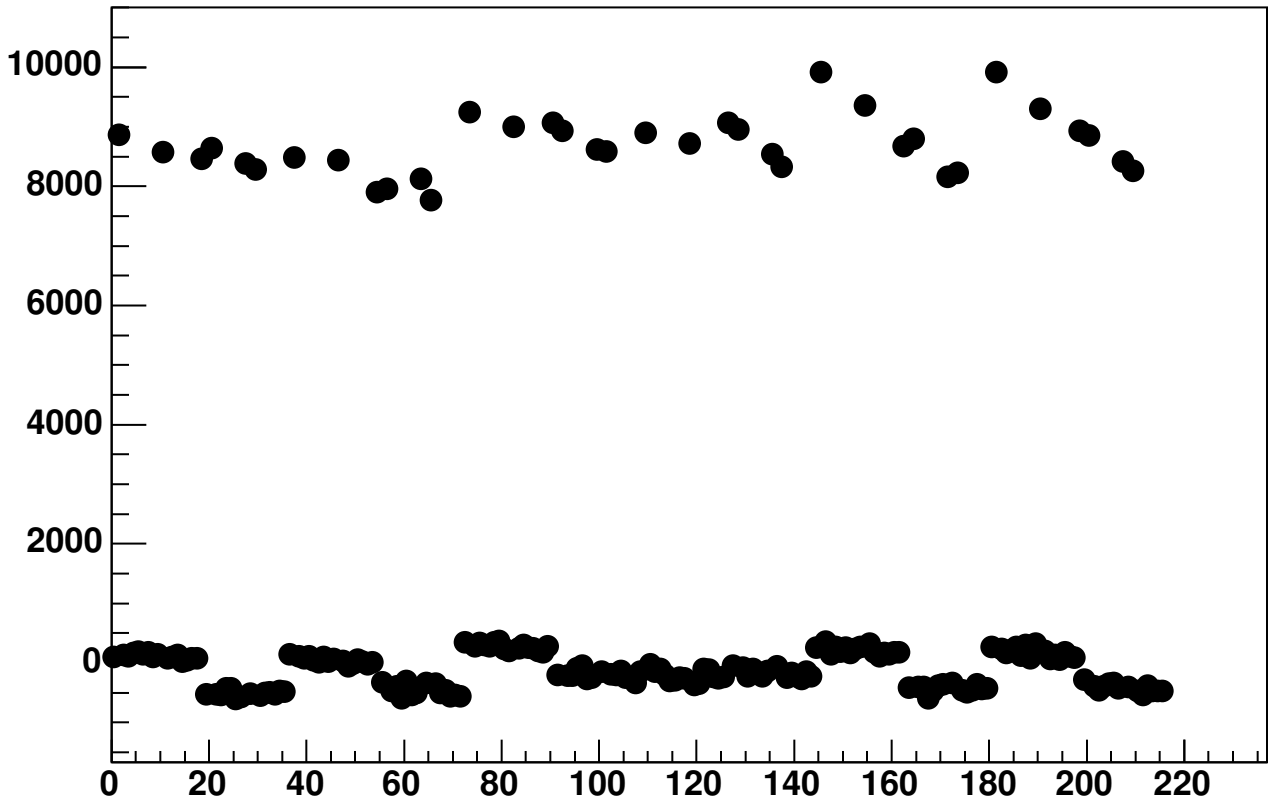
Enable 3, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



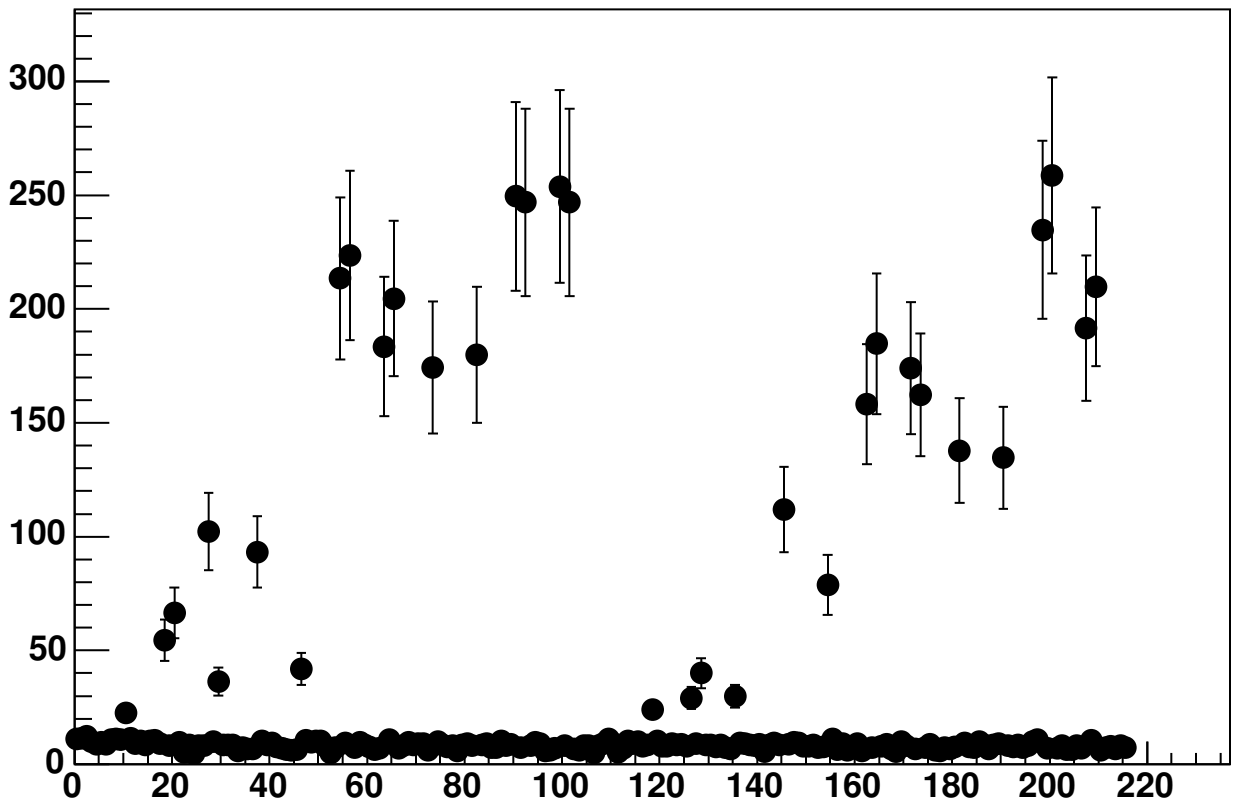
Enable 3, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



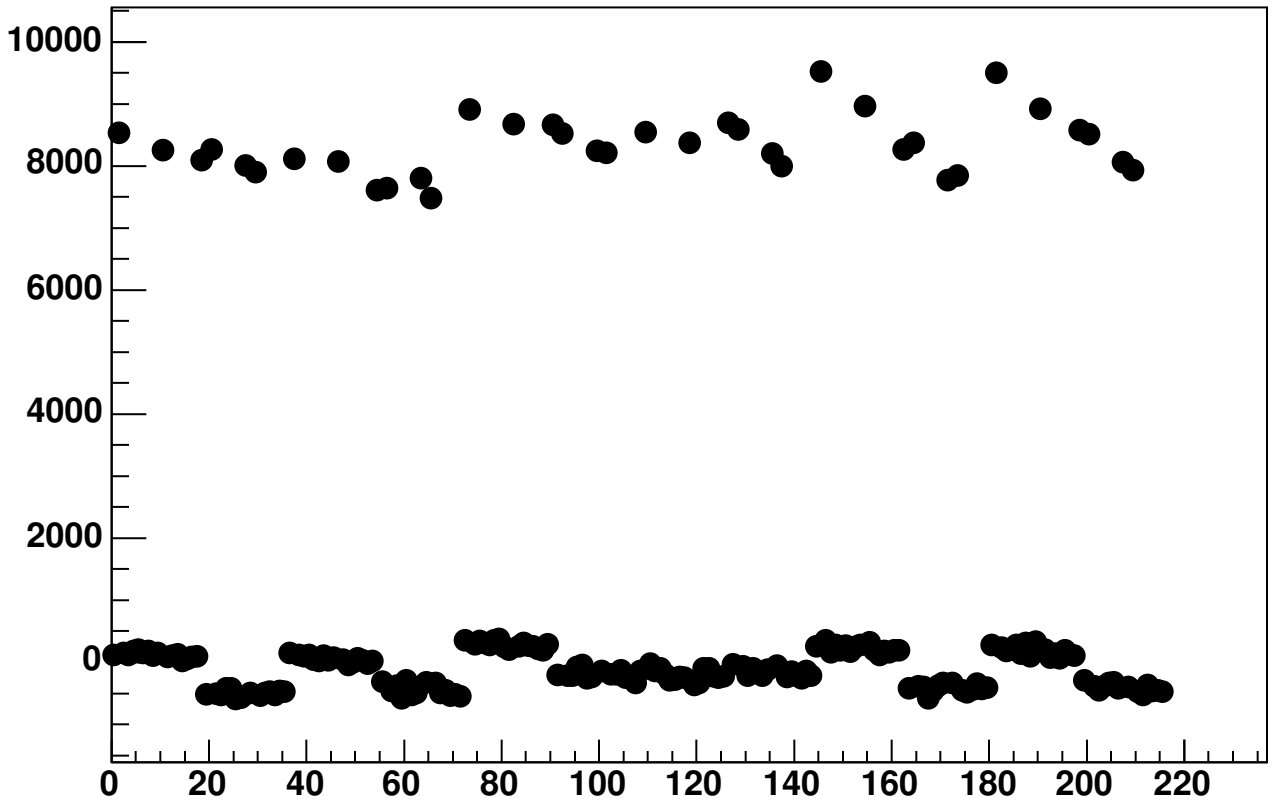
Enable 3, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



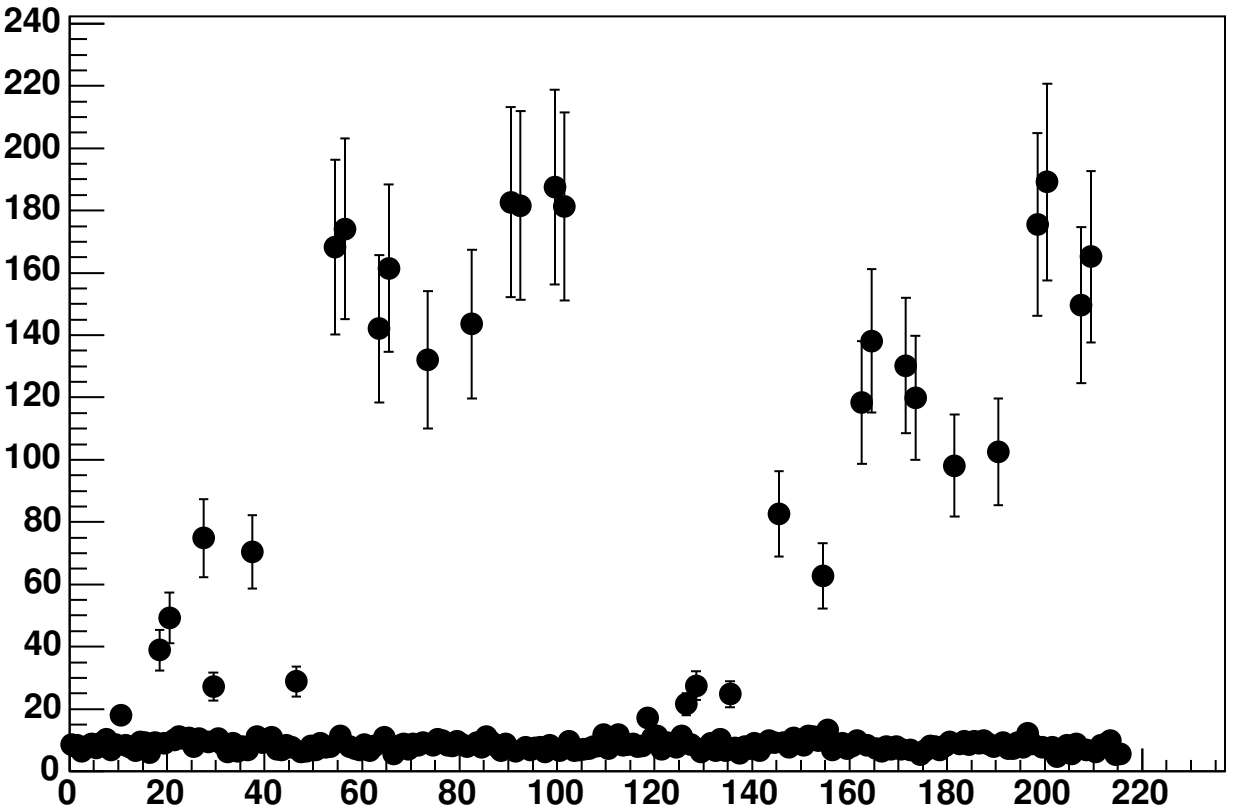
Enable 3, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



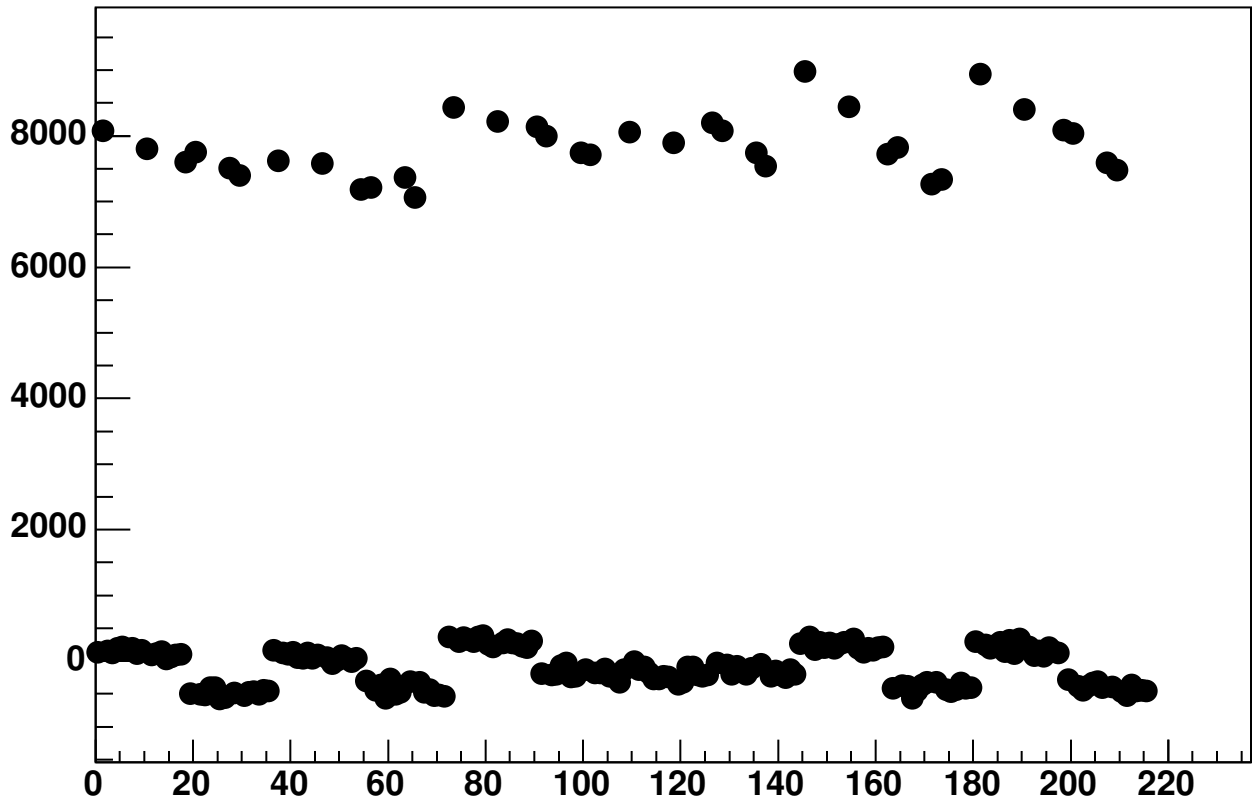
Enable 3, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



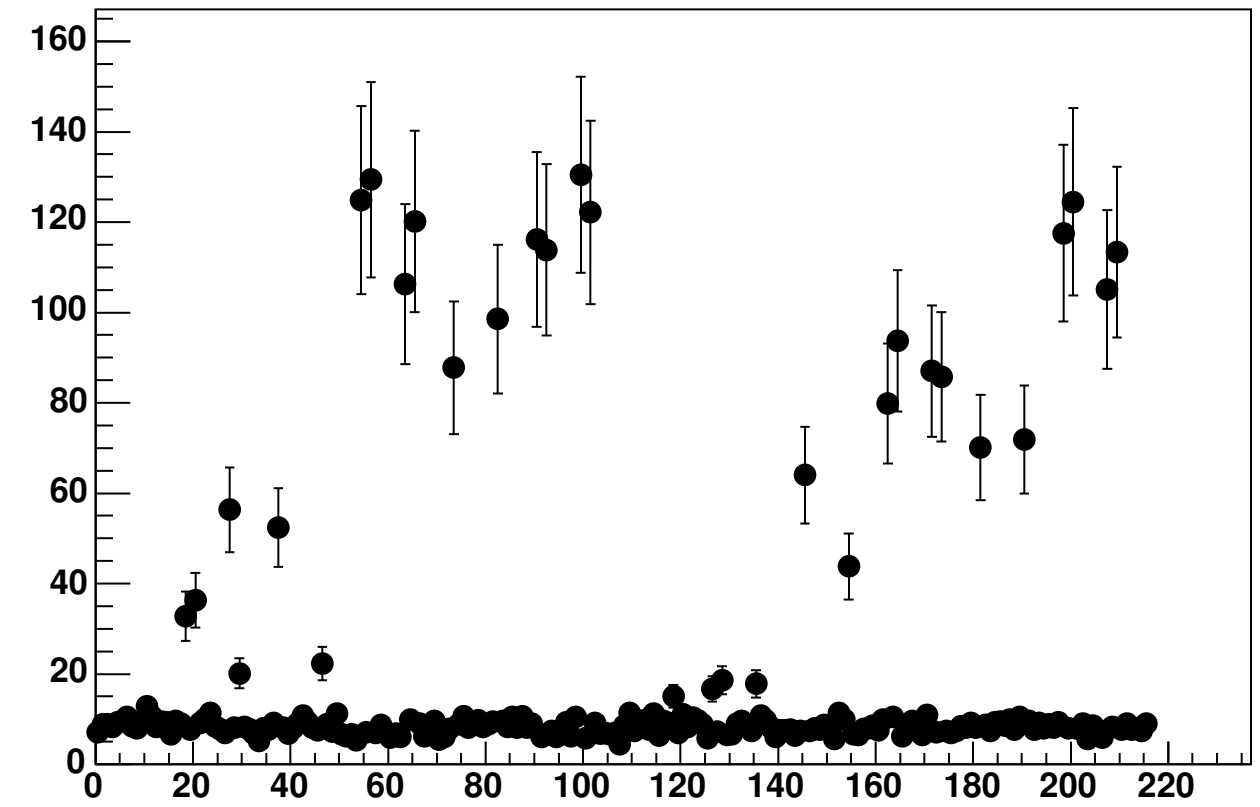
Enable 3, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



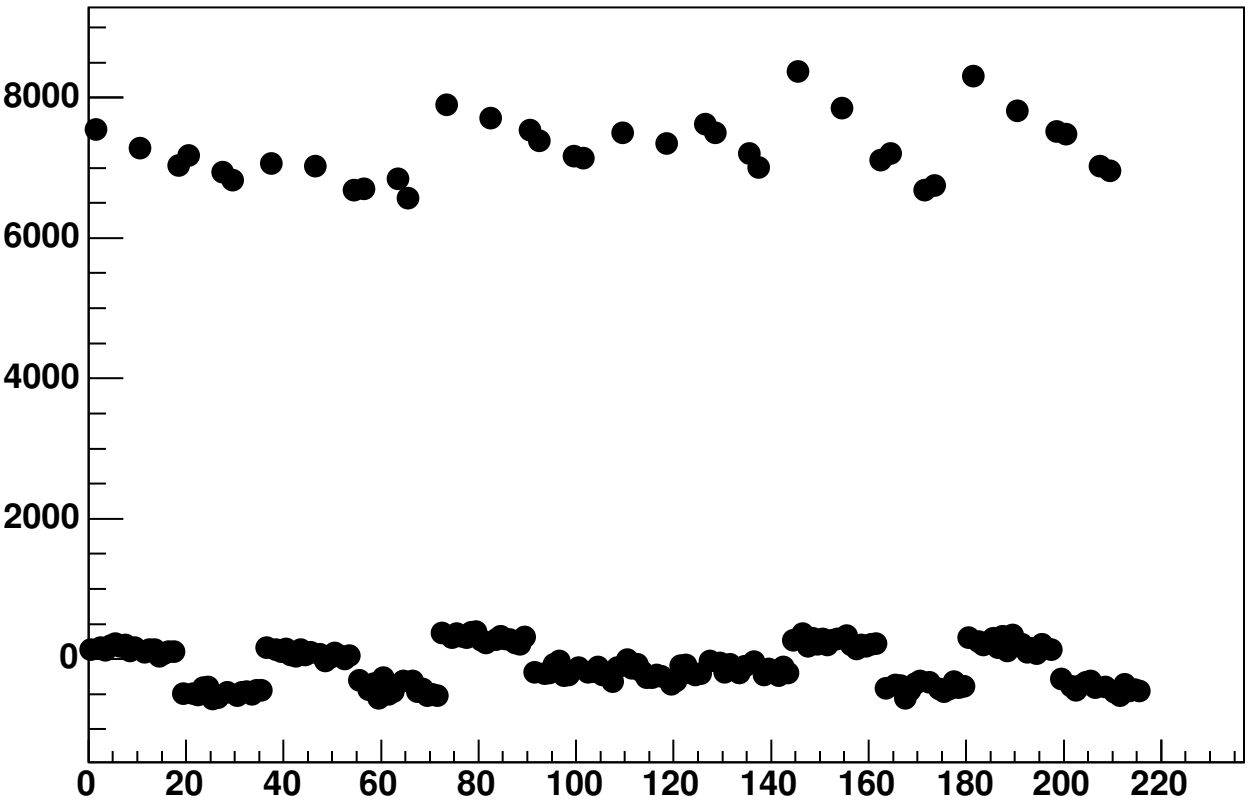
Enable 3, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



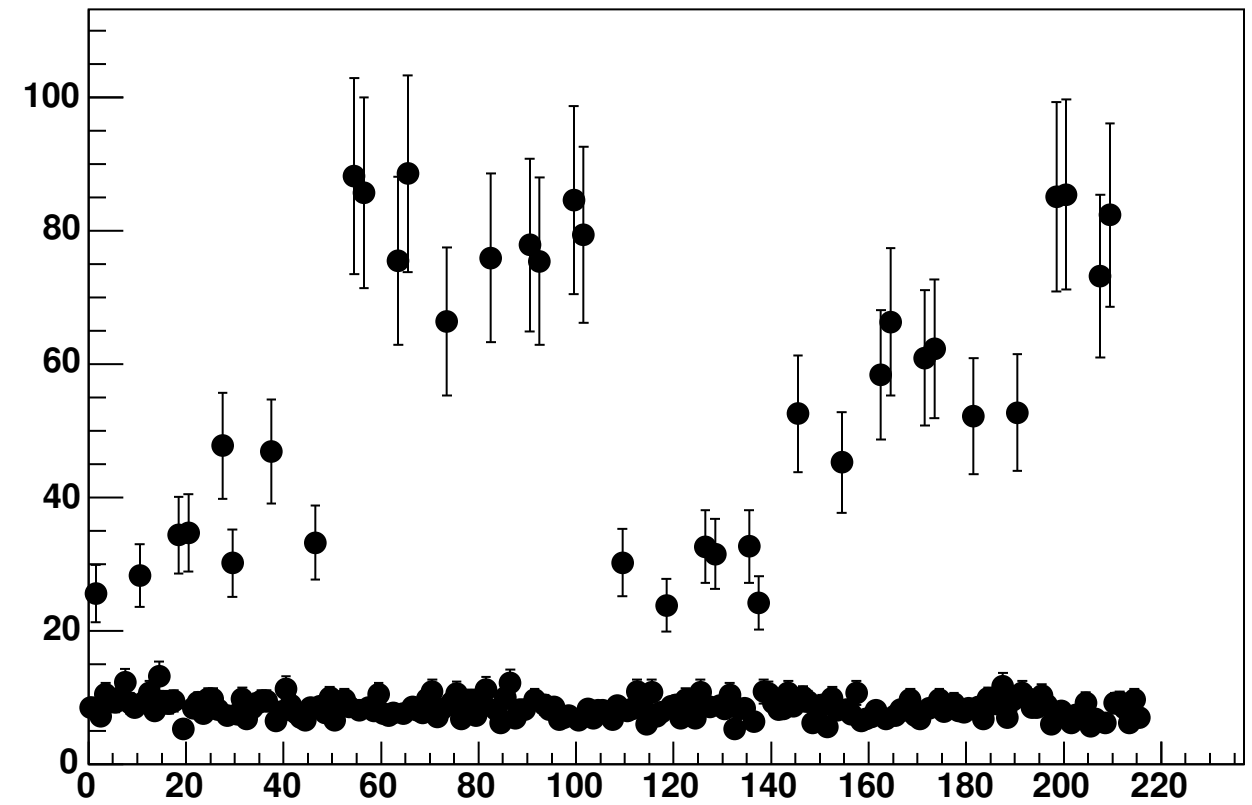
Enable 3, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



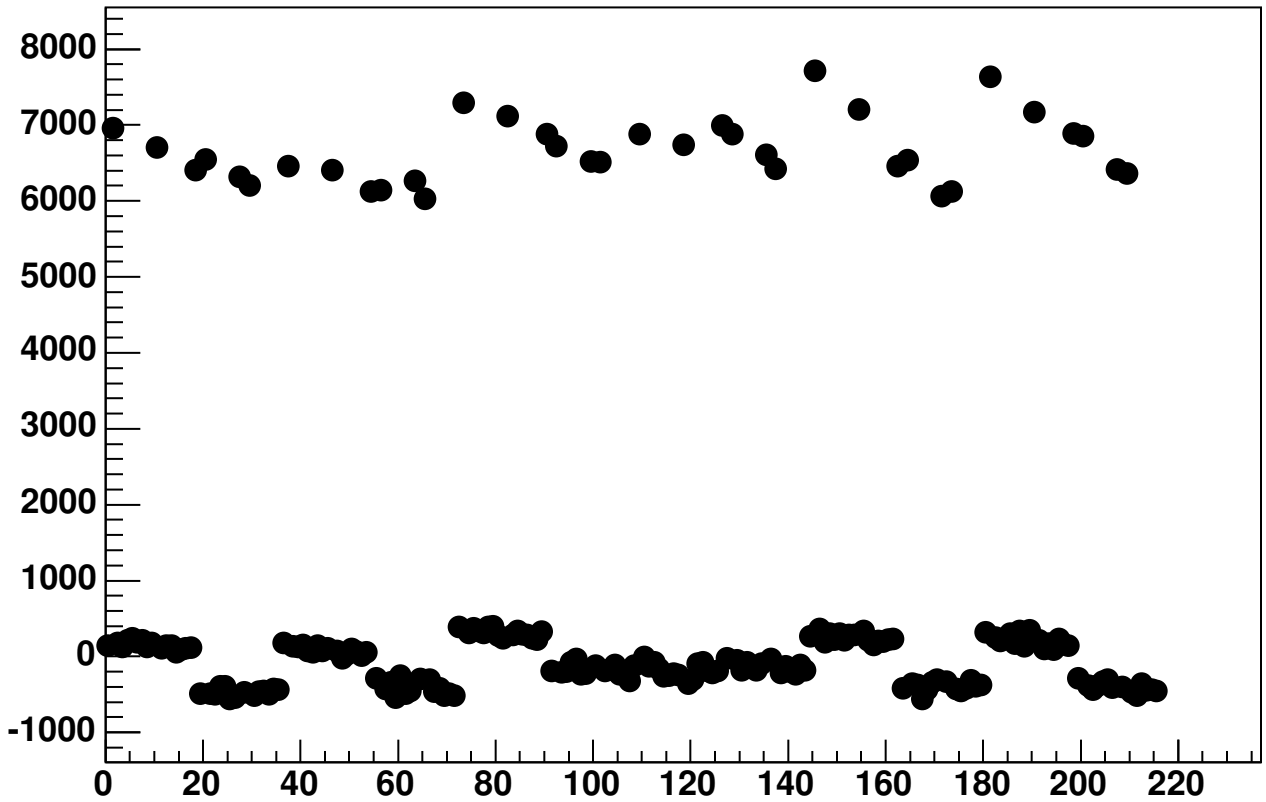
Enable 3, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



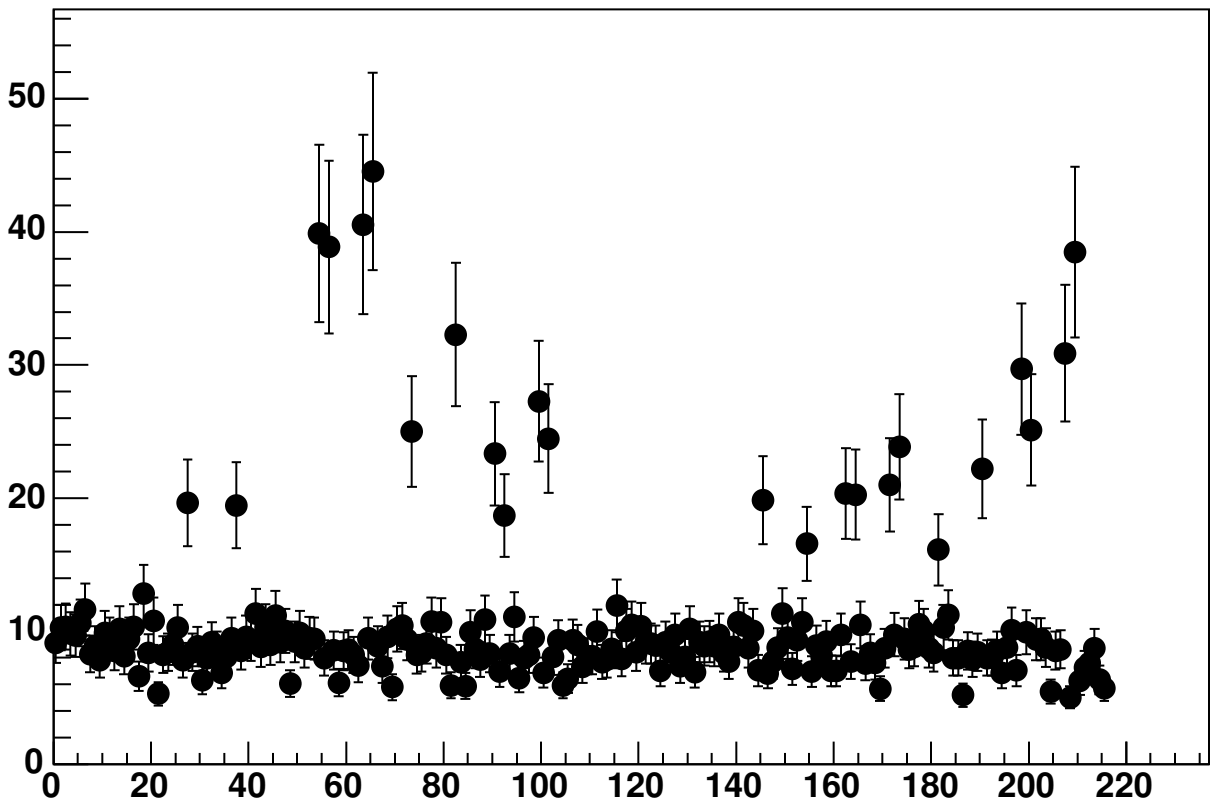
Enable 3, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



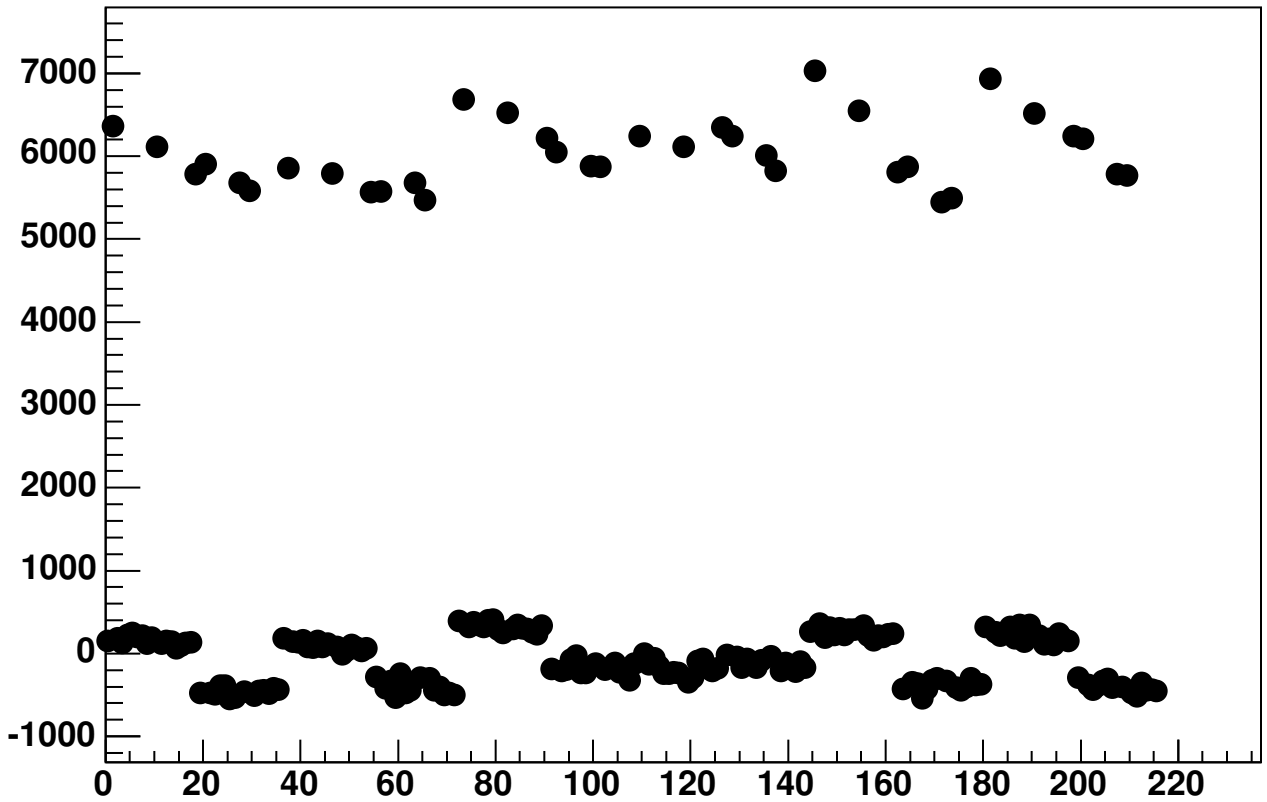
Enable 3, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



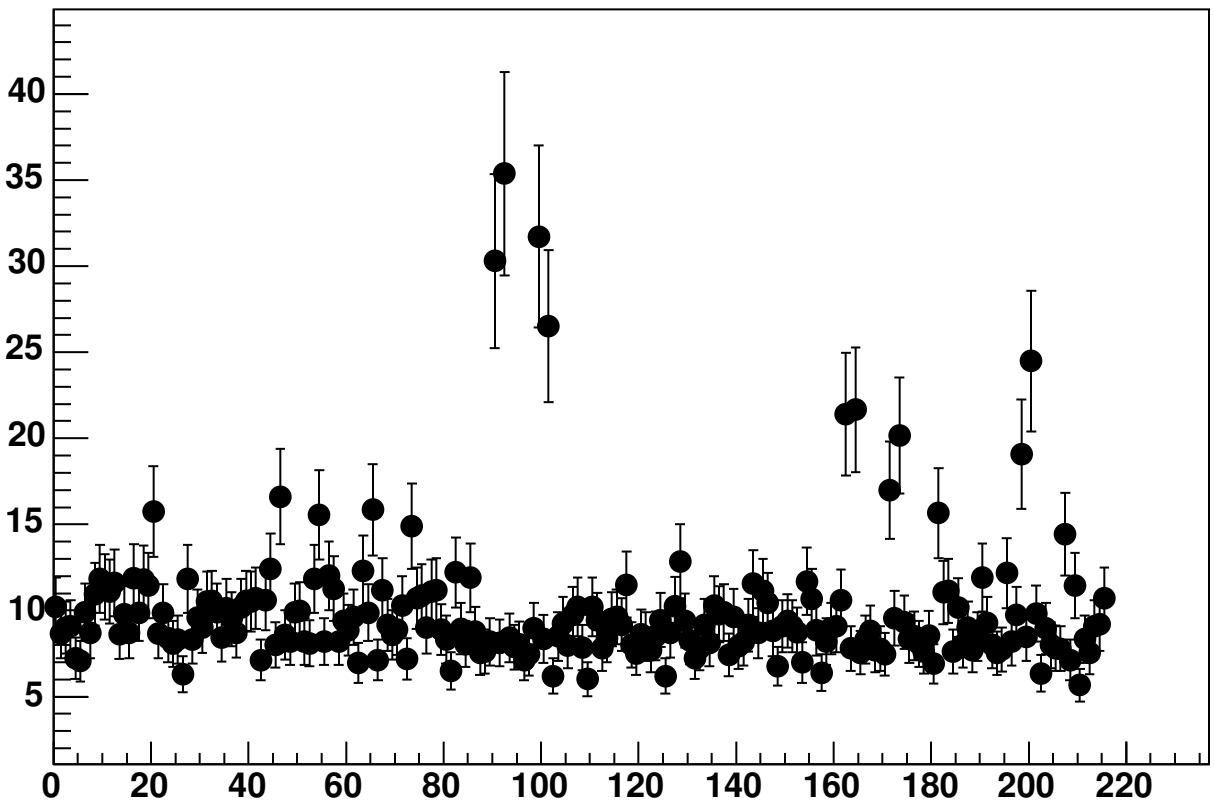
Enable 3, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



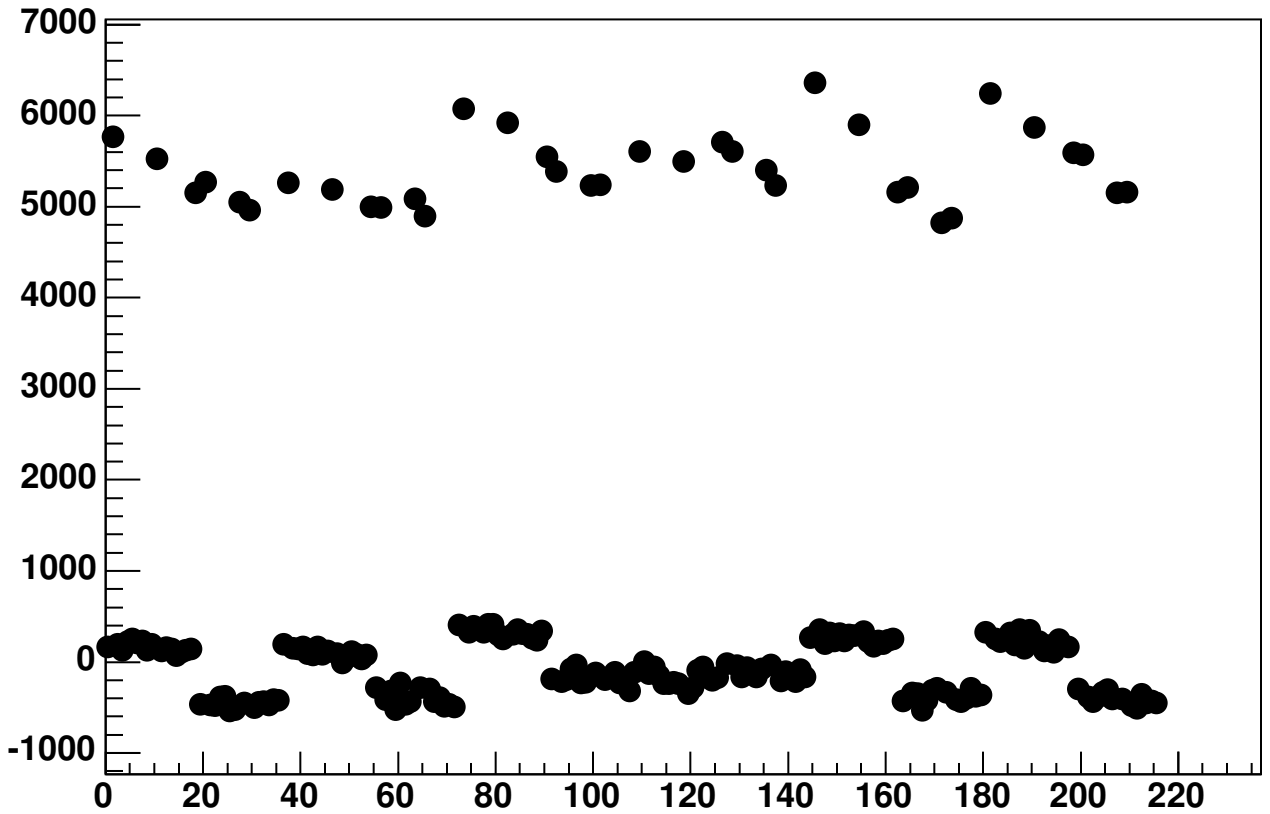
Enable 3, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



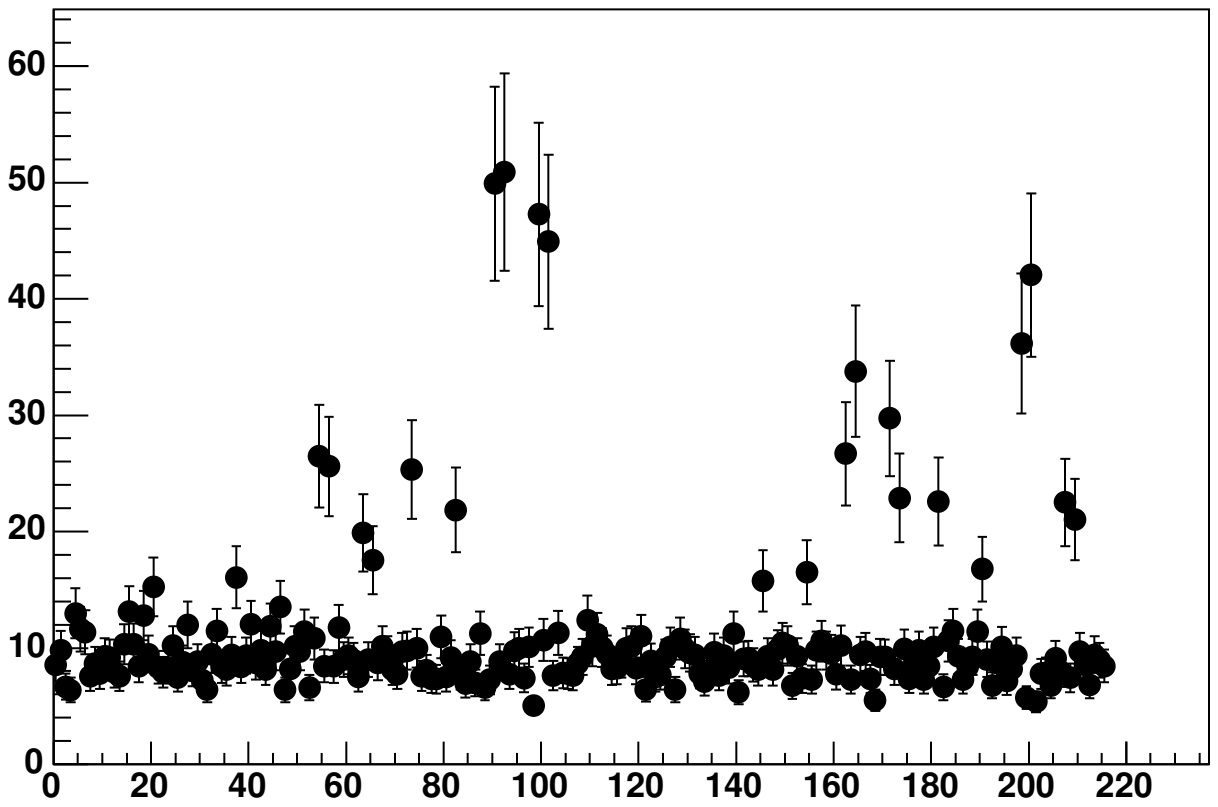
Enable 3, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



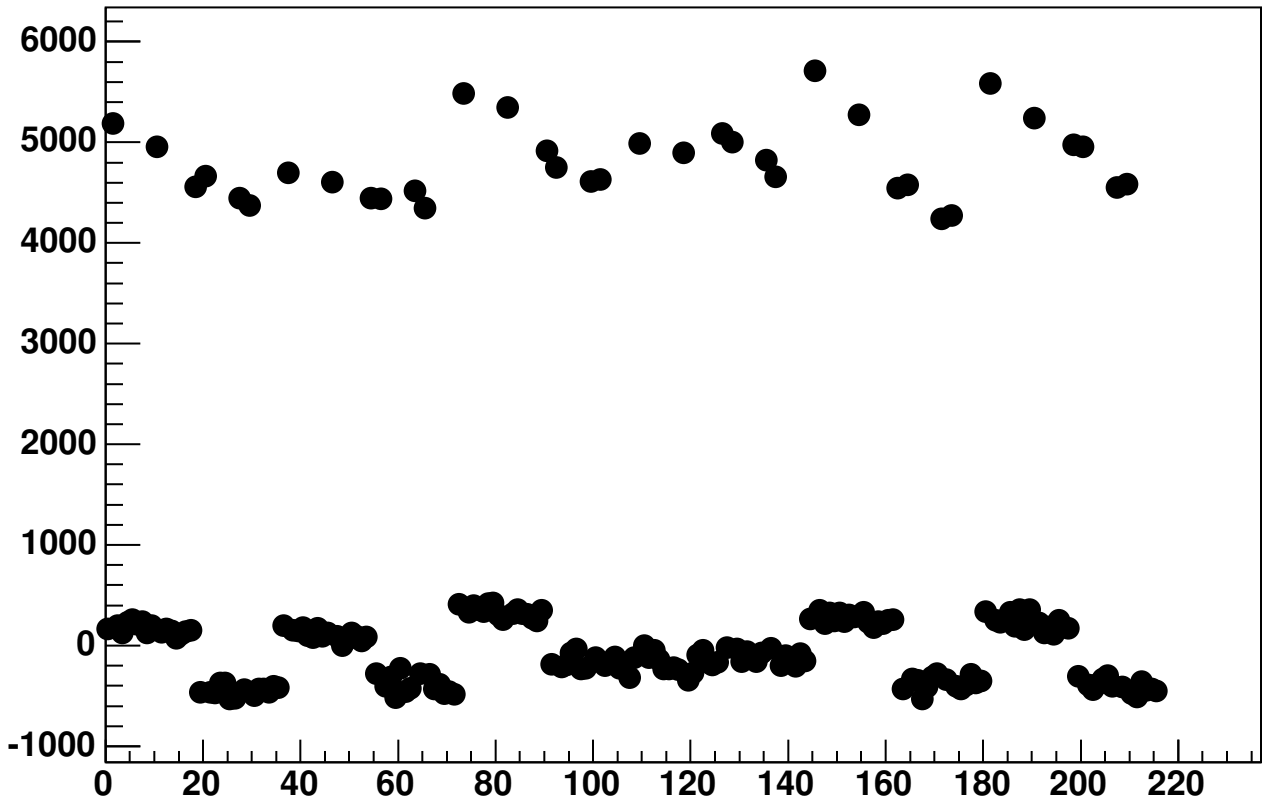
Enable 3, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



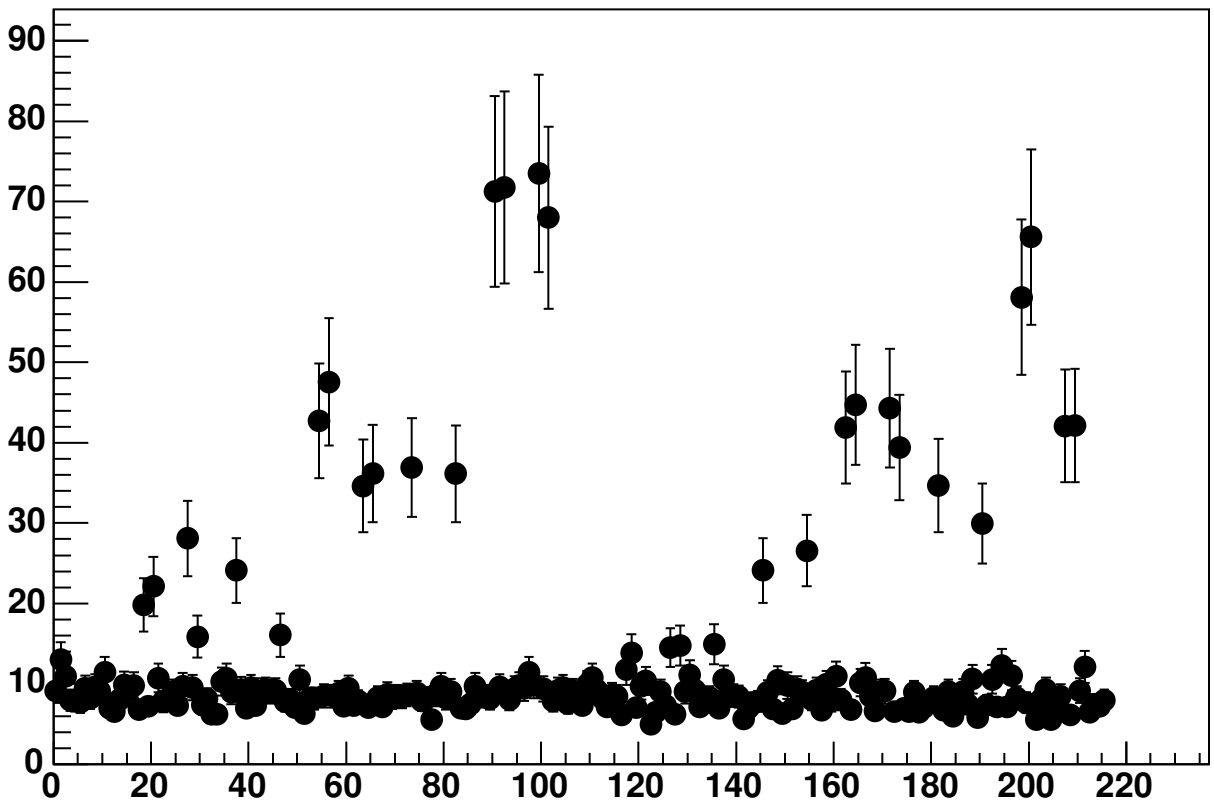
Enable 3, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



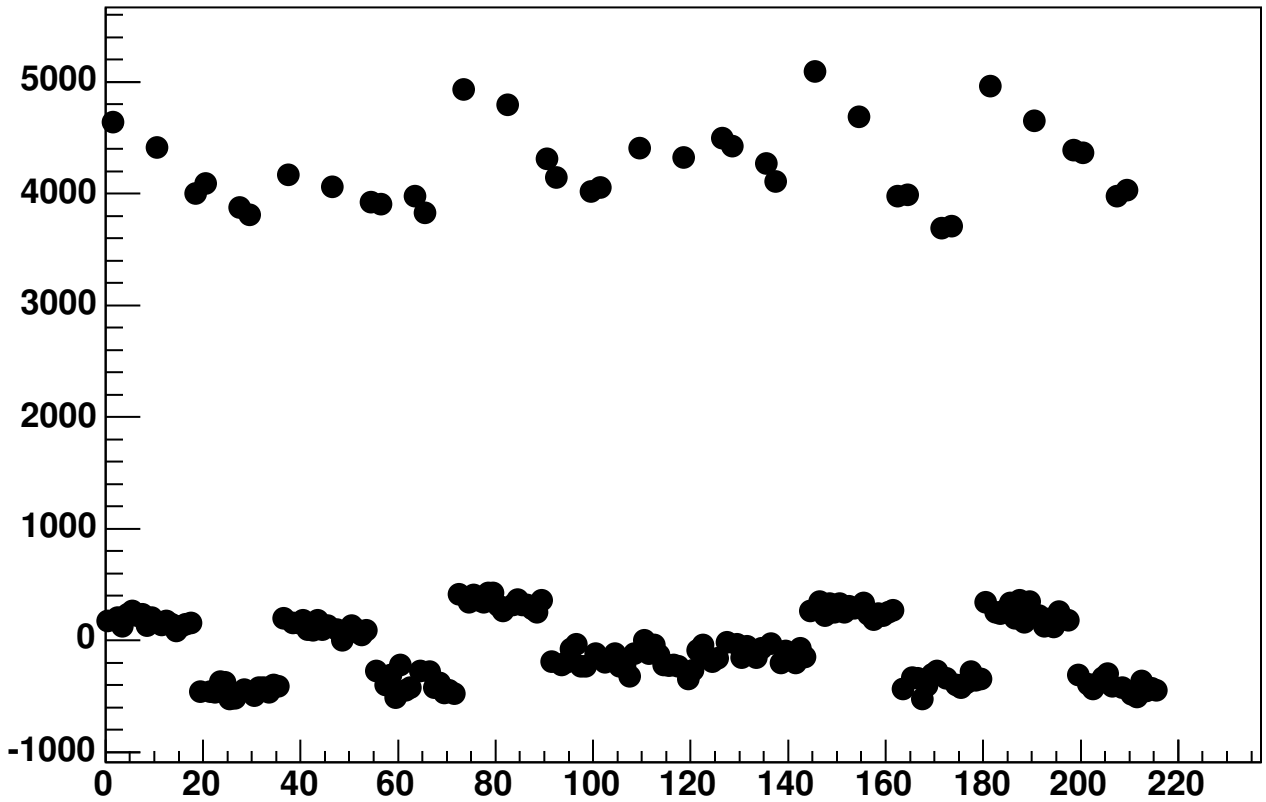
Enable 3, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



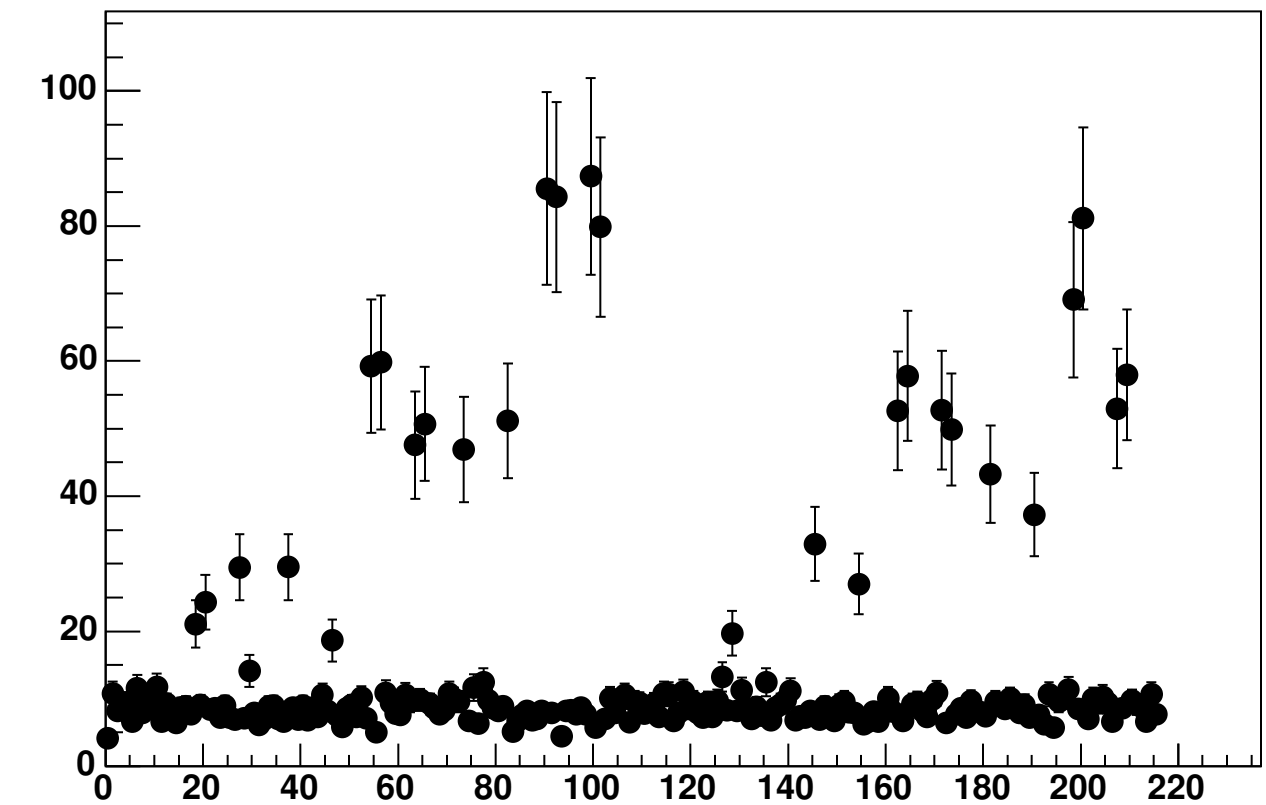
Enable 3, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



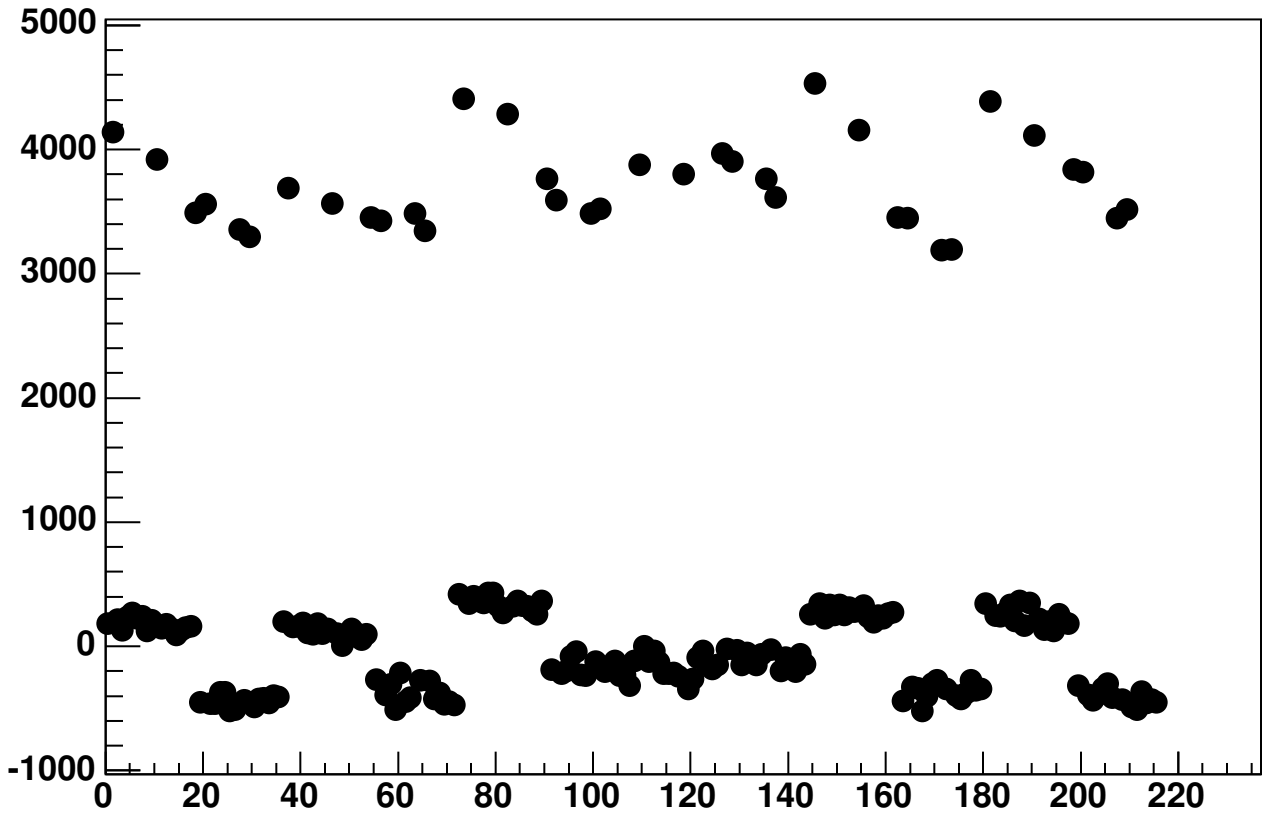
Enable 3, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



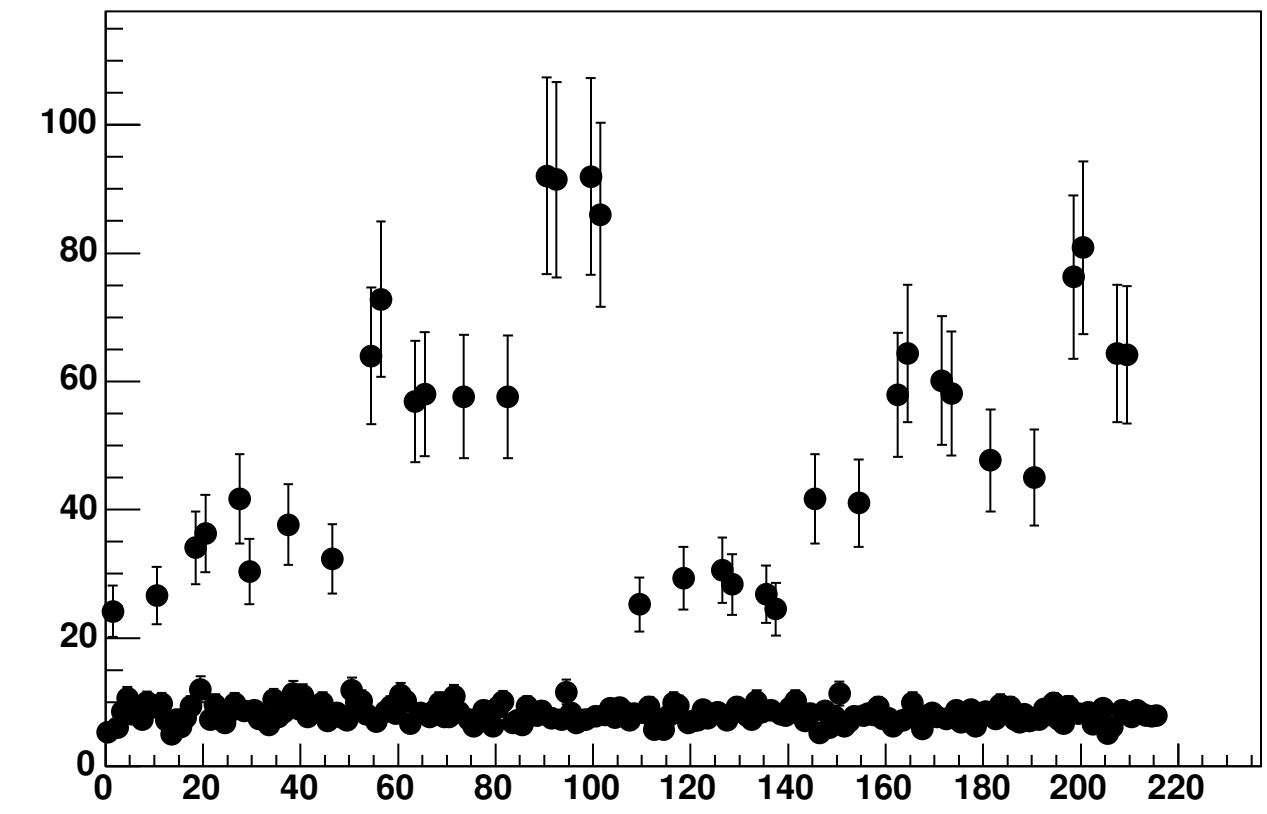
Enable 3, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



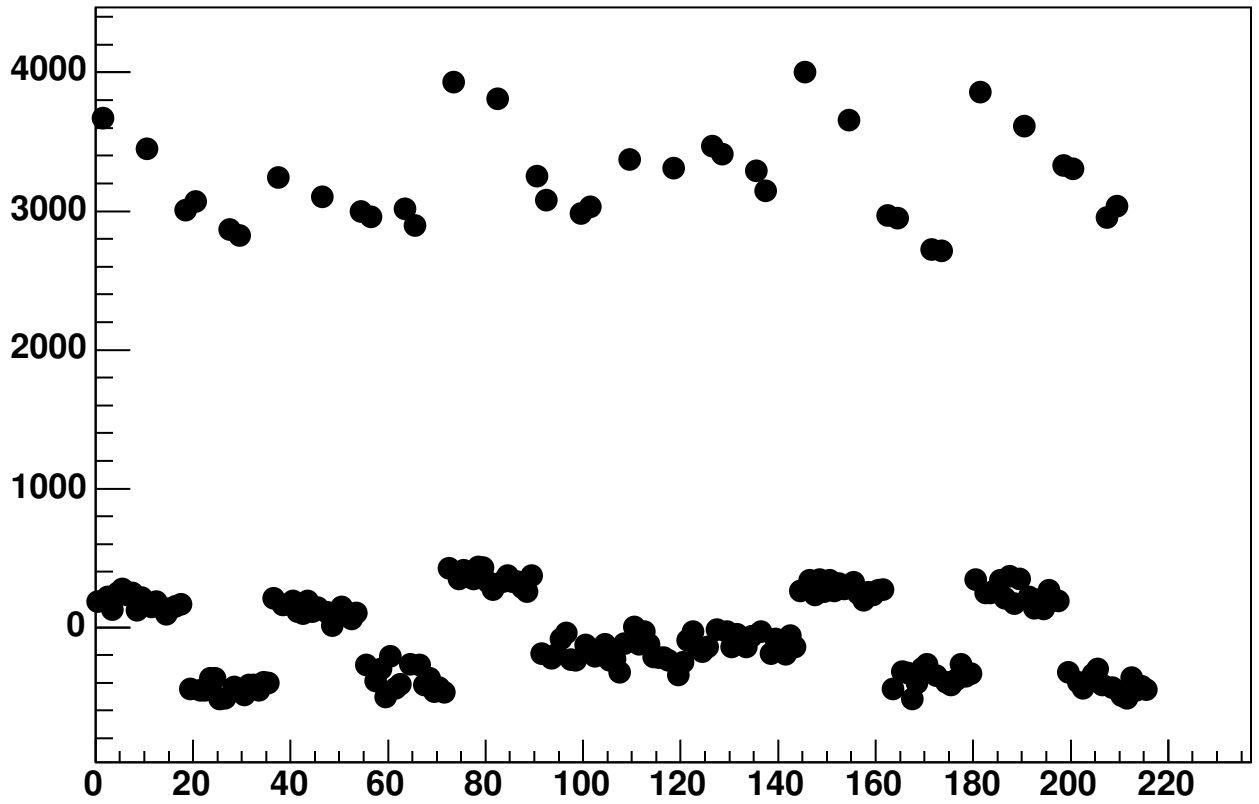
Enable 3, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



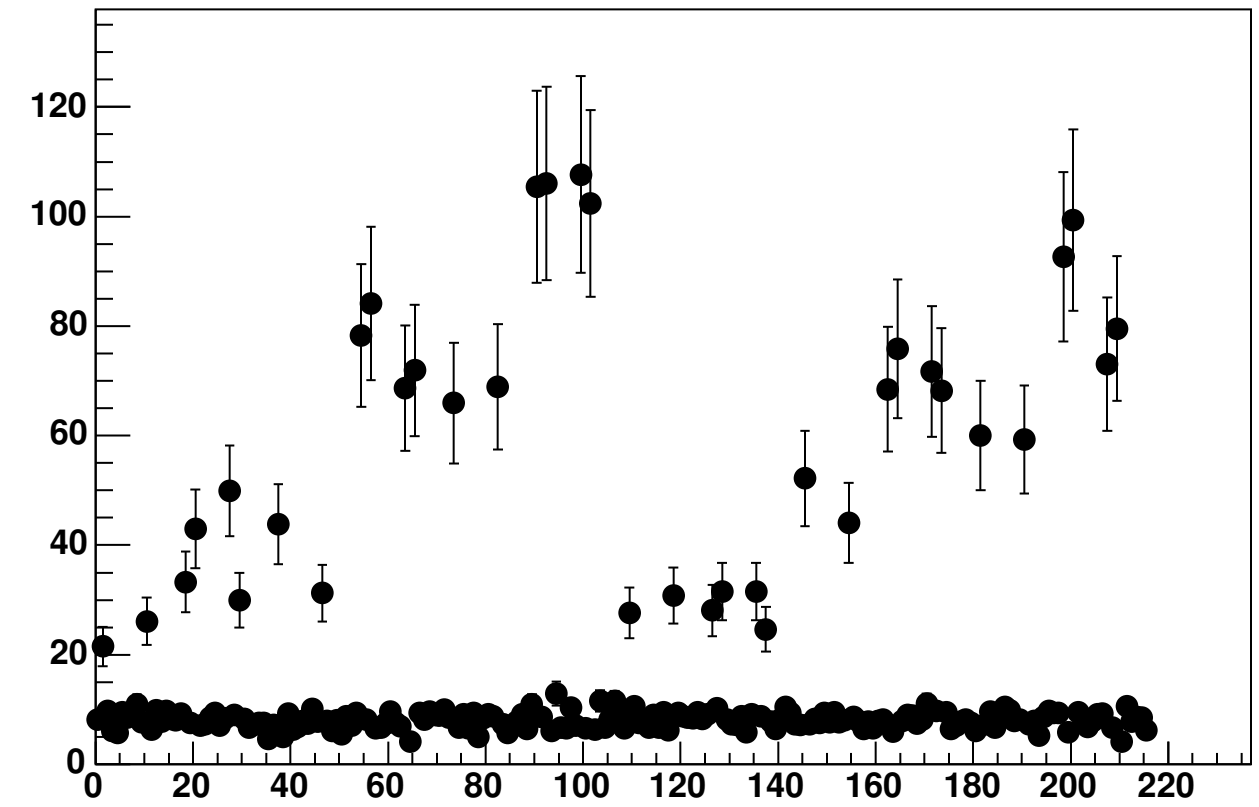
Enable 3, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



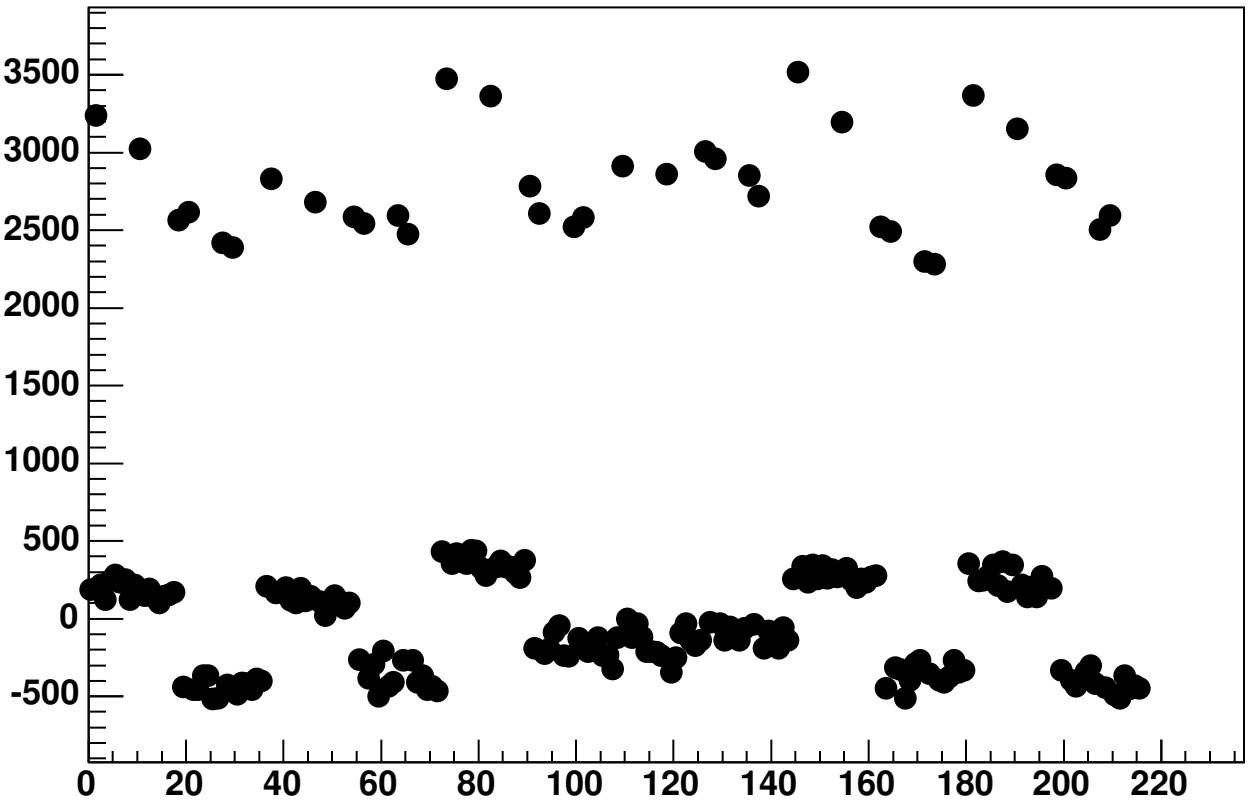
Enable 3, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



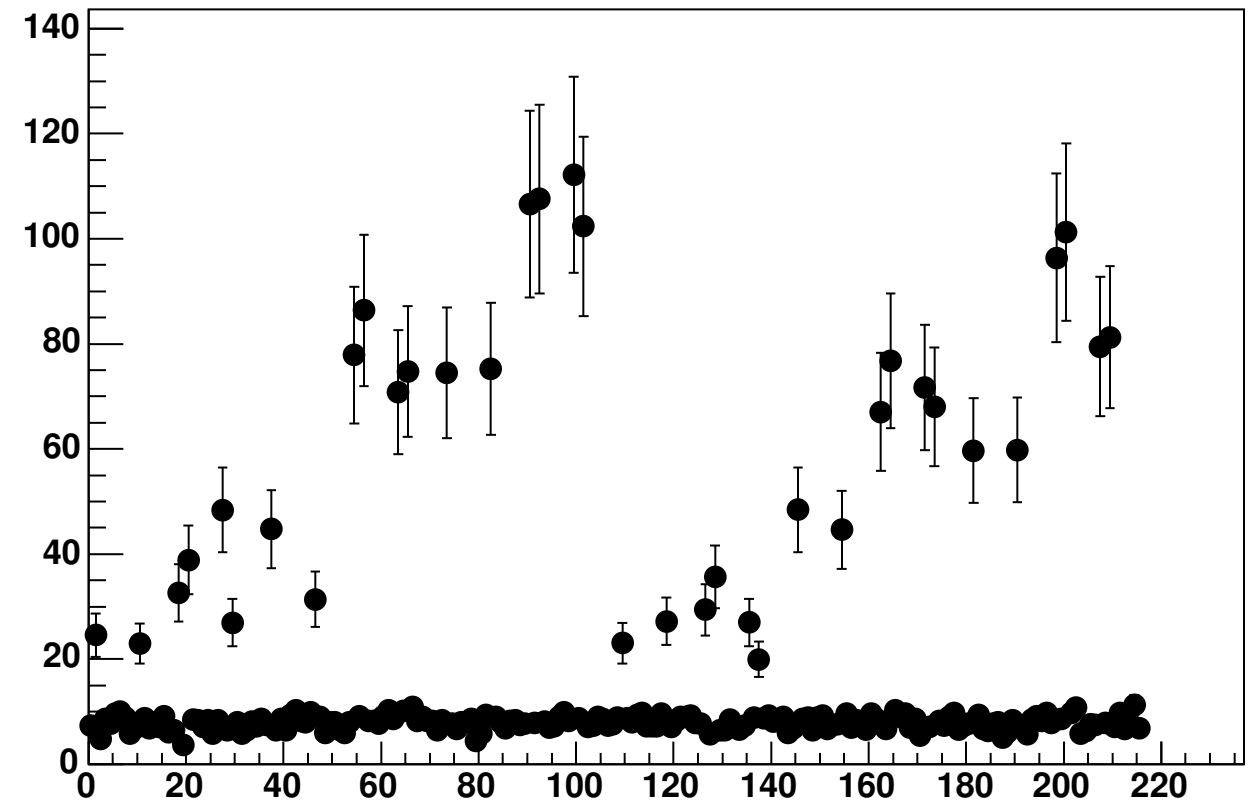
Enable 3, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



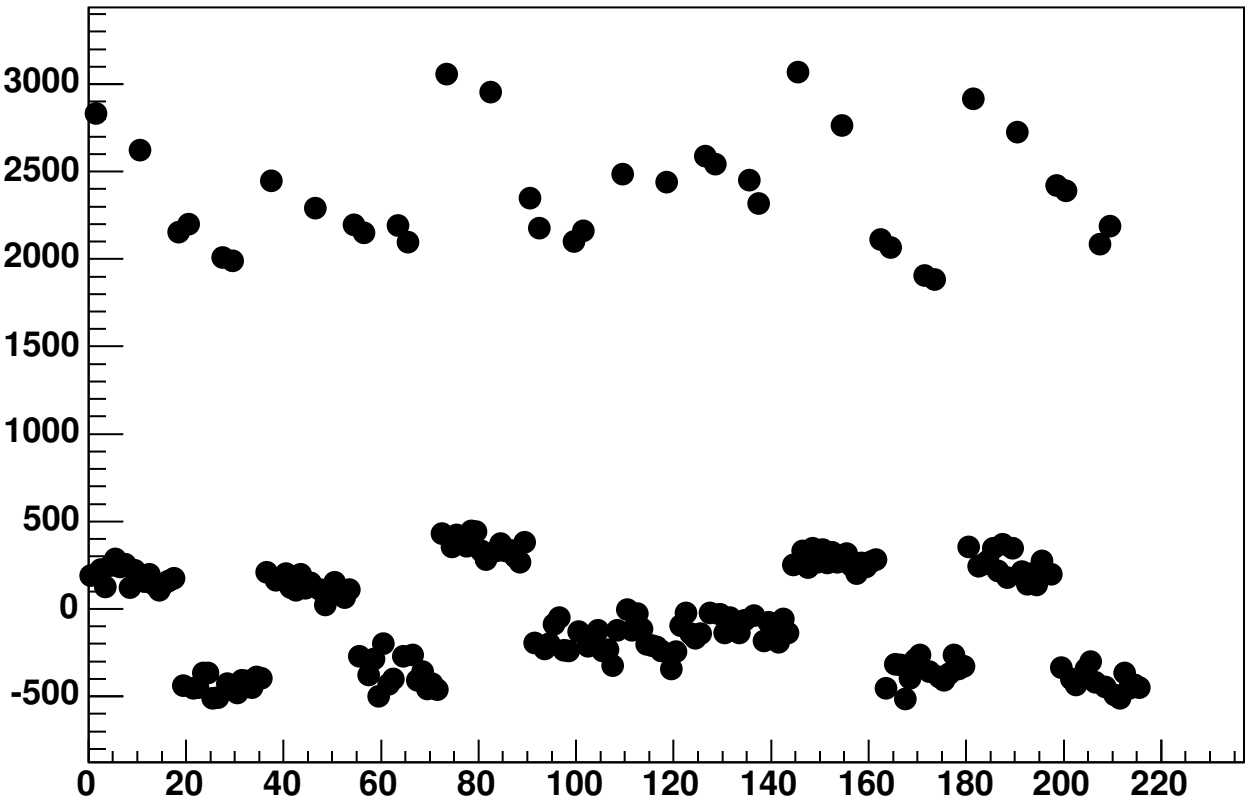
Enable 3, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



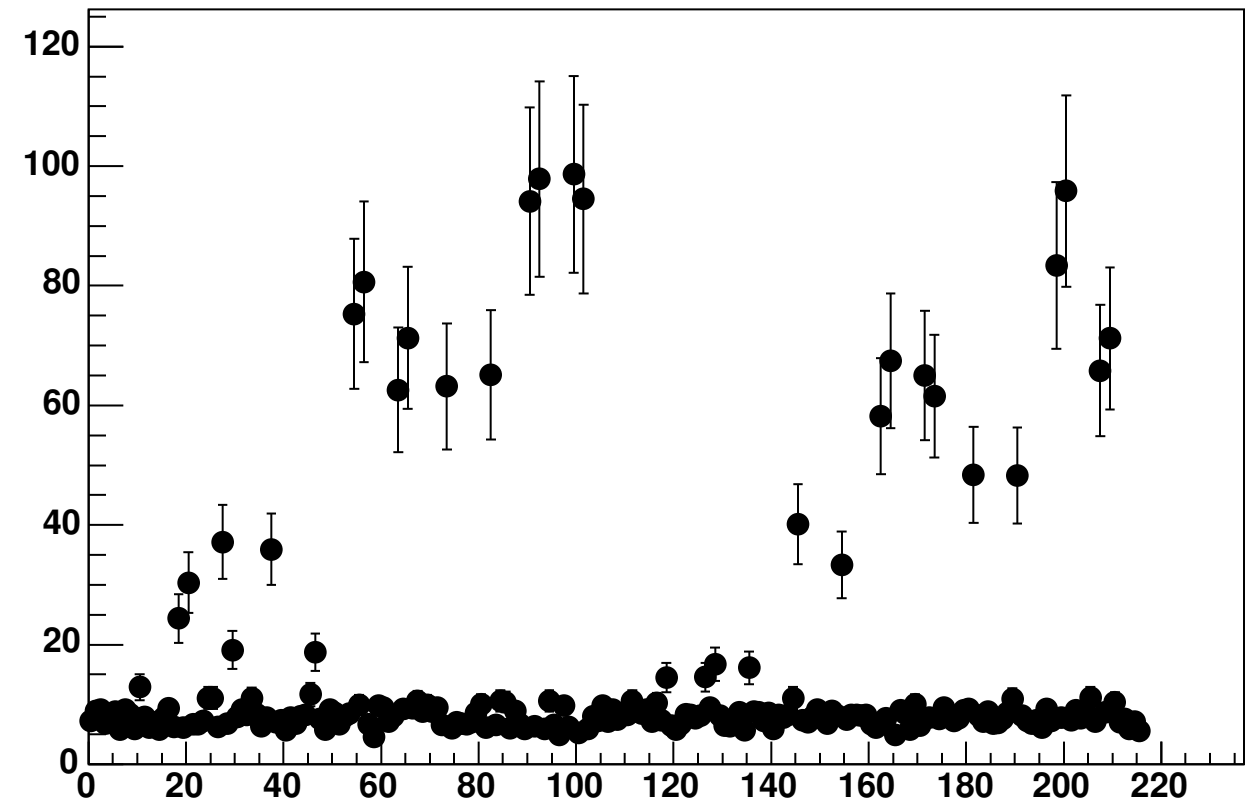
Enable 3, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



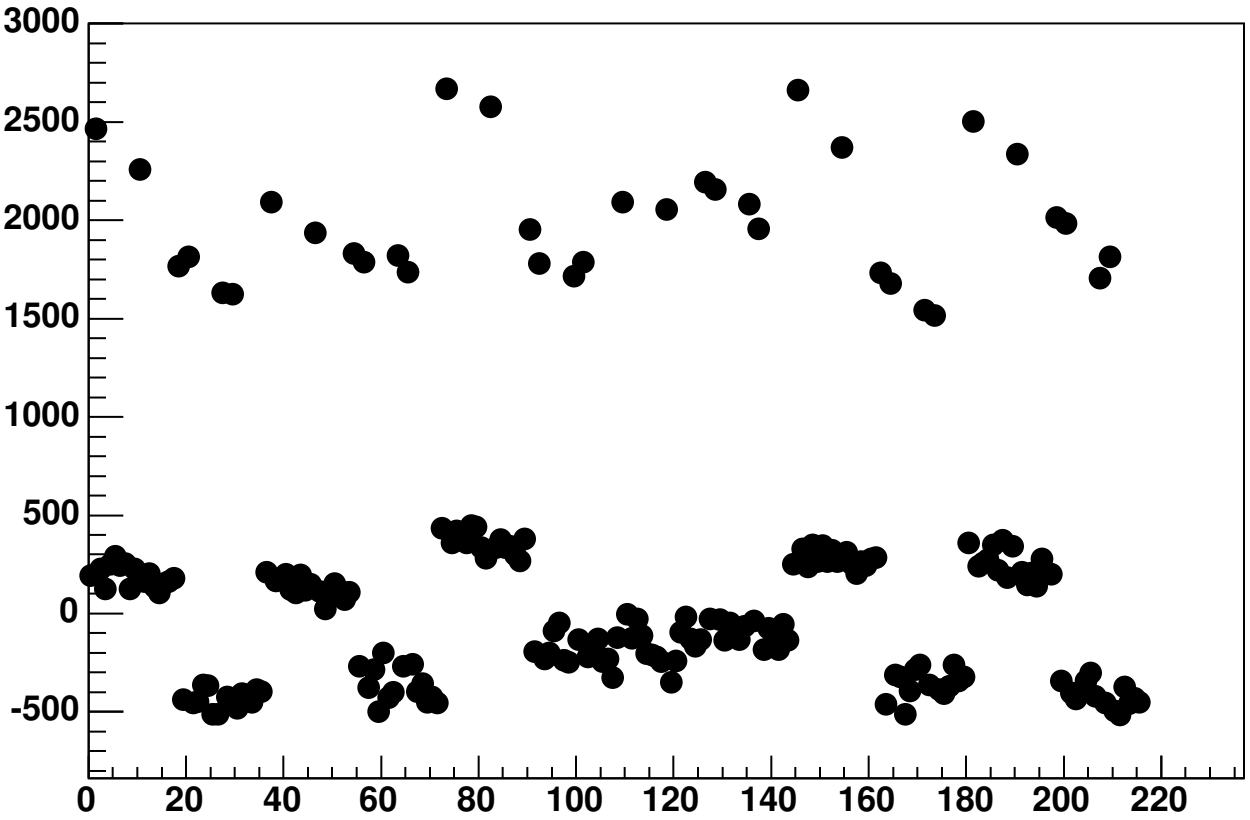
Enable 3, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



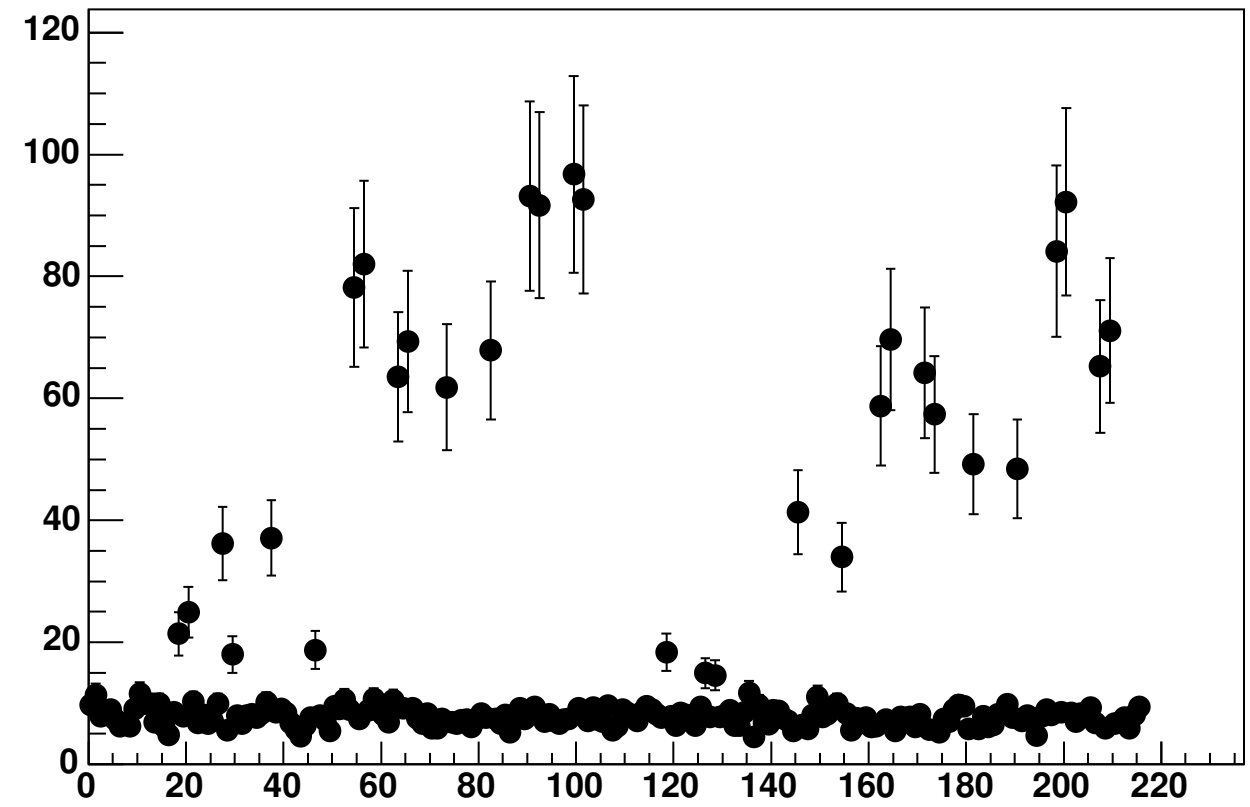
Enable 3, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



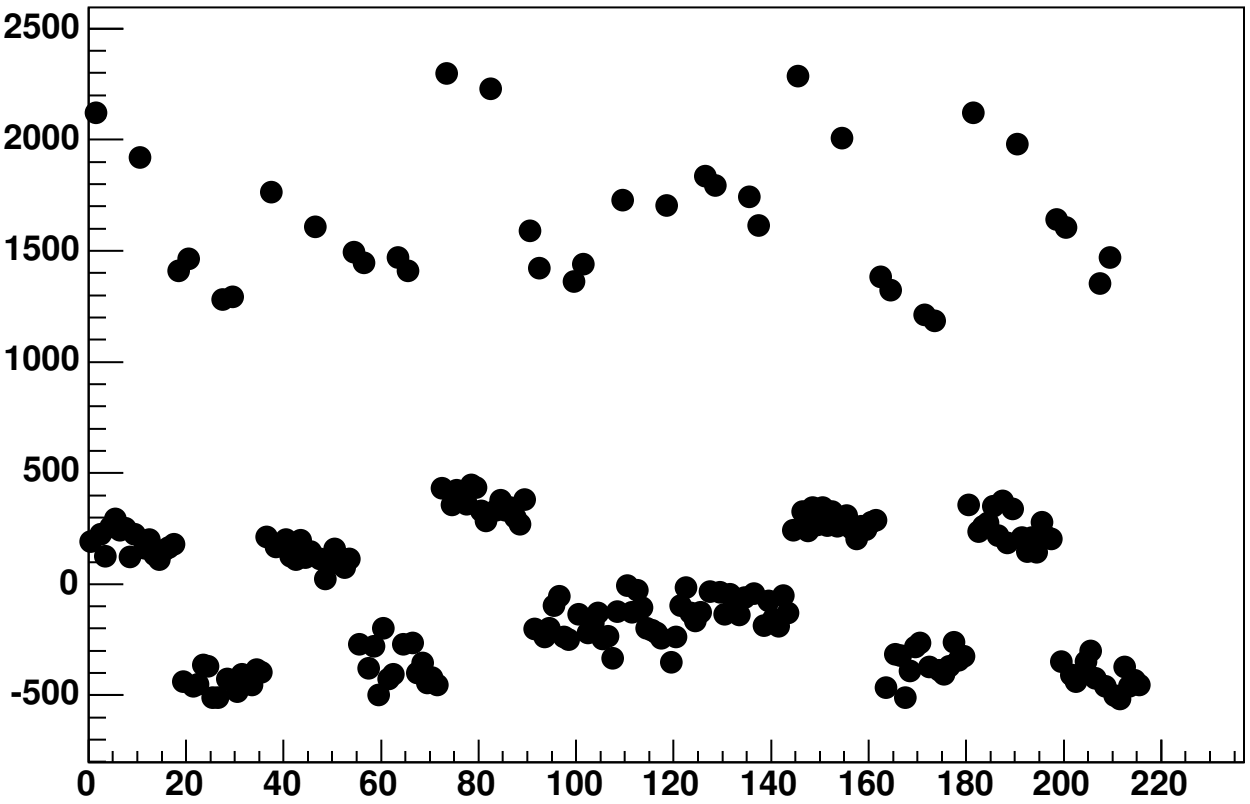
Enable 3, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



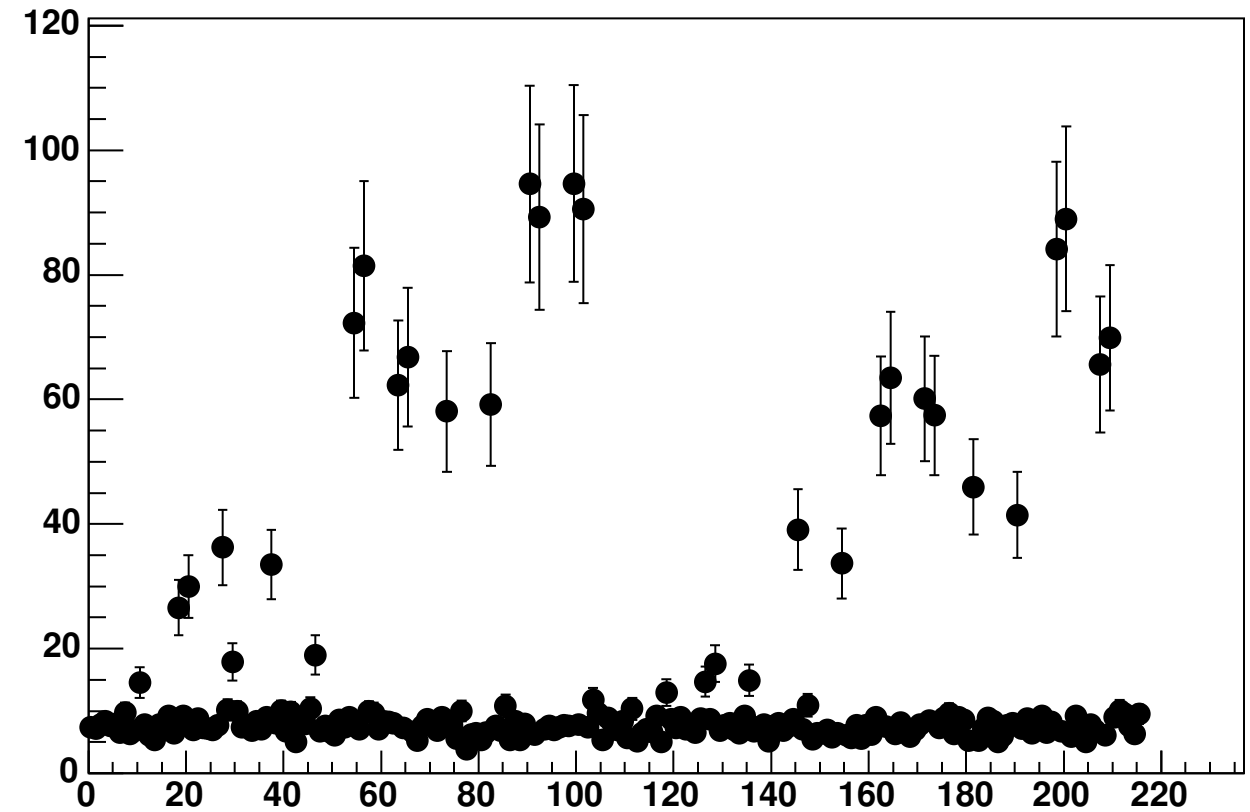
Enable 3, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



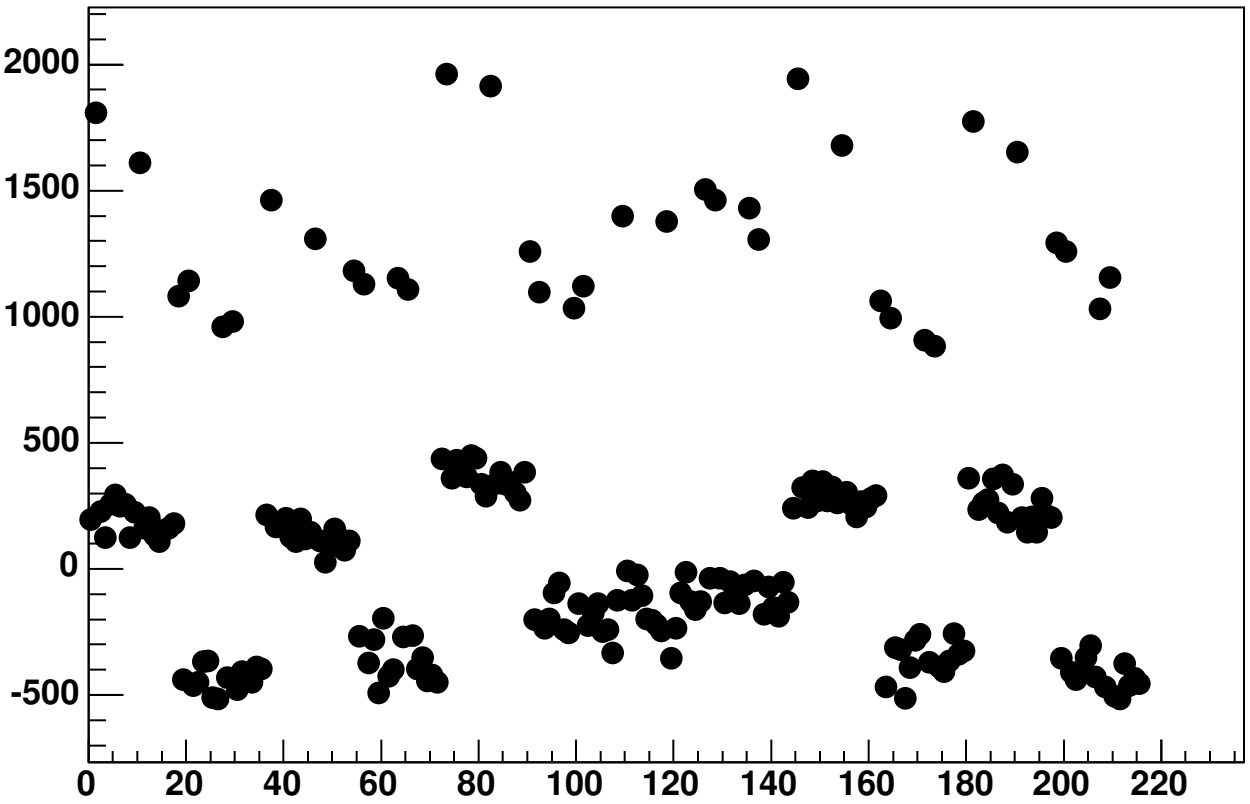
Enable 3, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



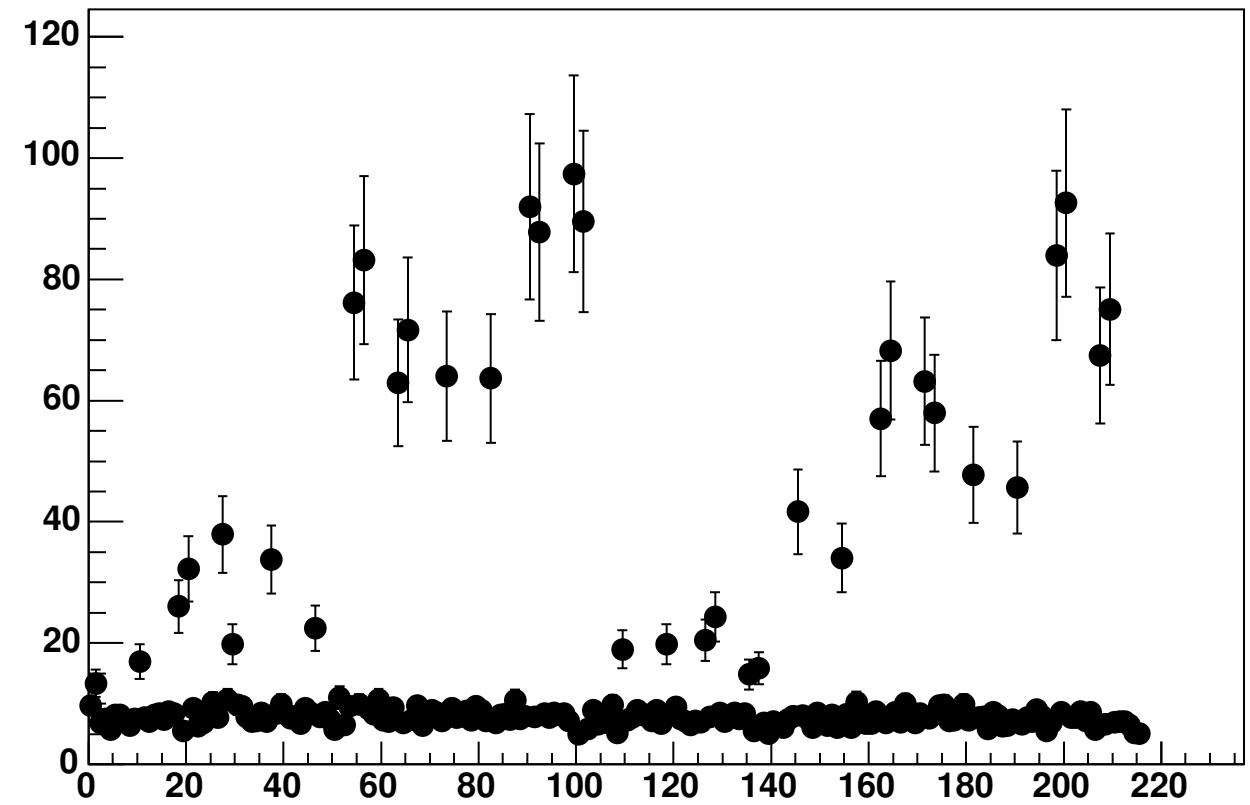
Enable 3, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



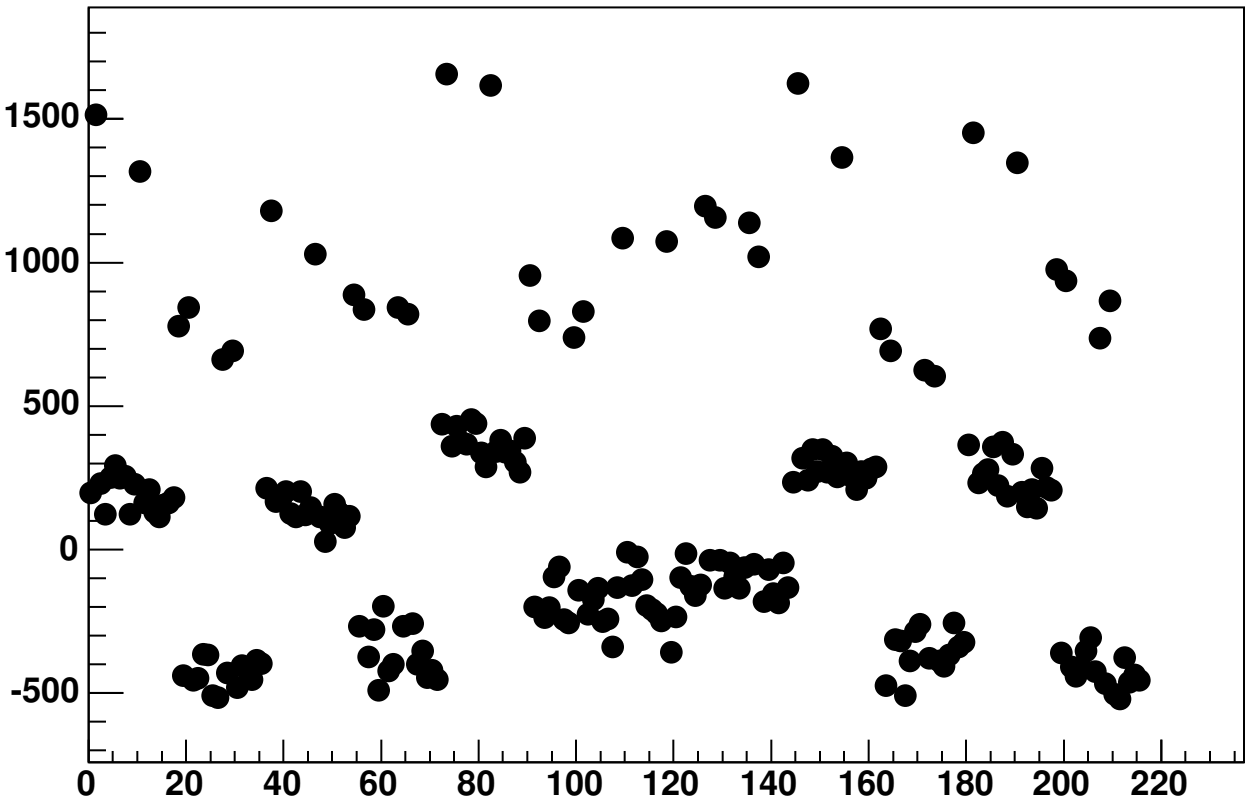
Enable 3, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



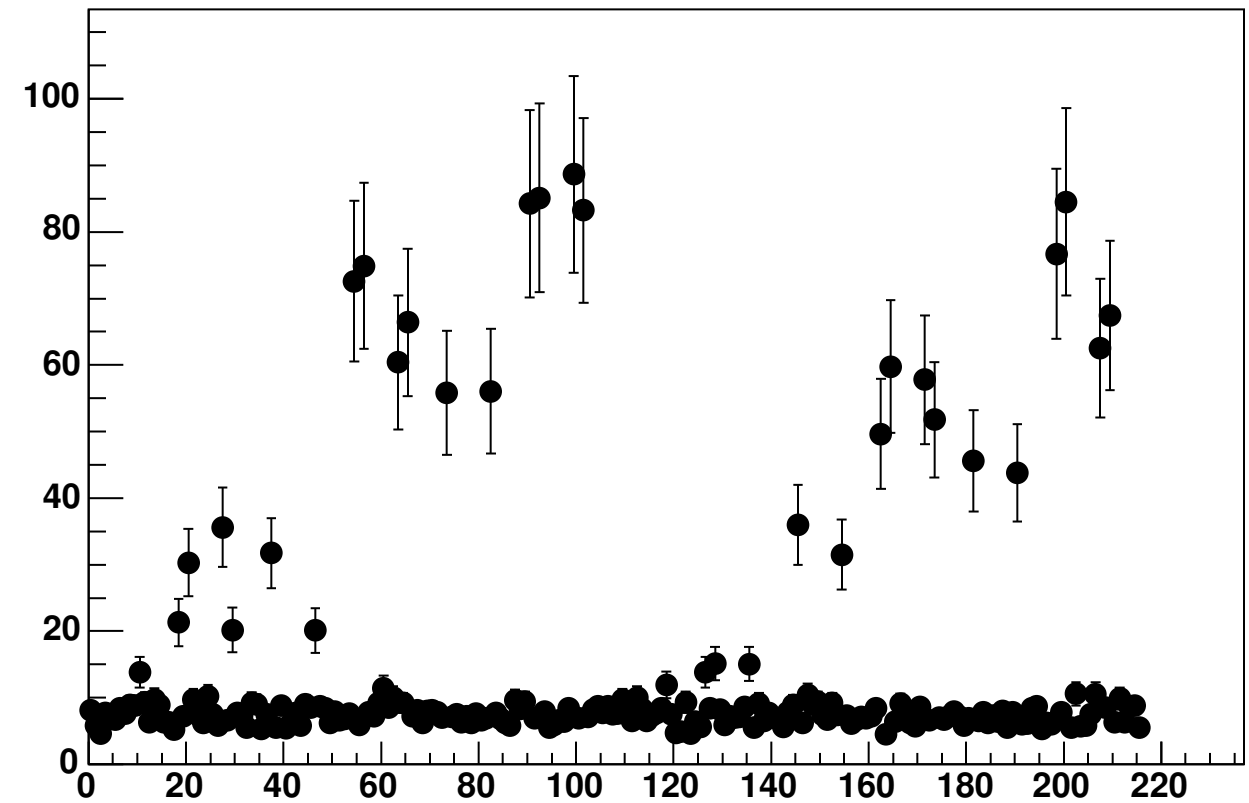
Enable 3, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



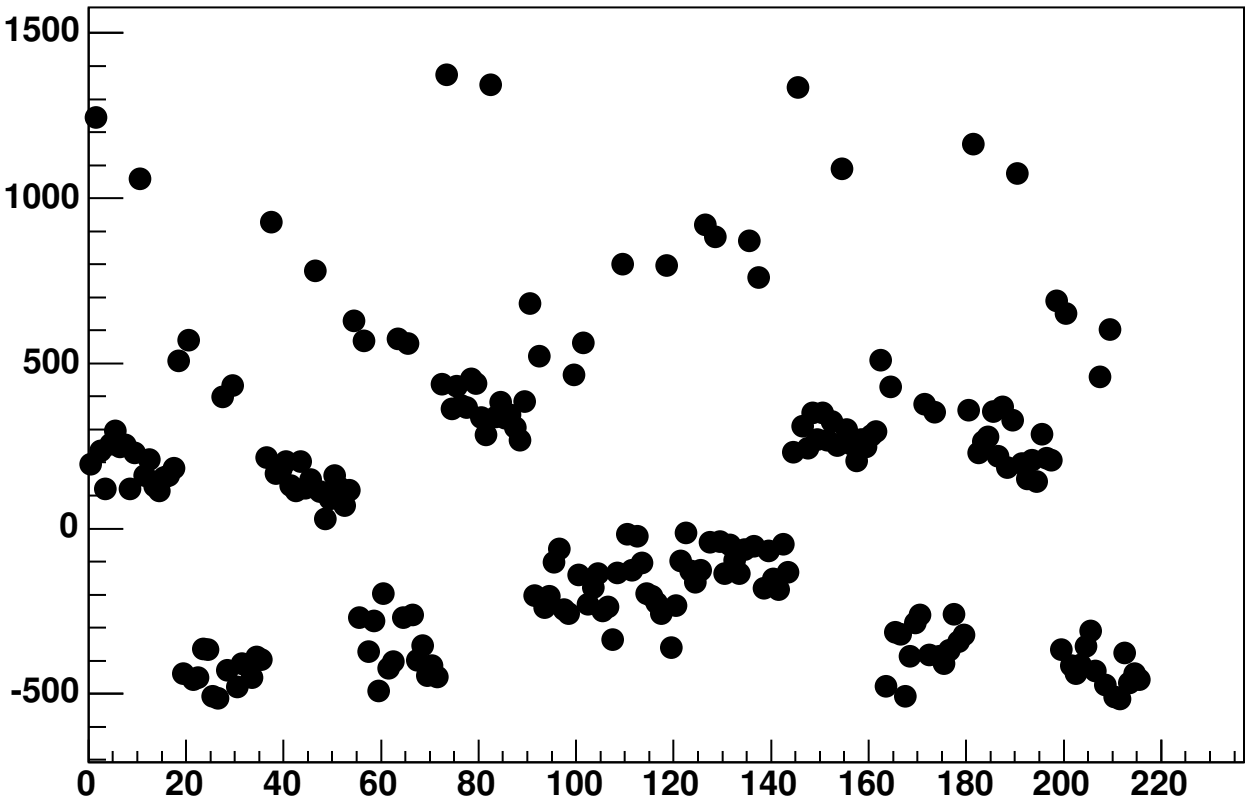
Enable 3, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



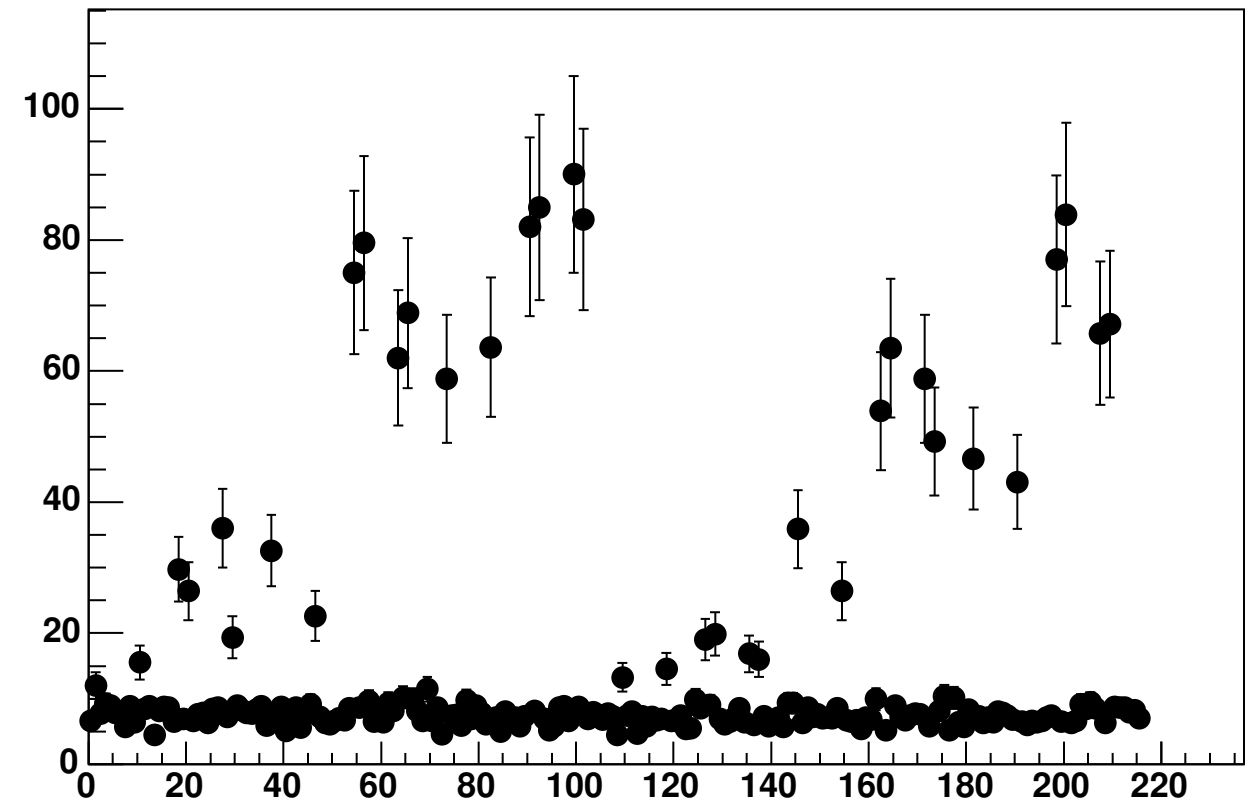
Enable 3, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



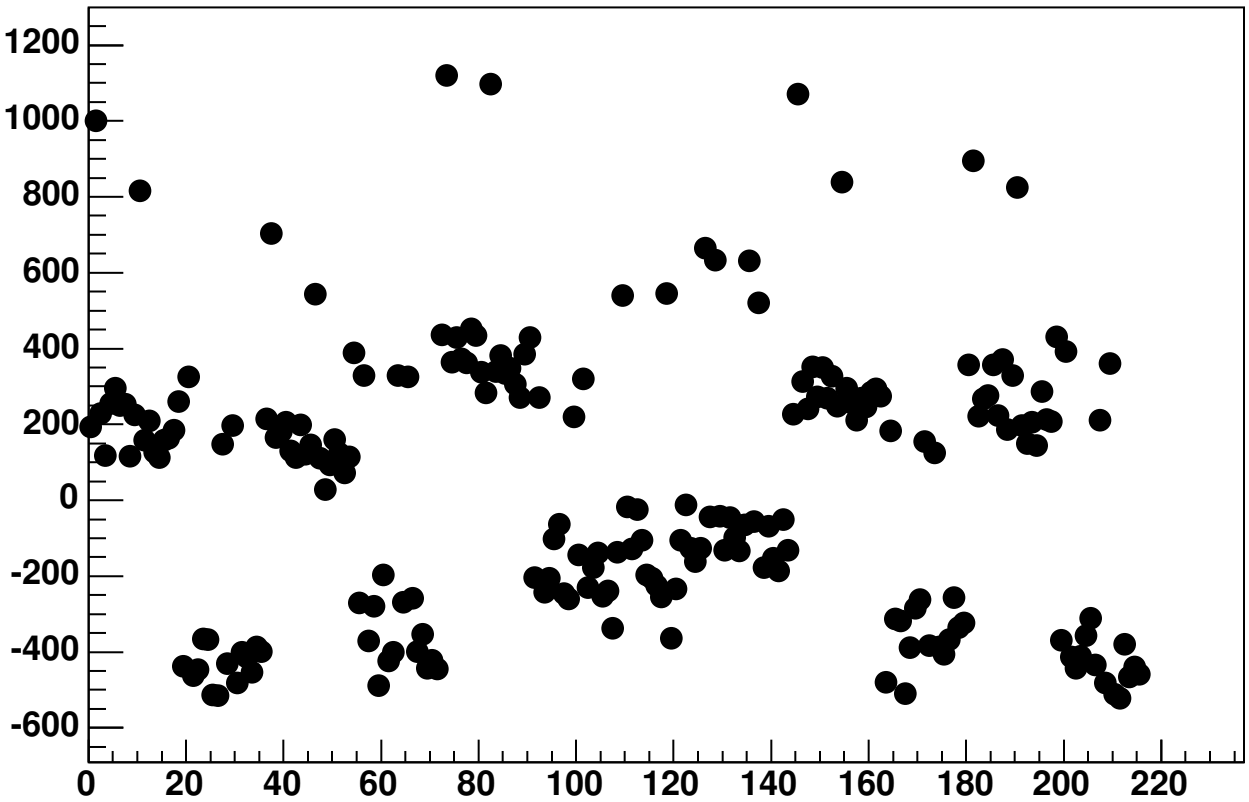
Enable 3, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



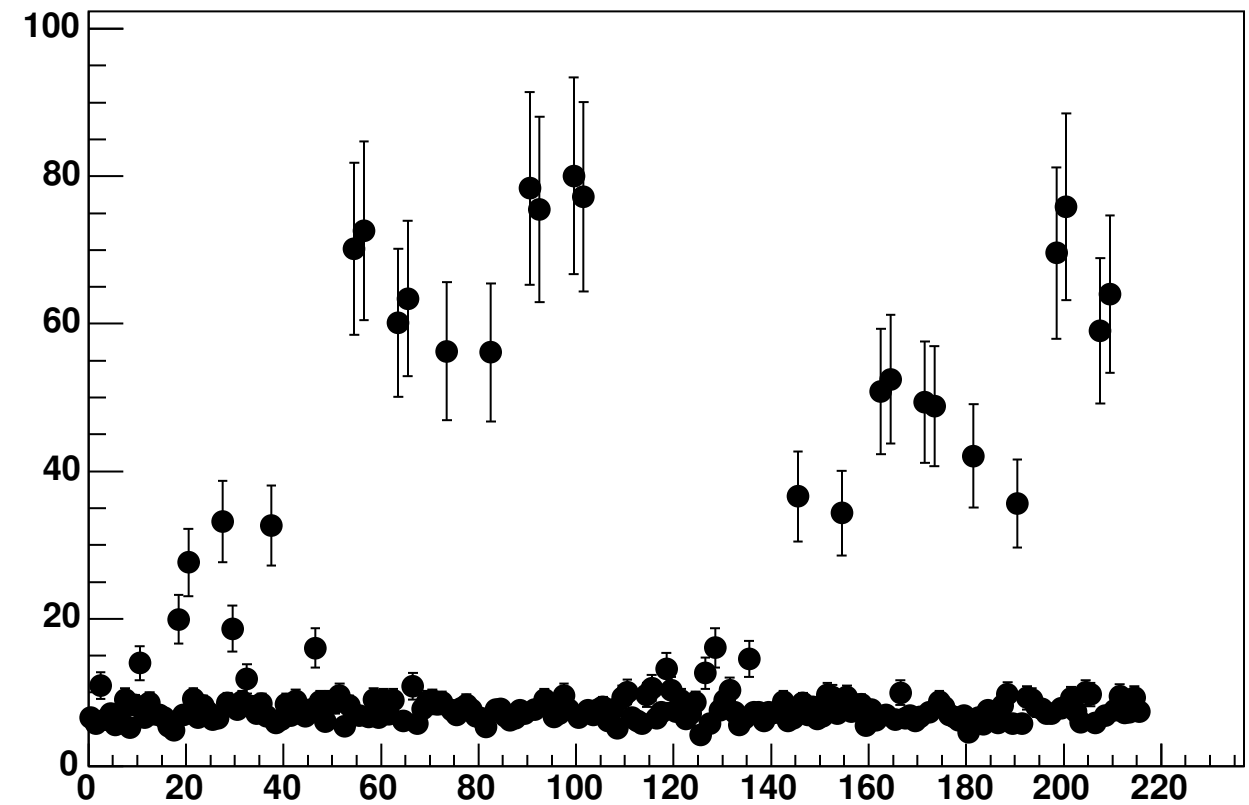
Enable 3, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



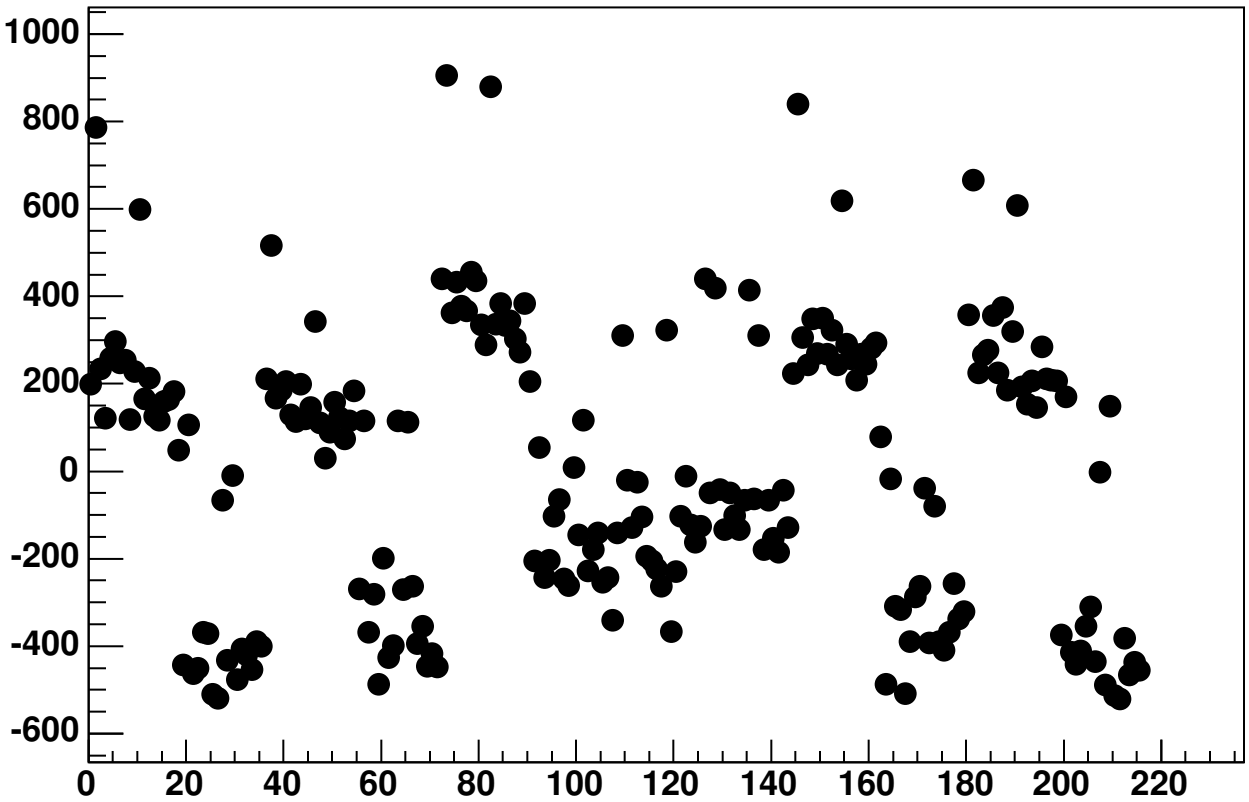
Enable 3, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



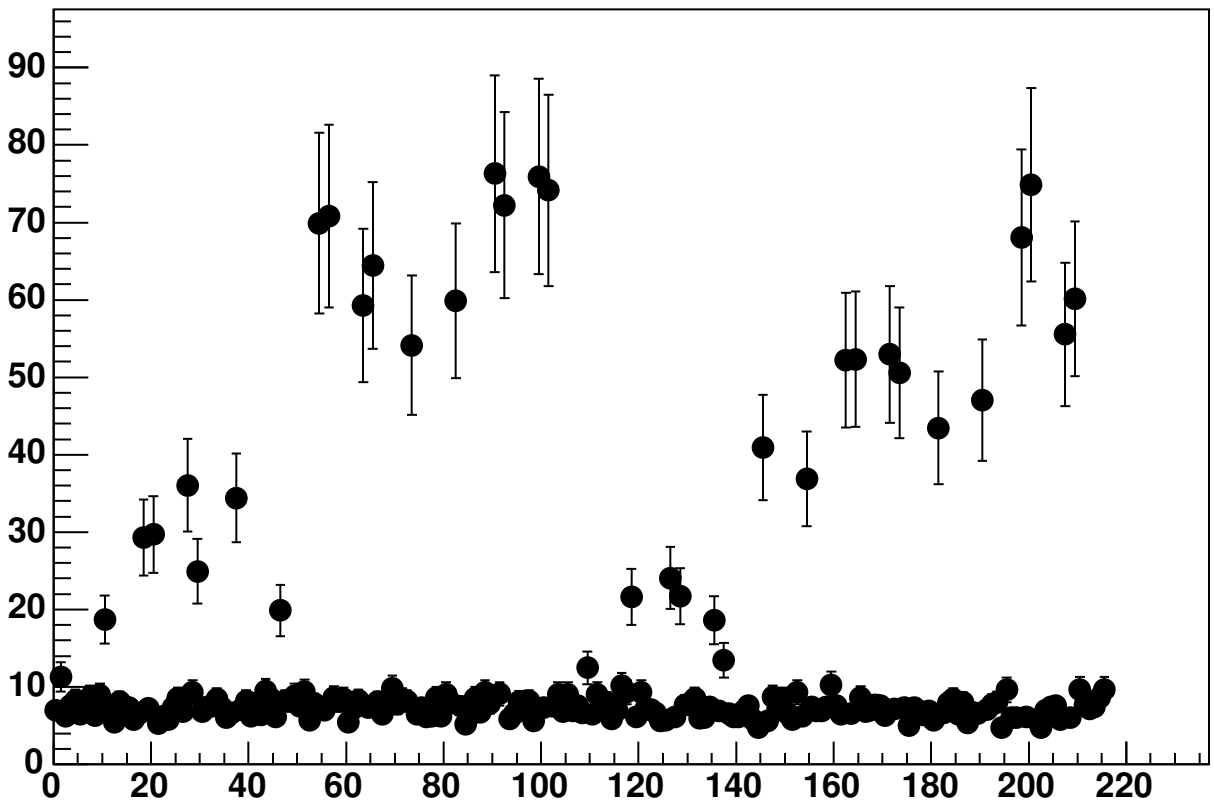
Enable 3, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



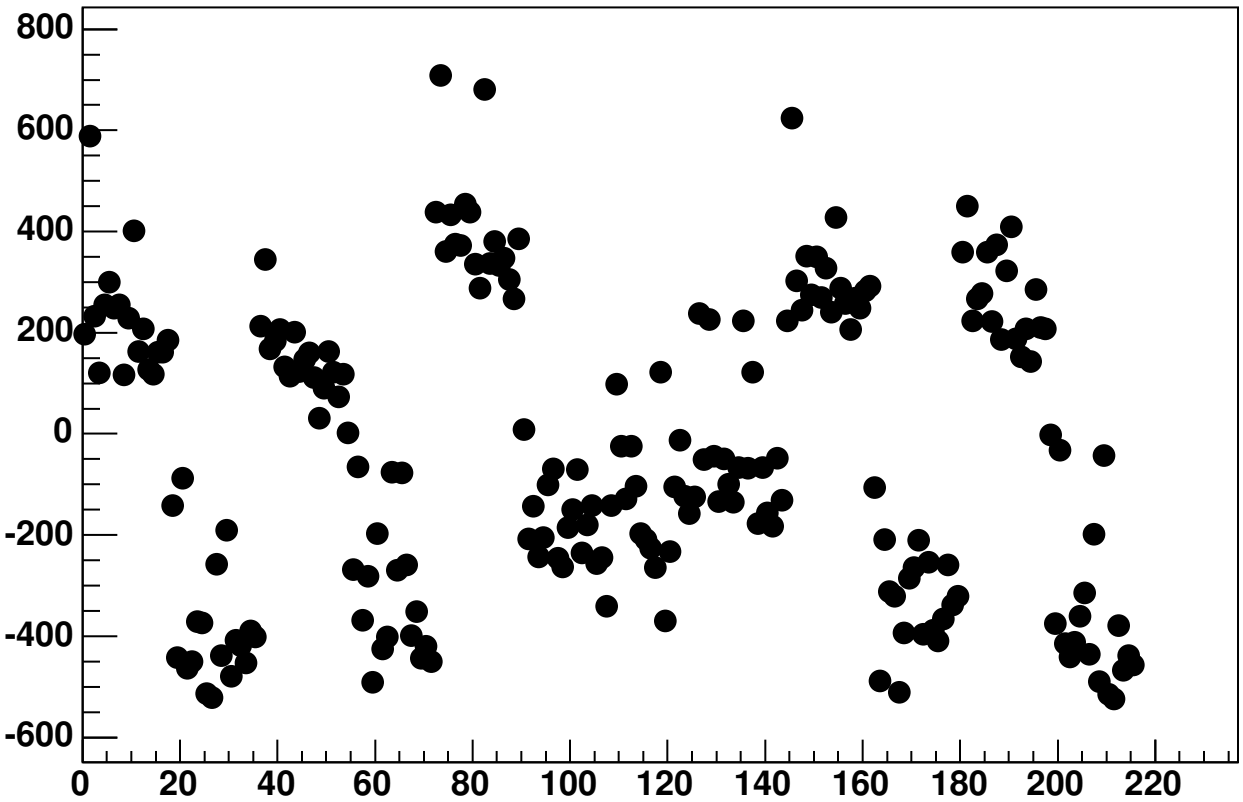
Enable 3, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



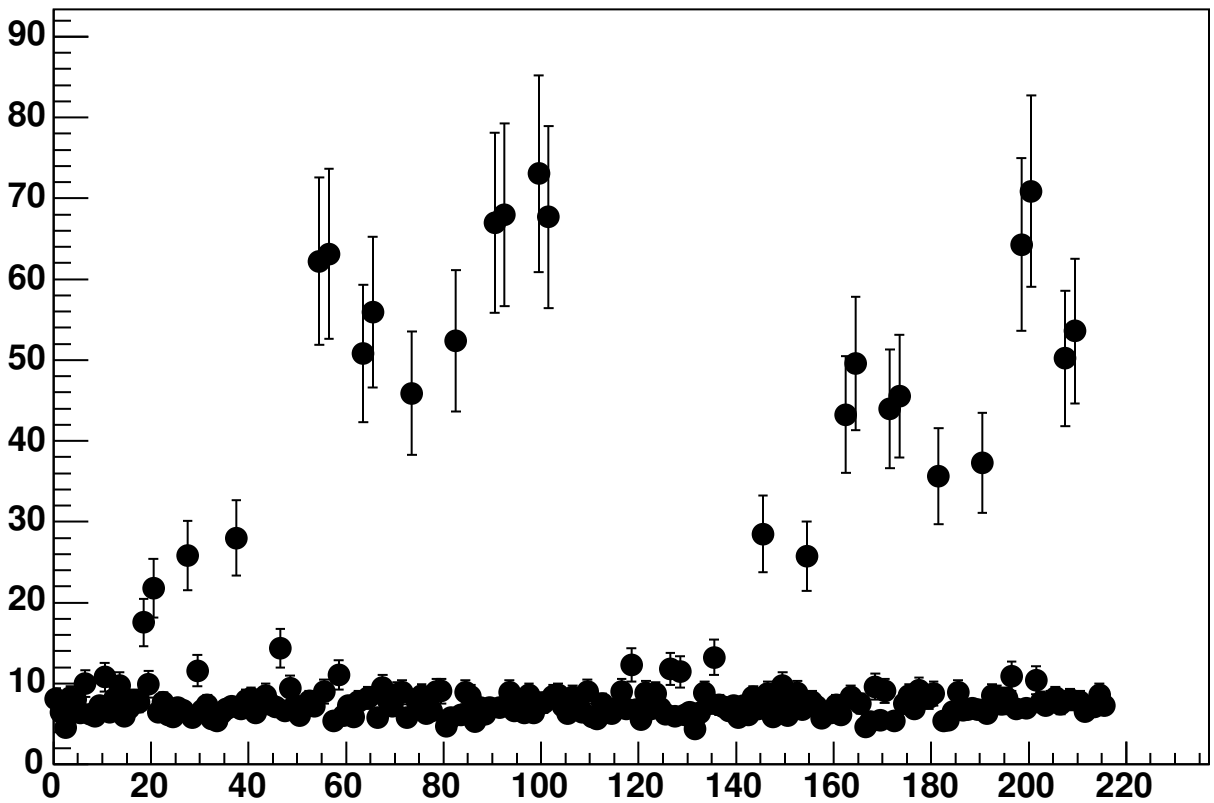
Enable 3, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



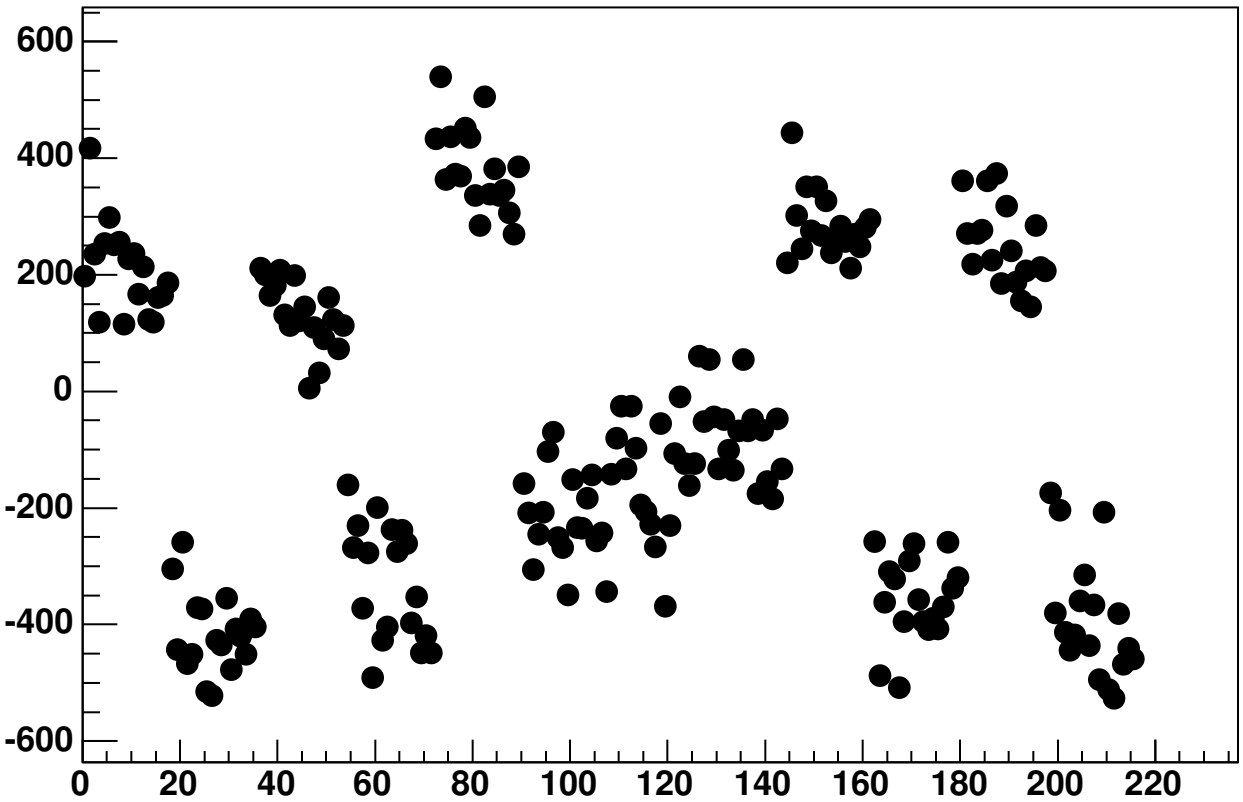
Enable 3, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



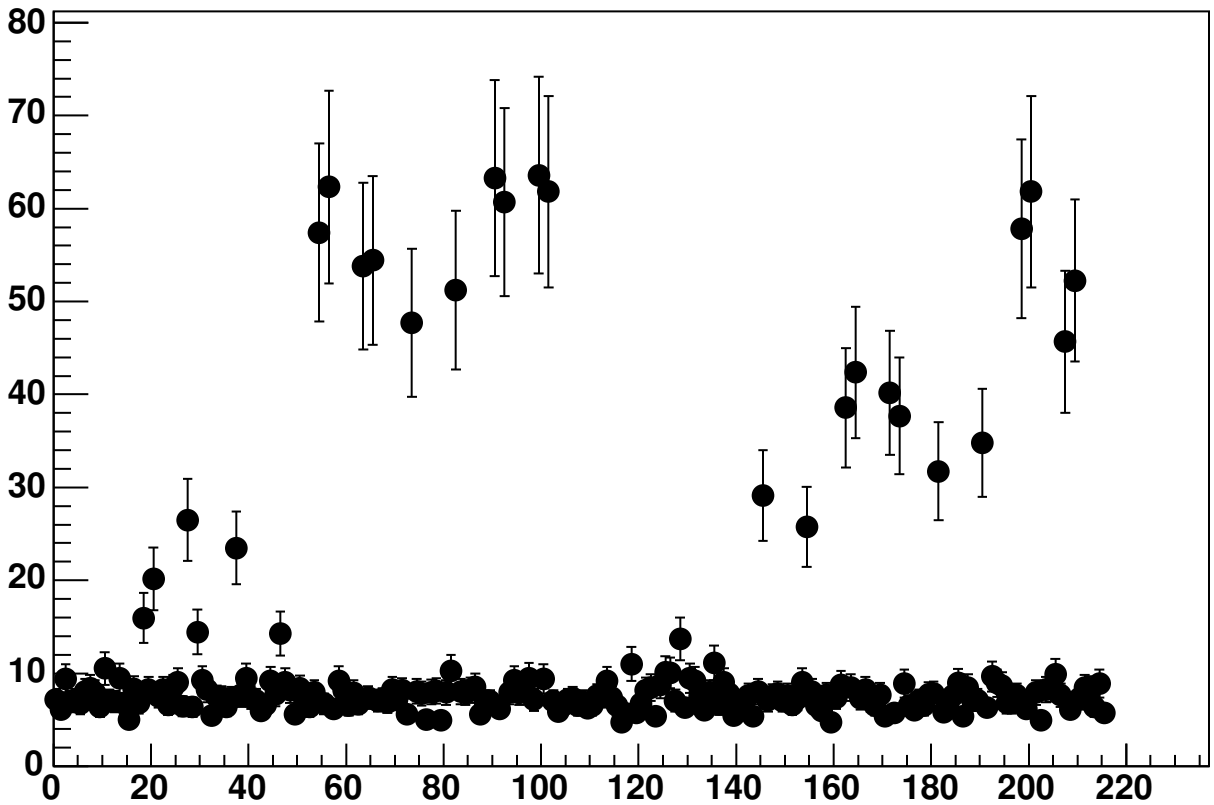
Enable 3, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



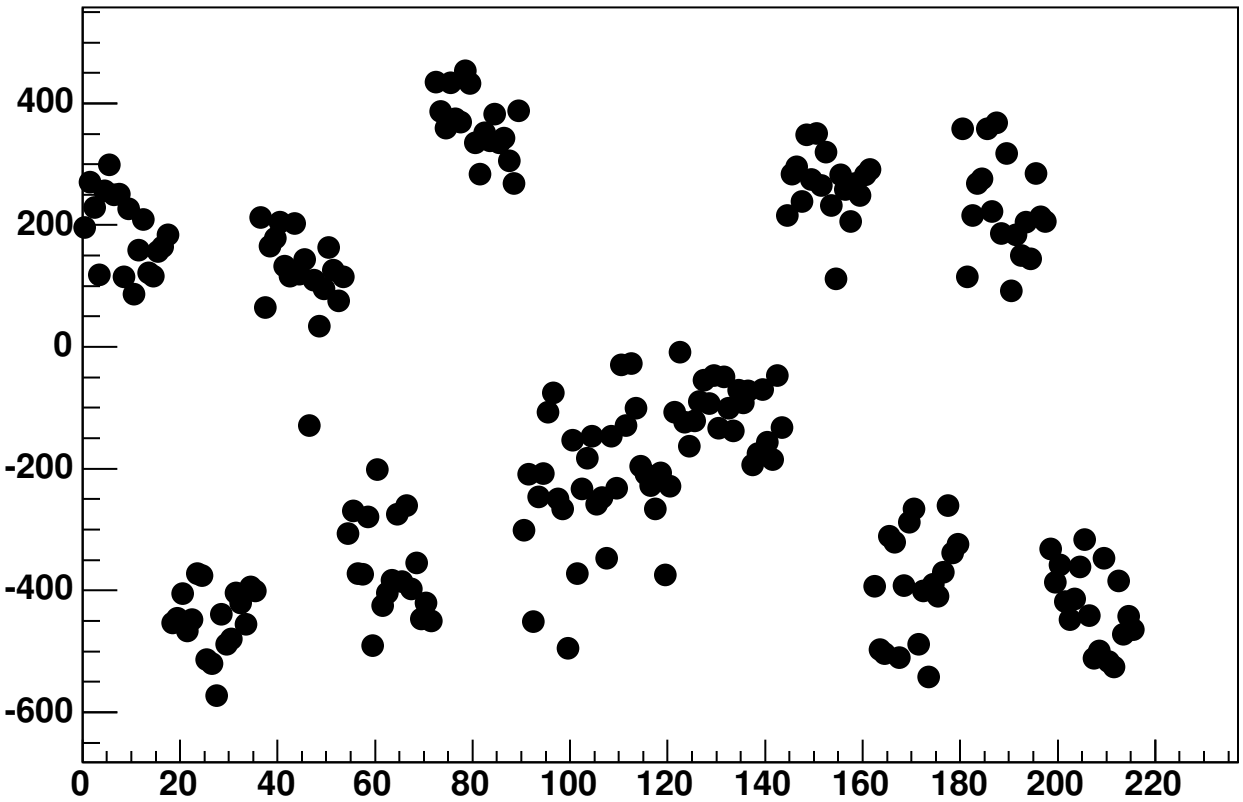
Enable 3, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



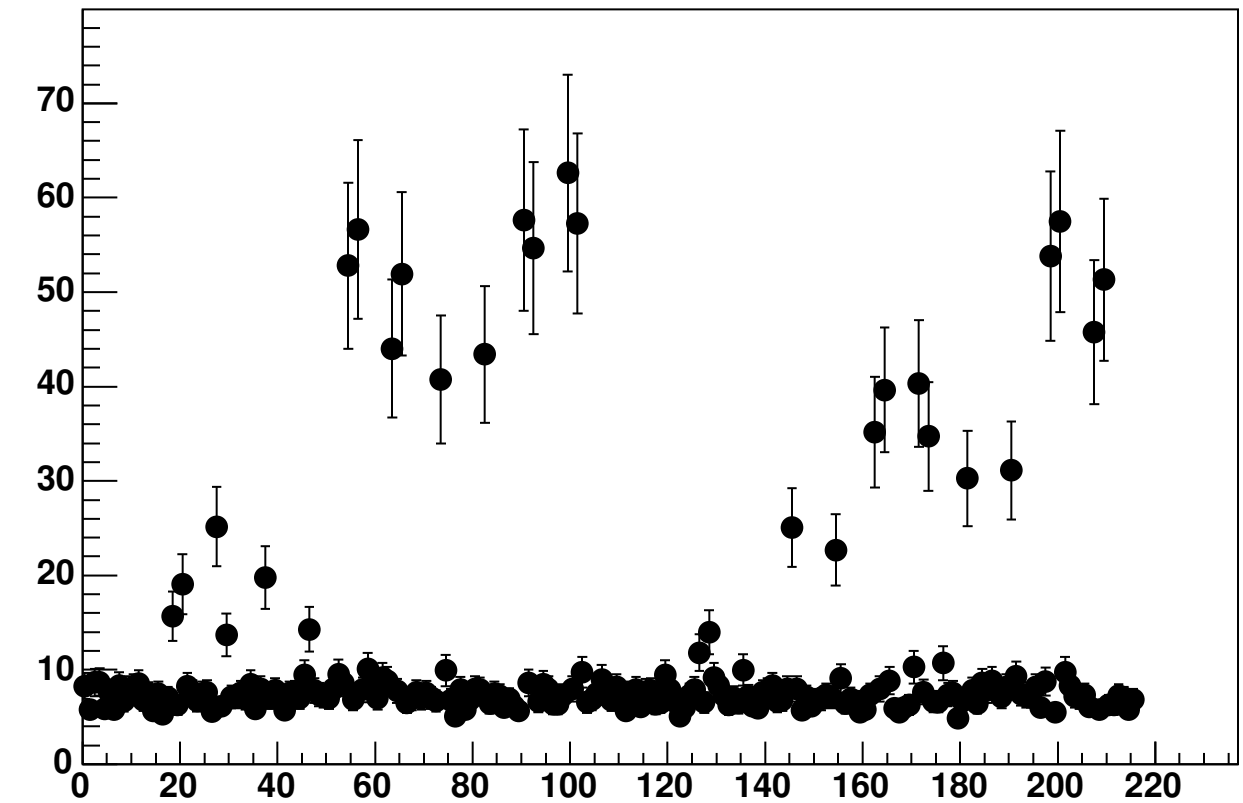
Enable 3, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



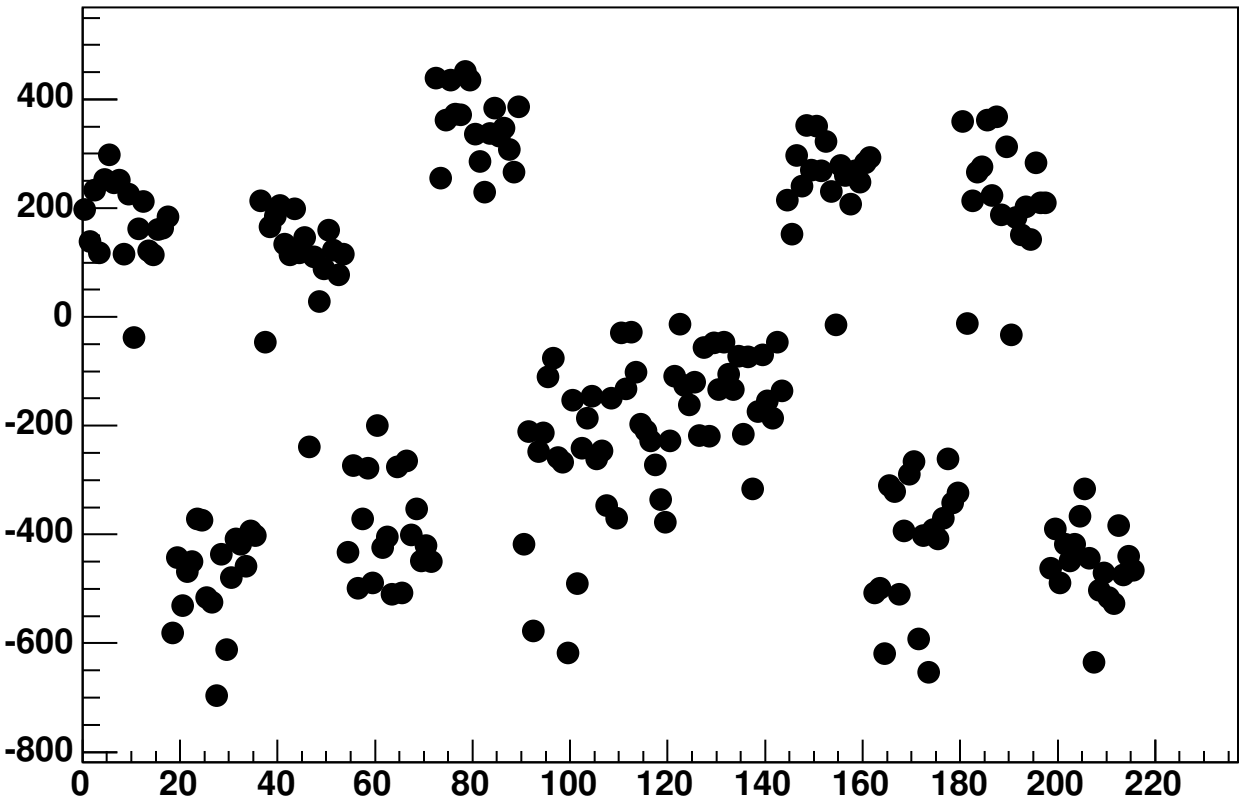
Enable 3, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



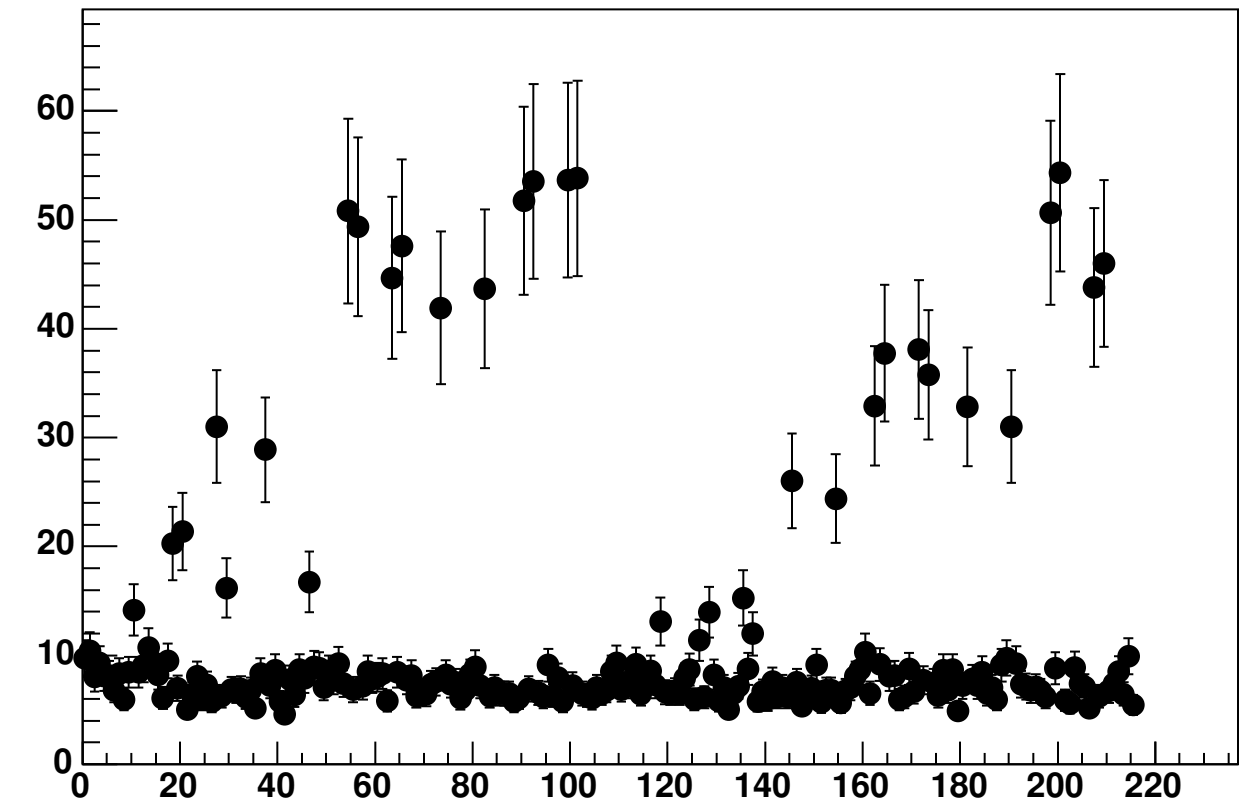
Enable 3, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



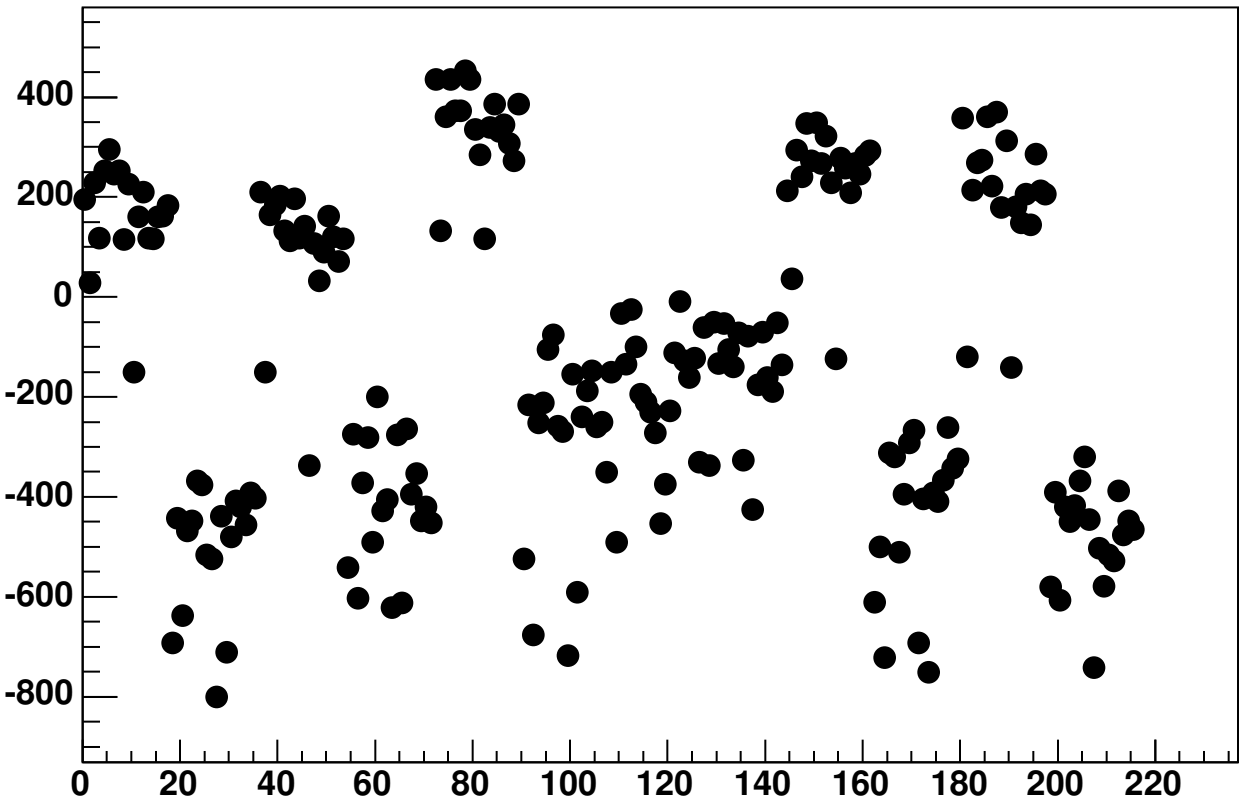
Enable 3, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



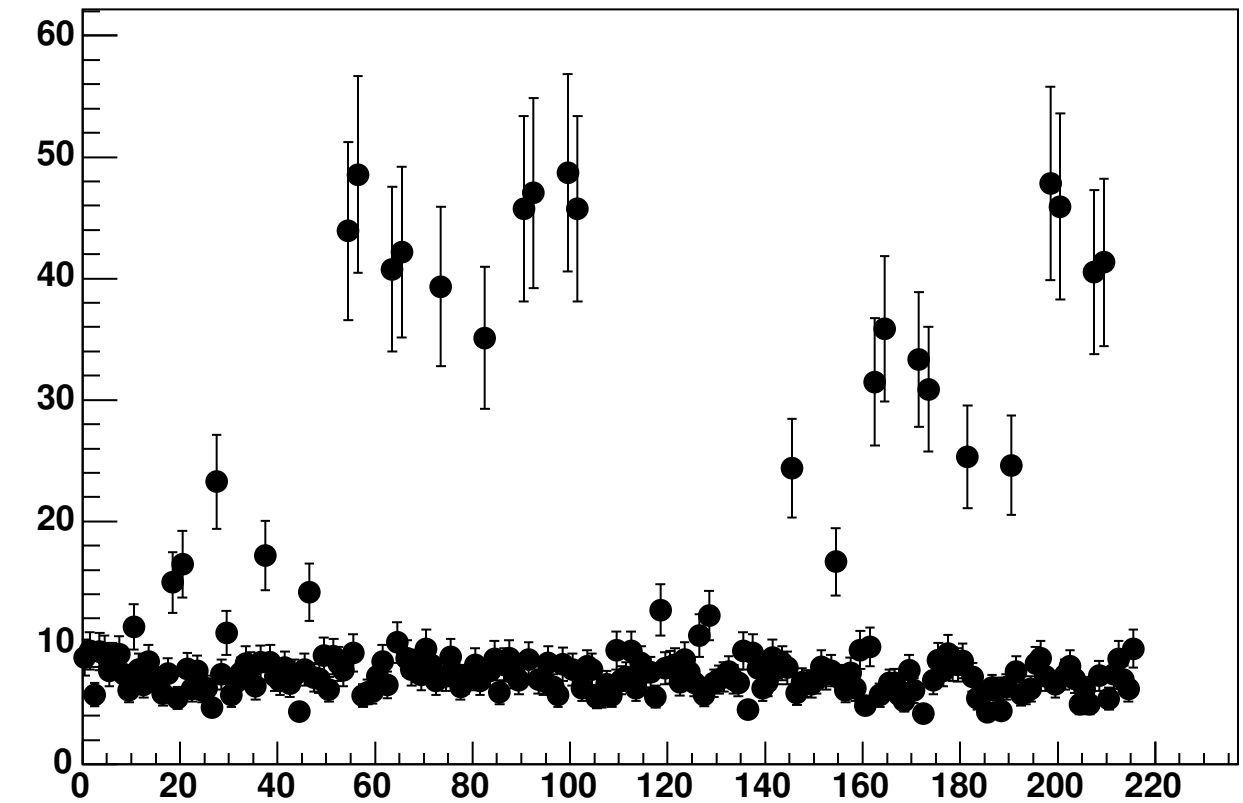
Enable 3, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



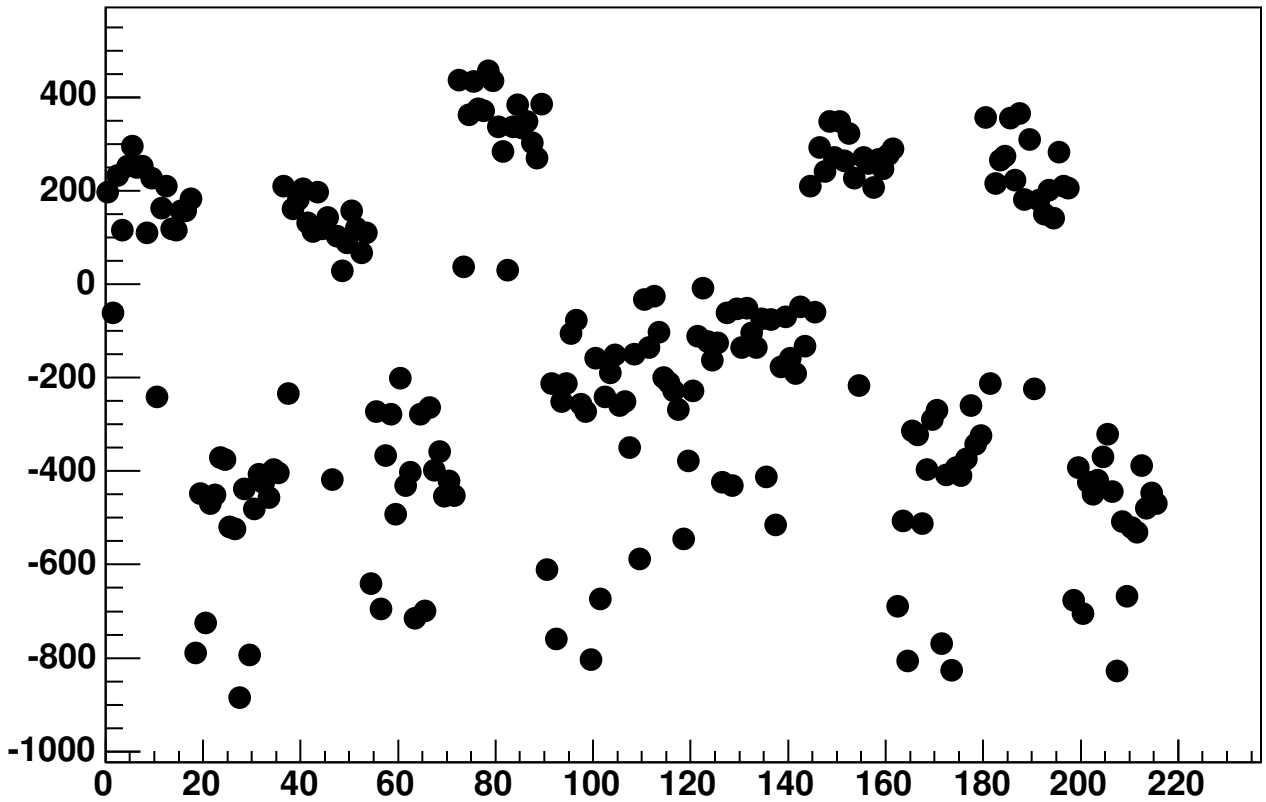
Enable 3, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



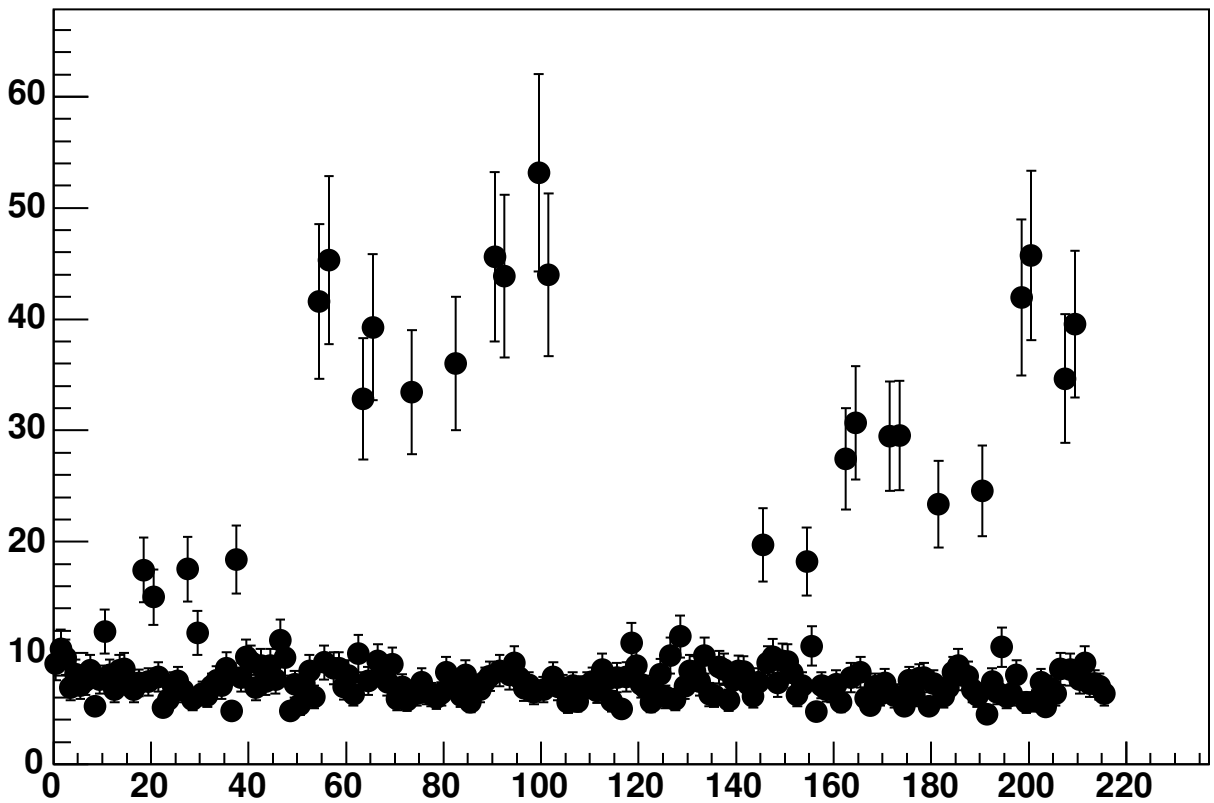
Enable 3, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



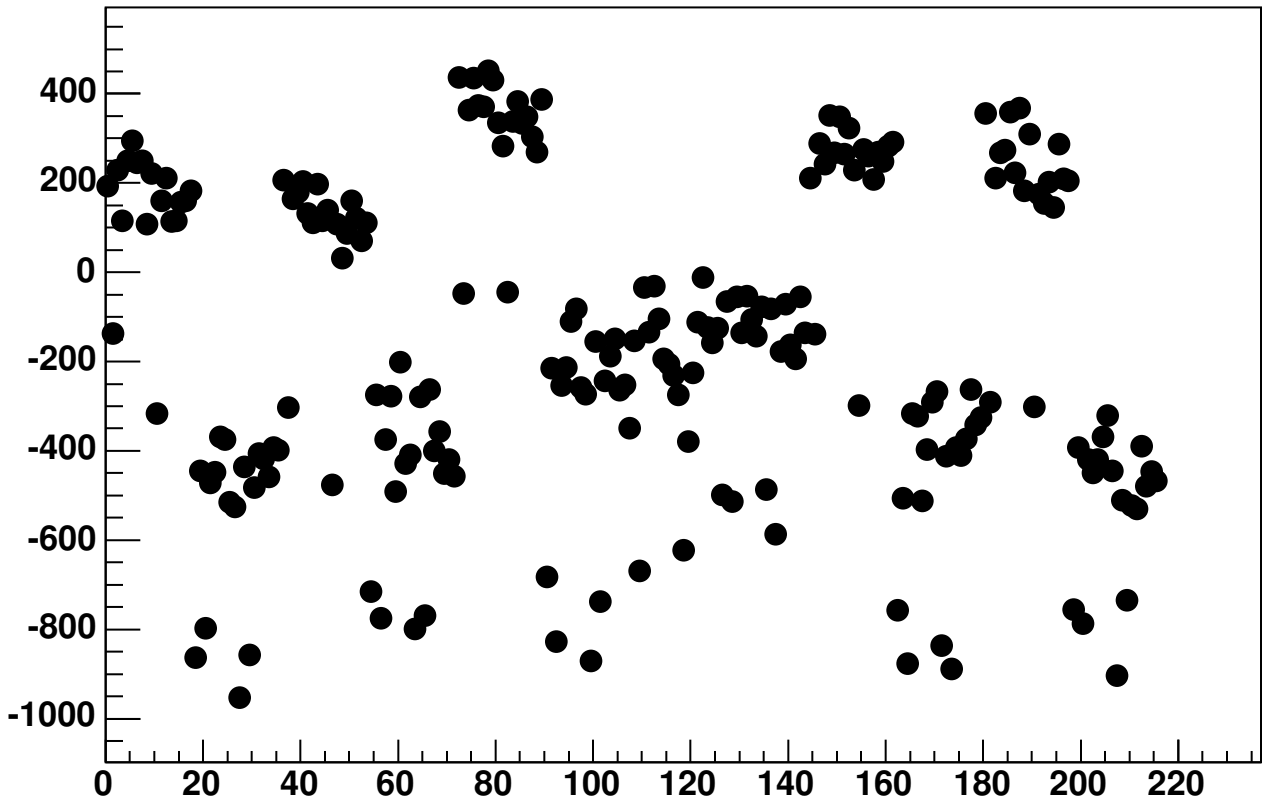
Enable 3, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



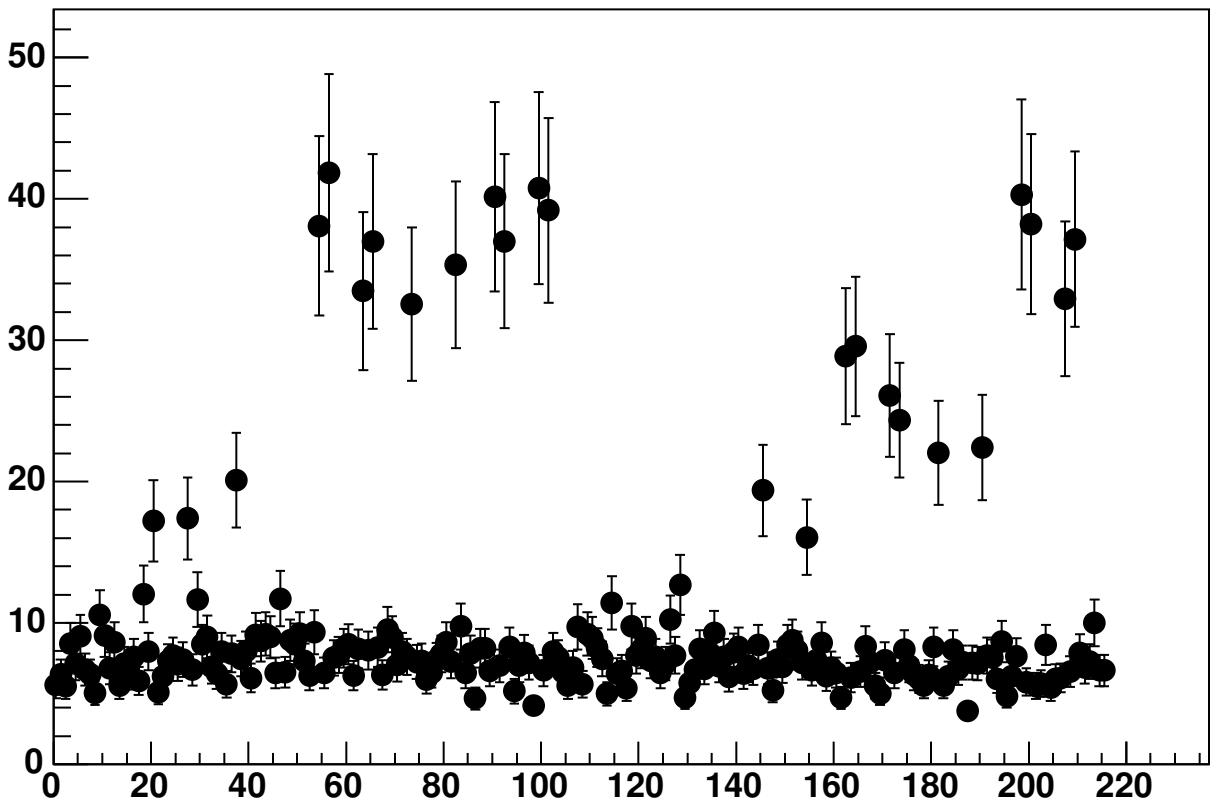
Enable 3, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



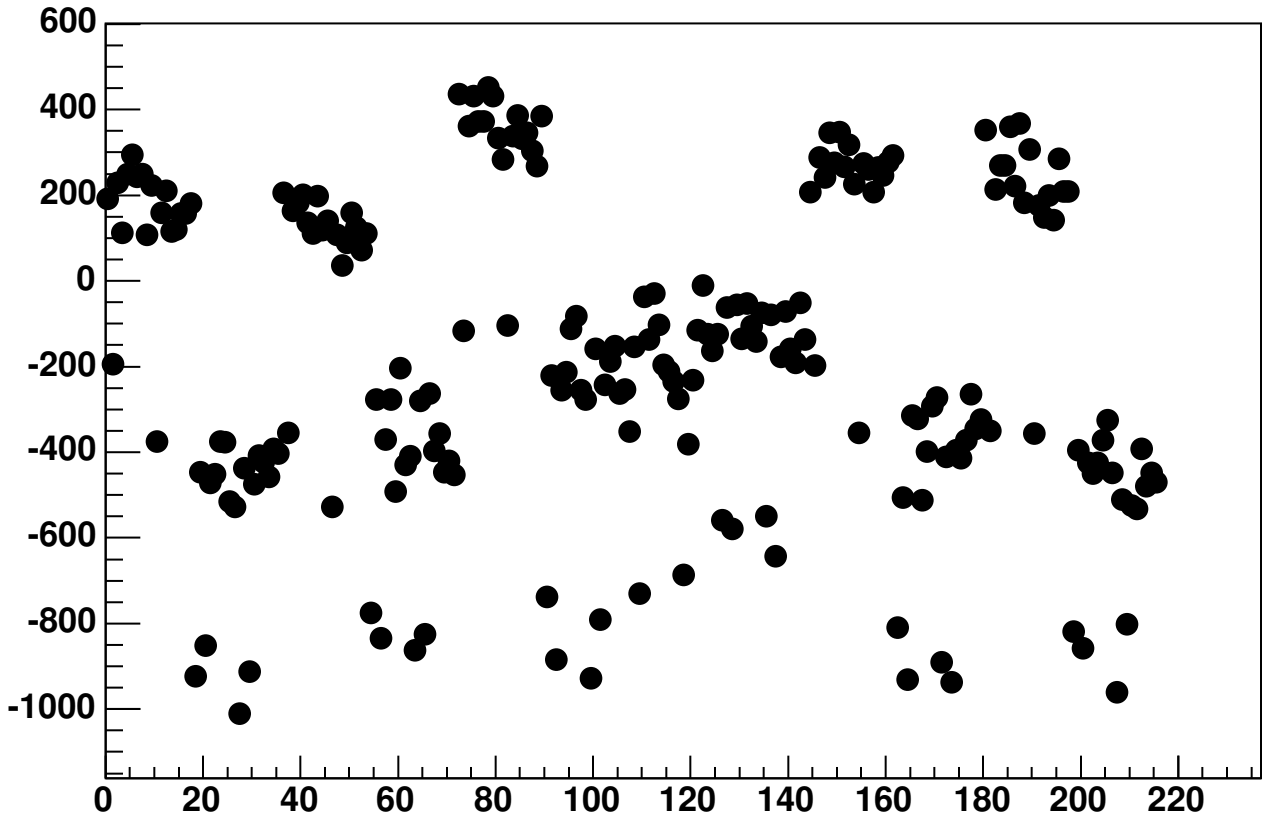
Enable 3, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



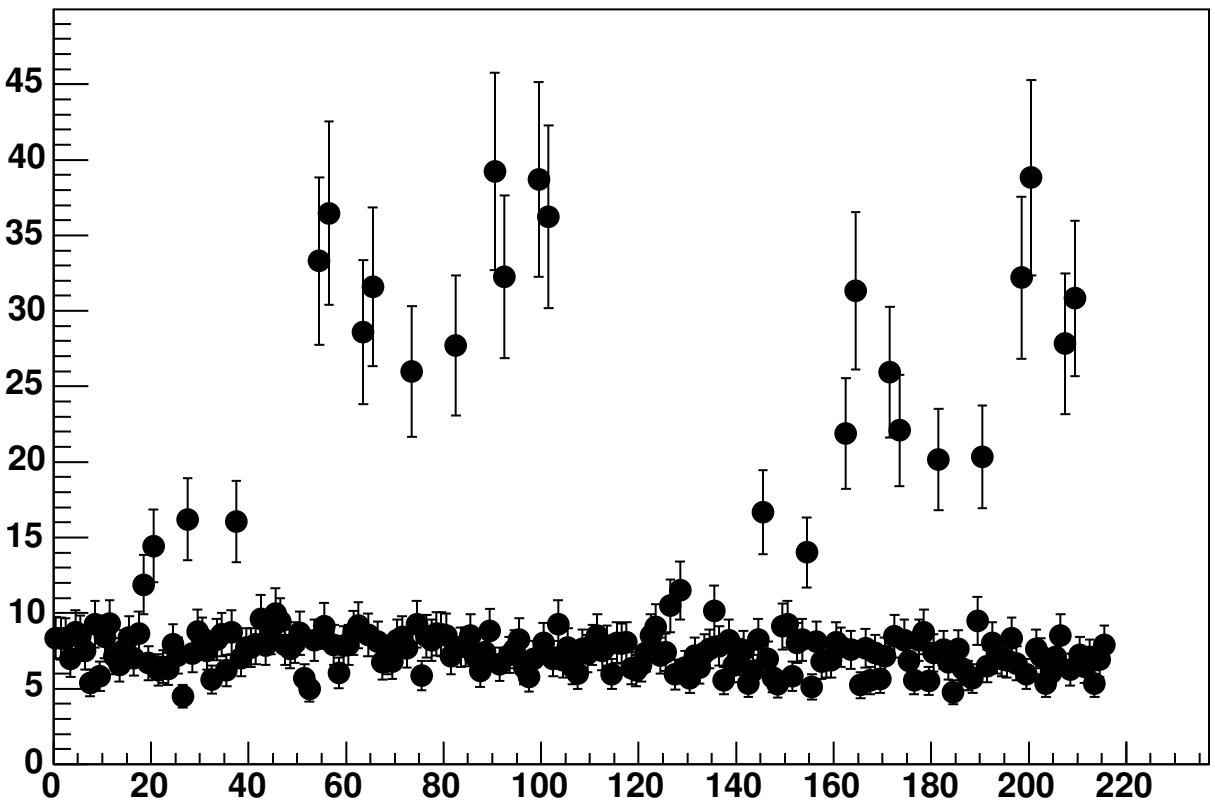
Enable 3, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



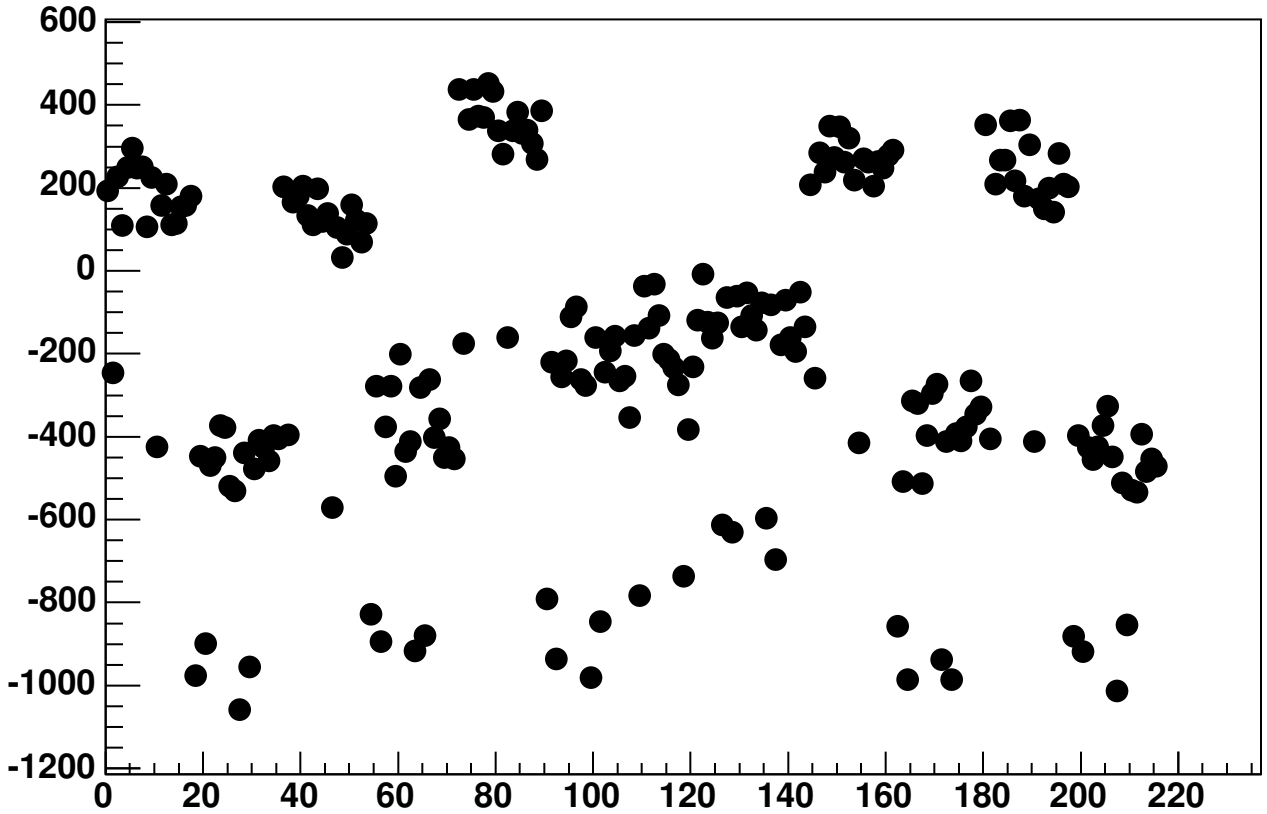
Enable 3, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



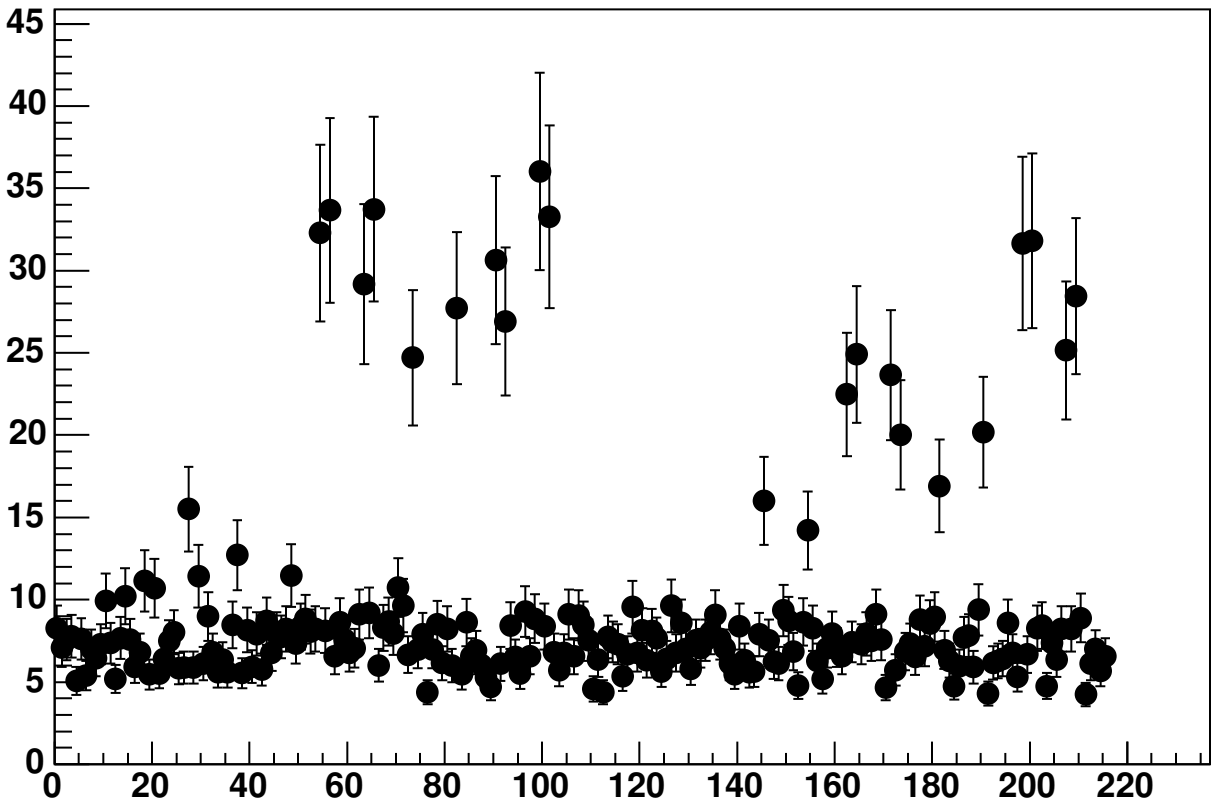
Enable 3, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



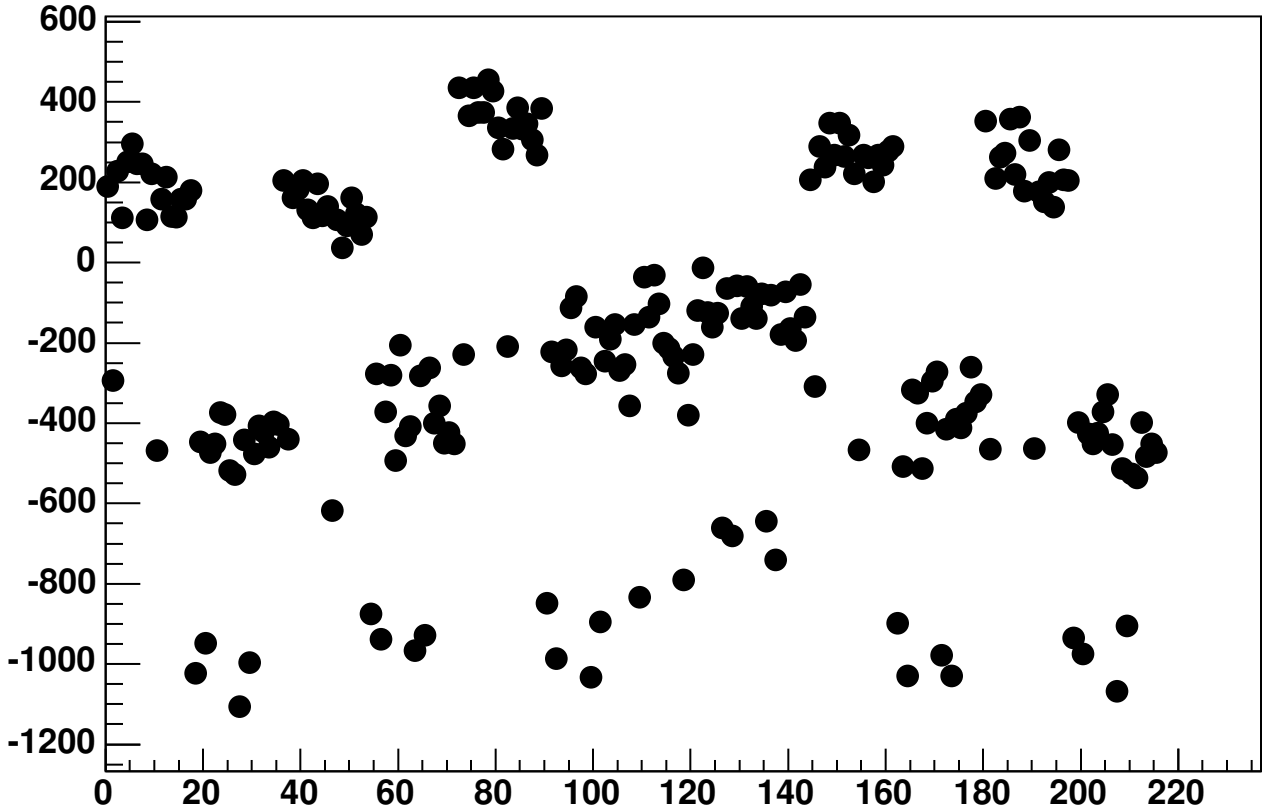
Enable 3, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



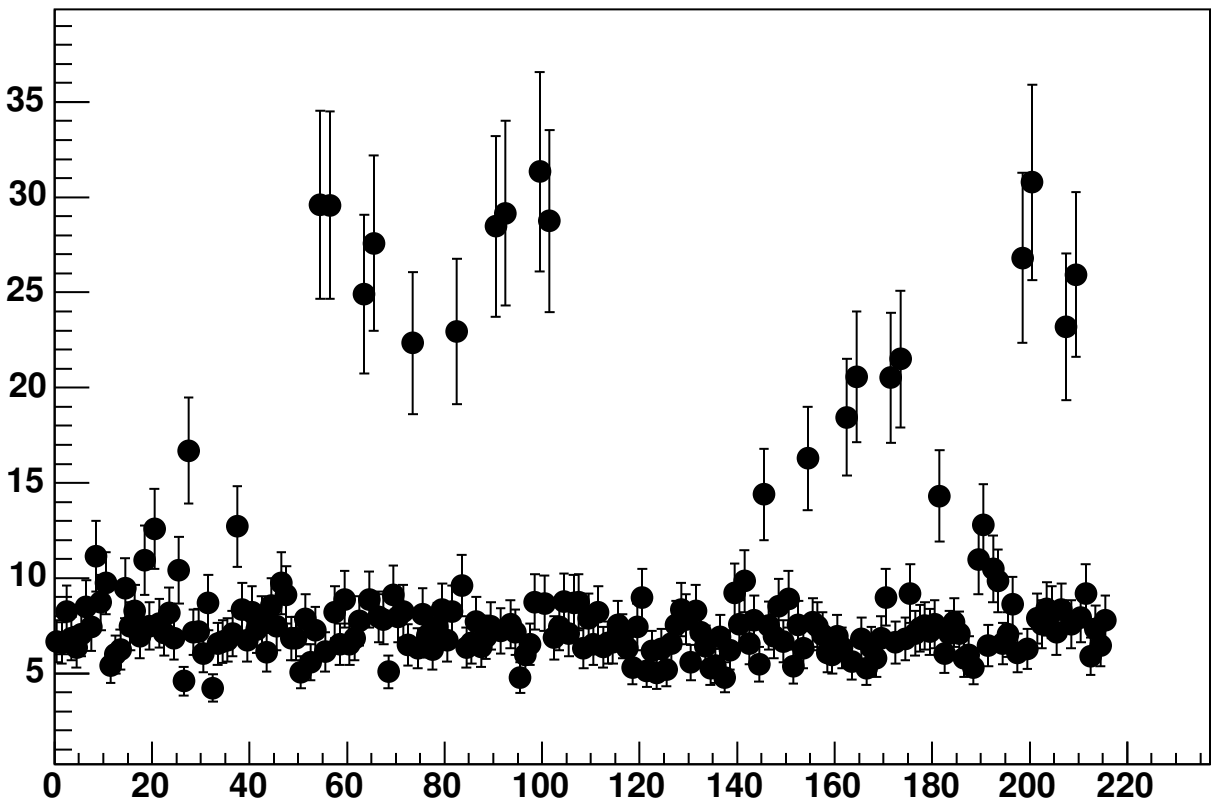
Enable 3, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



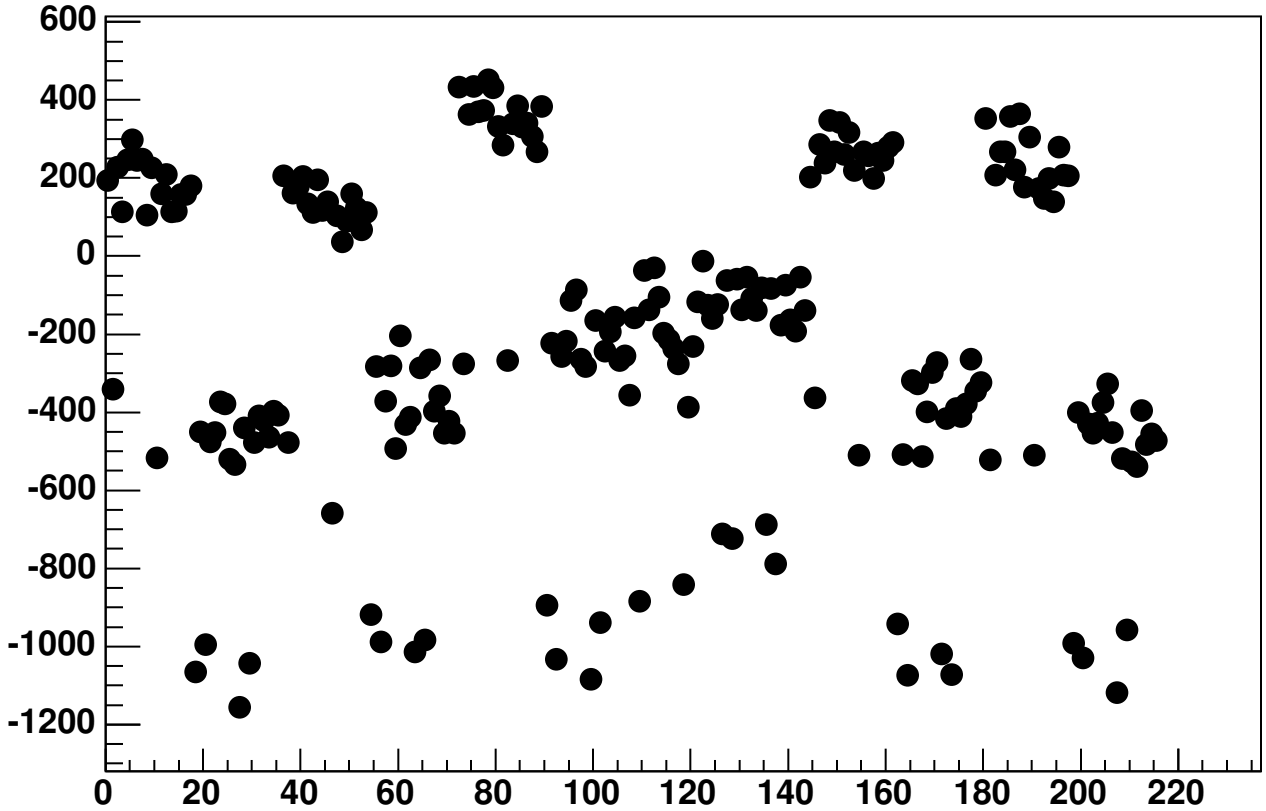
Enable 3, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



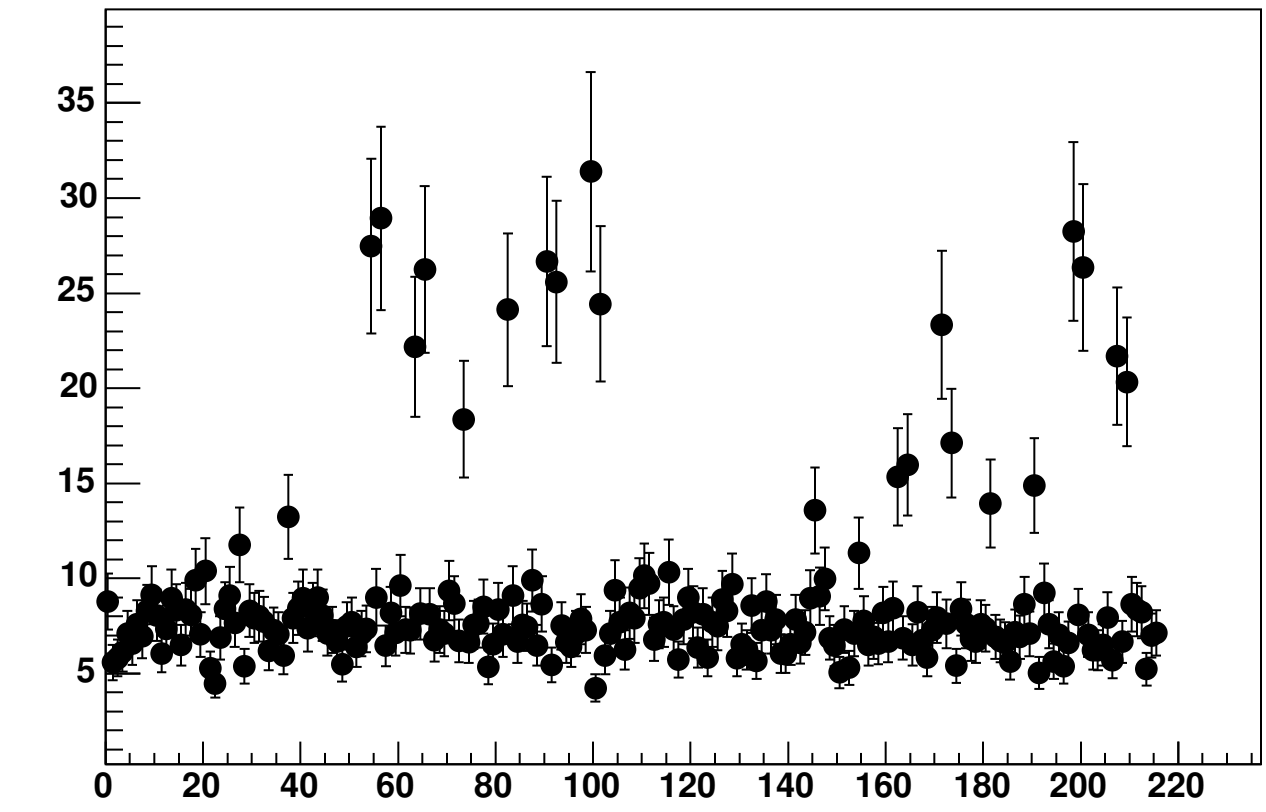
Enable 3, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



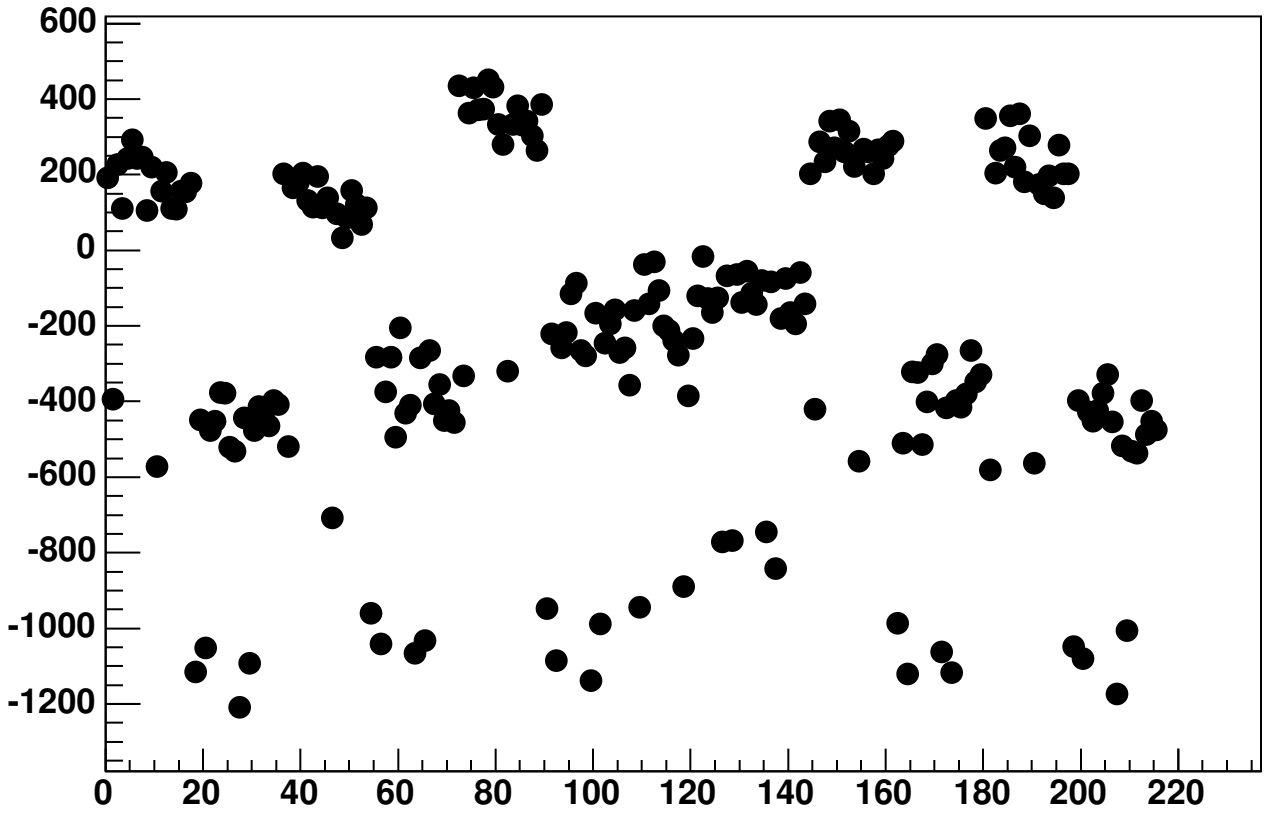
Enable 3, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



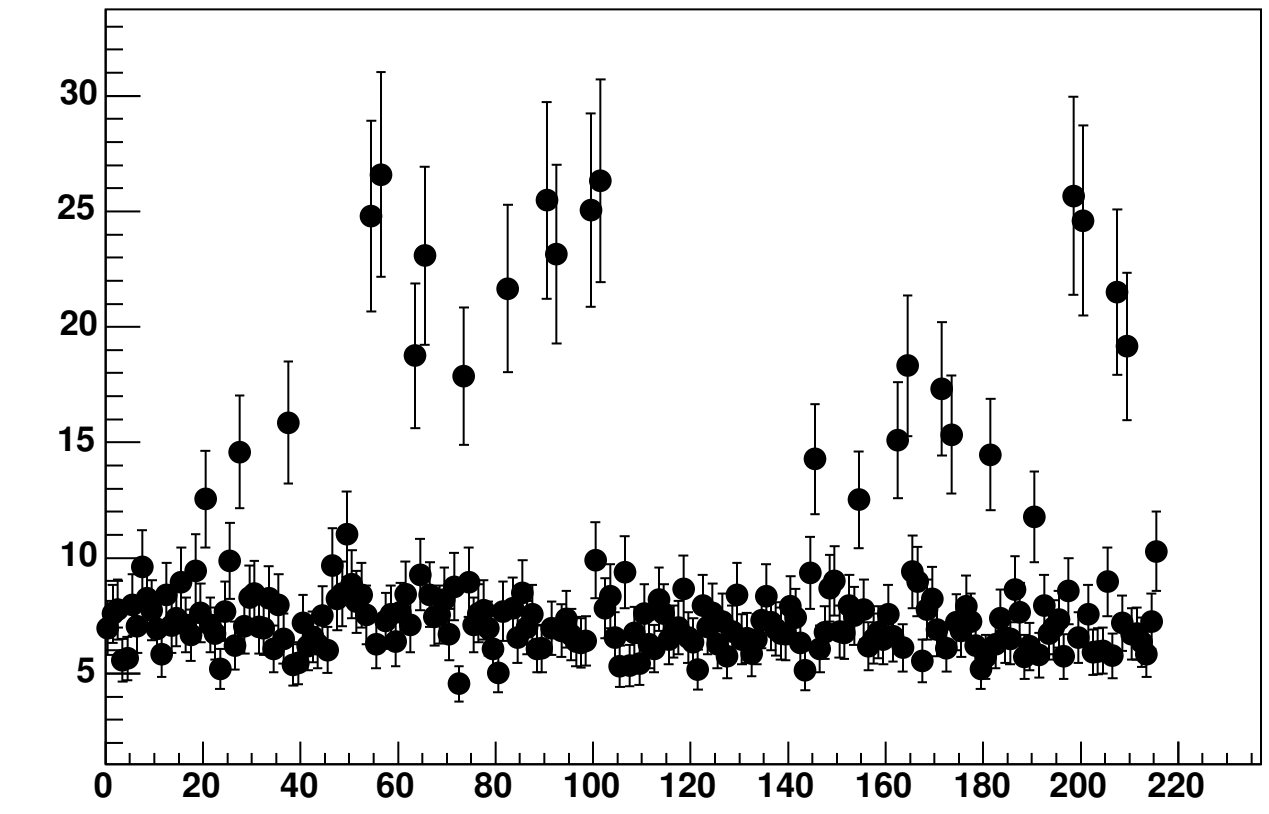
Enable 3, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



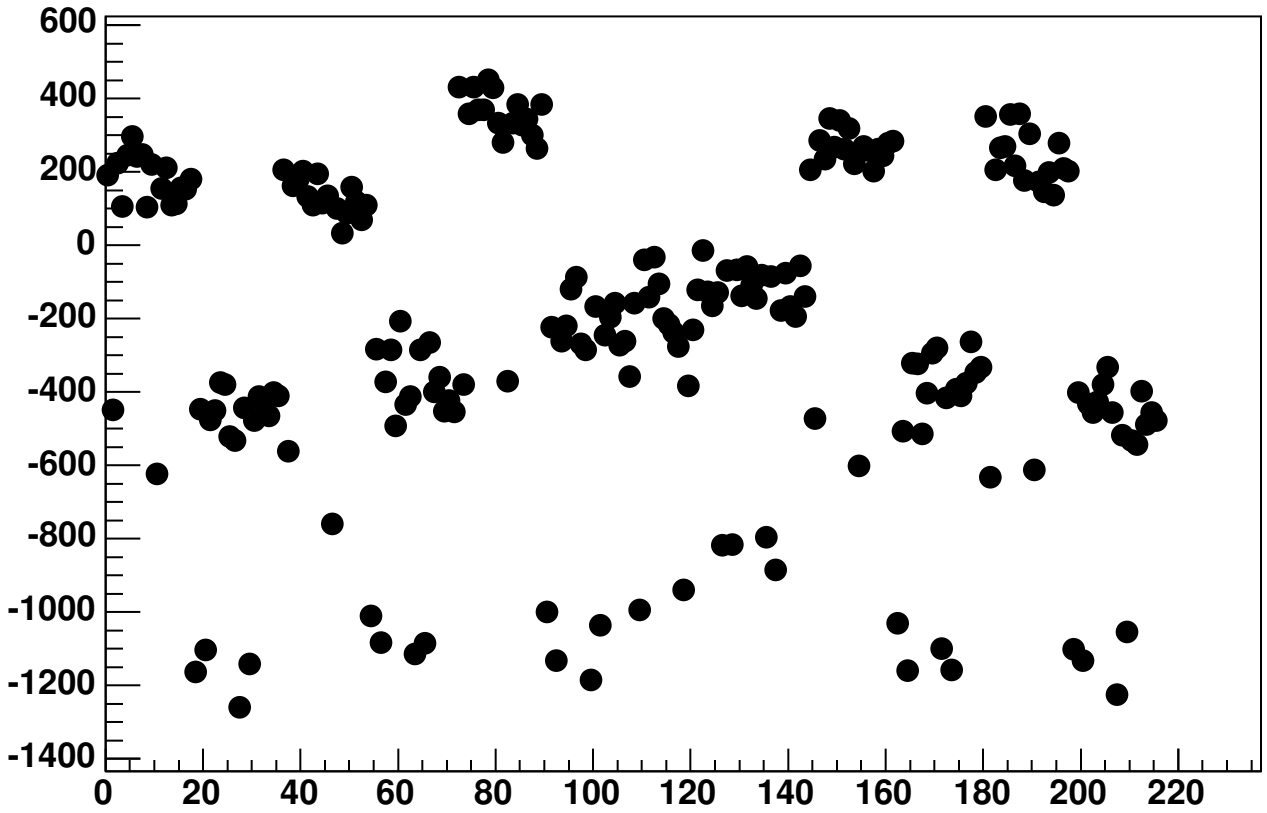
Enable 3, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



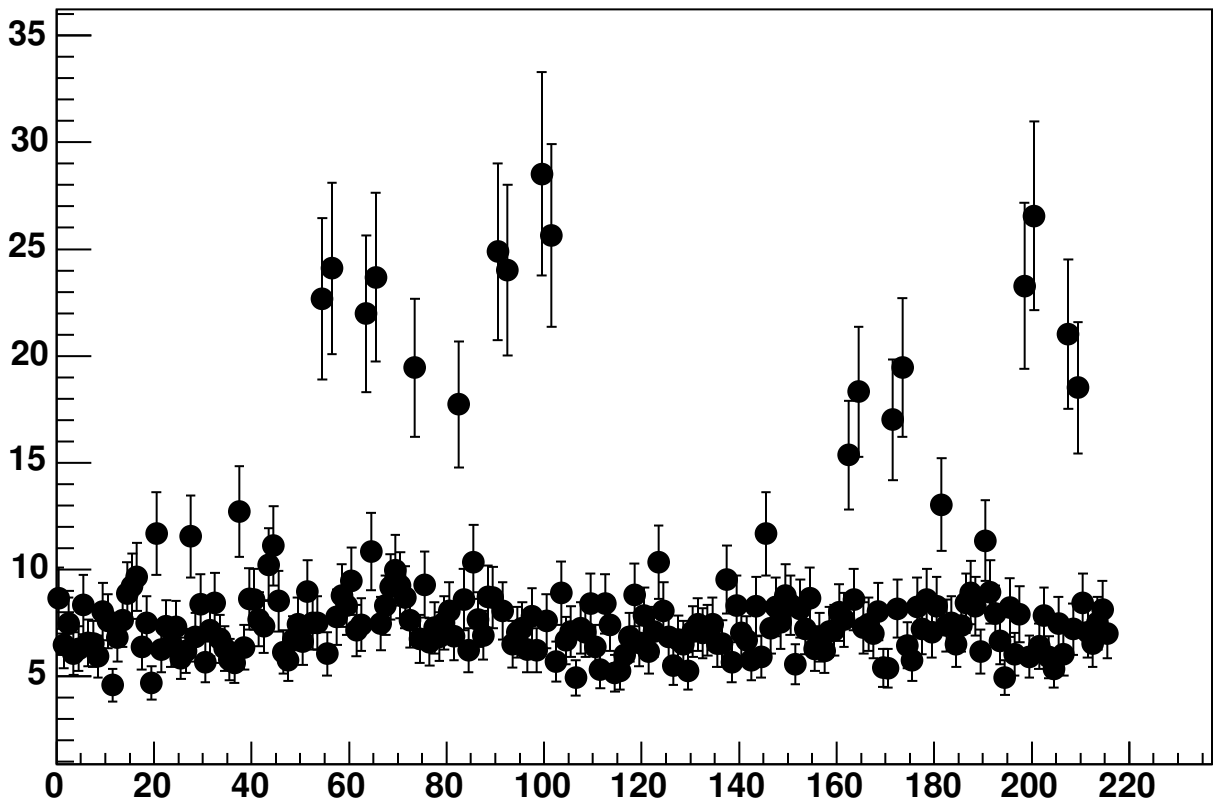
Enable 3, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



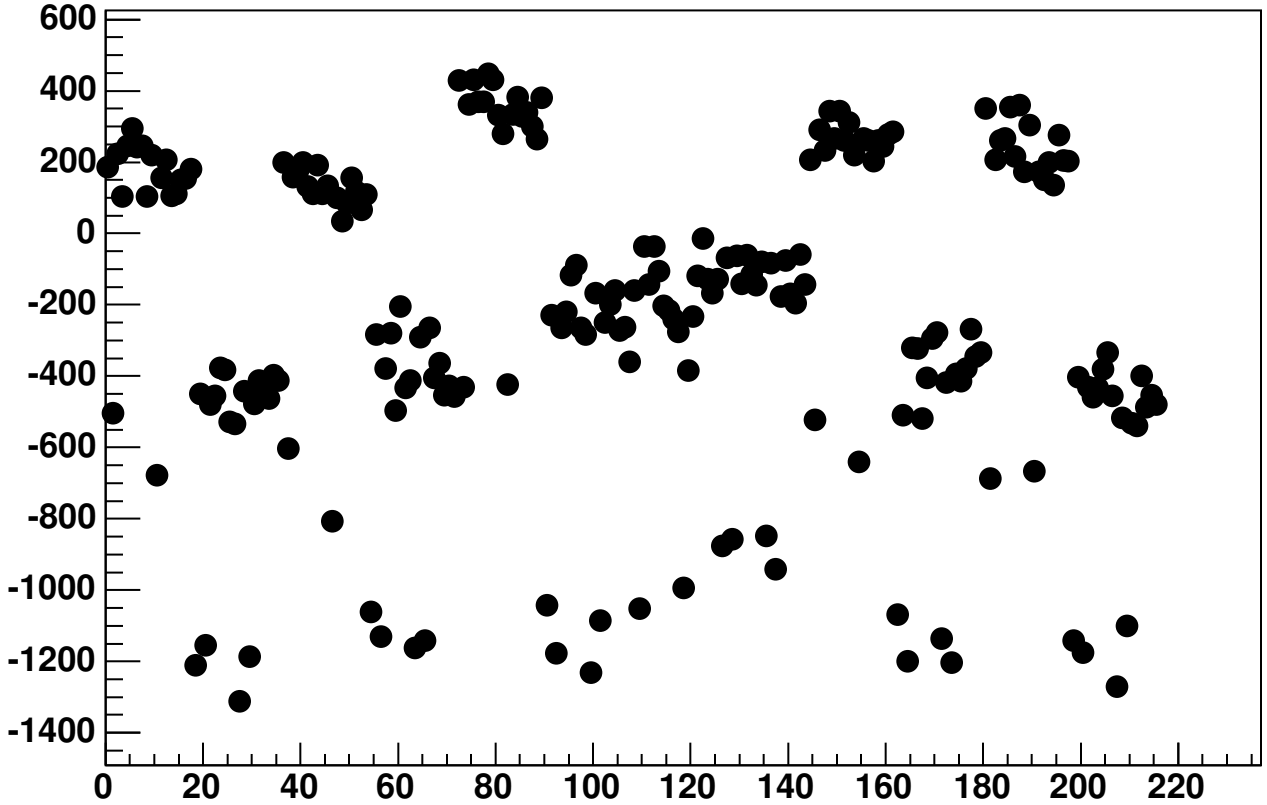
Enable 3, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



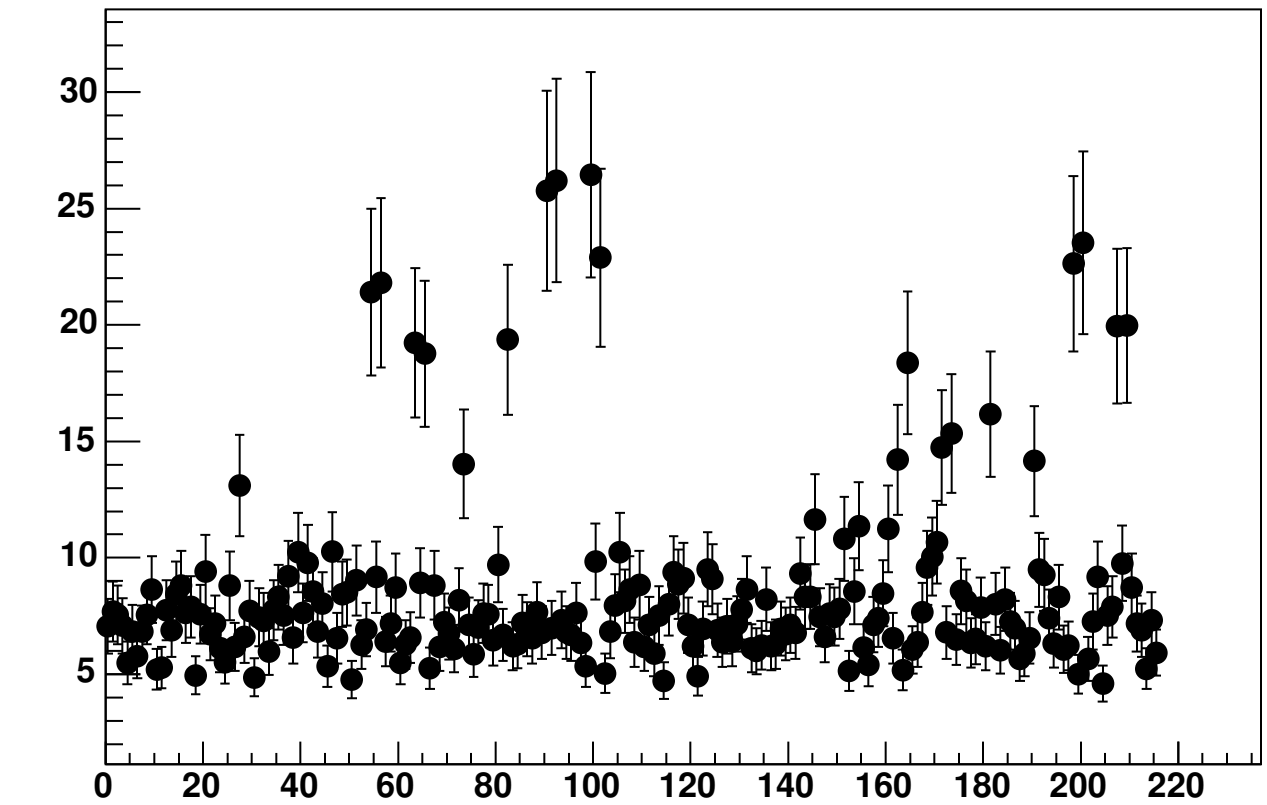
Enable 3, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



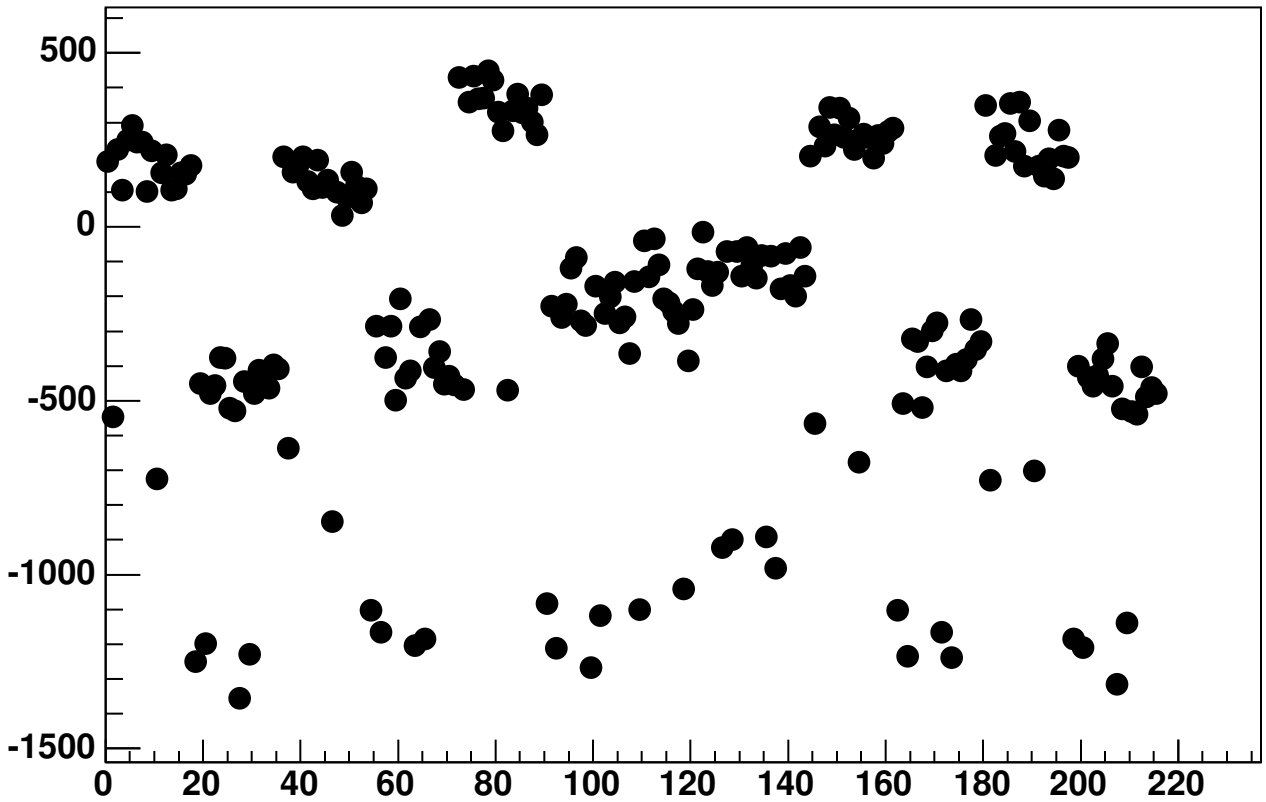
Enable 3, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



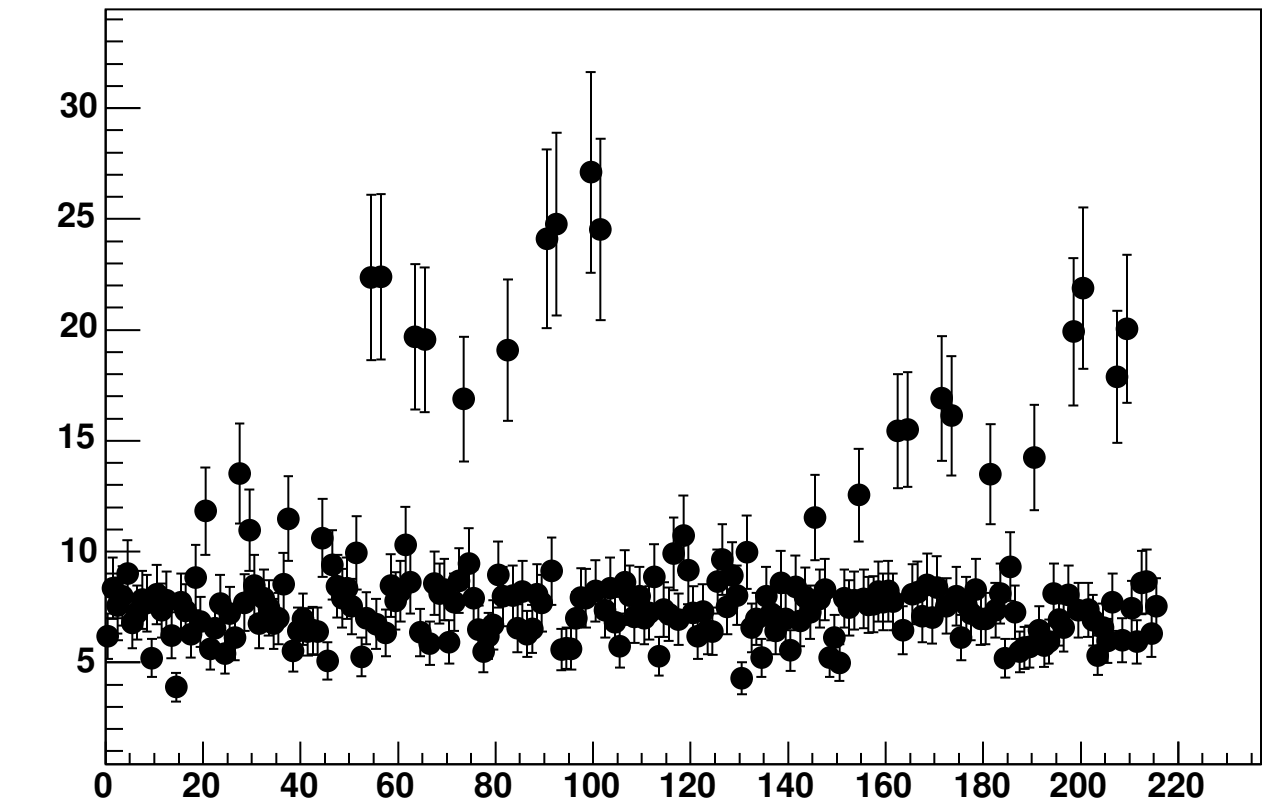
Enable 3, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



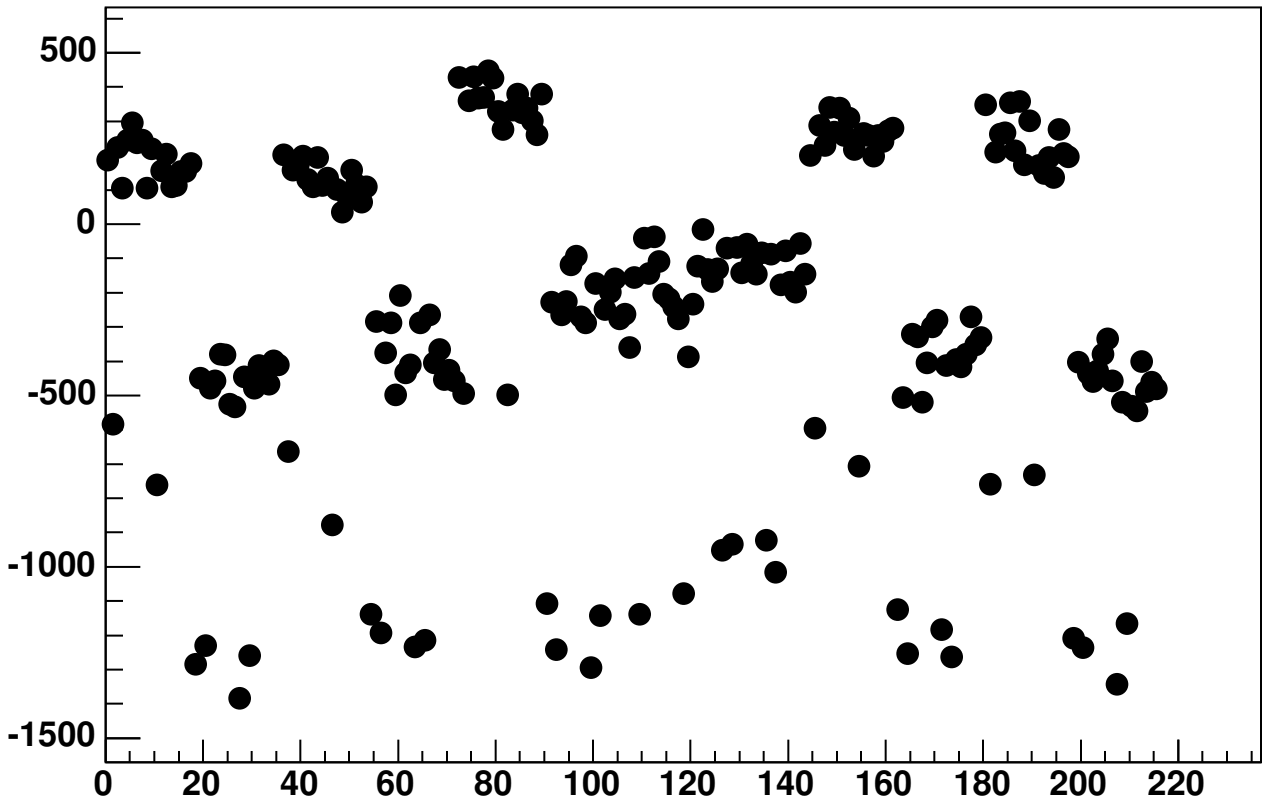
Enable 3, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



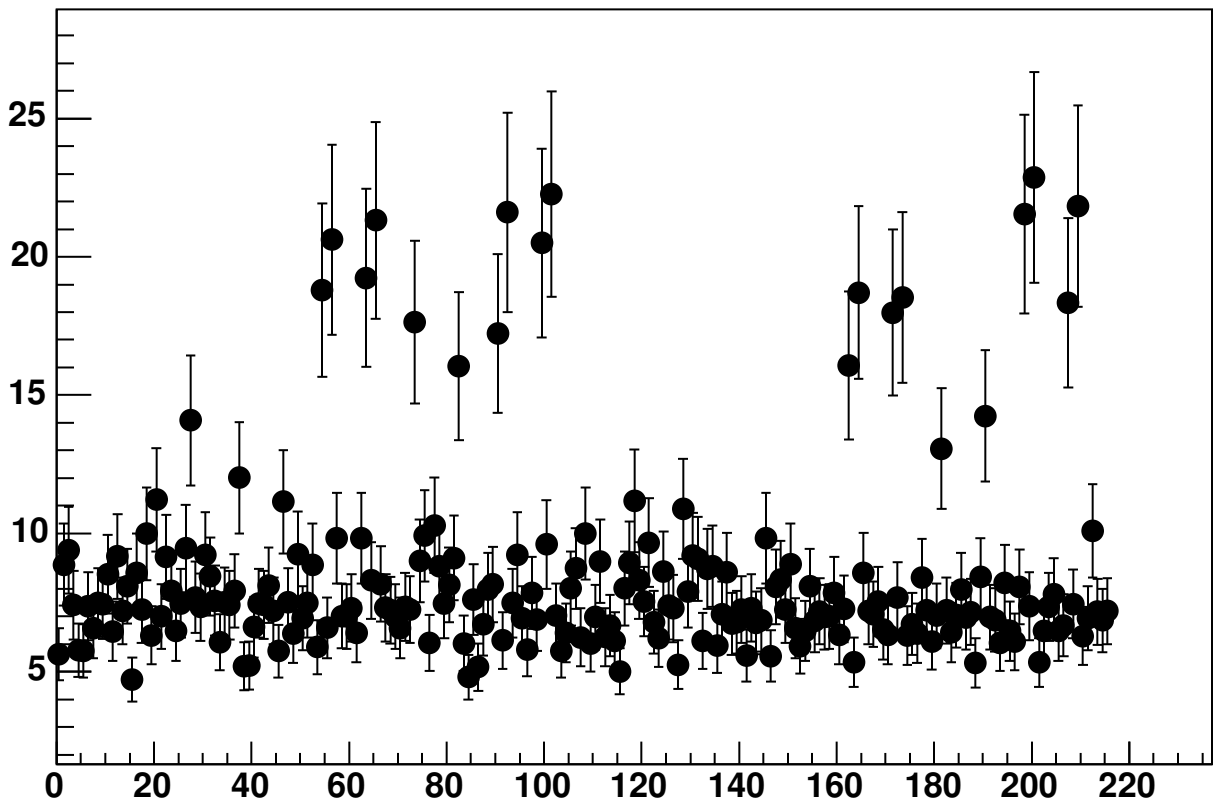
Enable 3, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



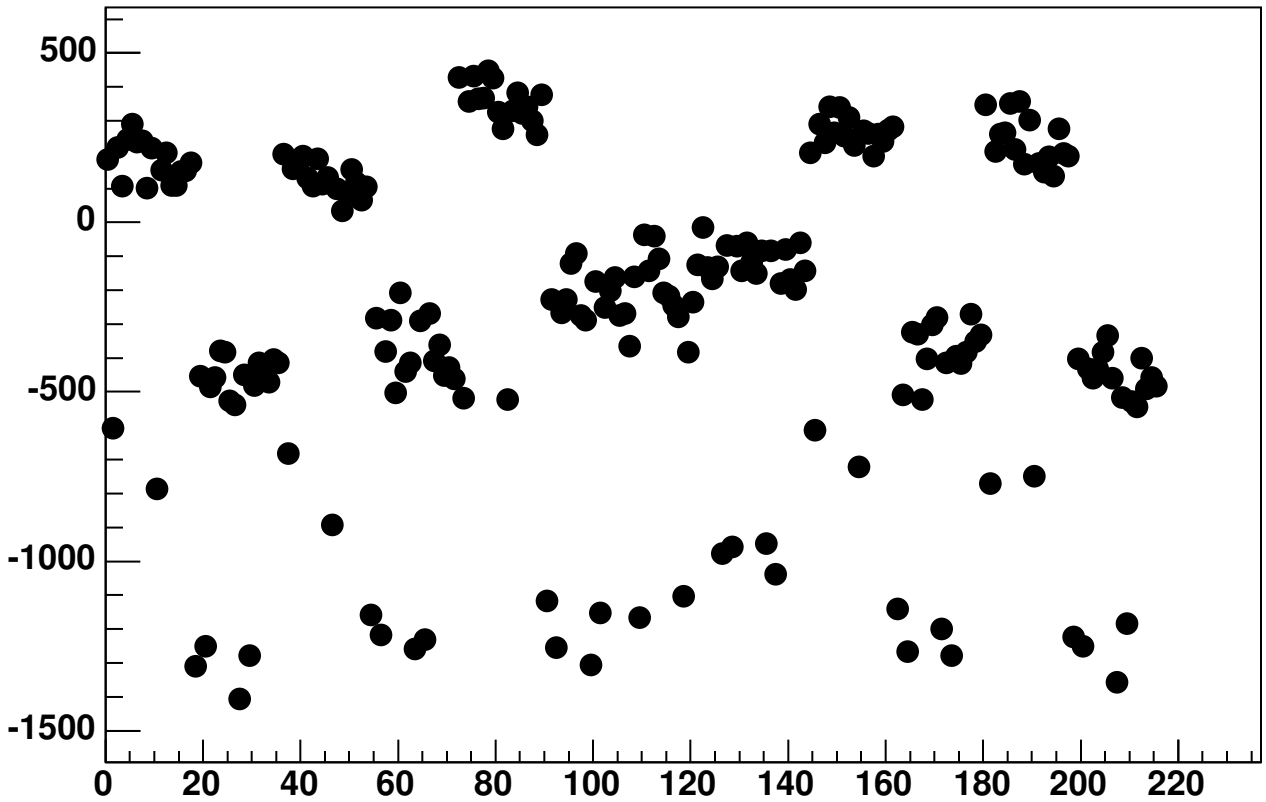
Enable 3, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



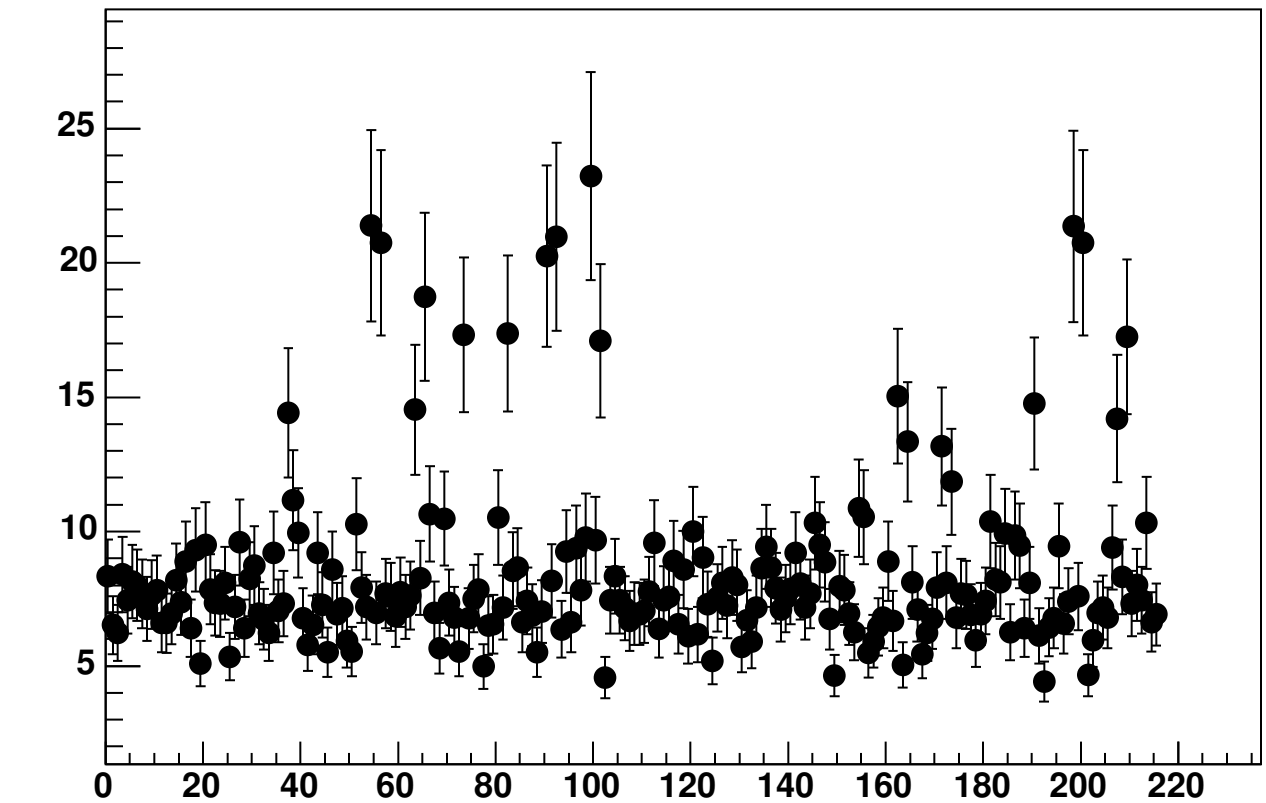
Enable 3, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



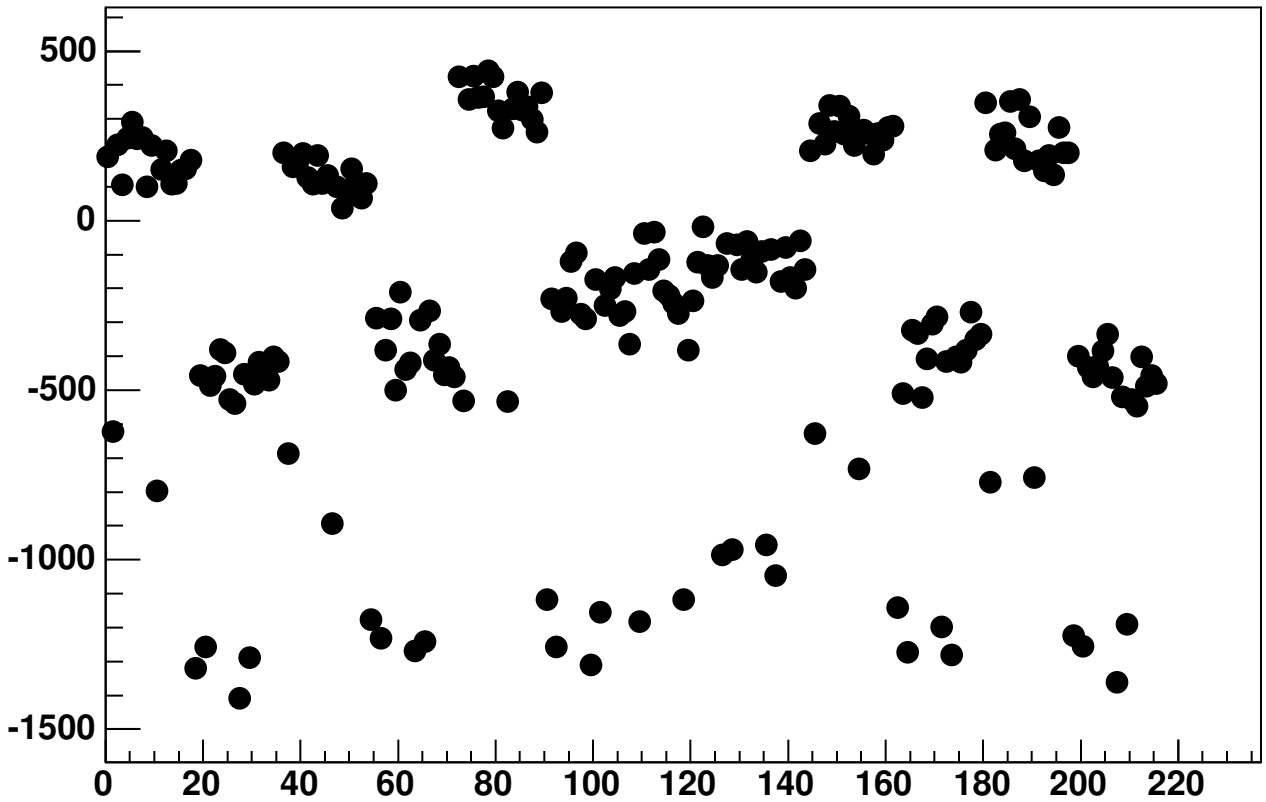
Enable 3, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



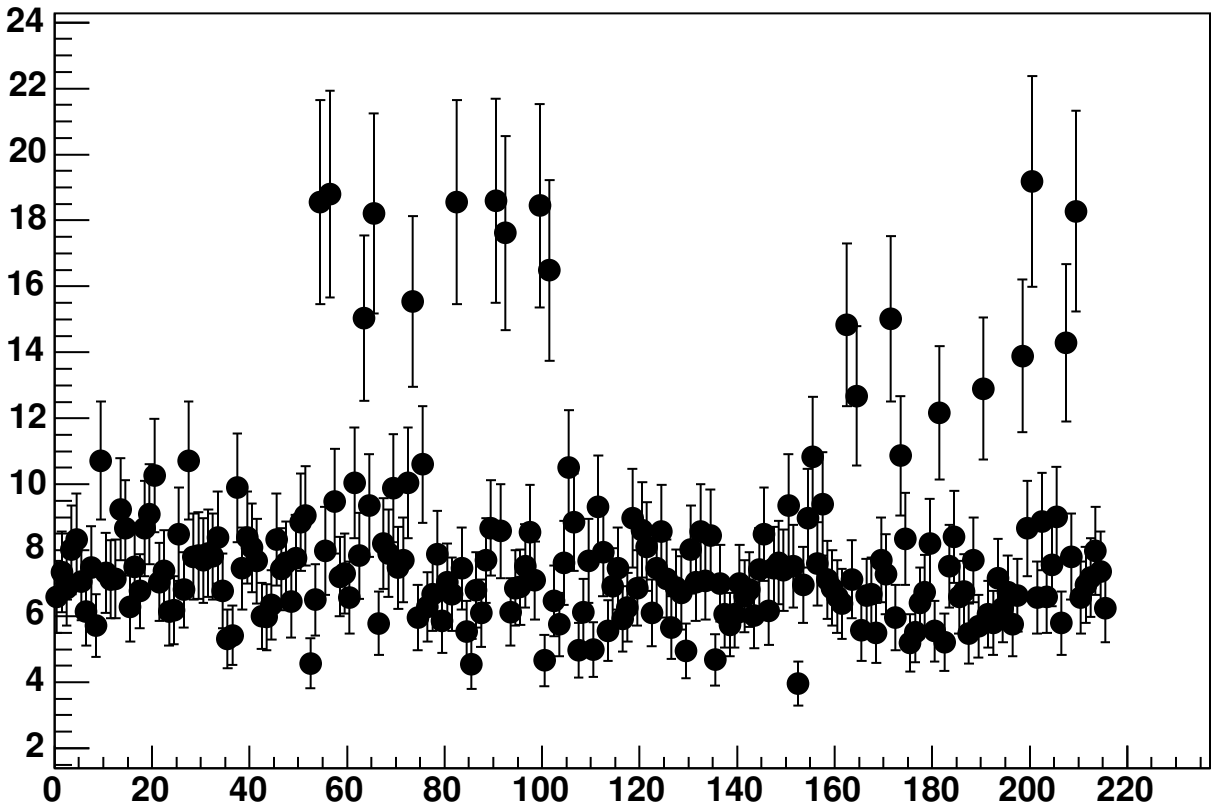
Enable 3, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



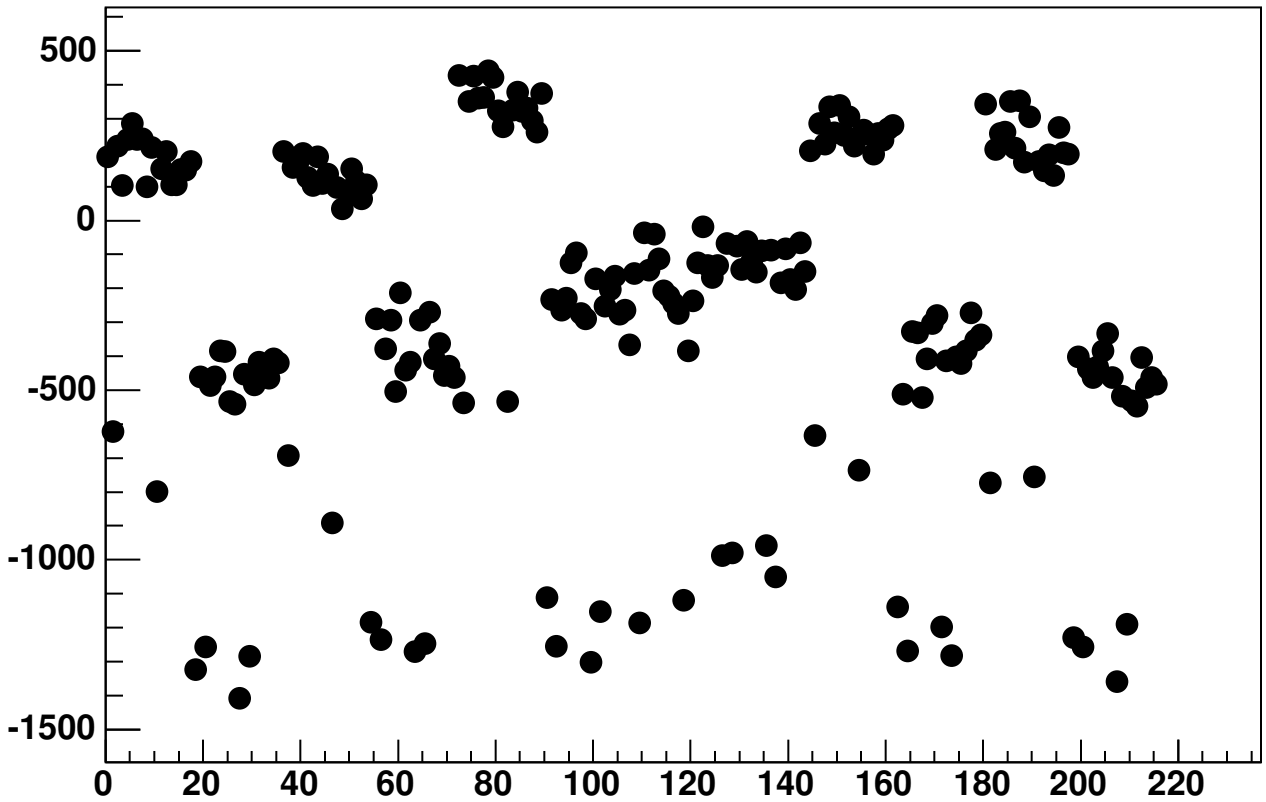
Enable 3, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



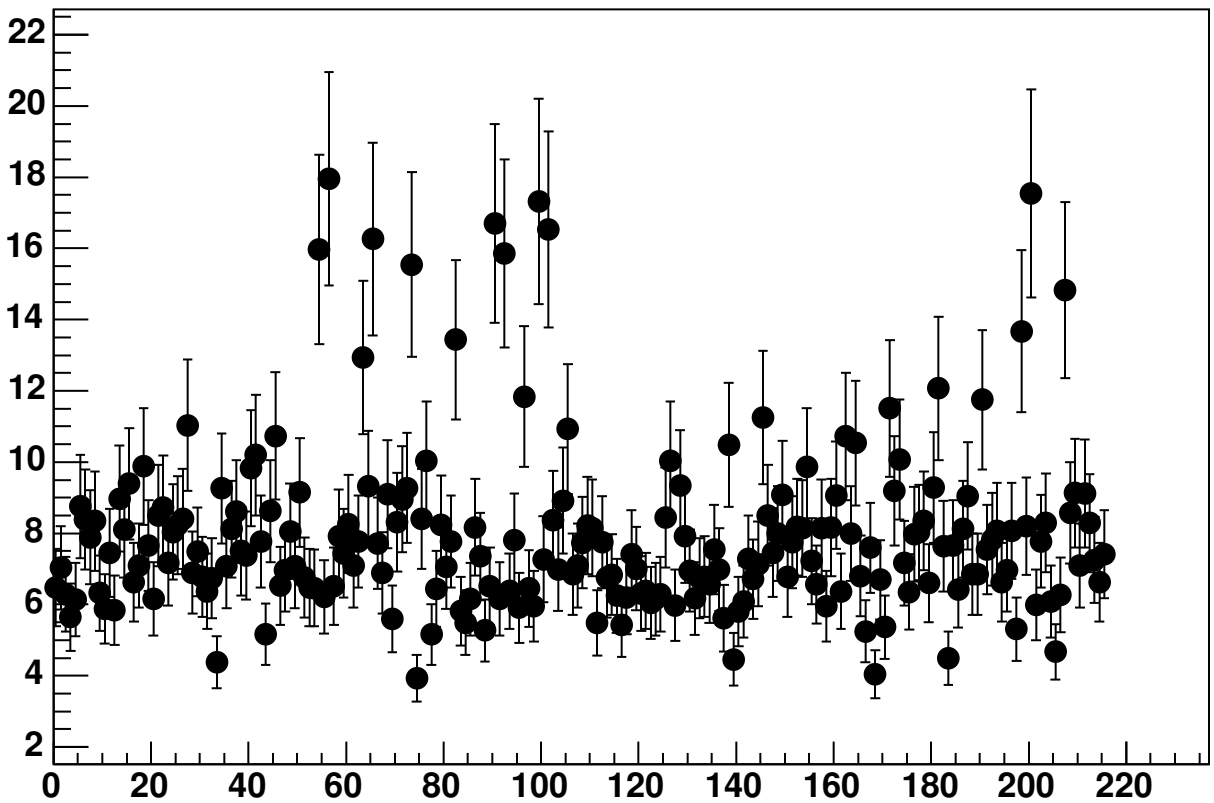
Enable 3, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



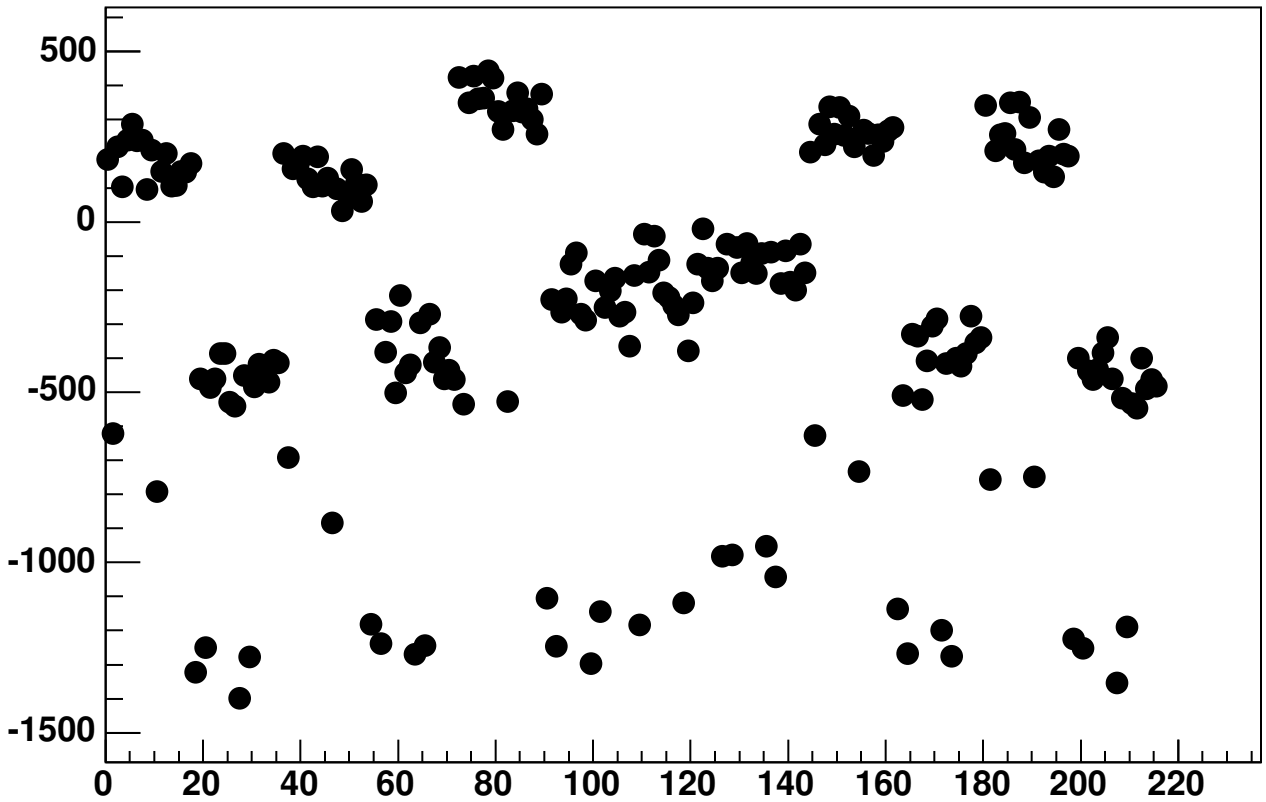
Enable 3, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



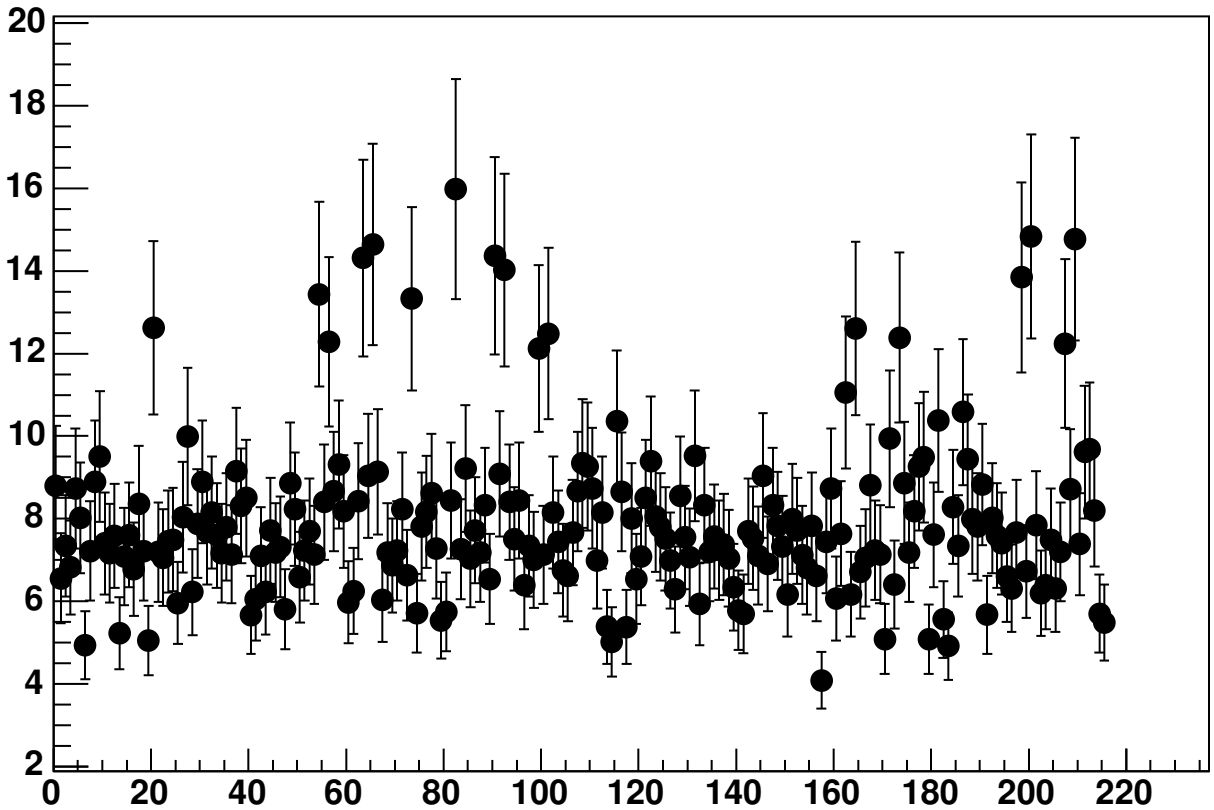
Enable 3, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



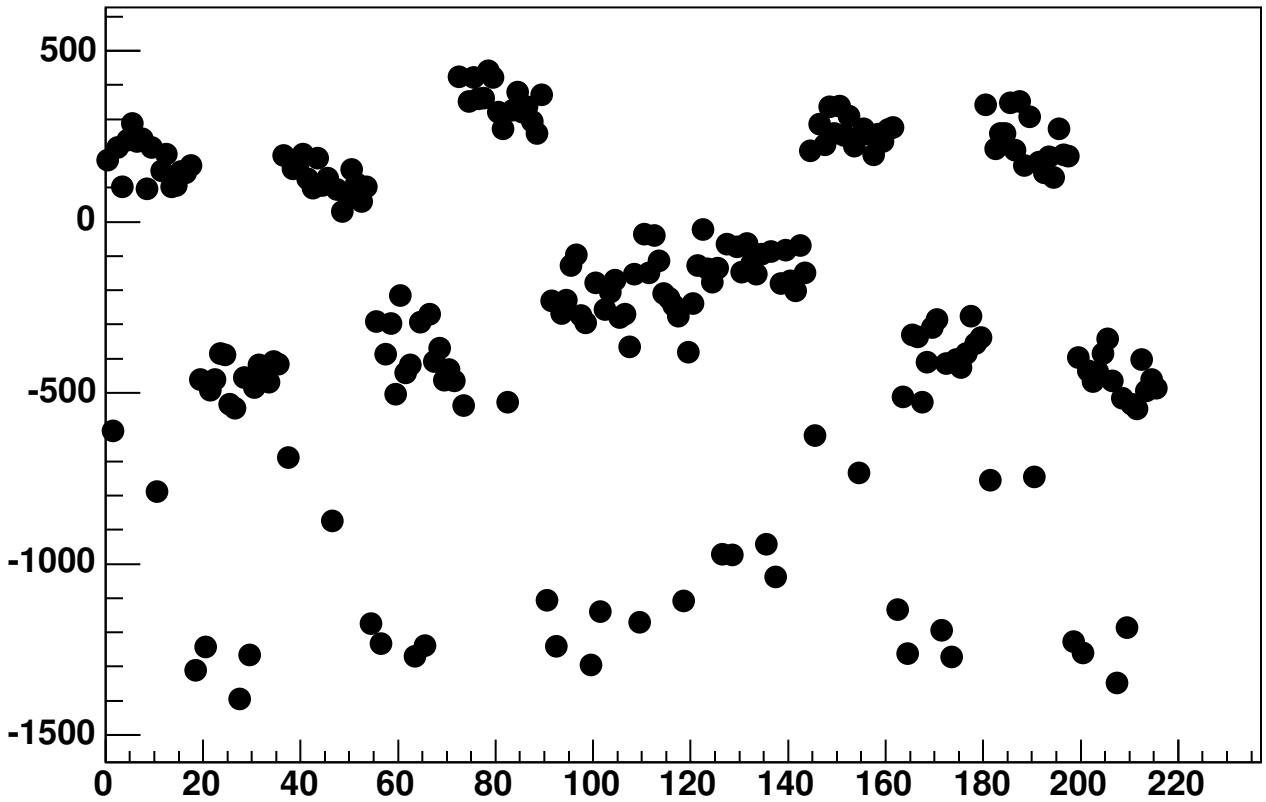
Enable 3, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



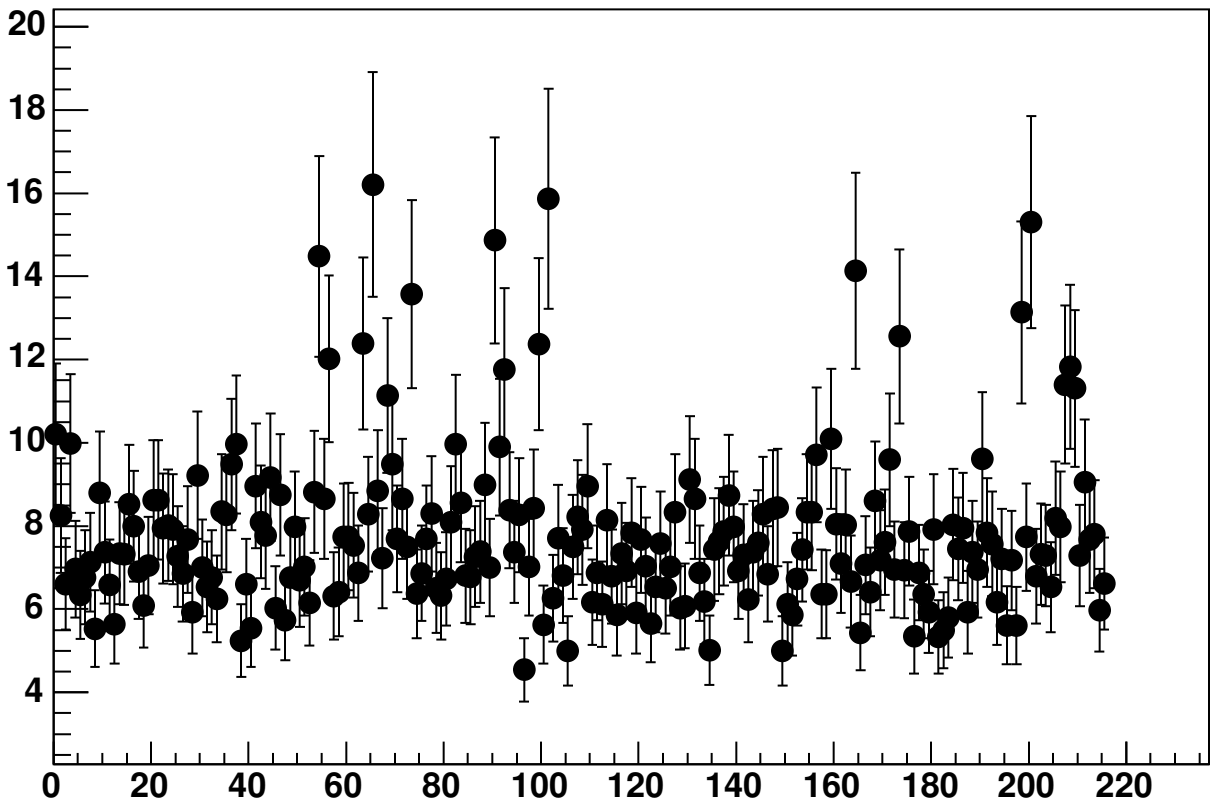
Enable 3, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



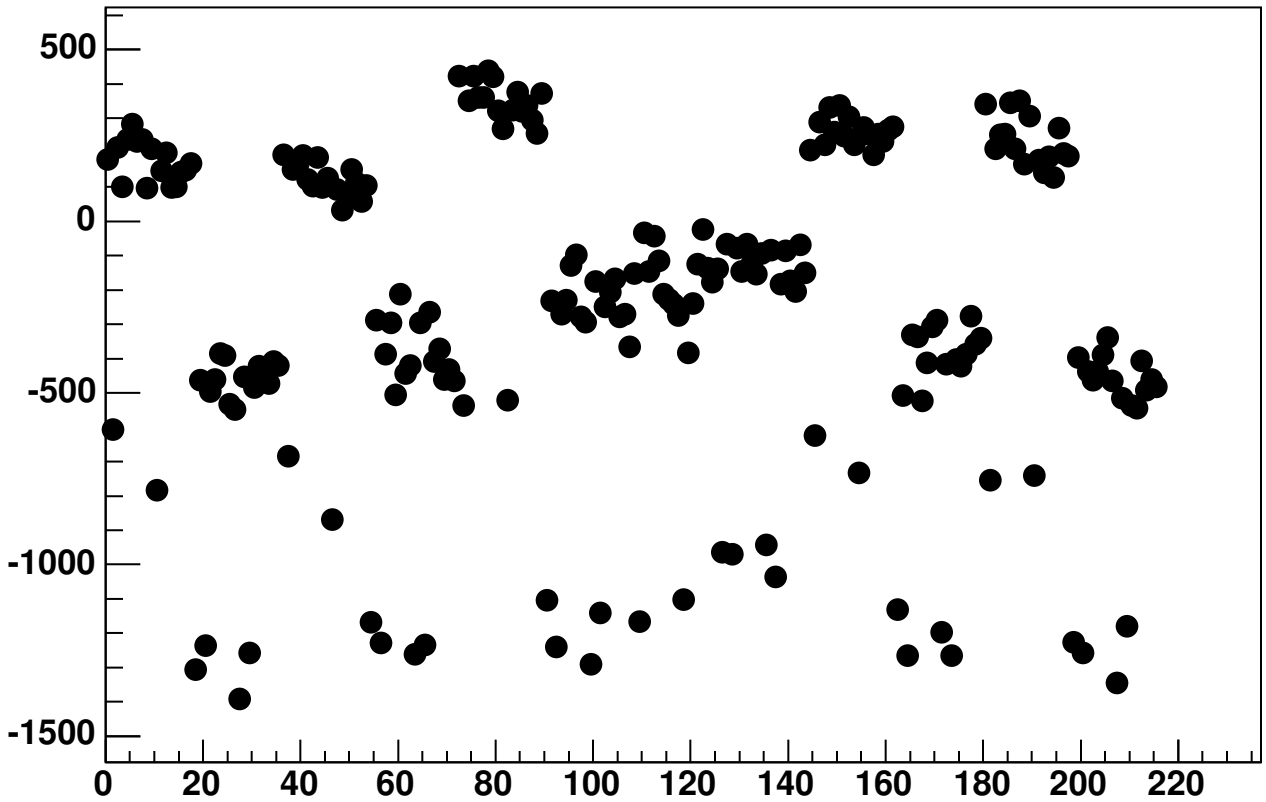
Enable 3, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



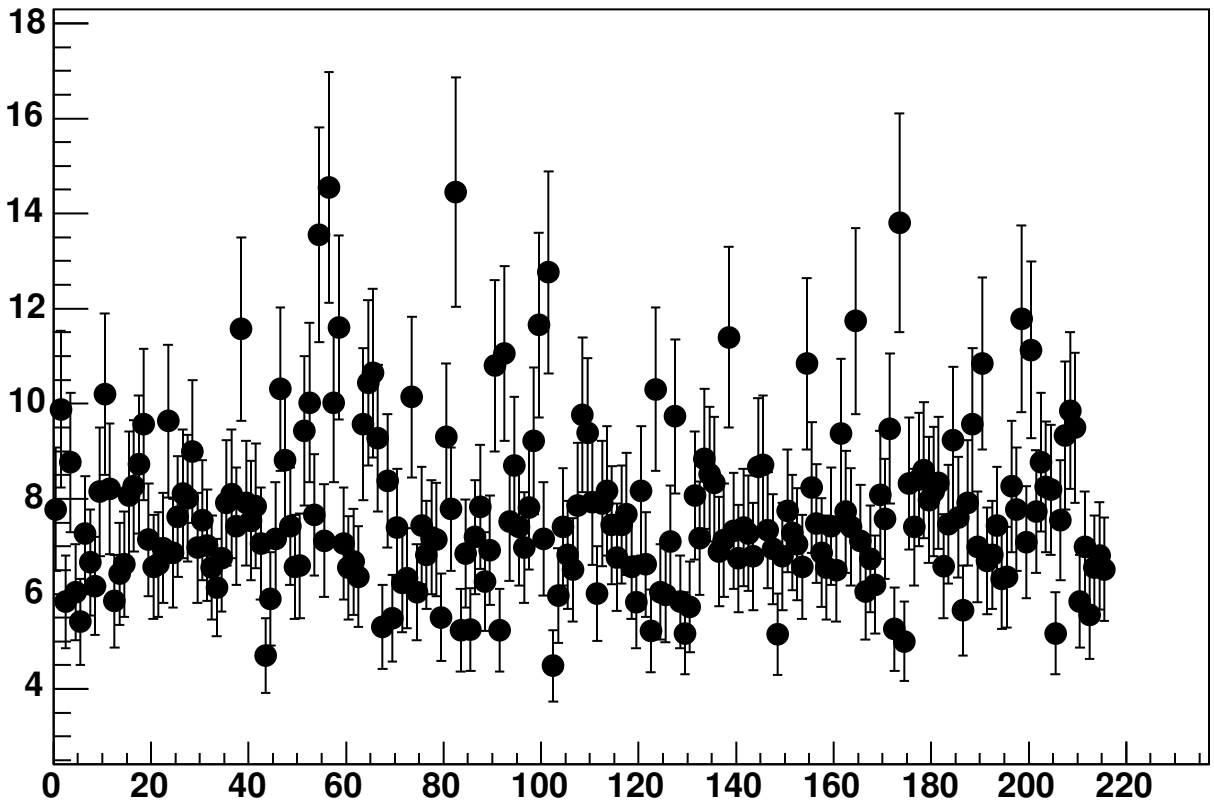
Enable 3, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan



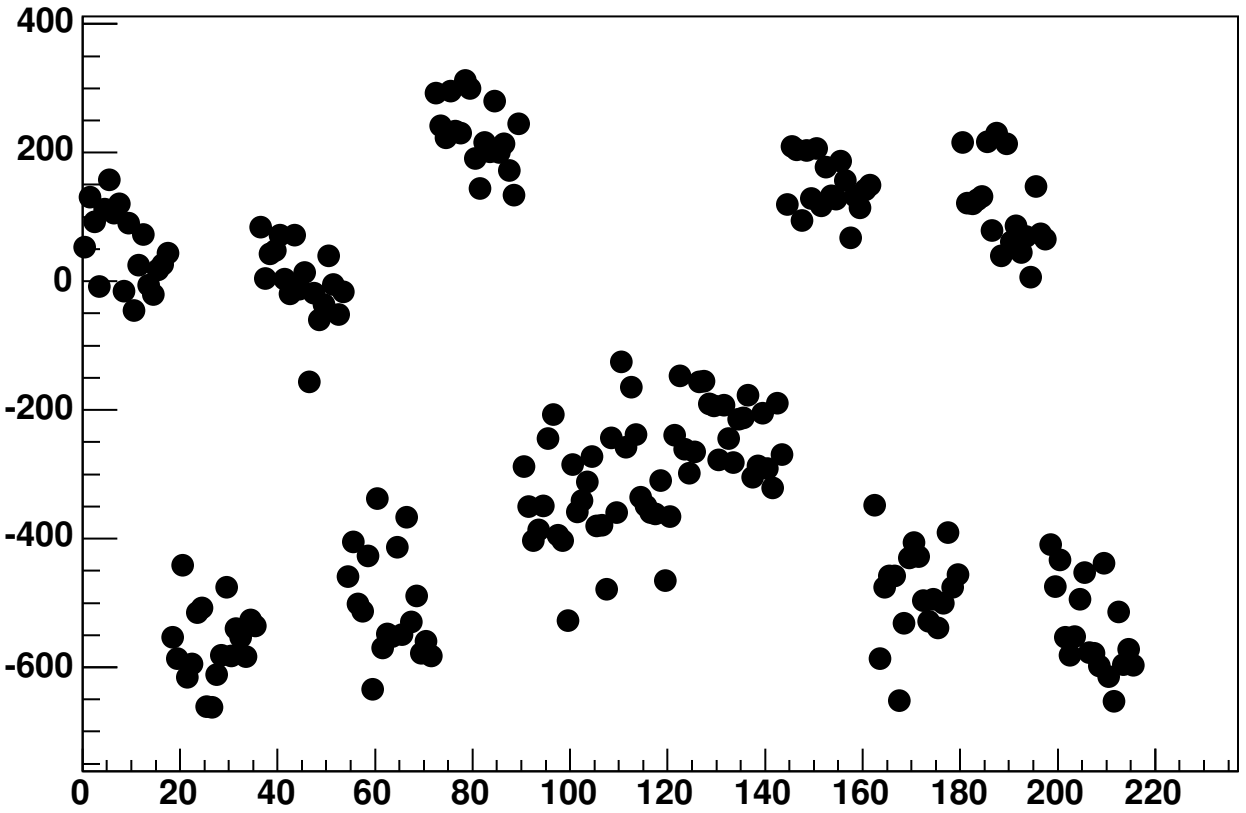
Enable 3, DAC=4000, Hold=245, ADC Mean vs 18*Chip+Chan



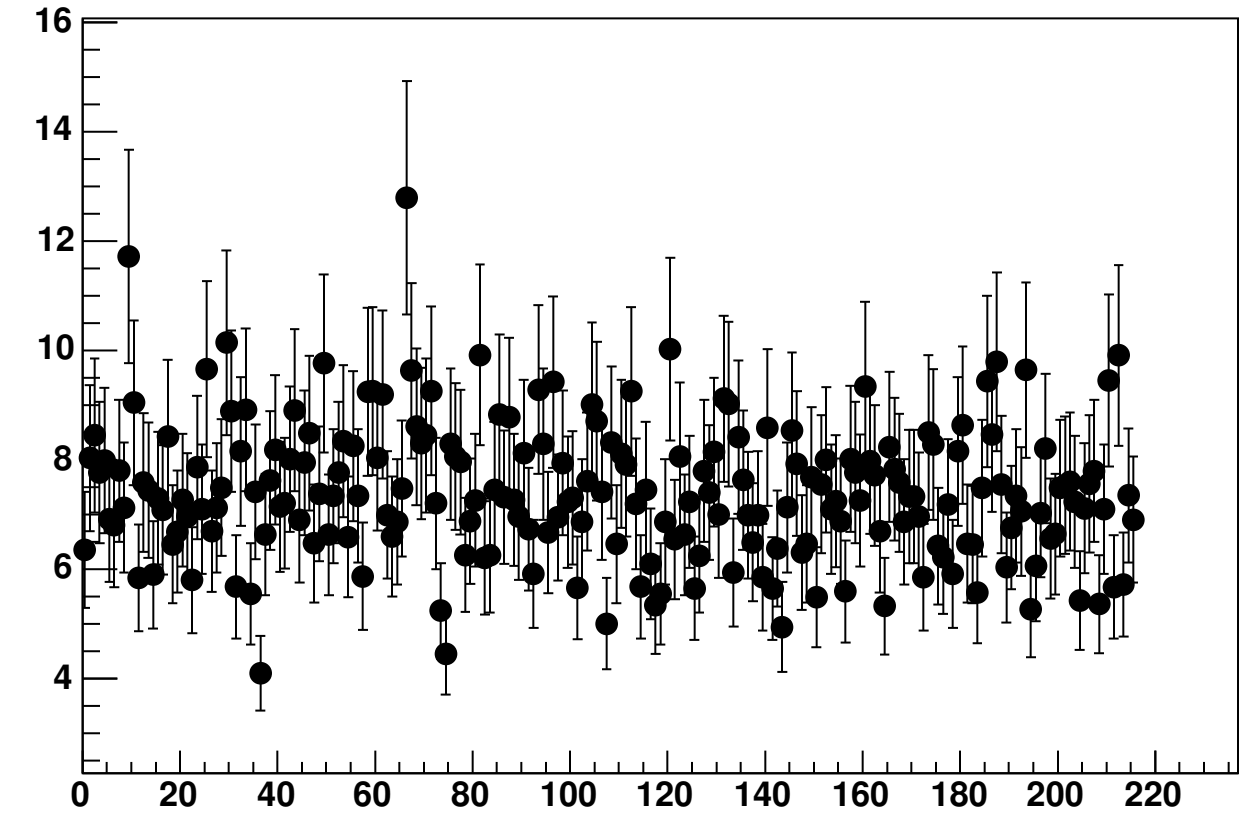
Enable 3, DAC=4000, Hold=245, ADC Noise vs 18*Chip+Chan



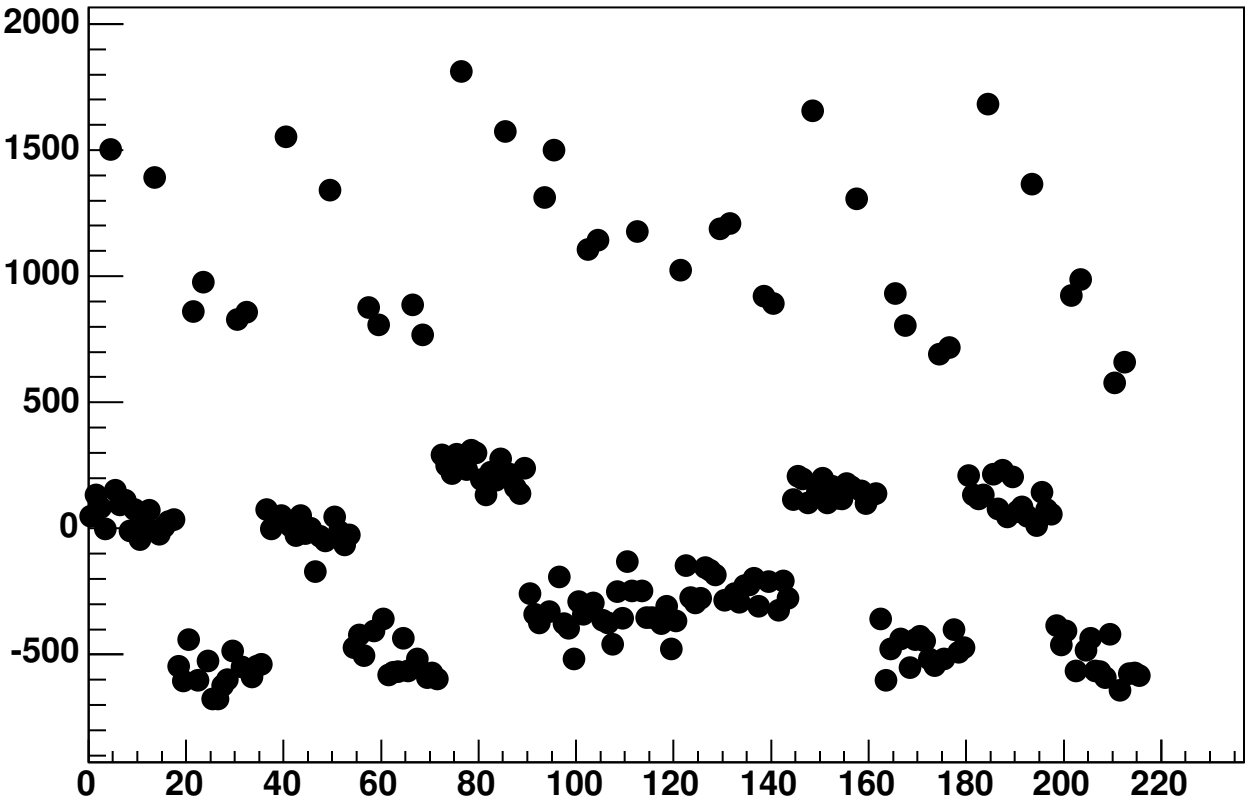
Enable 4, DAC=4000, Hold=0, ADC Mean vs 18*Chip+Chan



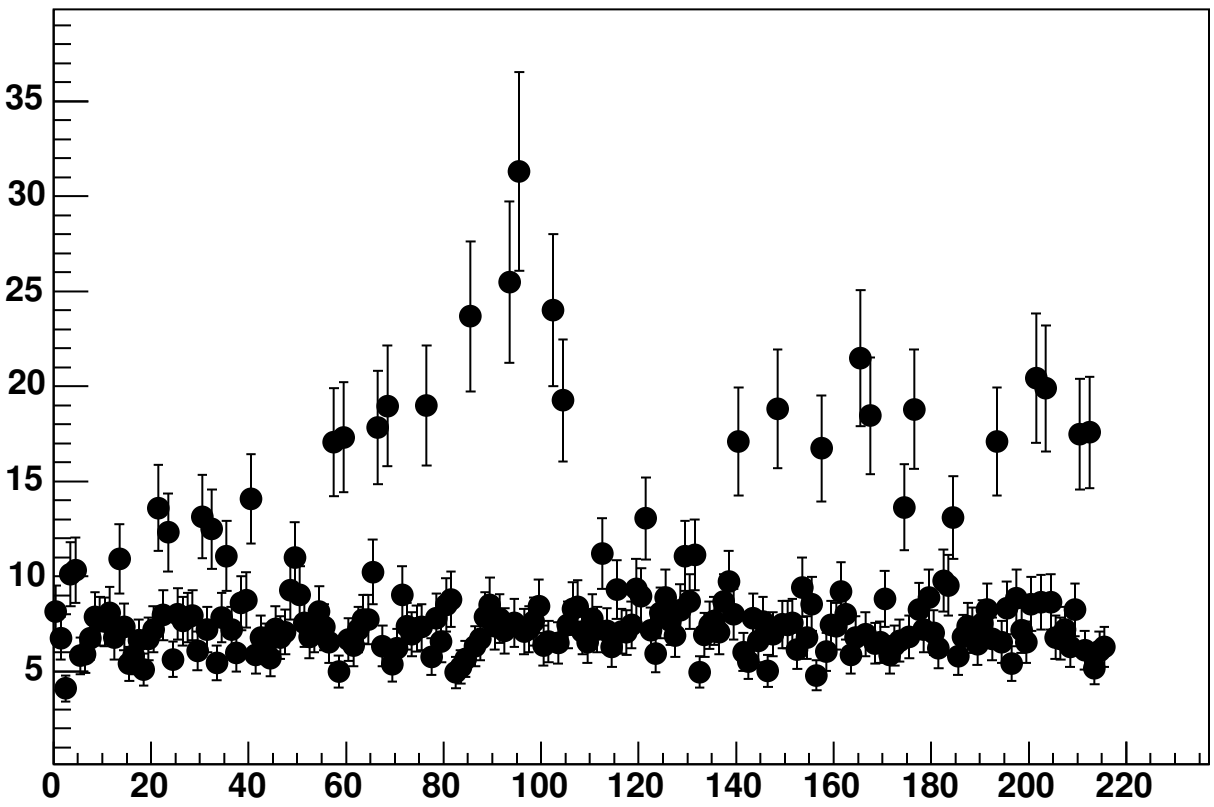
Enable 4, DAC=4000, Hold=0, ADC Noise vs 18*Chip+Chan



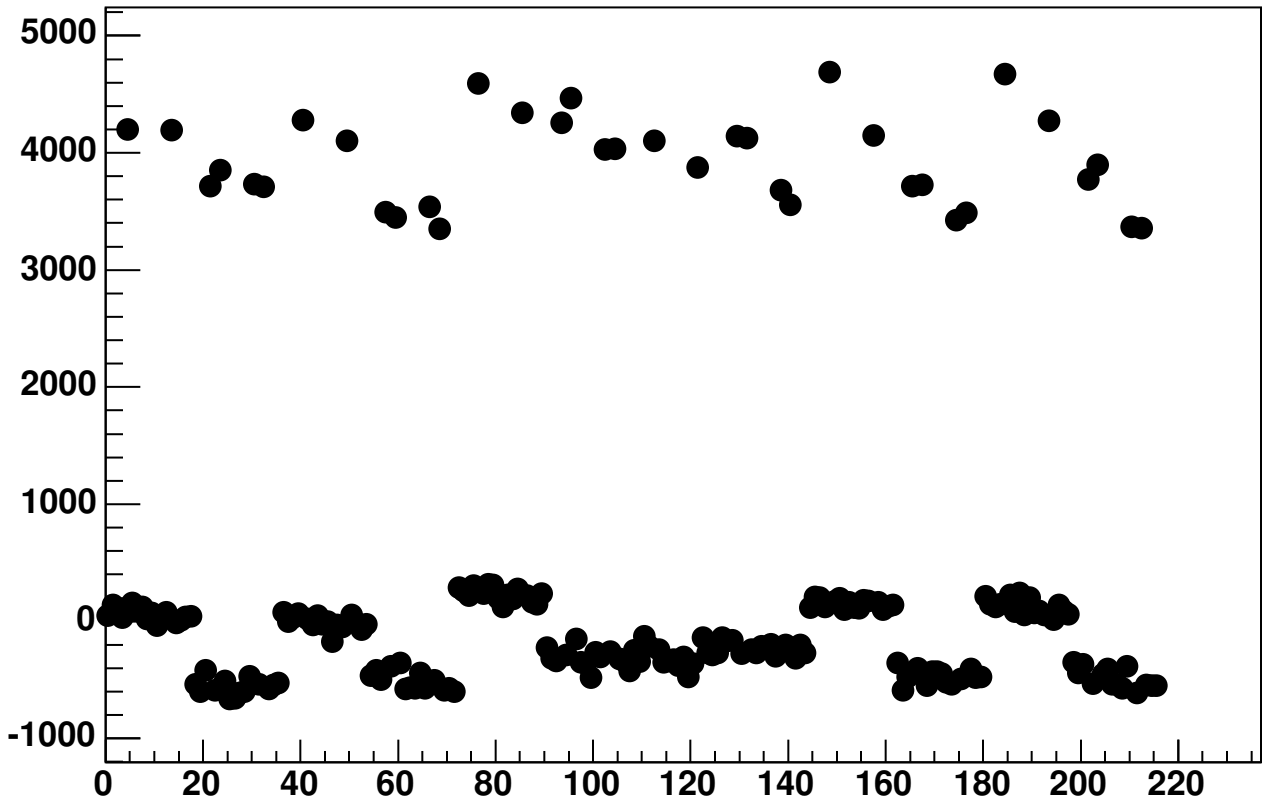
Enable 4, DAC=4000, Hold=5, ADC Mean vs 18*Chip+Chan



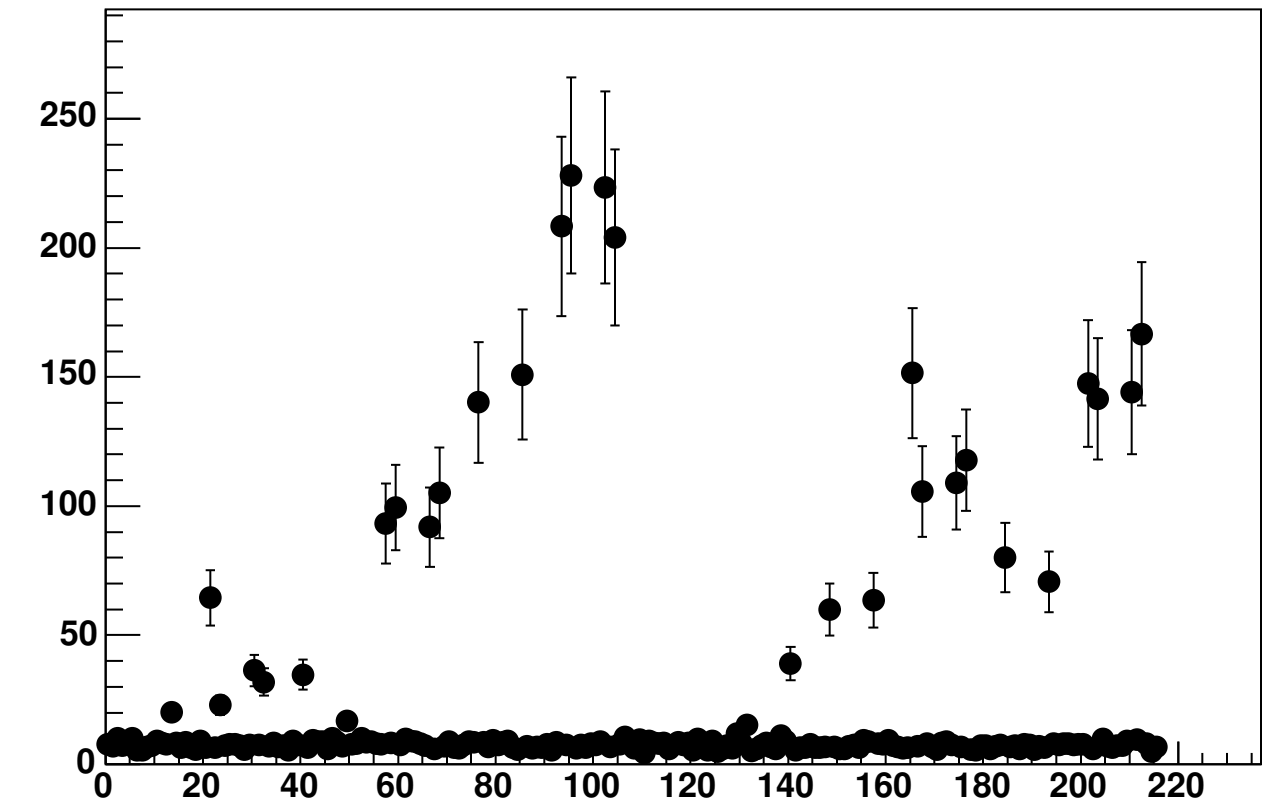
Enable 4, DAC=4000, Hold=5, ADC Noise vs 18*Chip+Chan



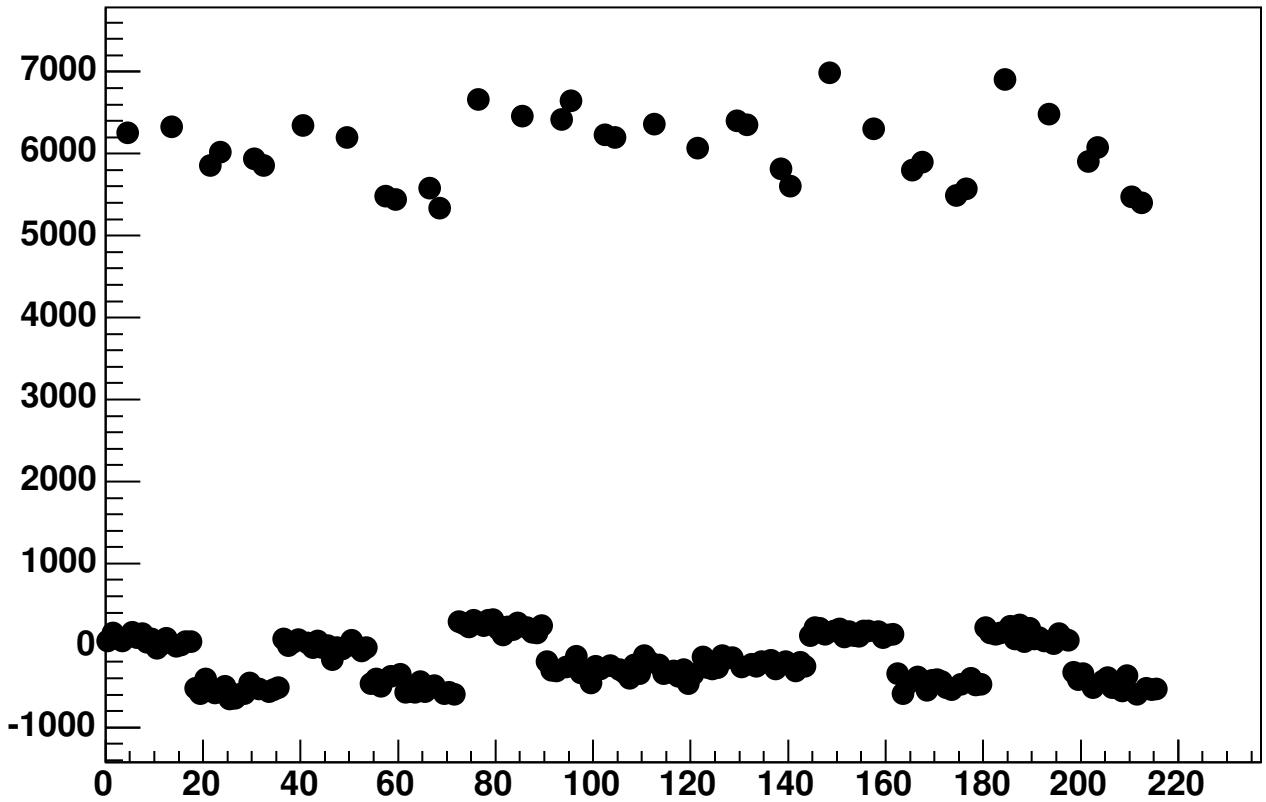
Enable 4, DAC=4000, Hold=10, ADC Mean vs 18*Chip+Chan



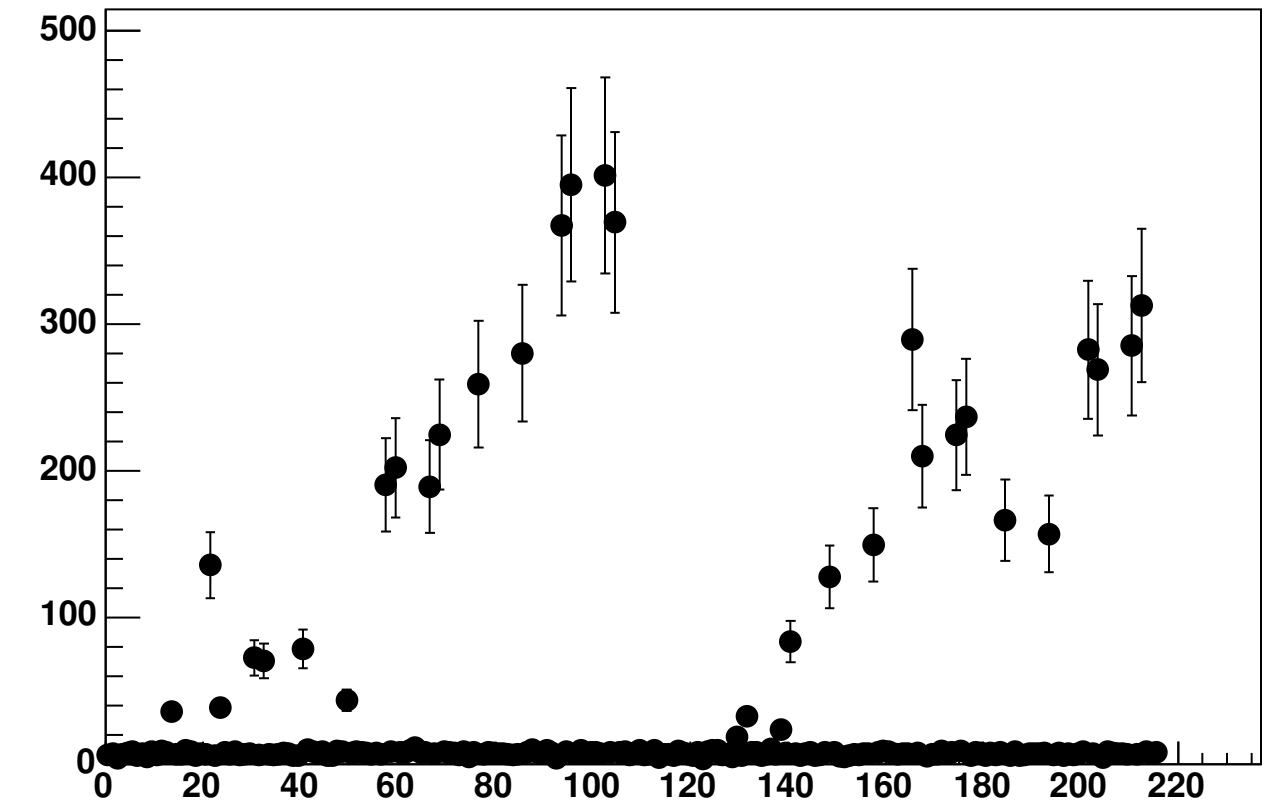
Enable 4, DAC=4000, Hold=10, ADC Noise vs 18*Chip+Chan



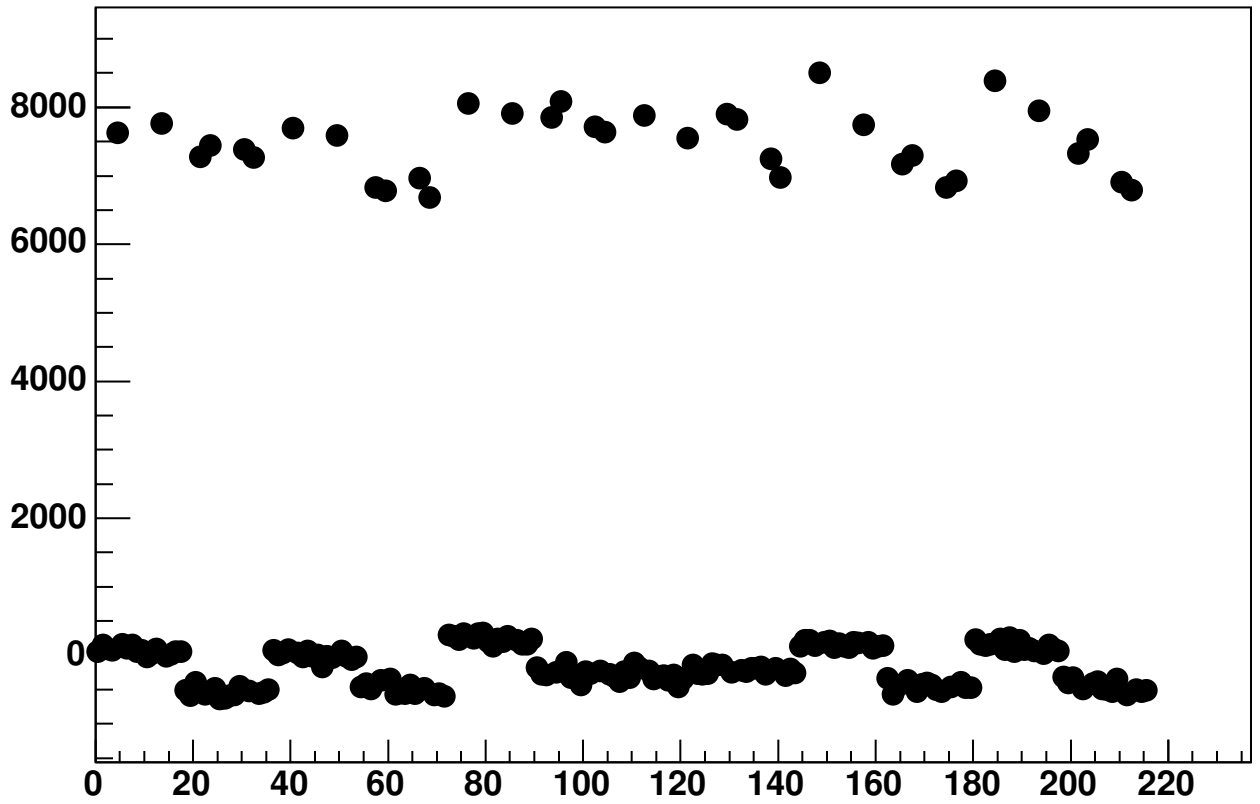
Enable 4, DAC=4000, Hold=15, ADC Mean vs 18*Chip+Chan



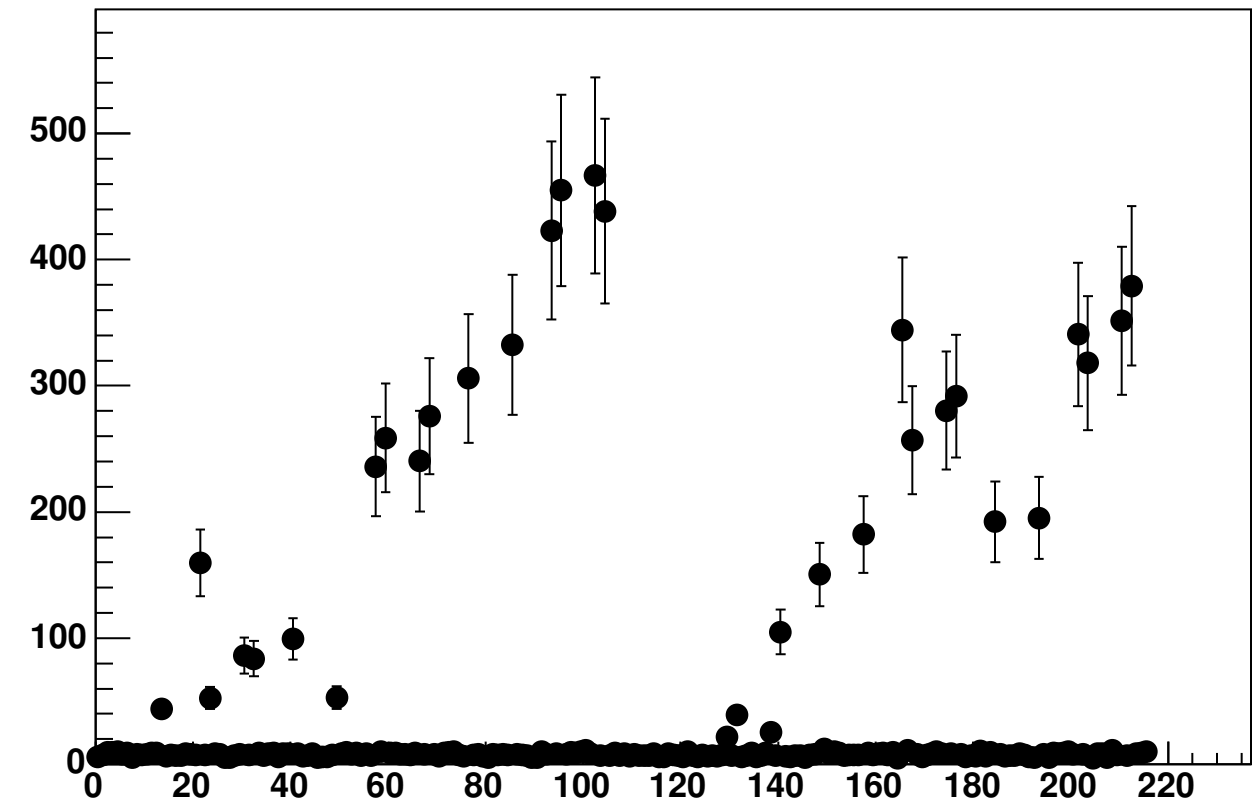
Enable 4, DAC=4000, Hold=15, ADC Noise vs 18*Chip+Chan



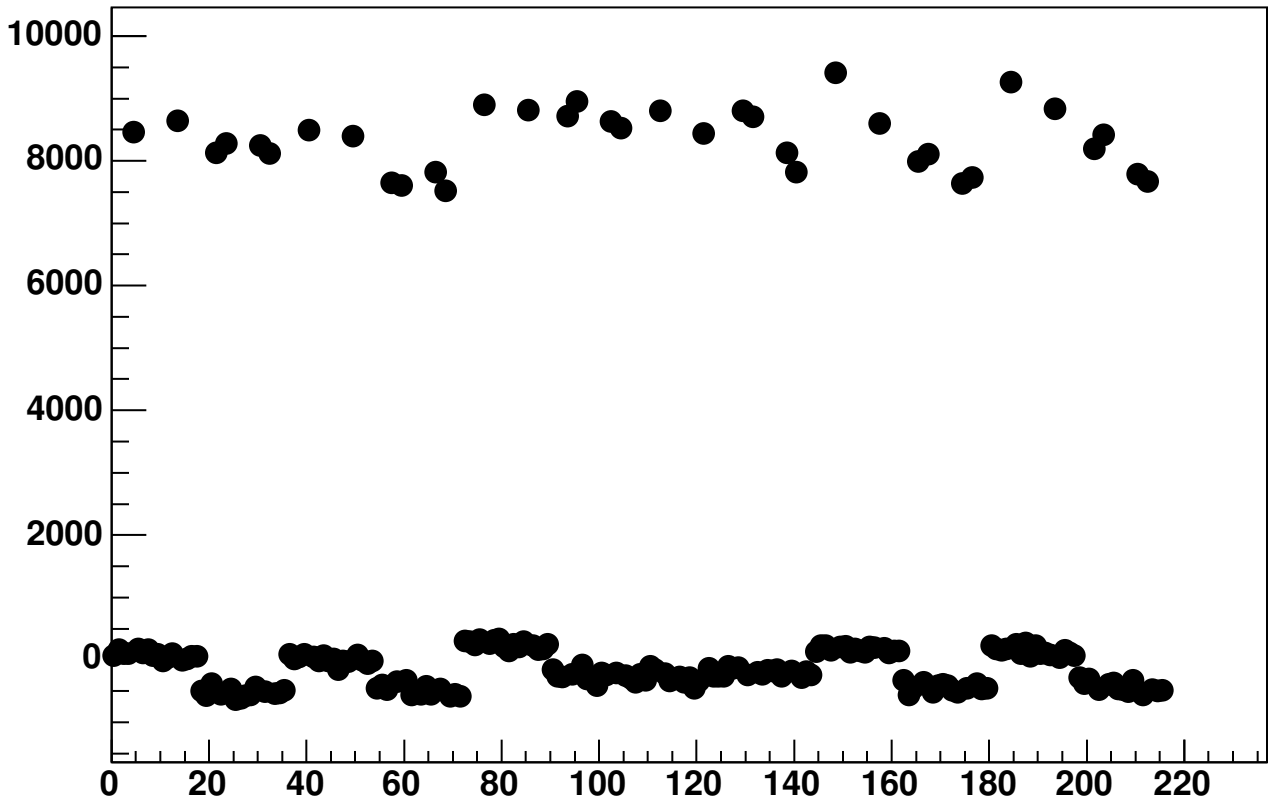
Enable 4, DAC=4000, Hold=20, ADC Mean vs 18*Chip+Chan



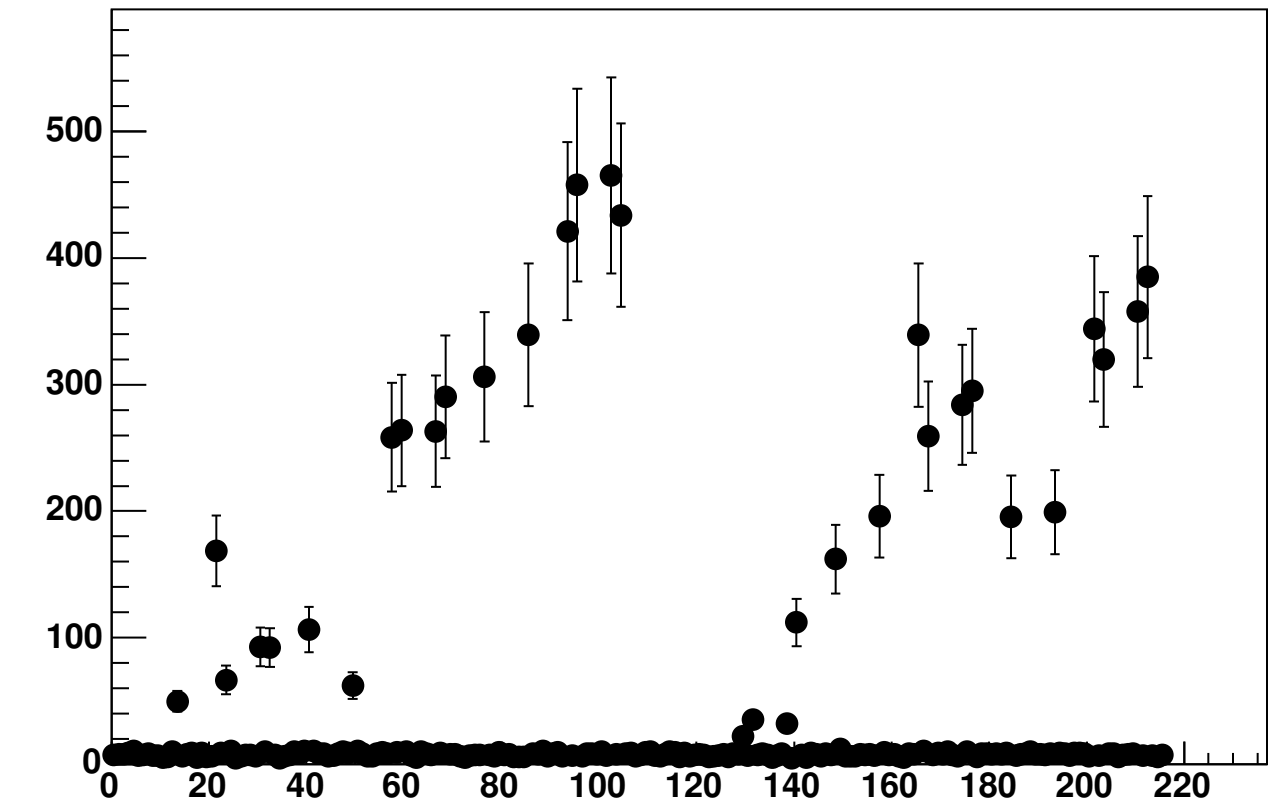
Enable 4, DAC=4000, Hold=20, ADC Noise vs 18*Chip+Chan



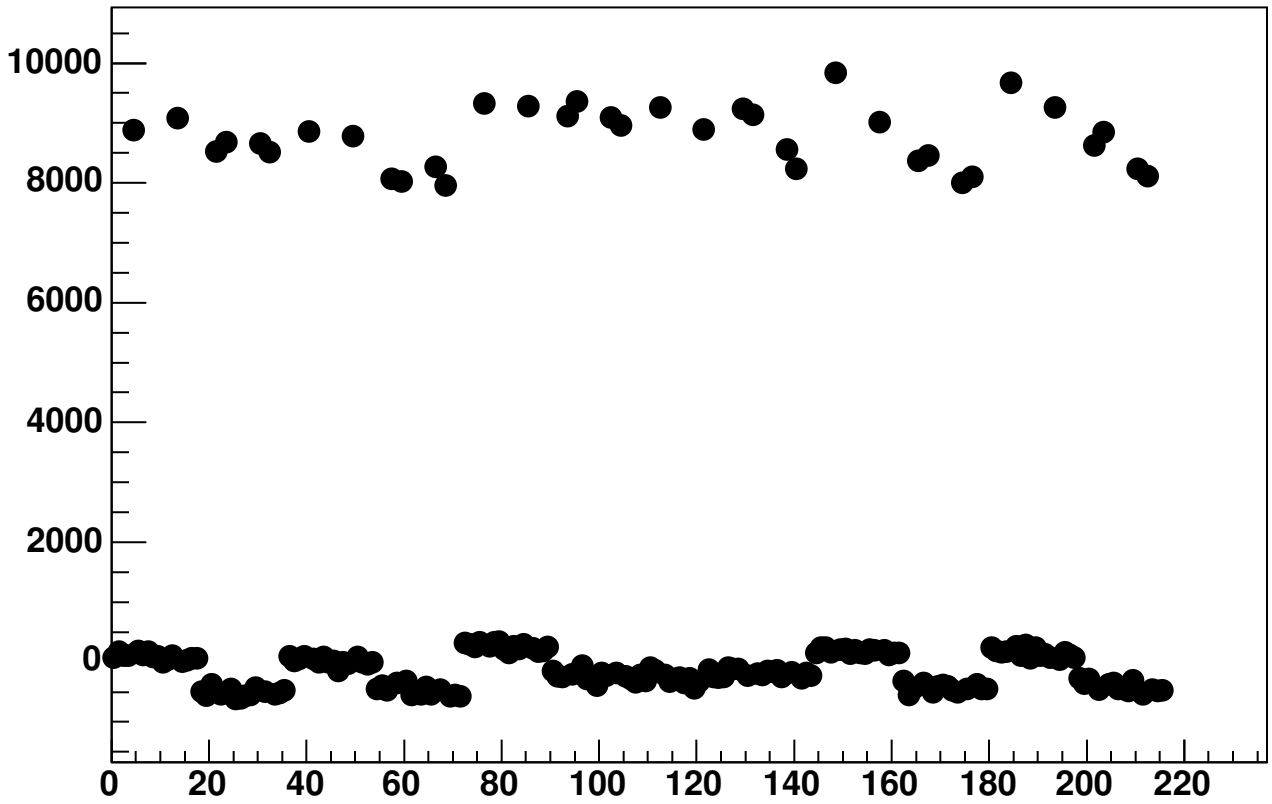
Enable 4, DAC=4000, Hold=25, ADC Mean vs 18*Chip+Chan



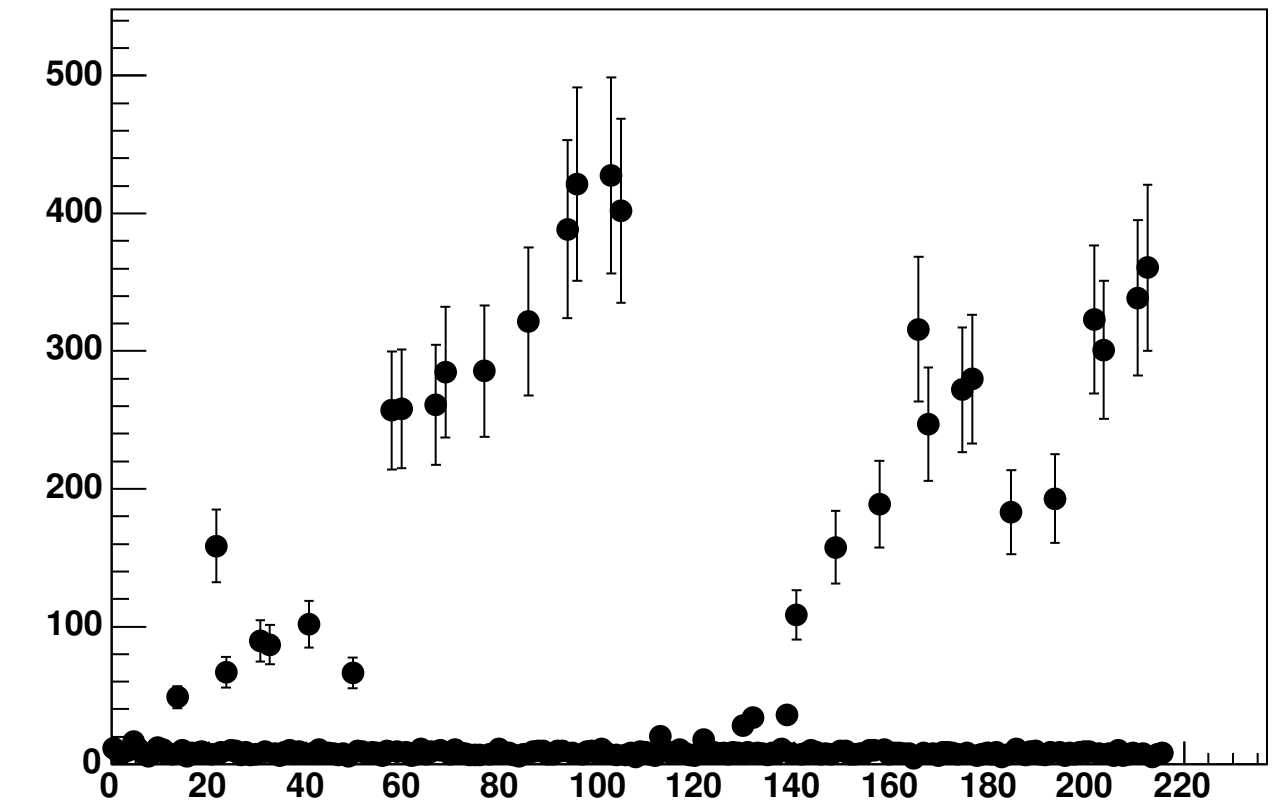
Enable 4, DAC=4000, Hold=25, ADC Noise vs 18*Chip+Chan



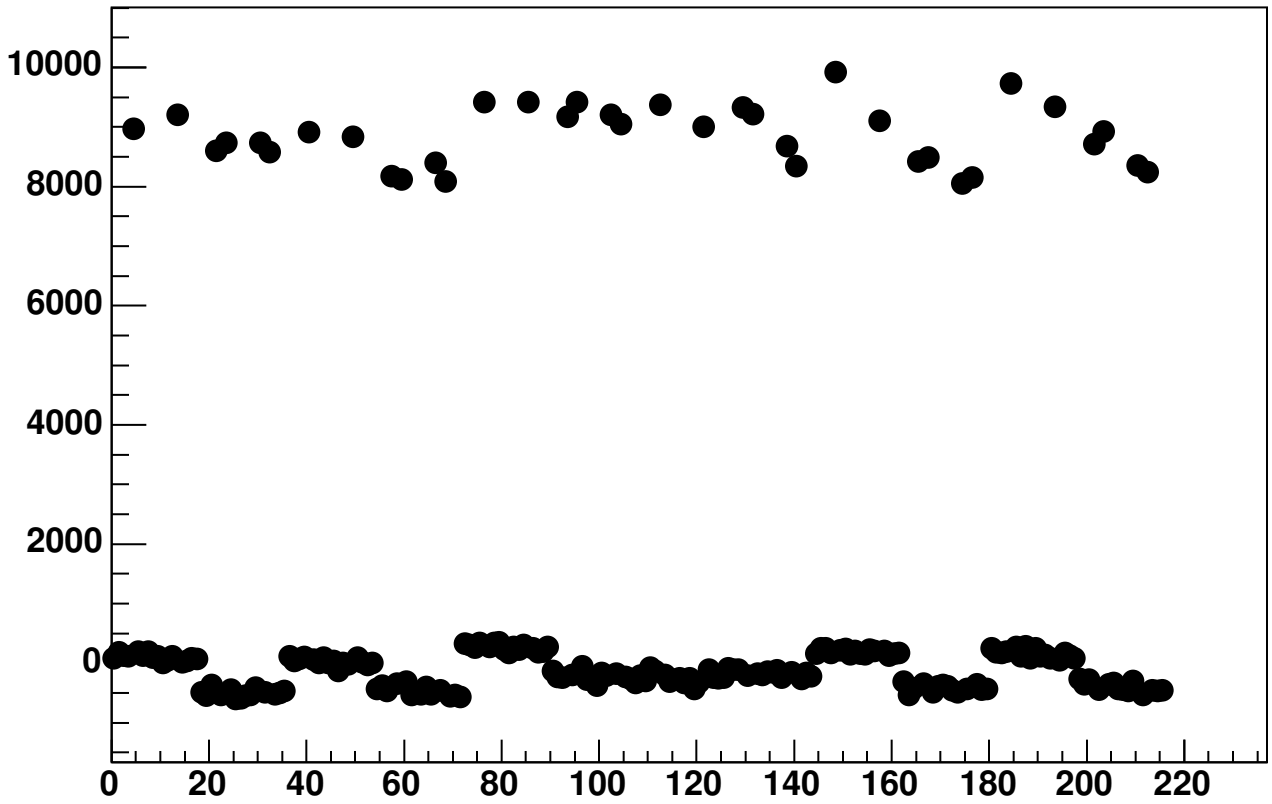
Enable 4, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



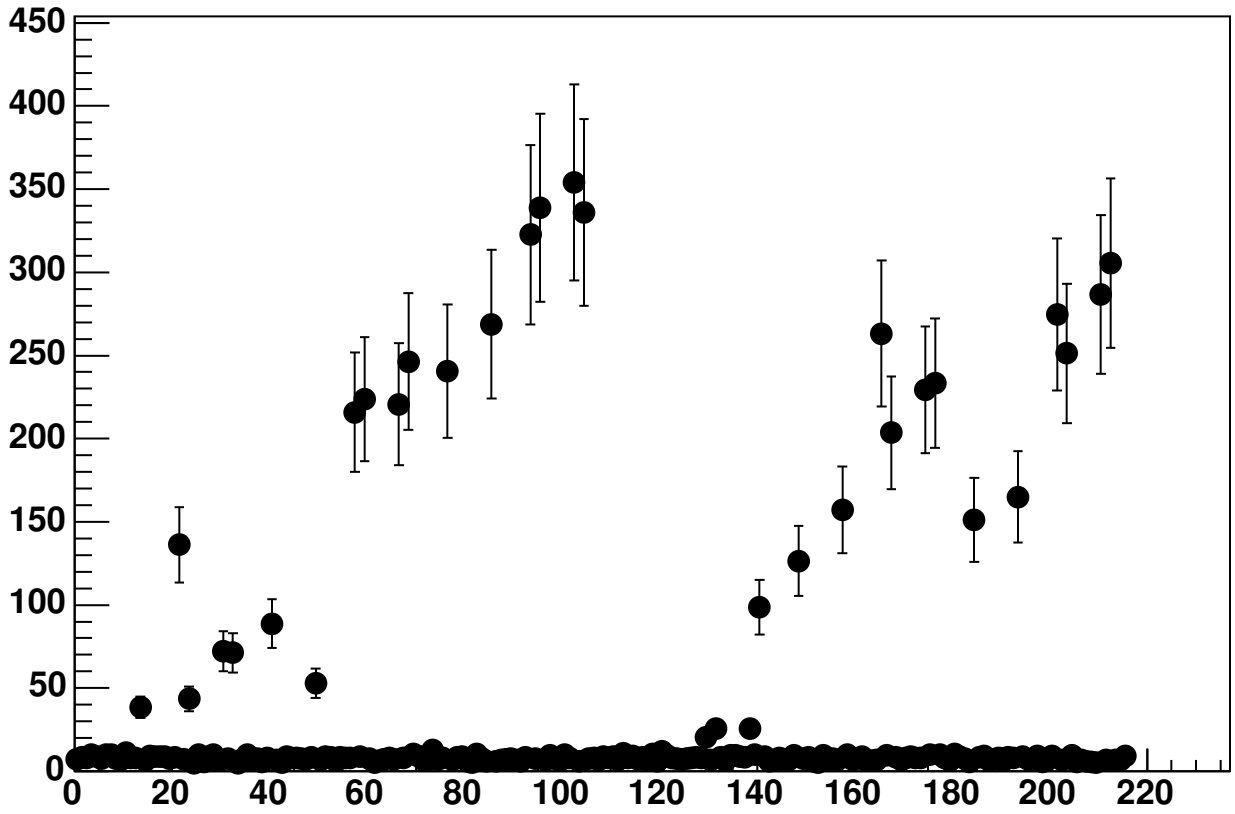
Enable 4, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



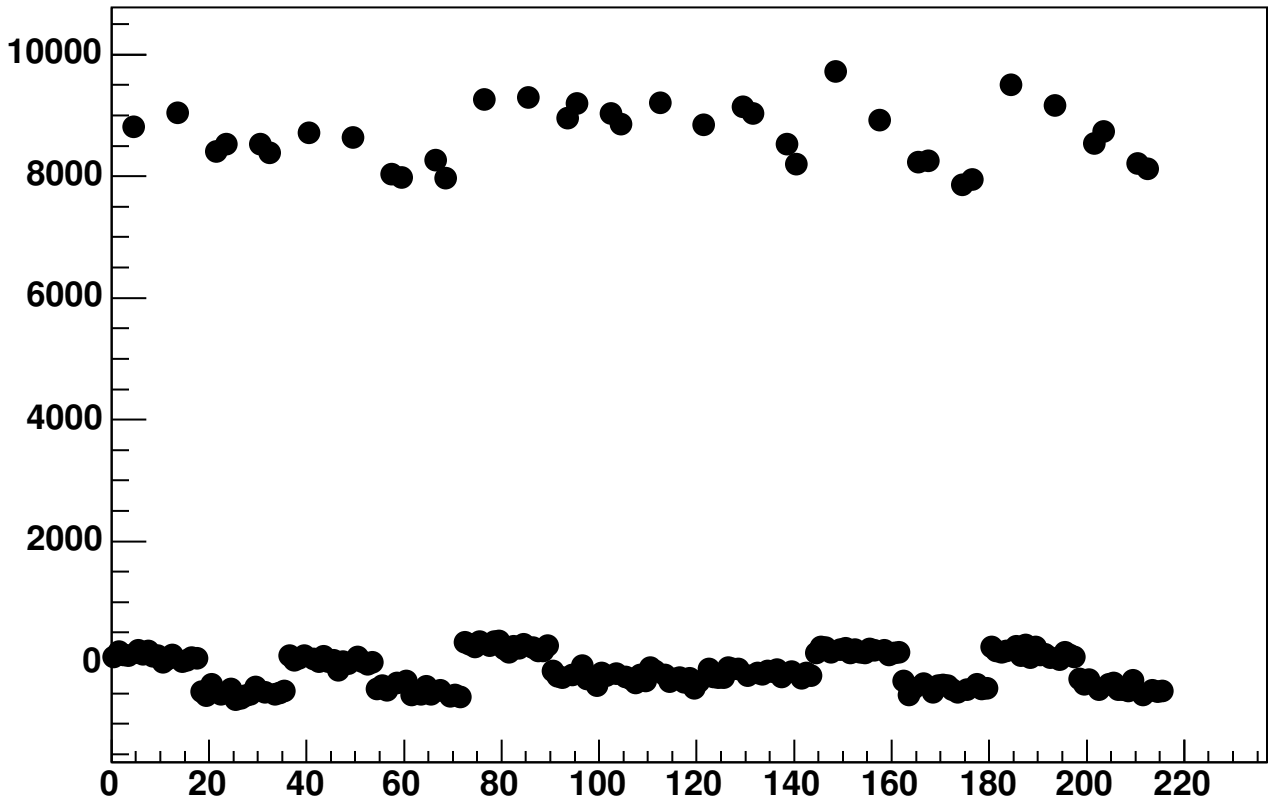
Enable 4, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



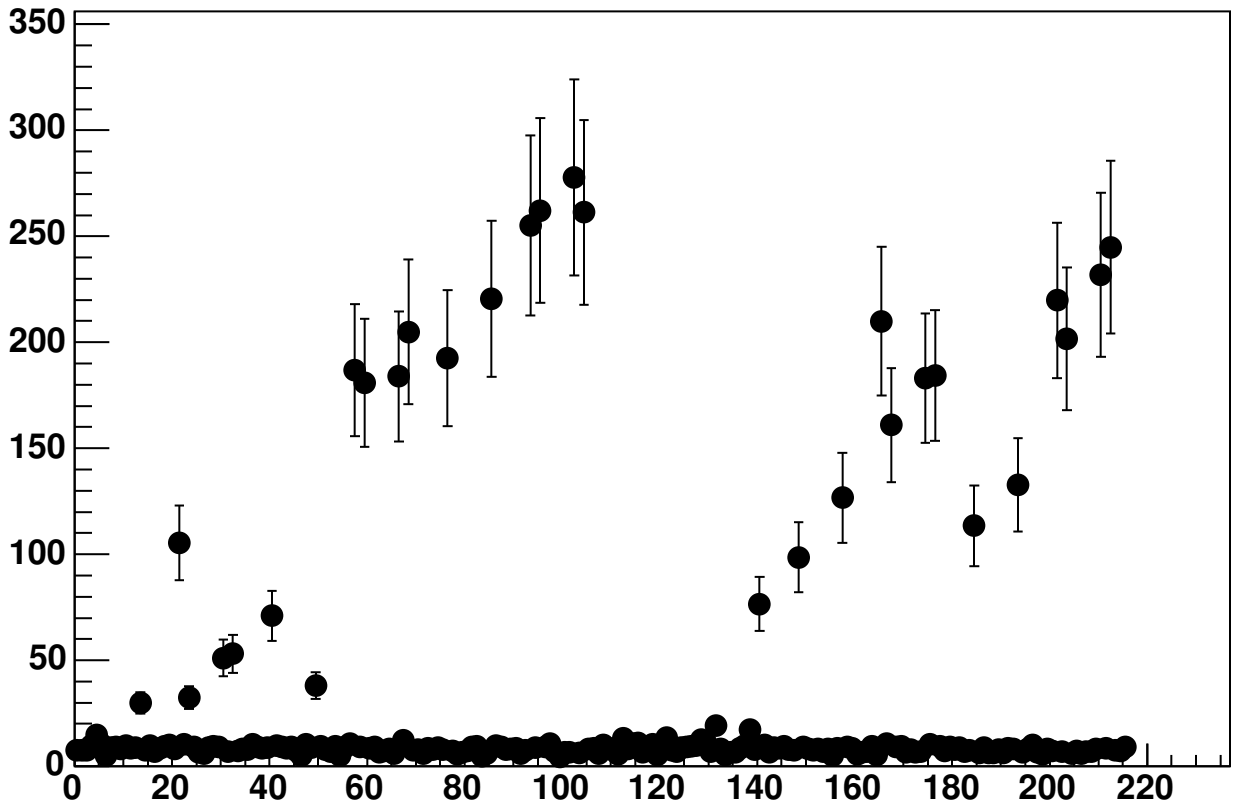
Enable 4, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



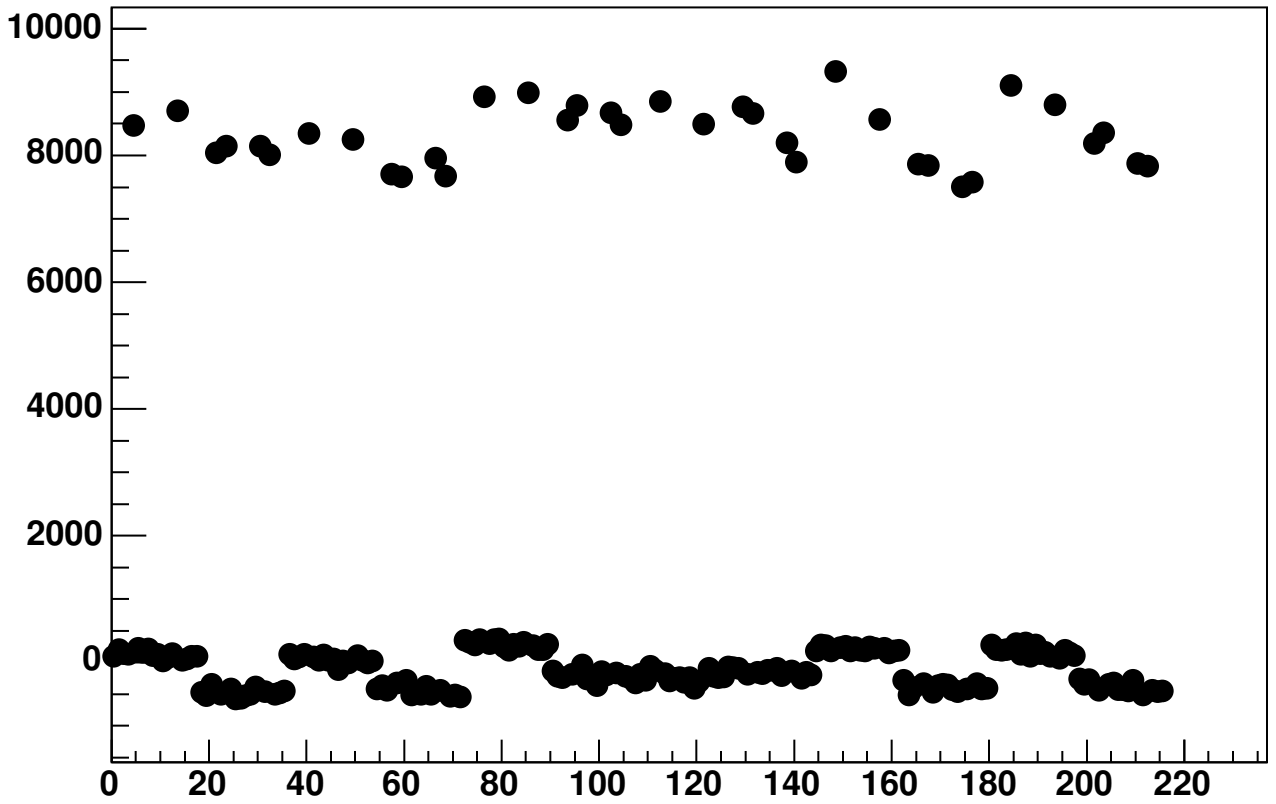
Enable 4, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



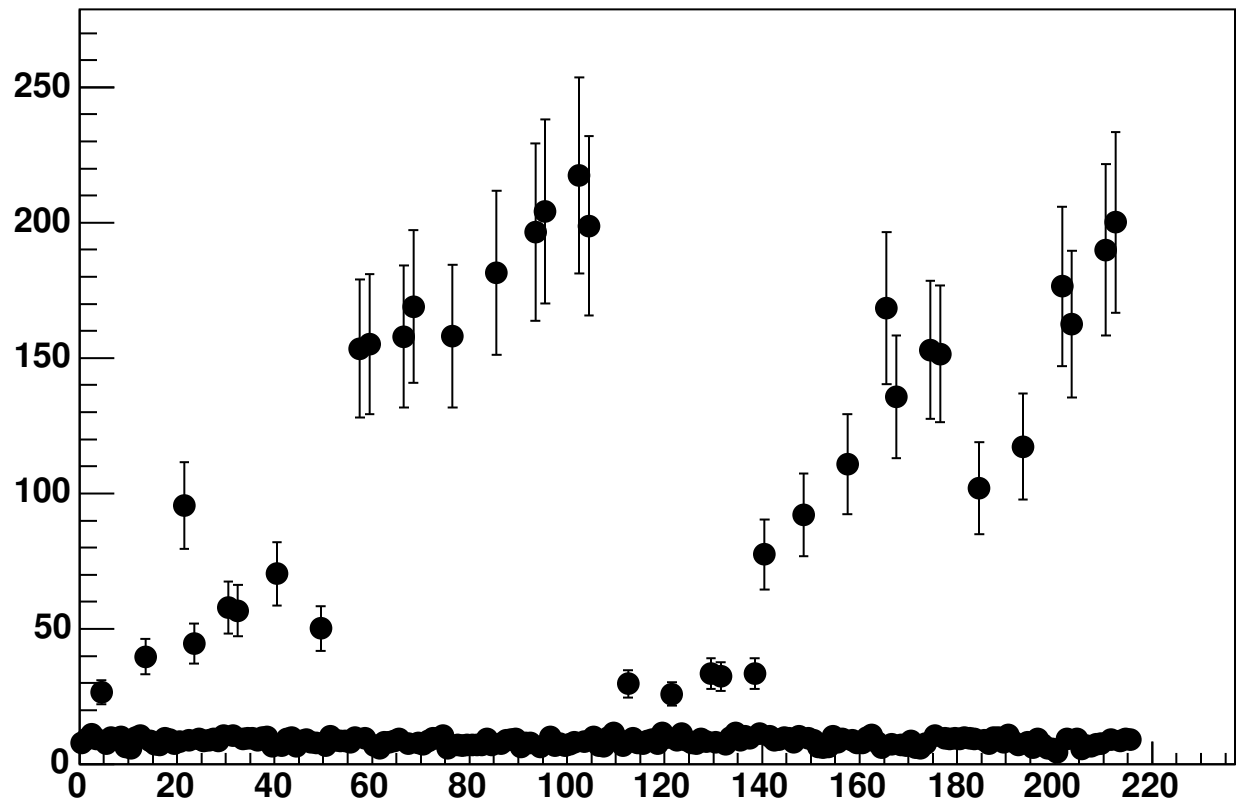
Enable 4, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



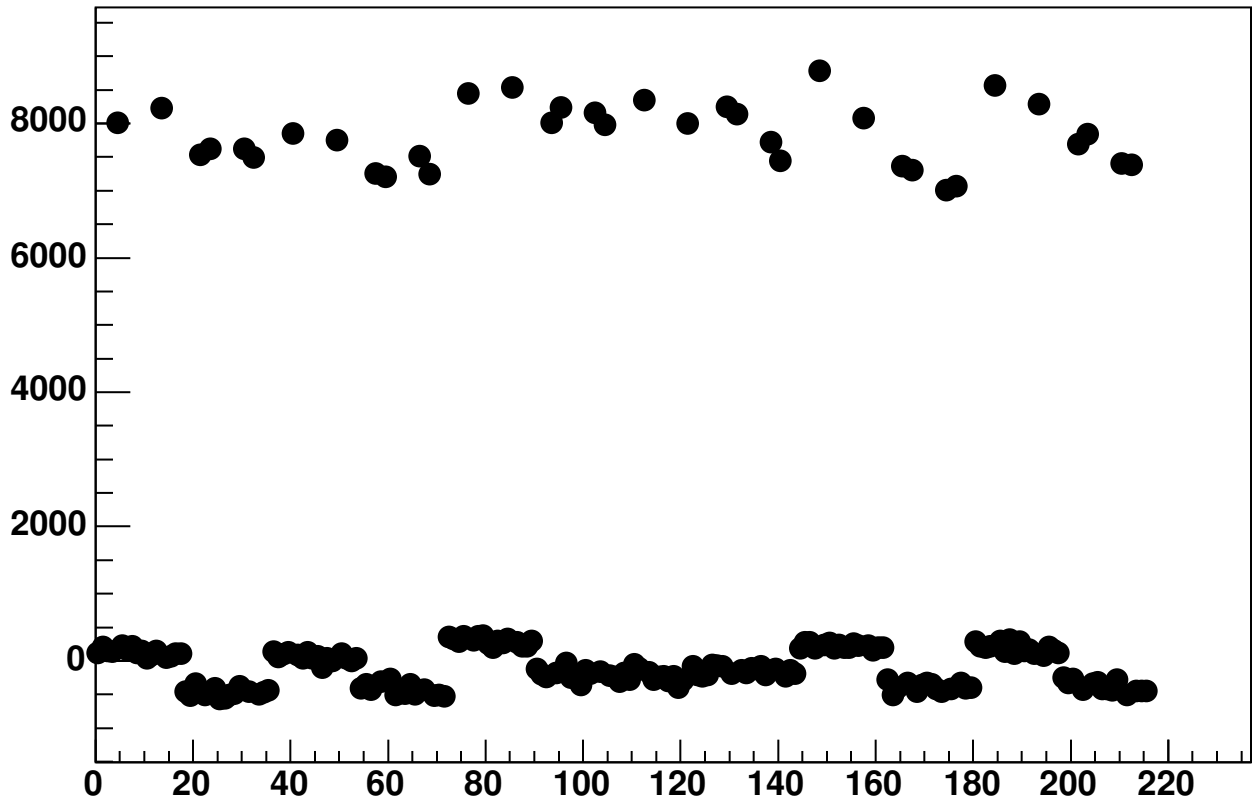
Enable 4, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



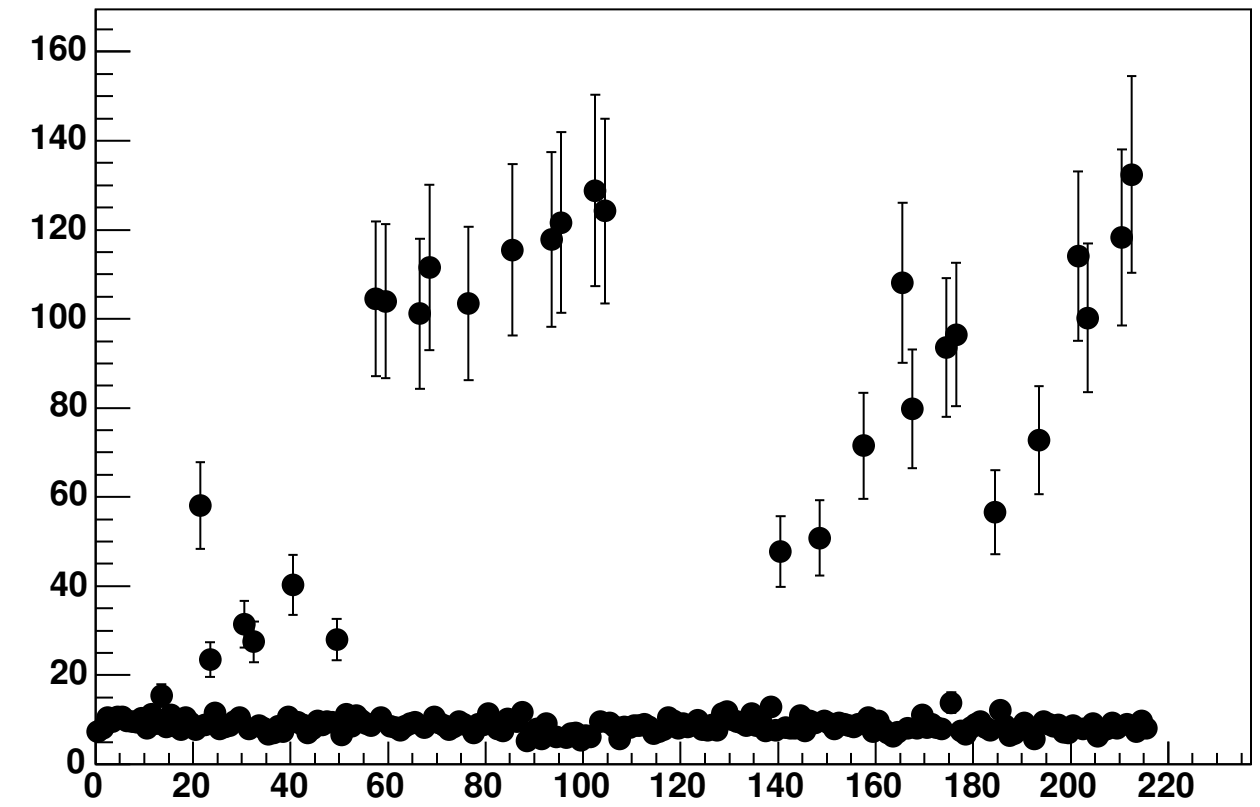
Enable 4, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



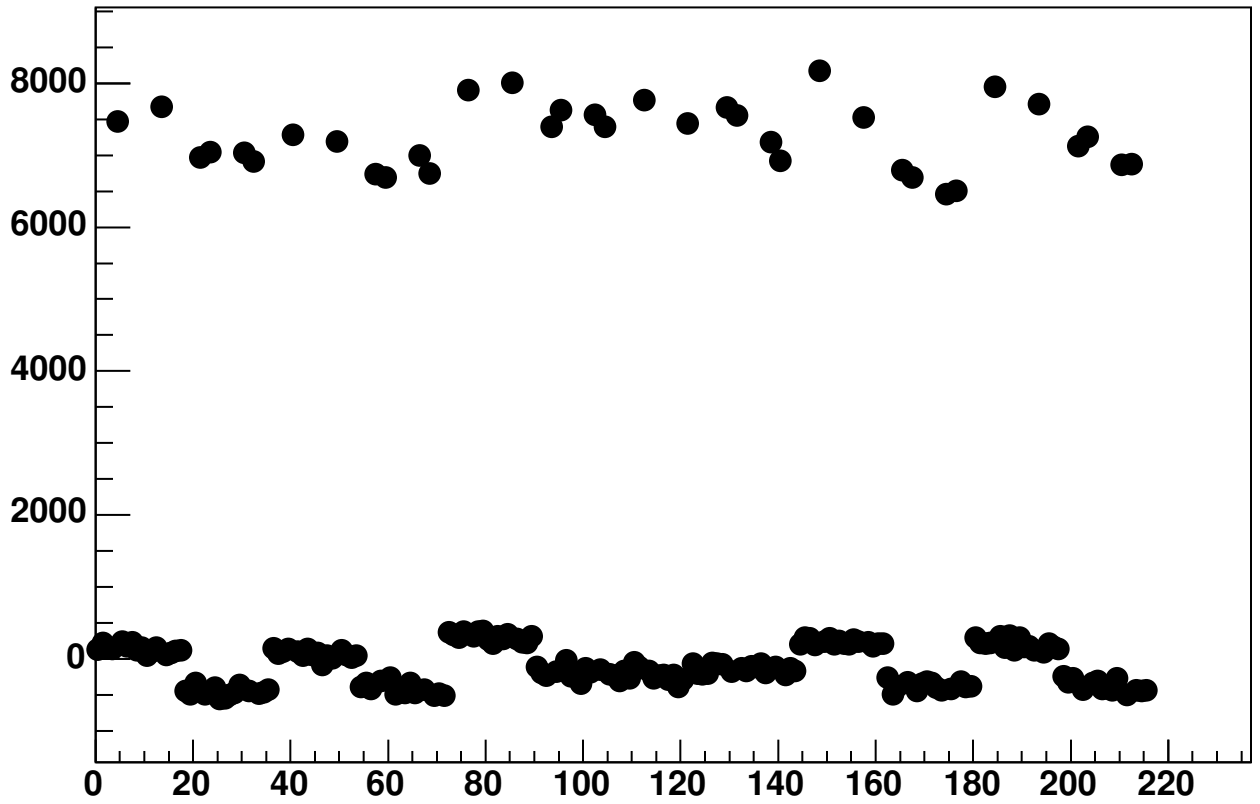
Enable 4, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



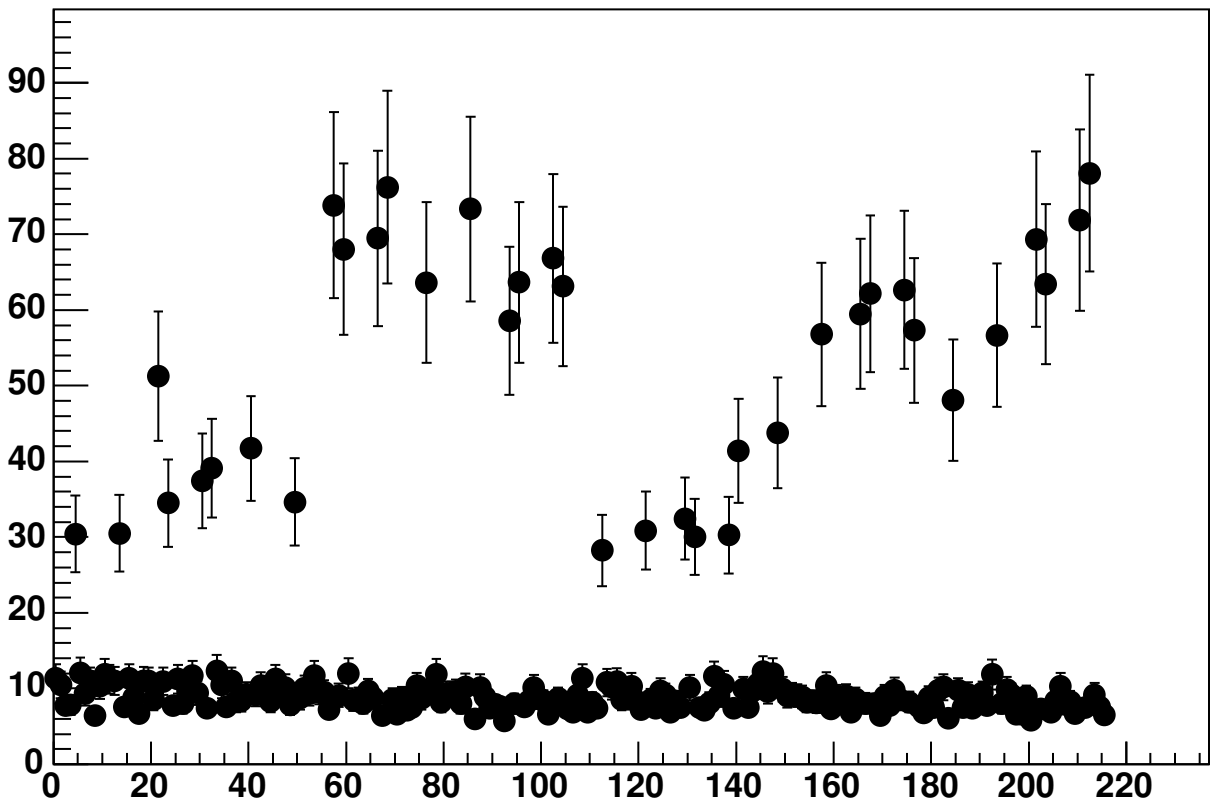
Enable 4, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



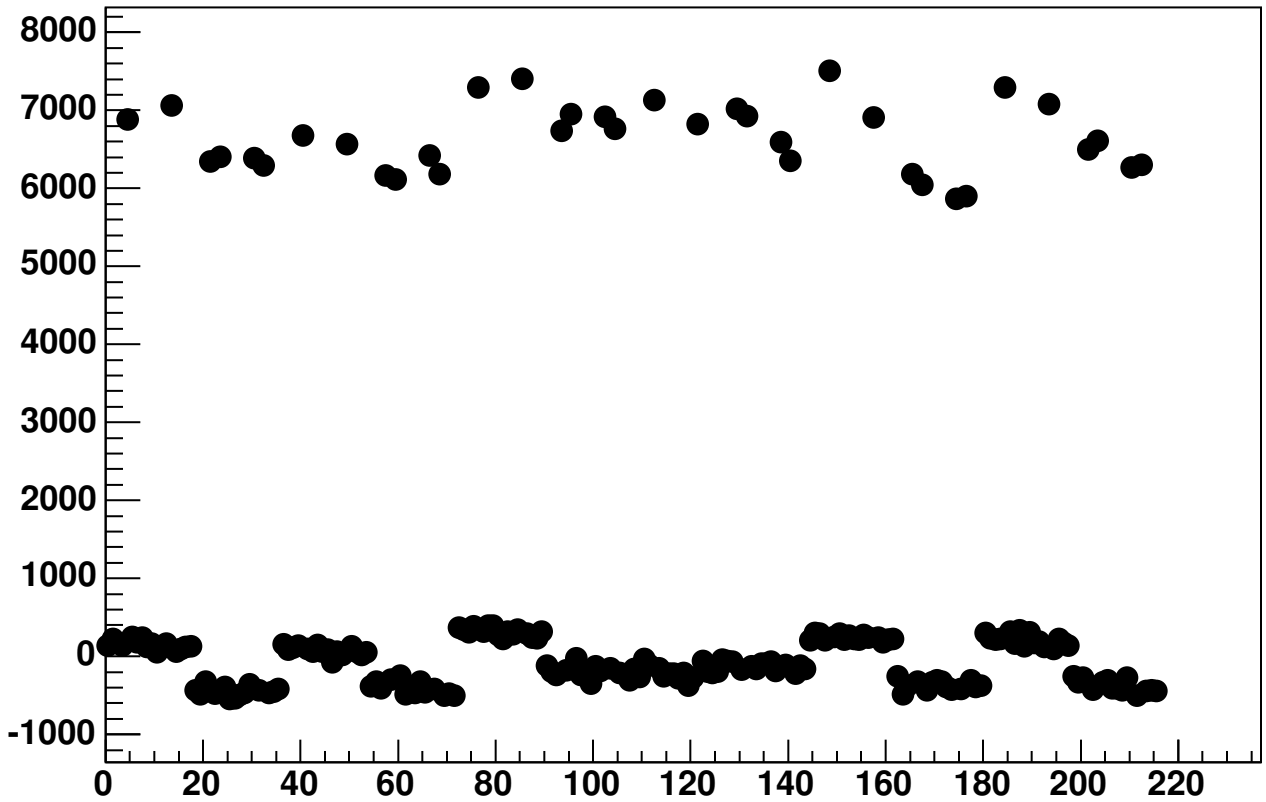
Enable 4, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



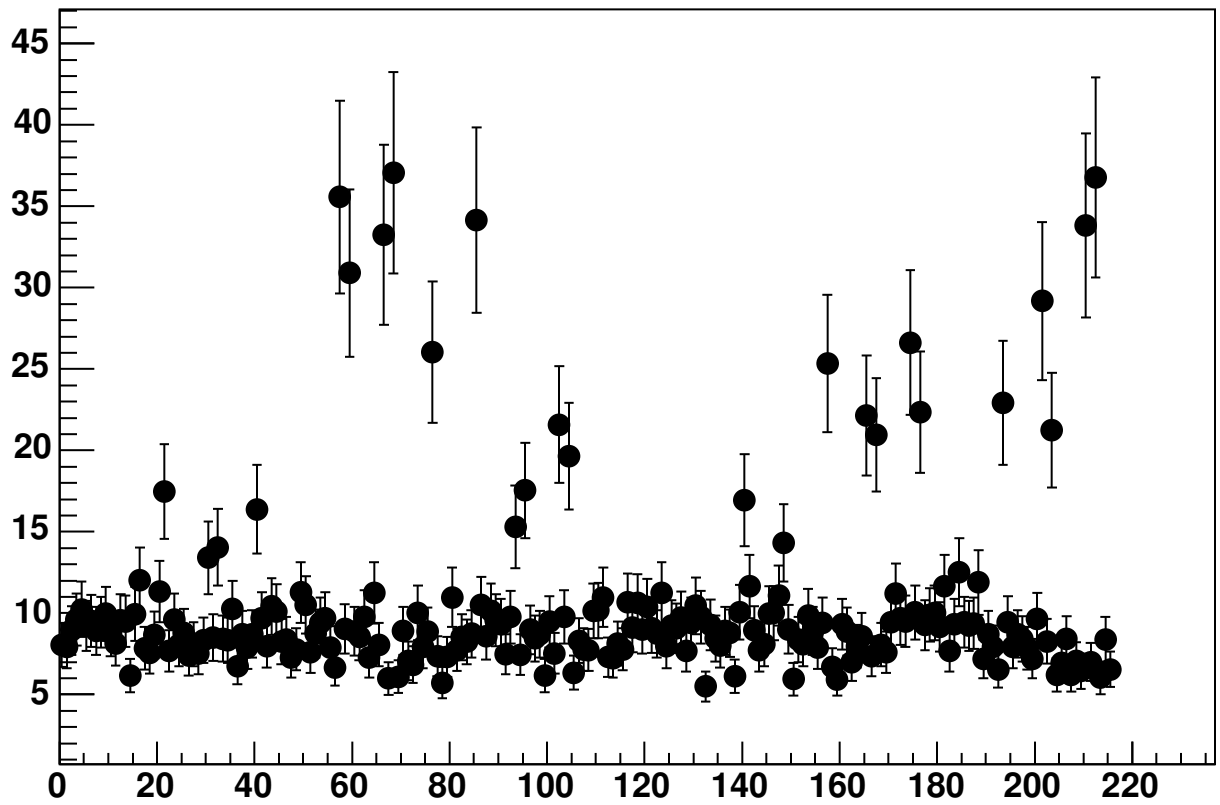
Enable 4, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



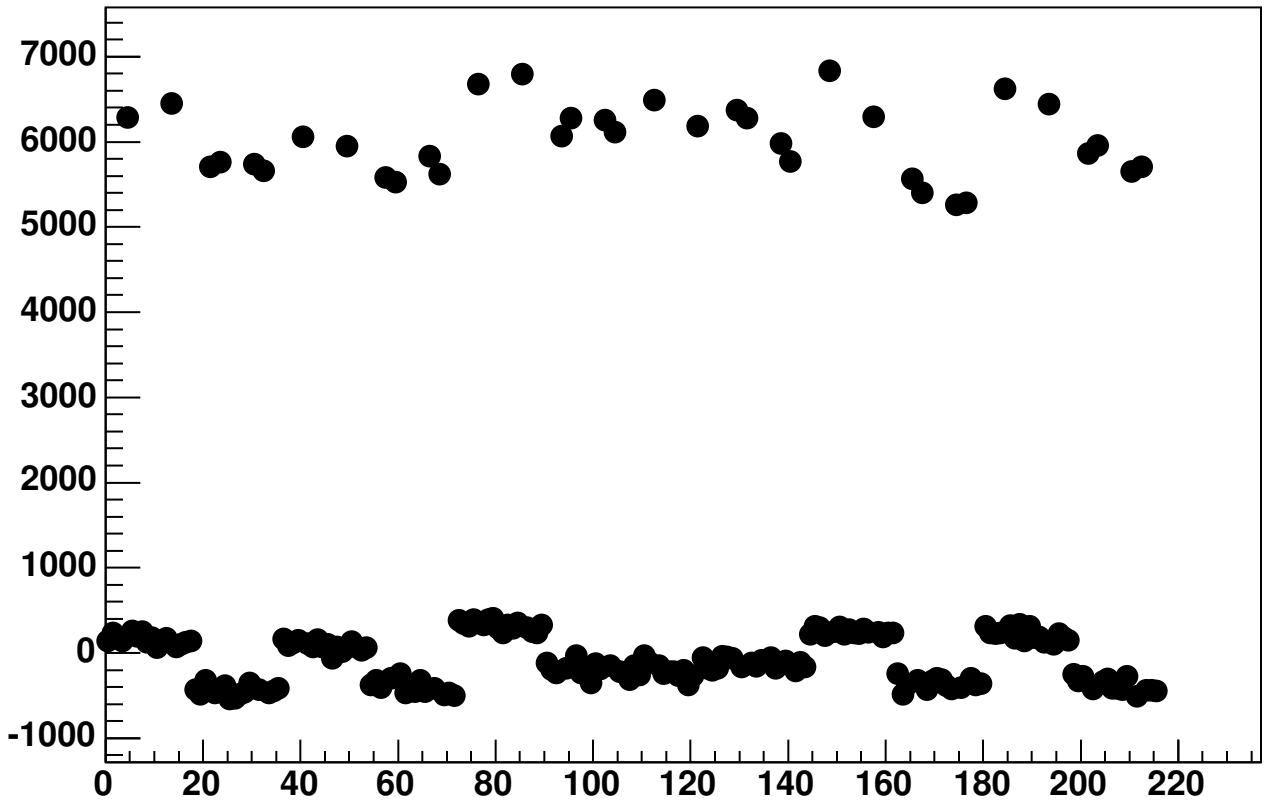
Enable 4, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



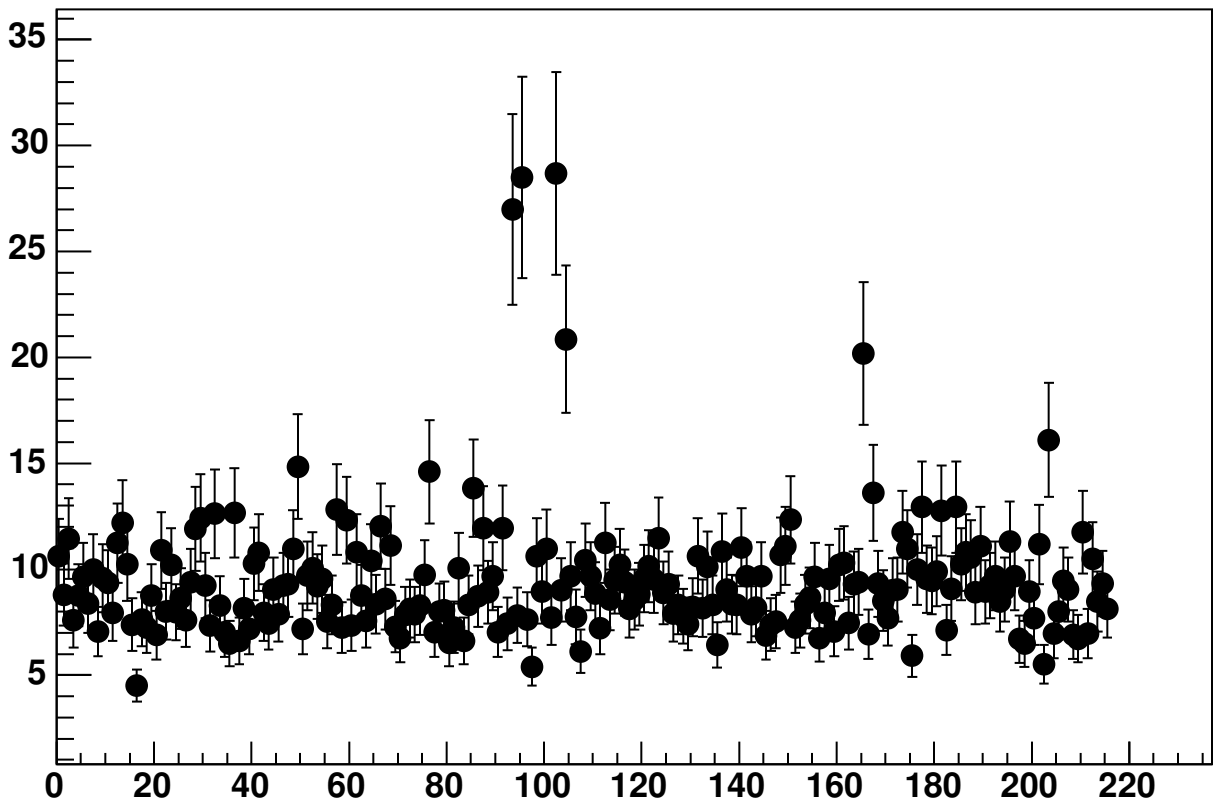
Enable 4, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



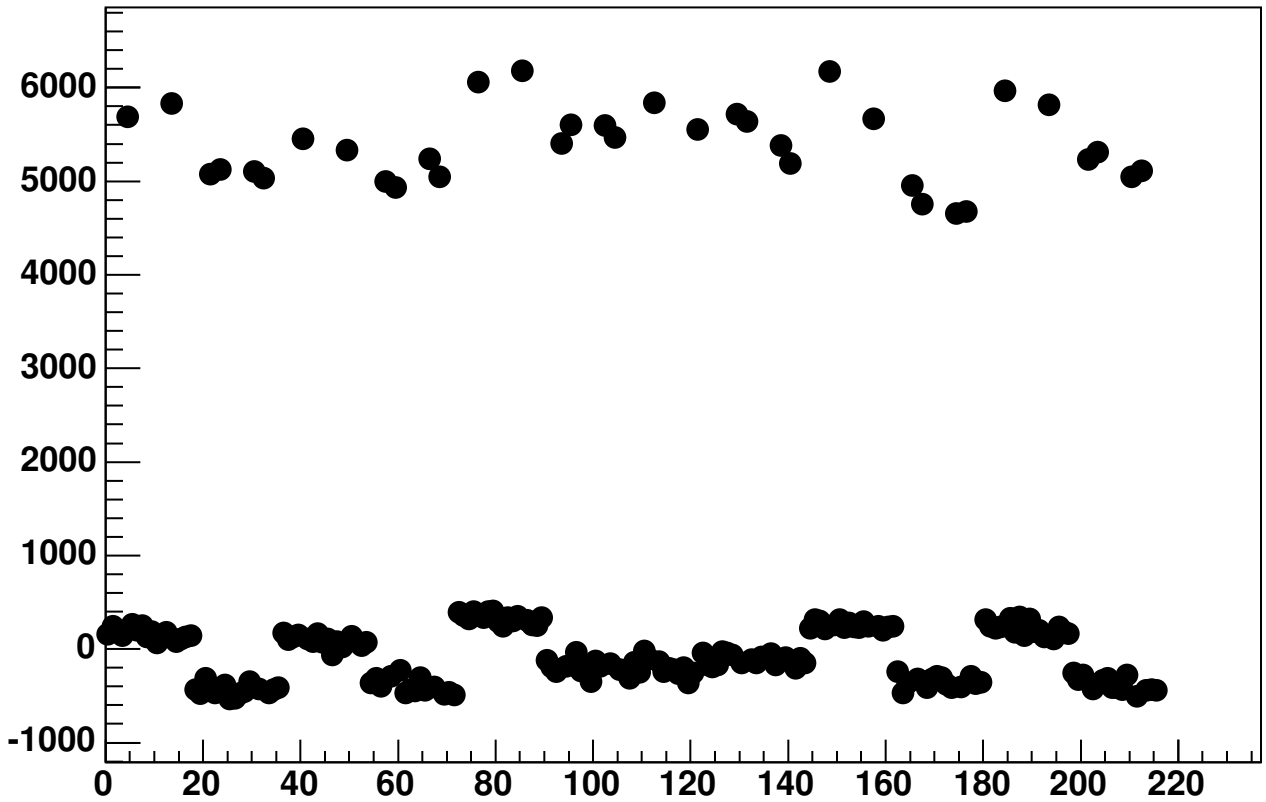
Enable 4, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



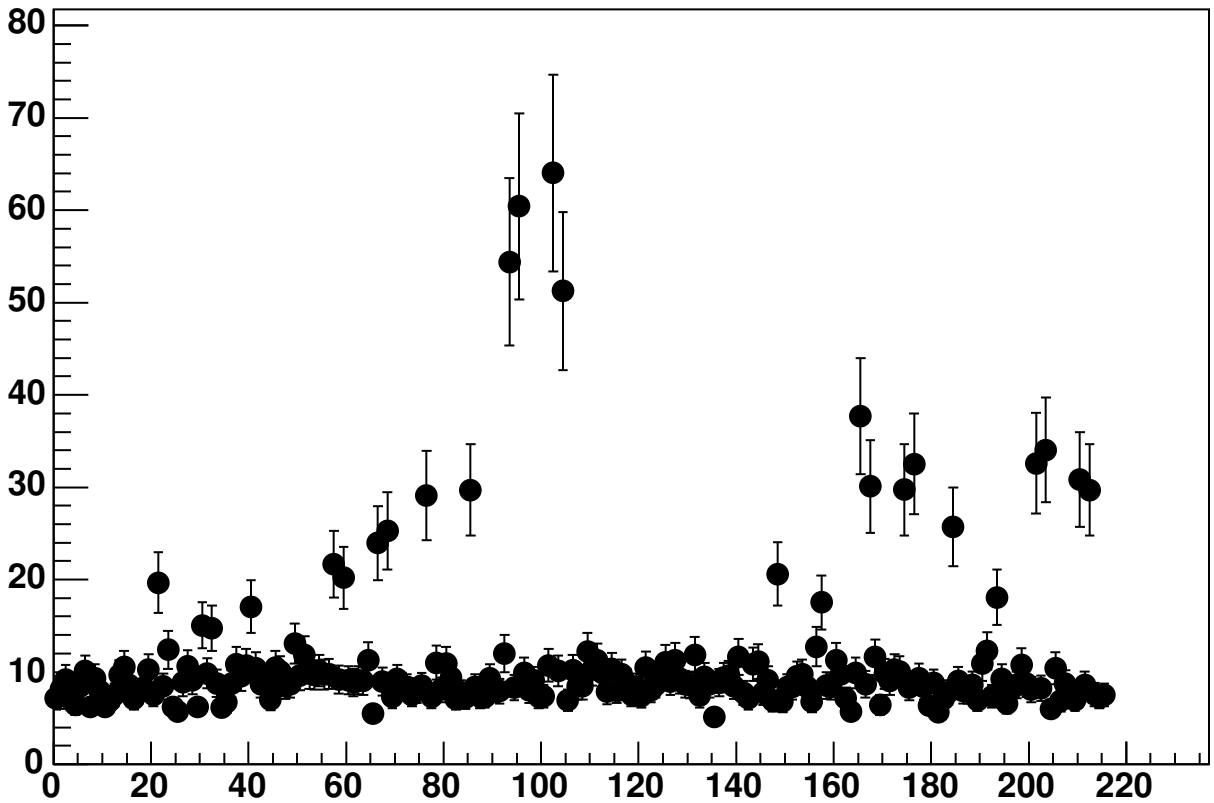
Enable 4, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



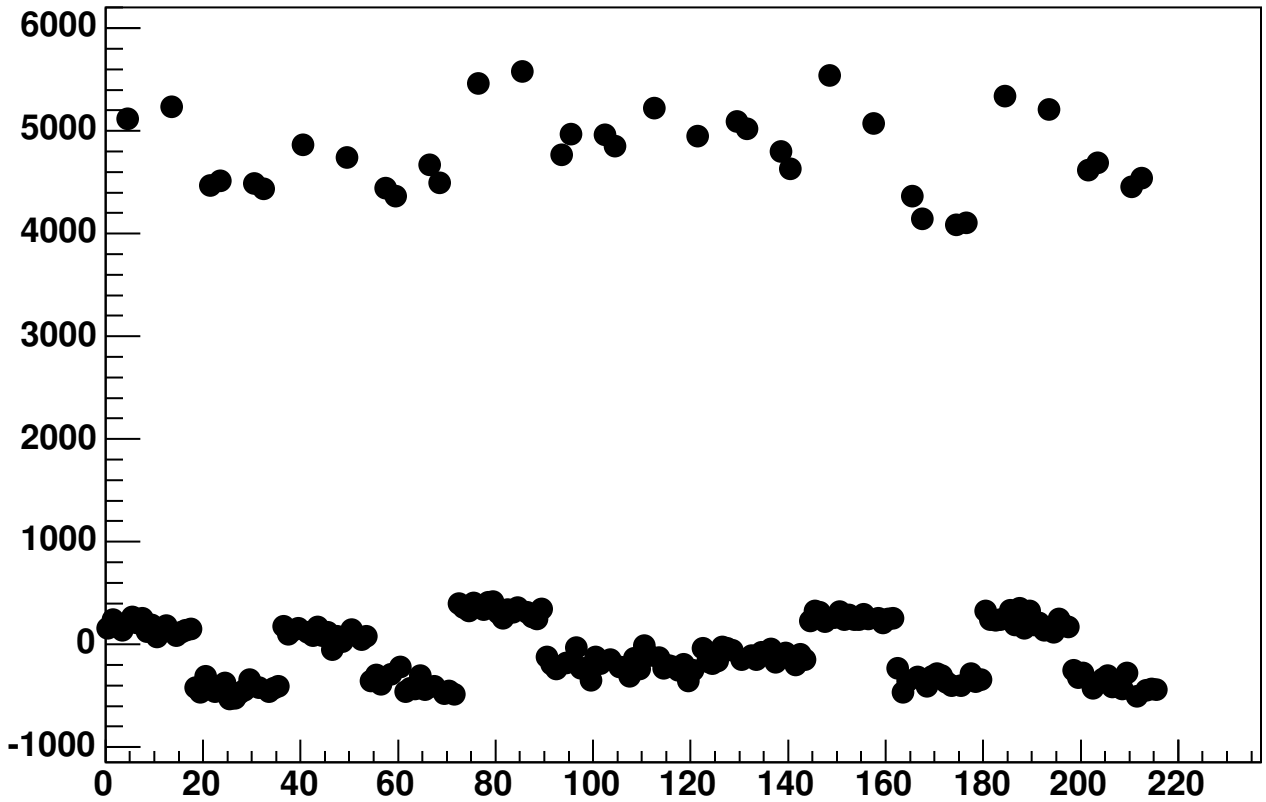
Enable 4, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



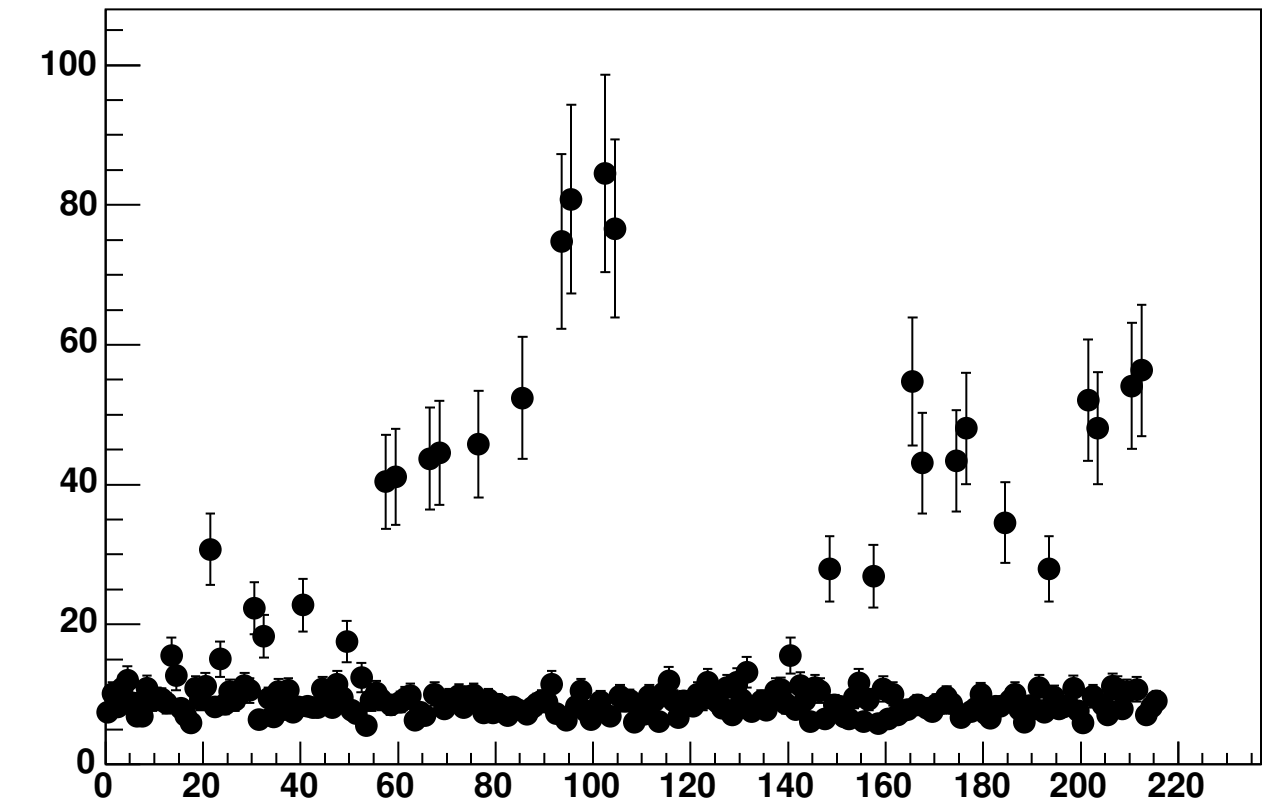
Enable 4, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



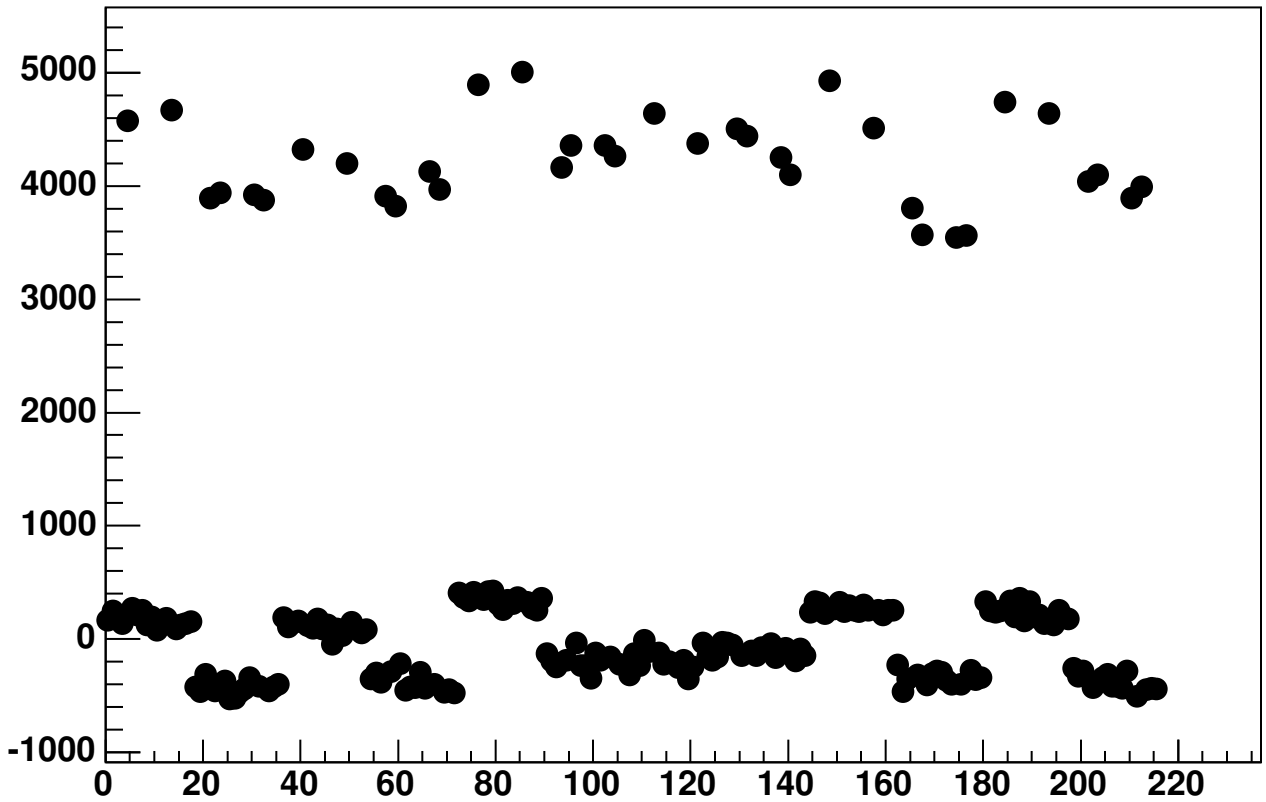
Enable 4, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



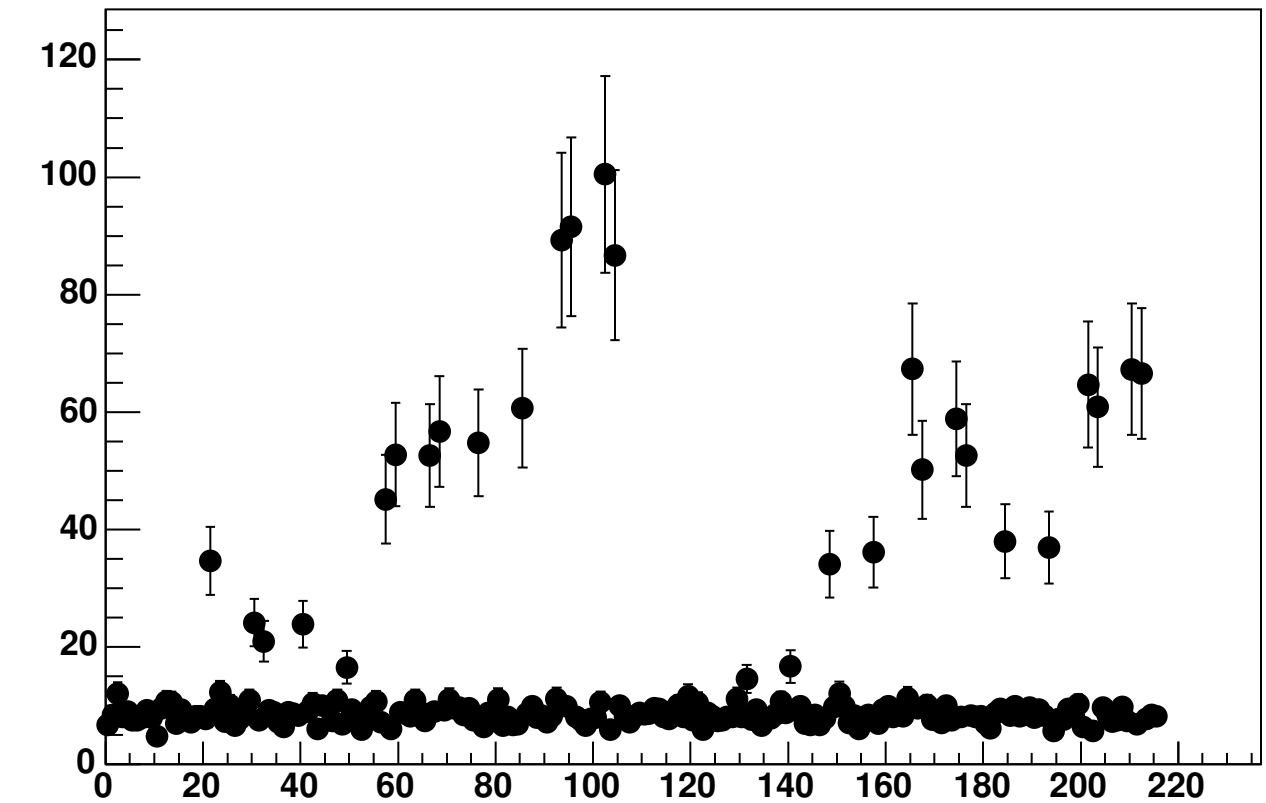
Enable 4, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



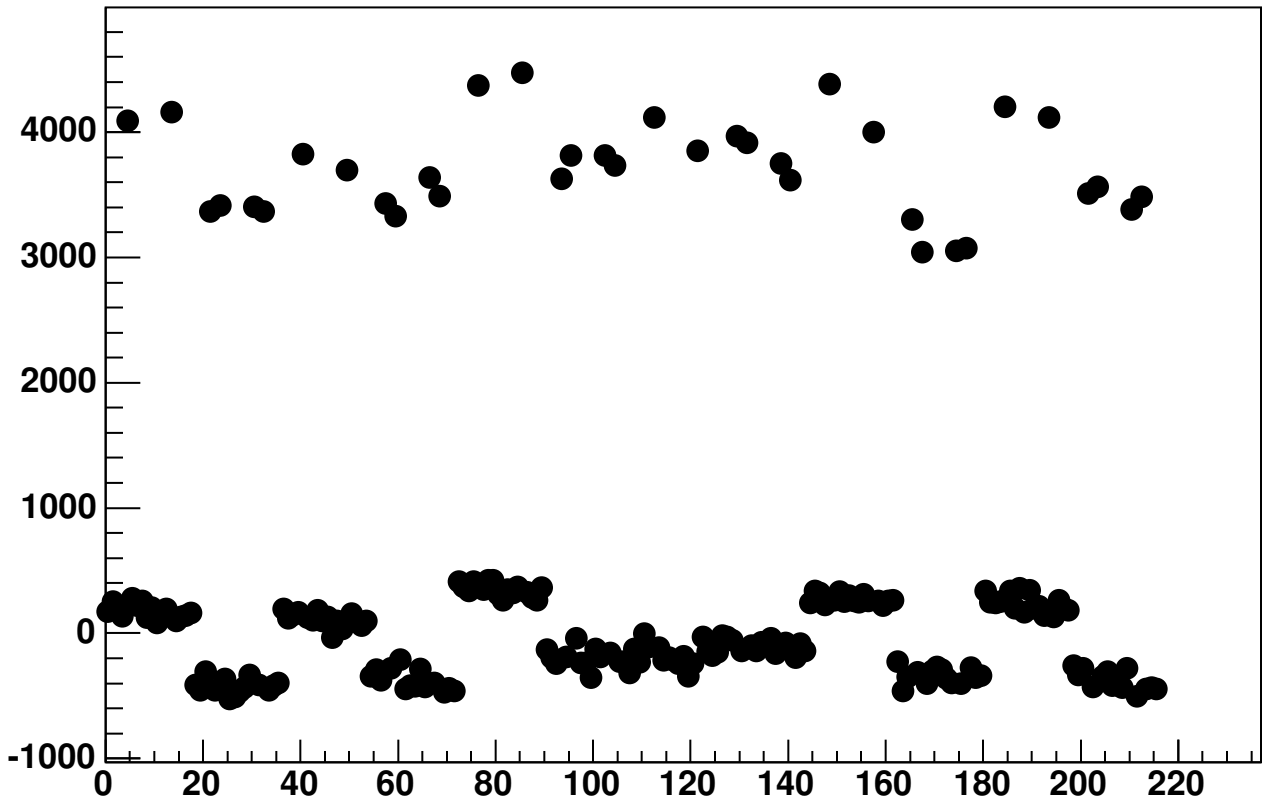
Enable 4, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



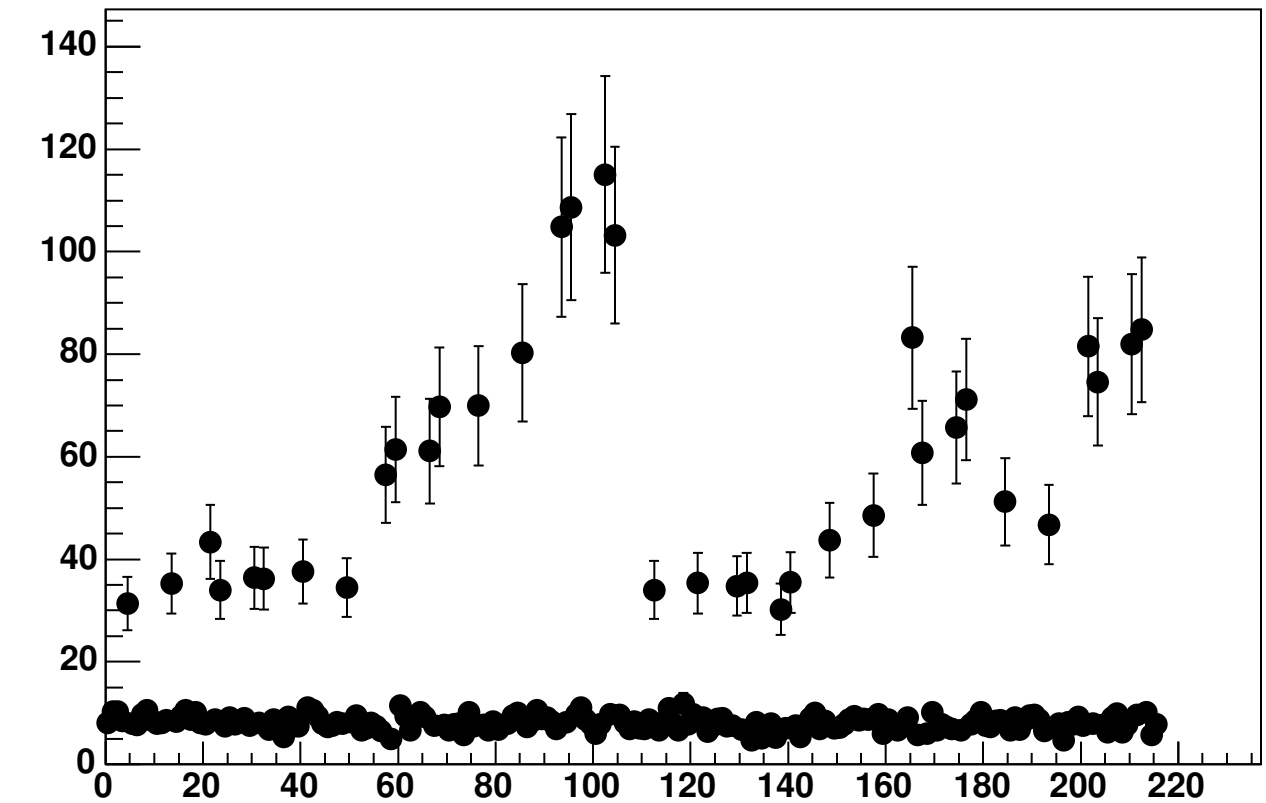
Enable 4, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



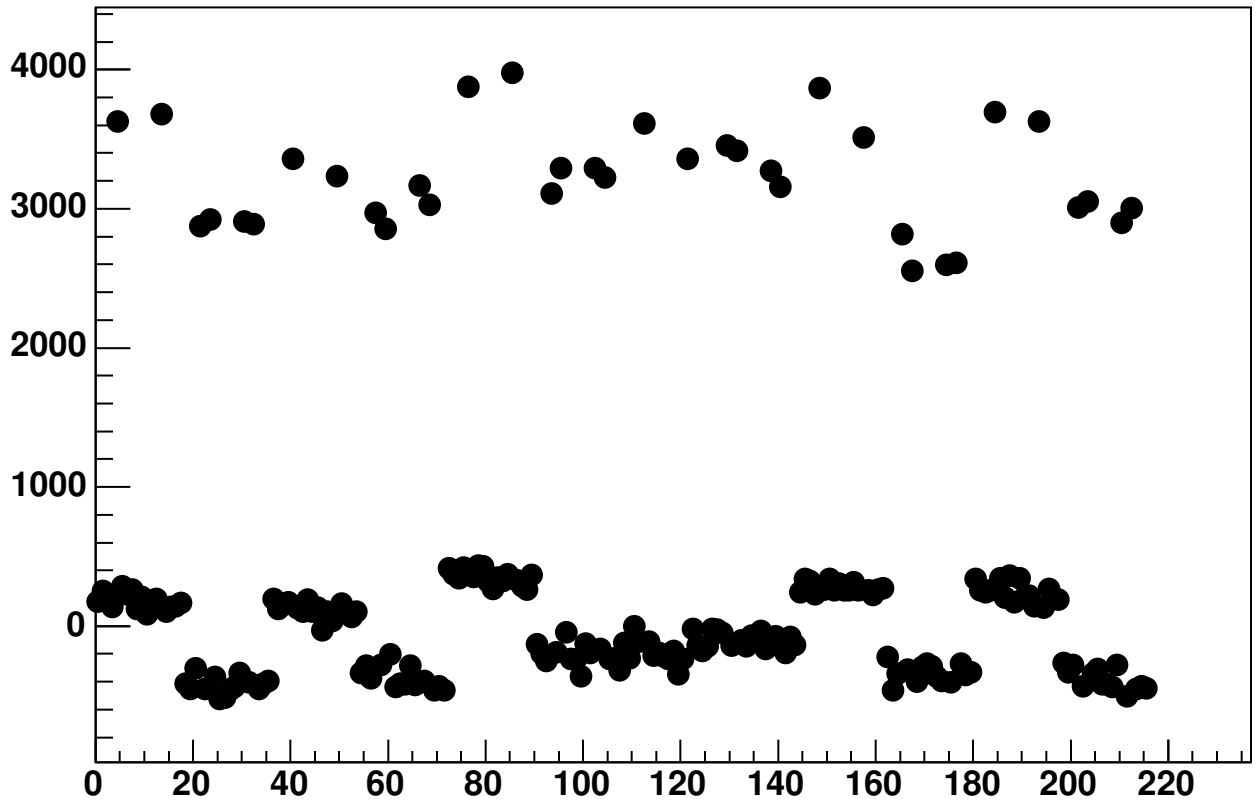
Enable 4, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



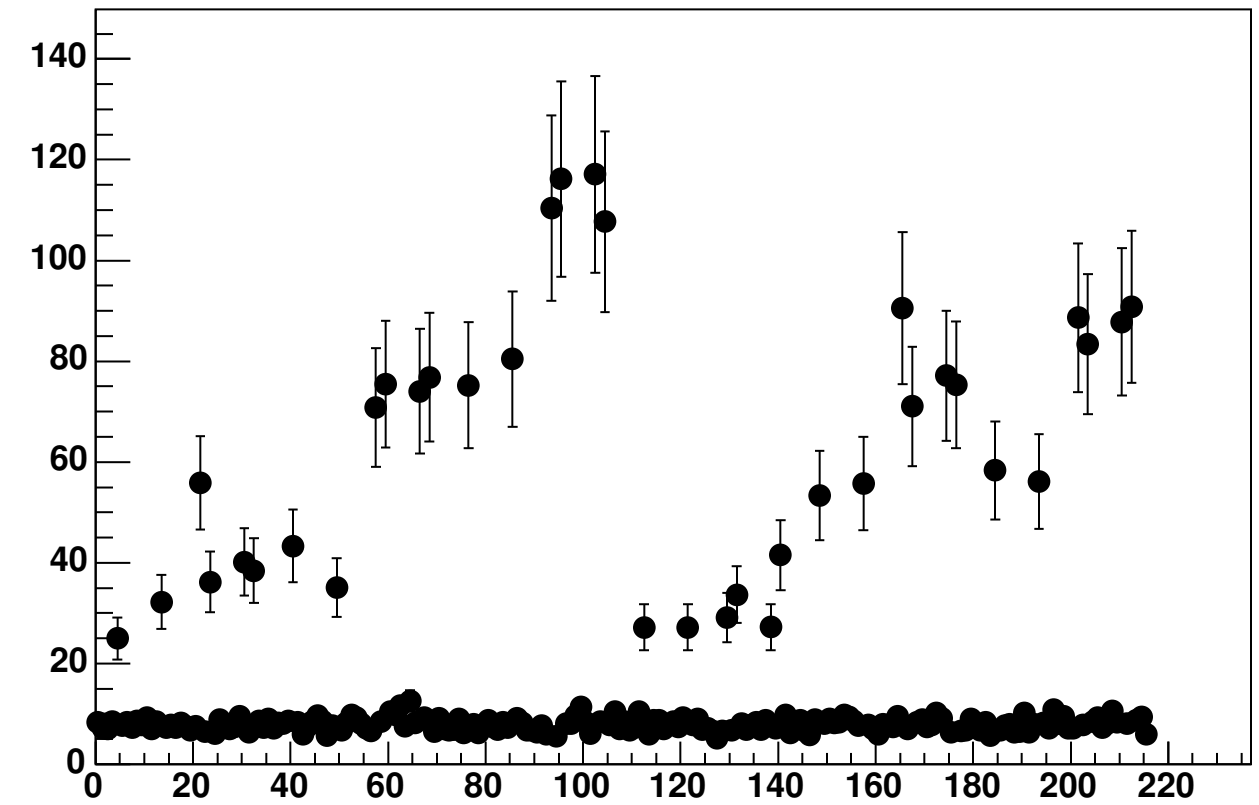
Enable 4, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



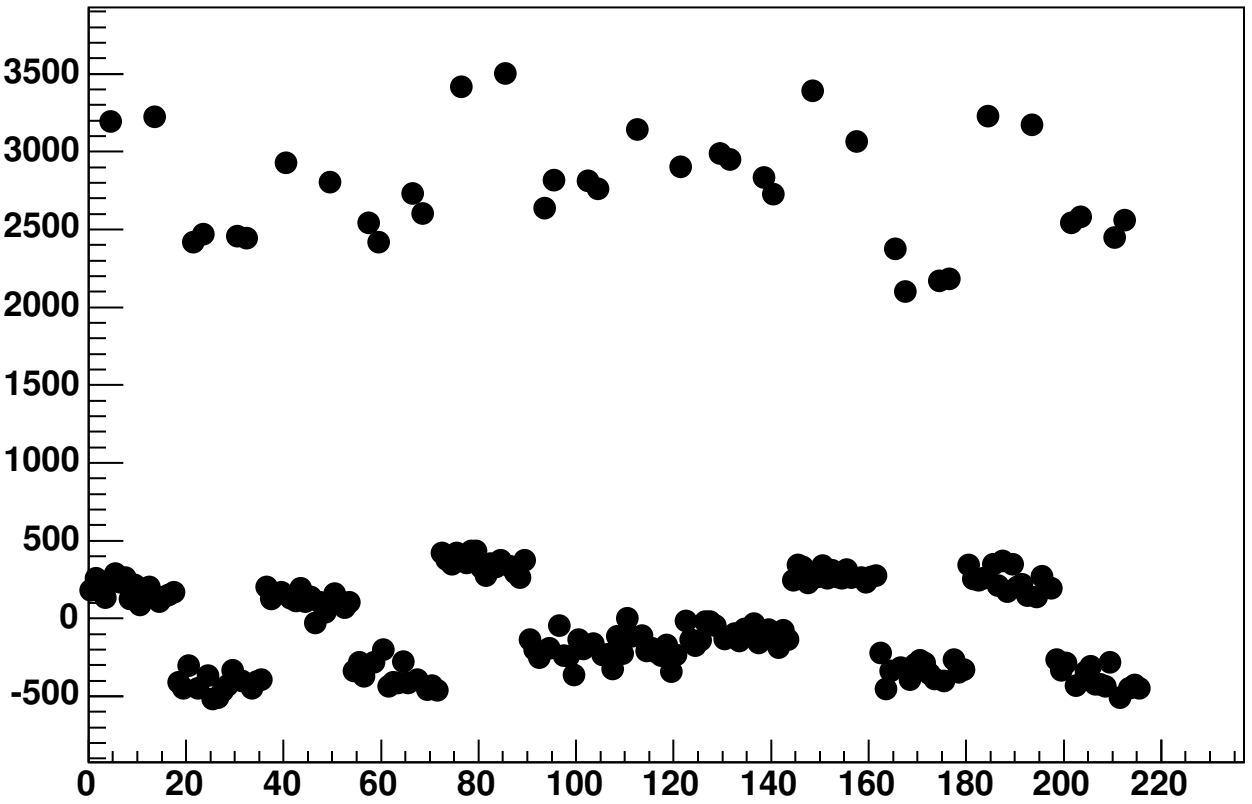
Enable 4, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



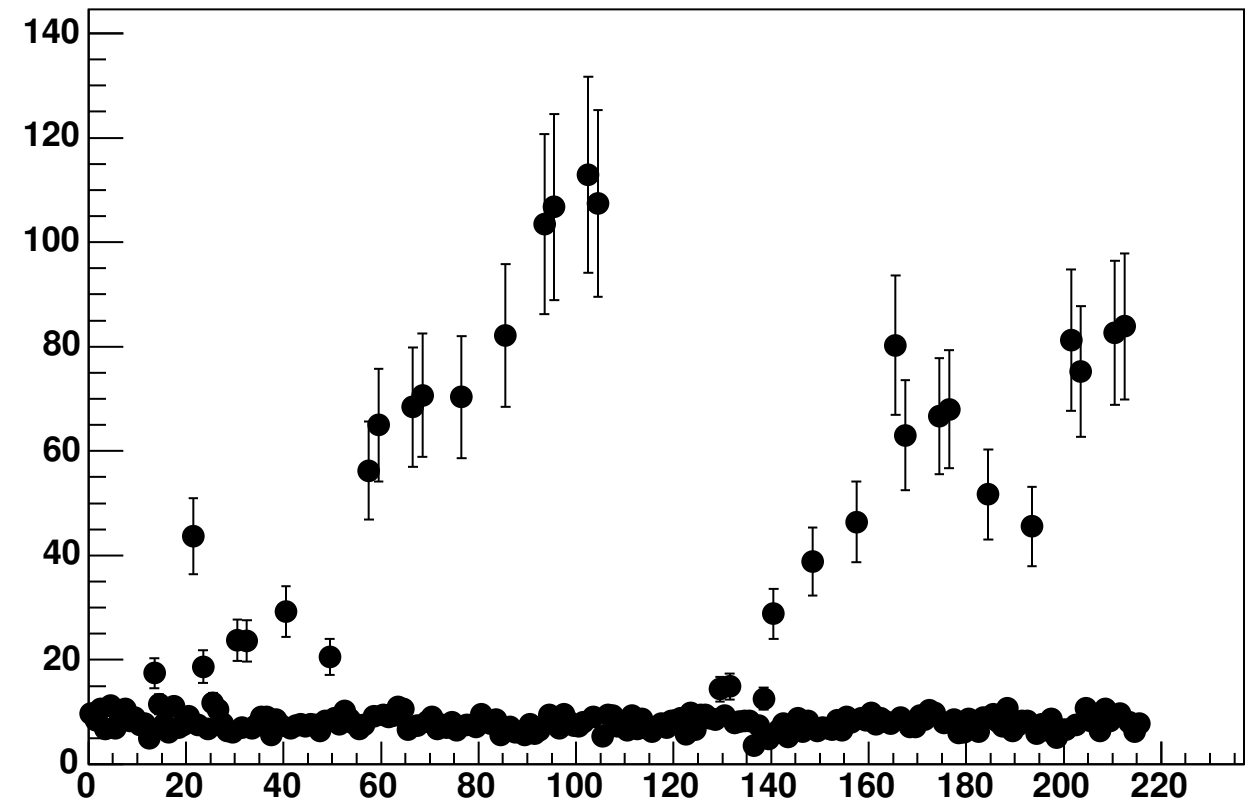
Enable 4, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



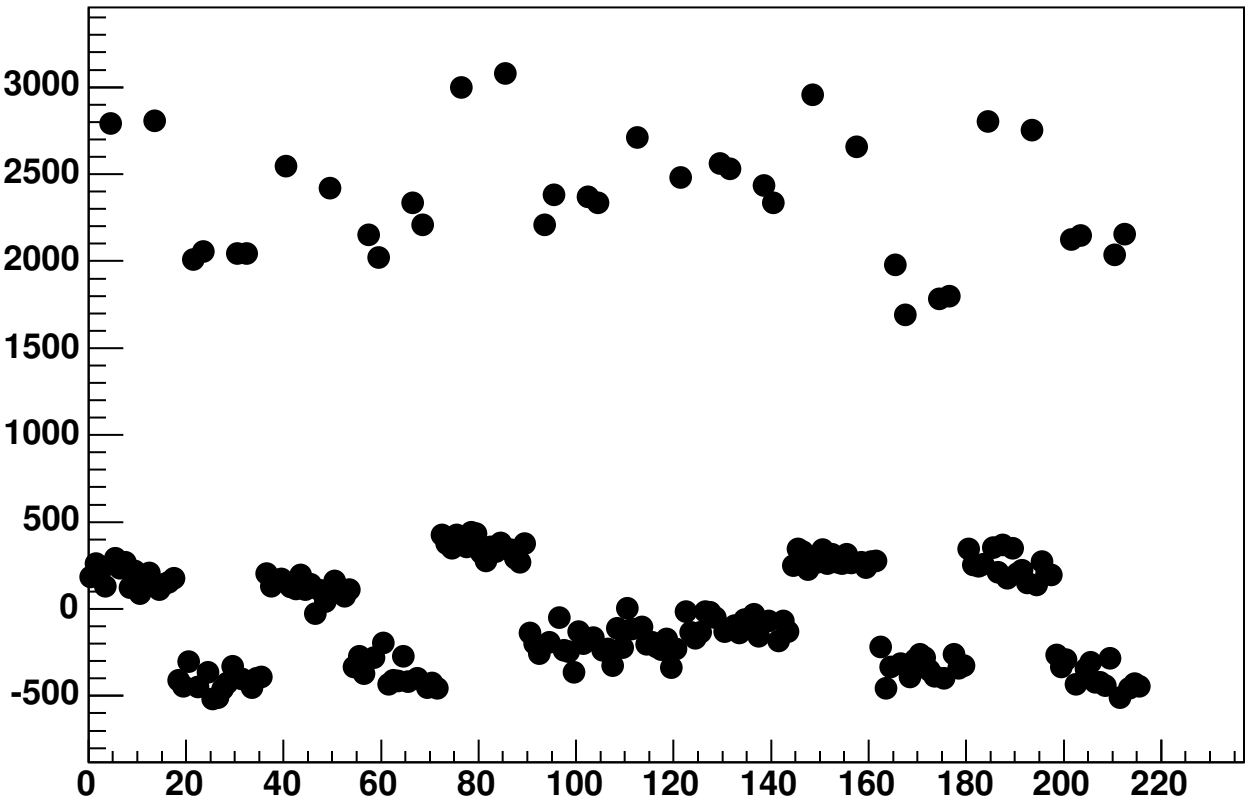
Enable 4, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



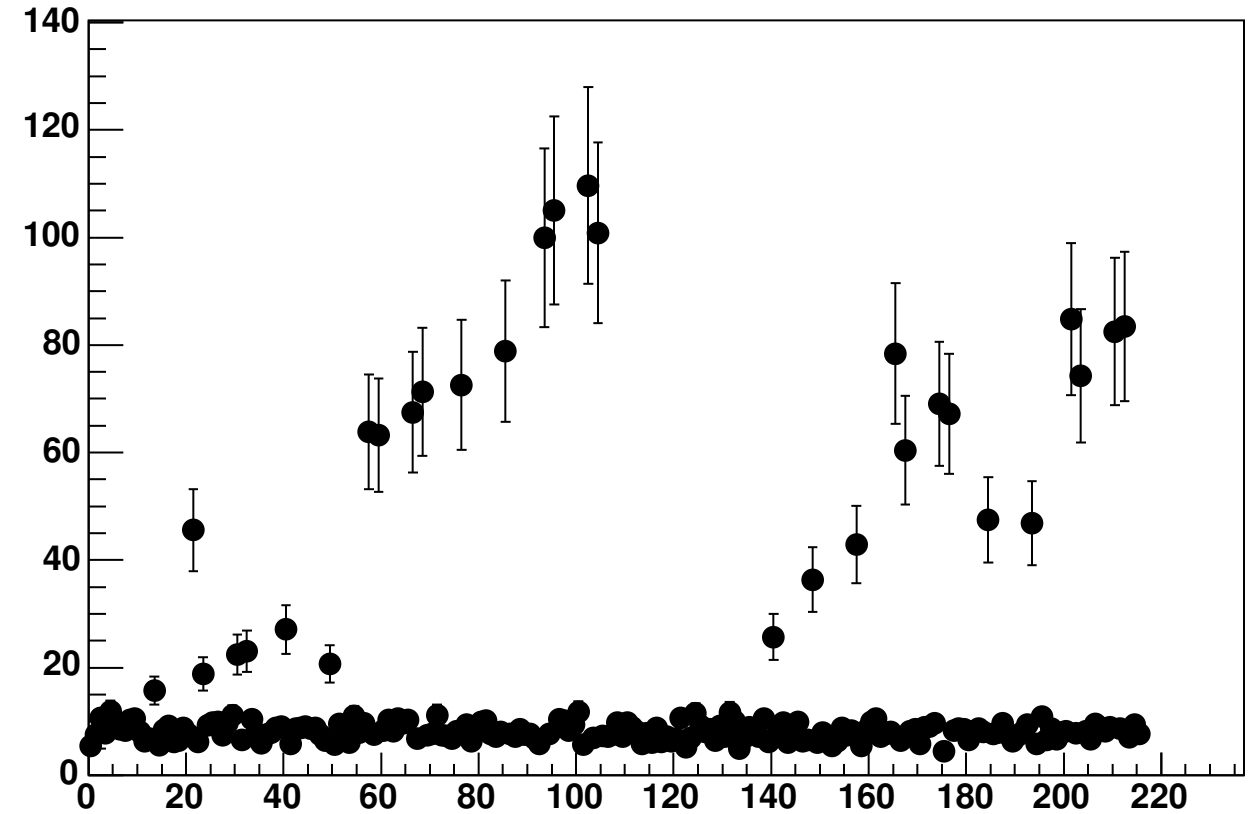
Enable 4, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



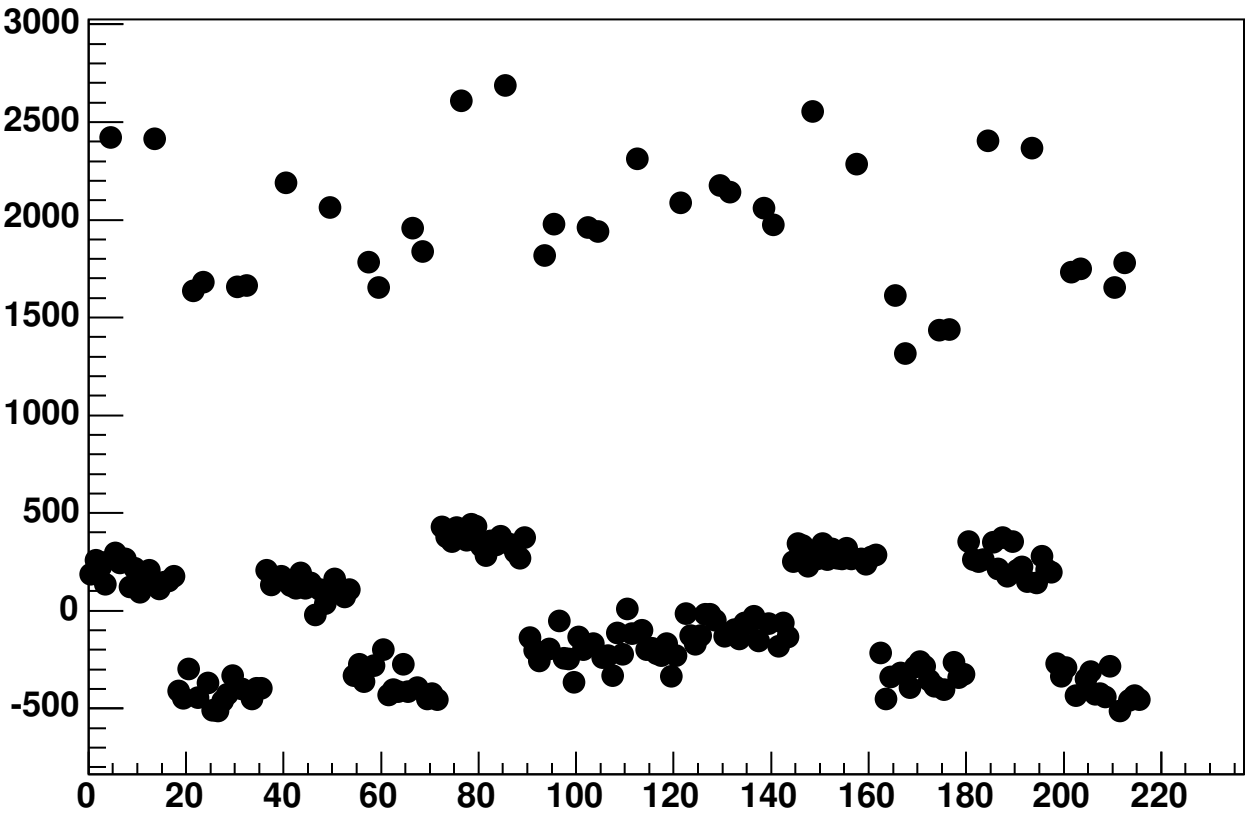
Enable 4, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



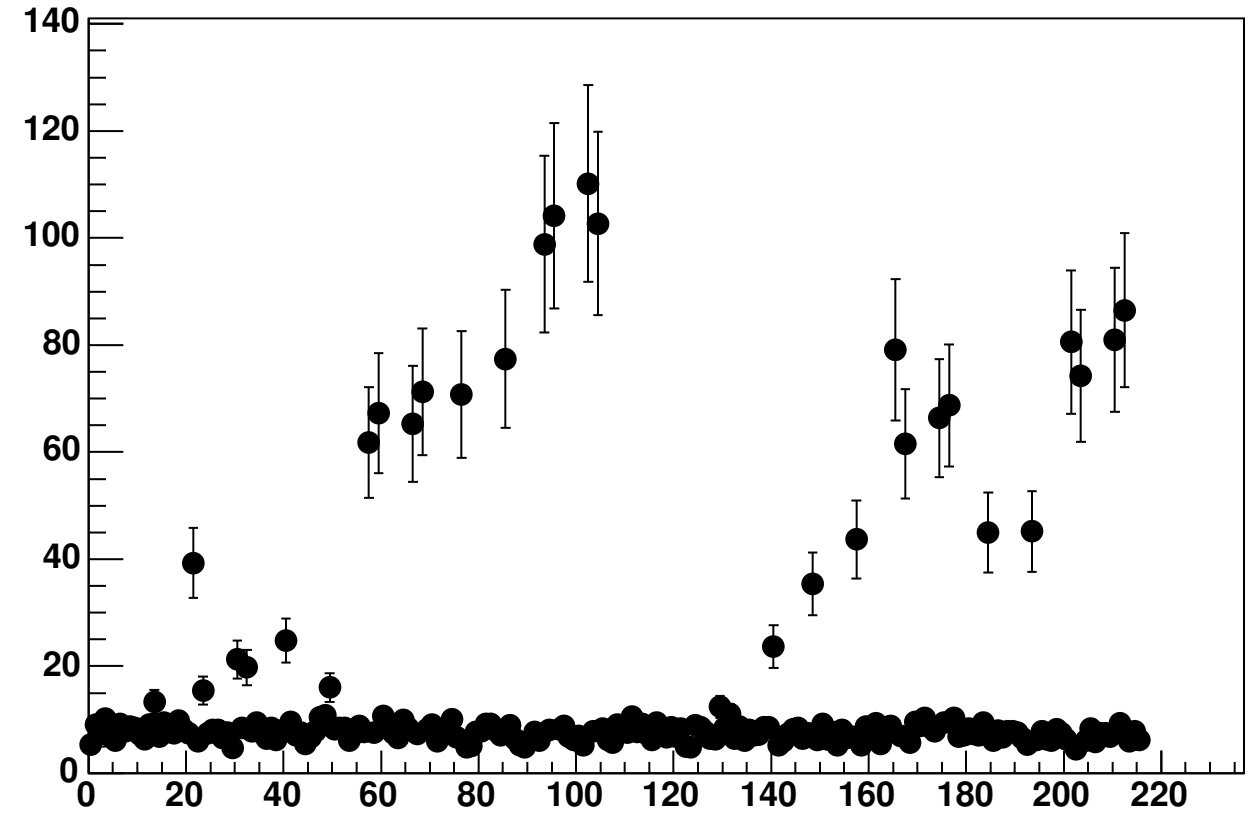
Enable 4, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



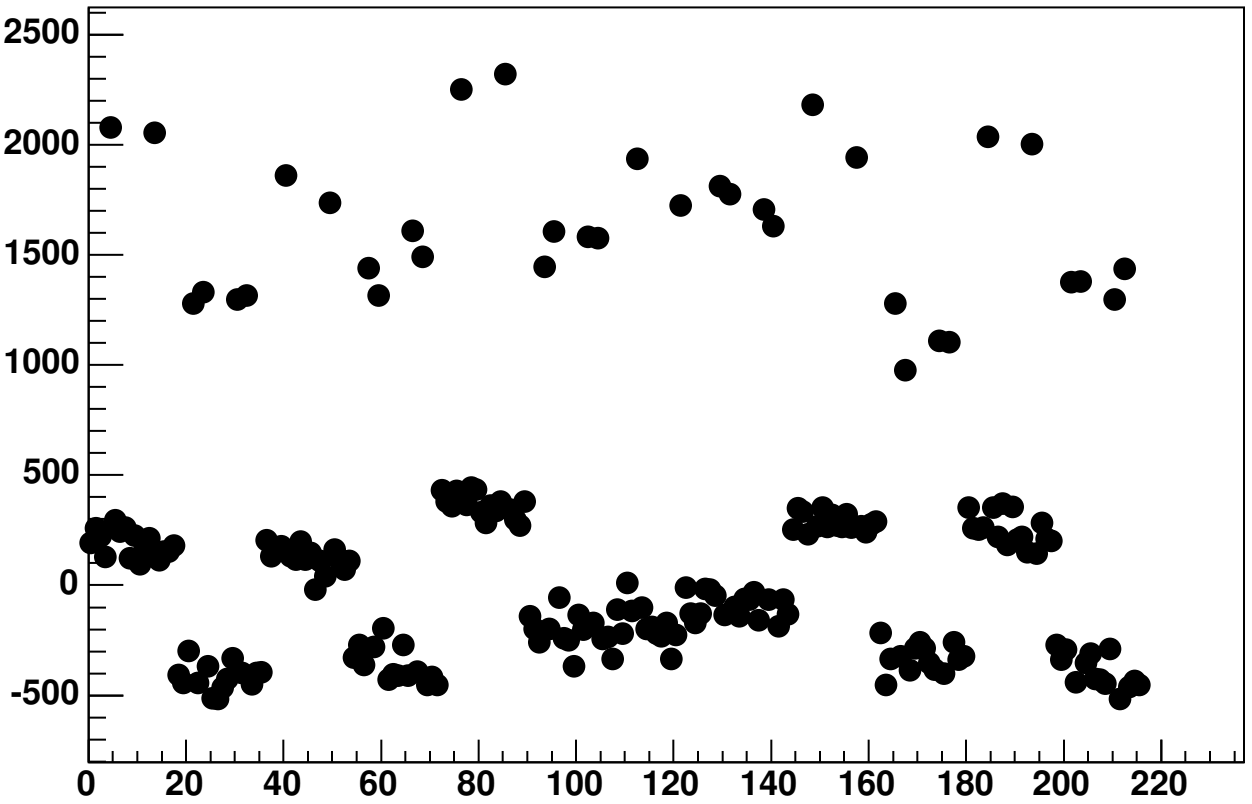
Enable 4, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



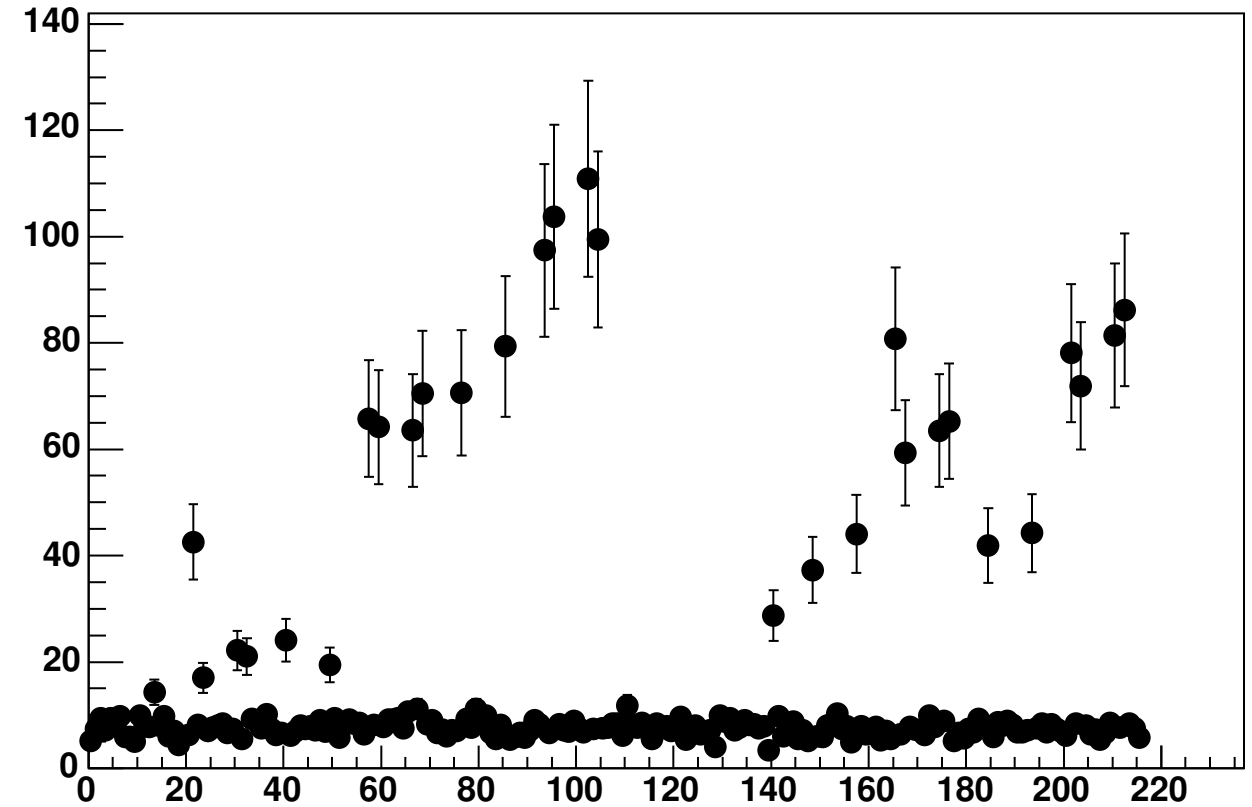
Enable 4, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



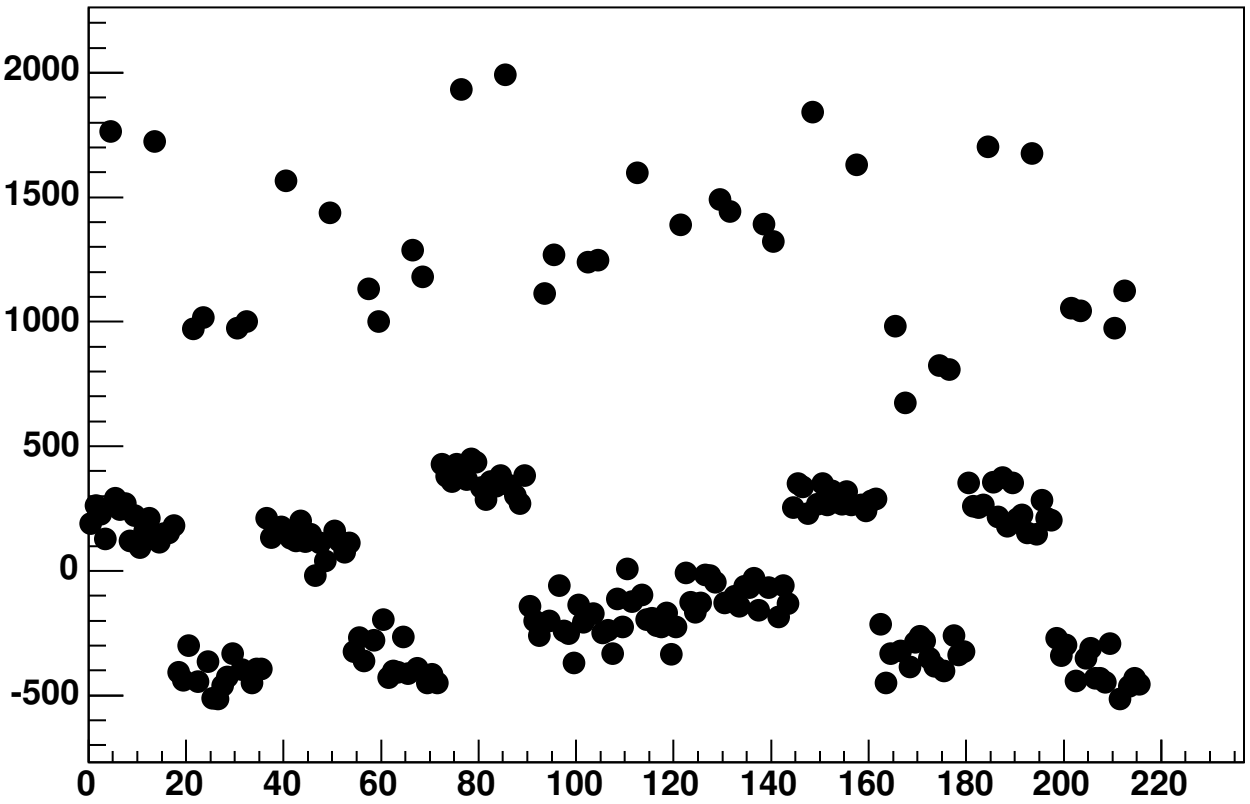
Enable 4, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



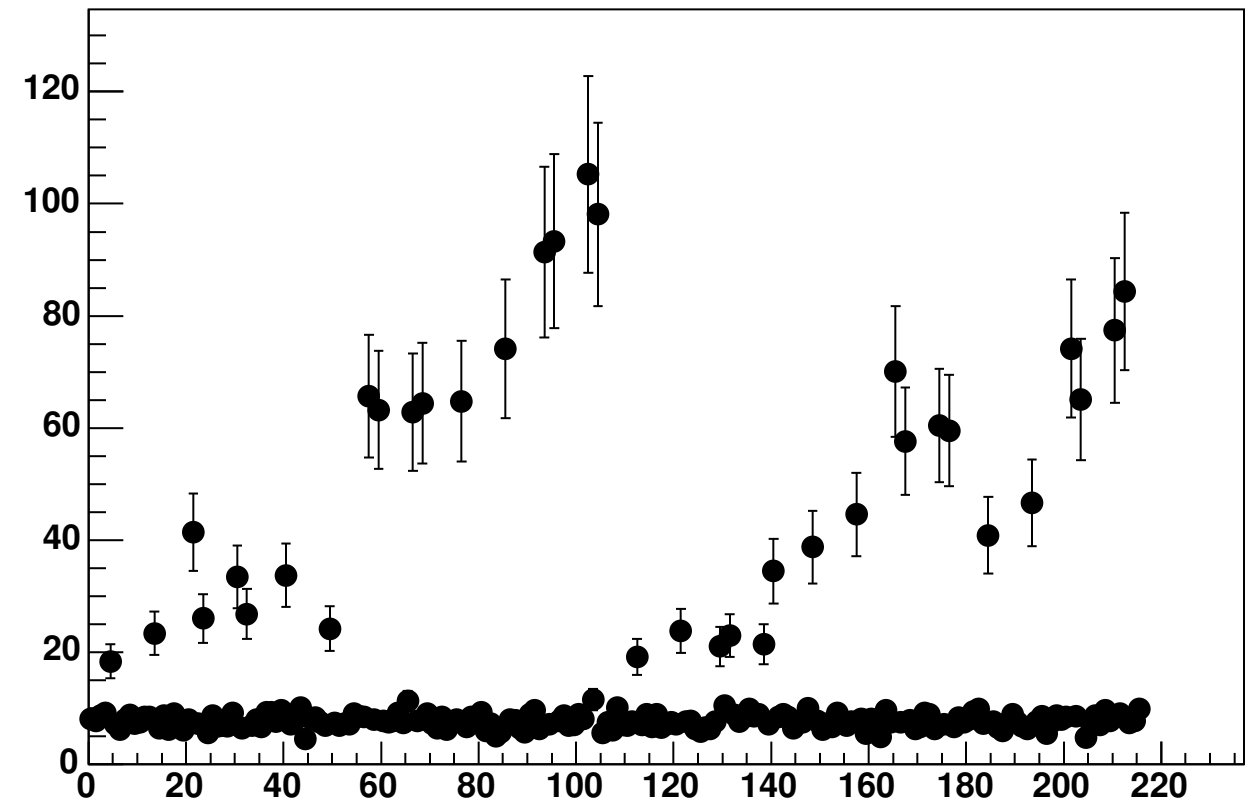
Enable 4, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



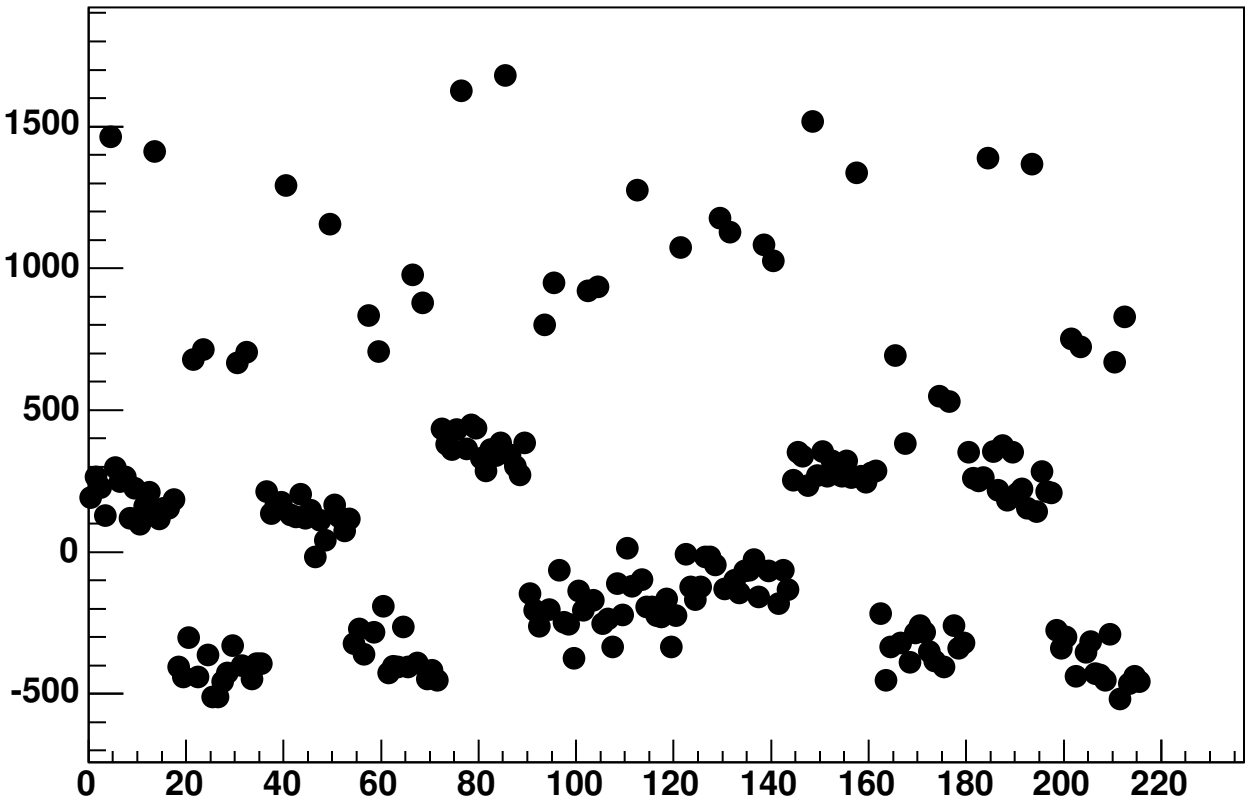
Enable 4, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



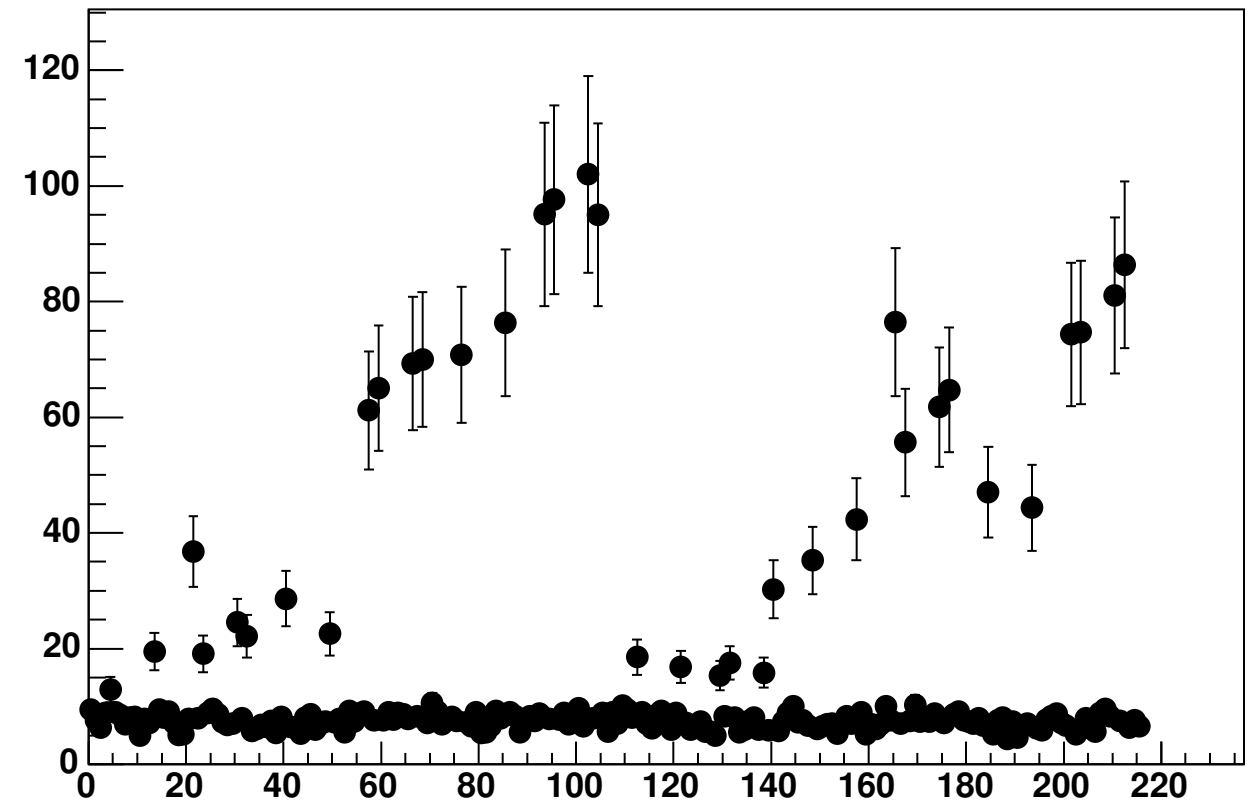
Enable 4, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



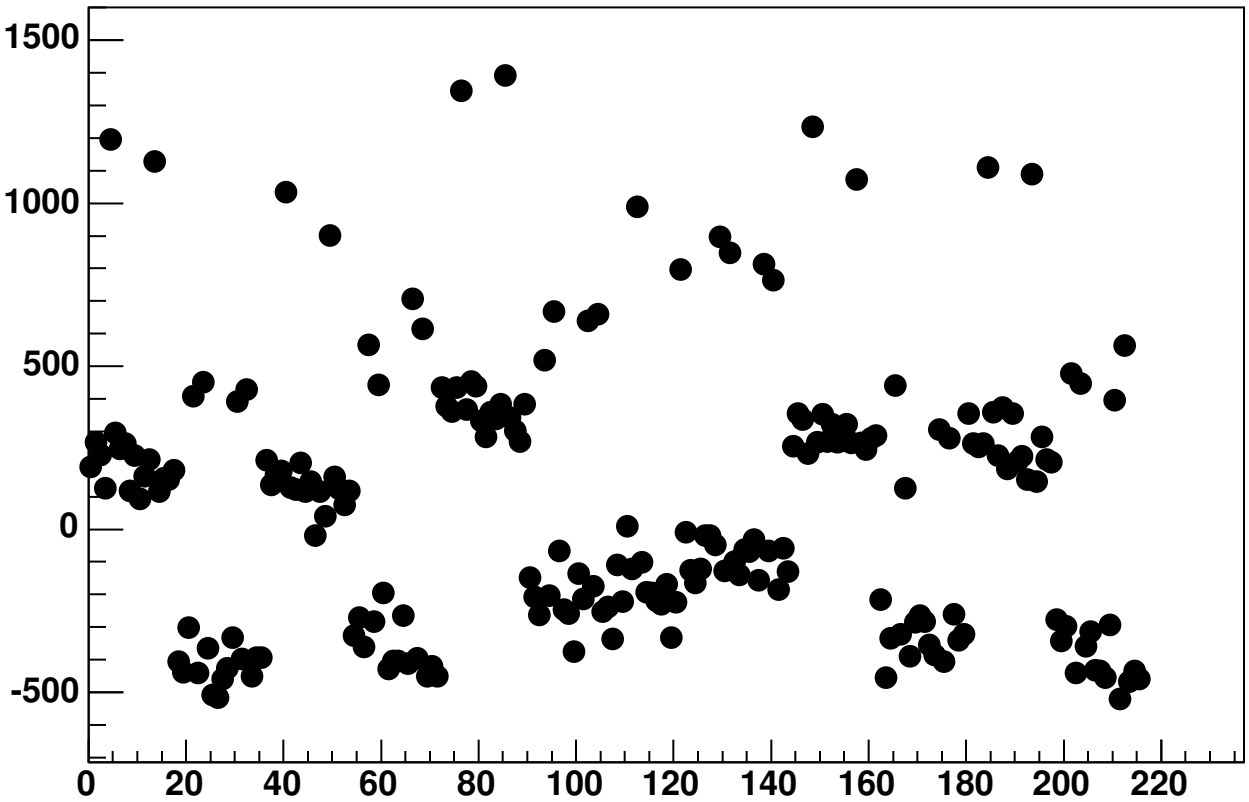
Enable 4, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



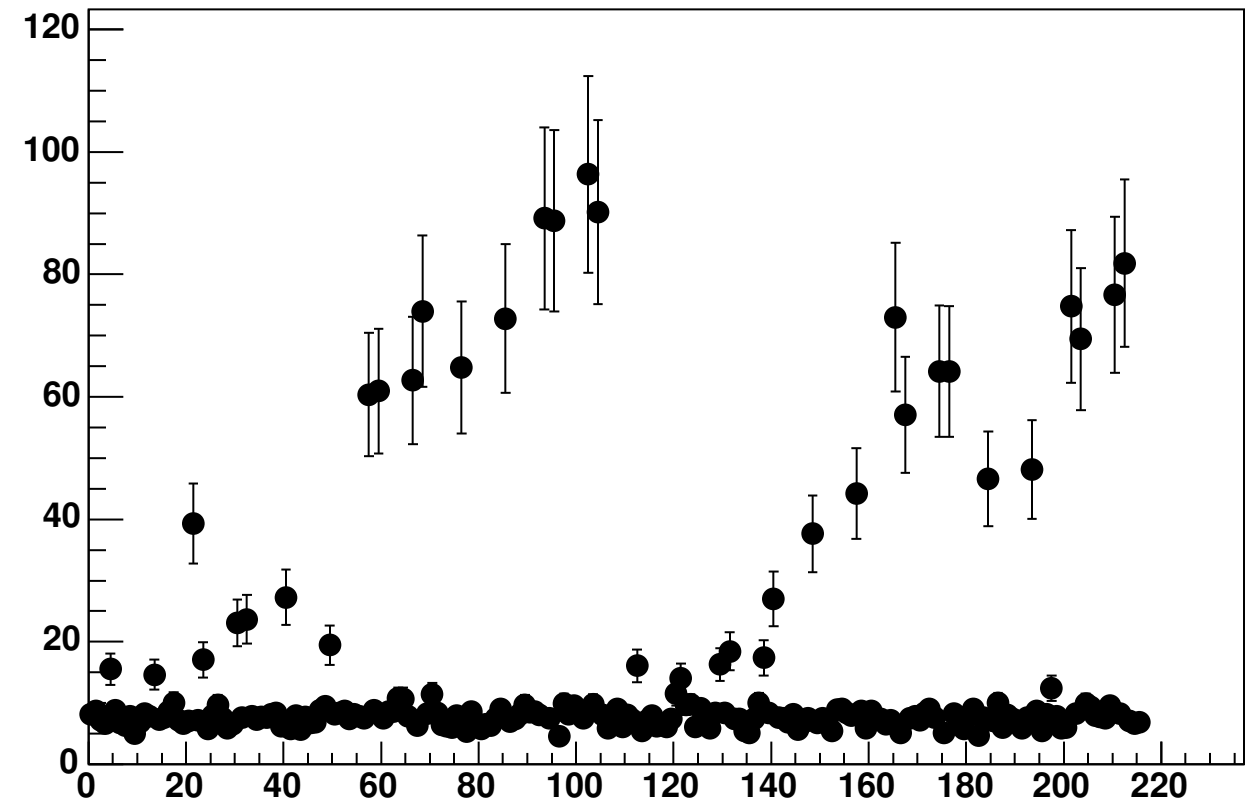
Enable 4, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



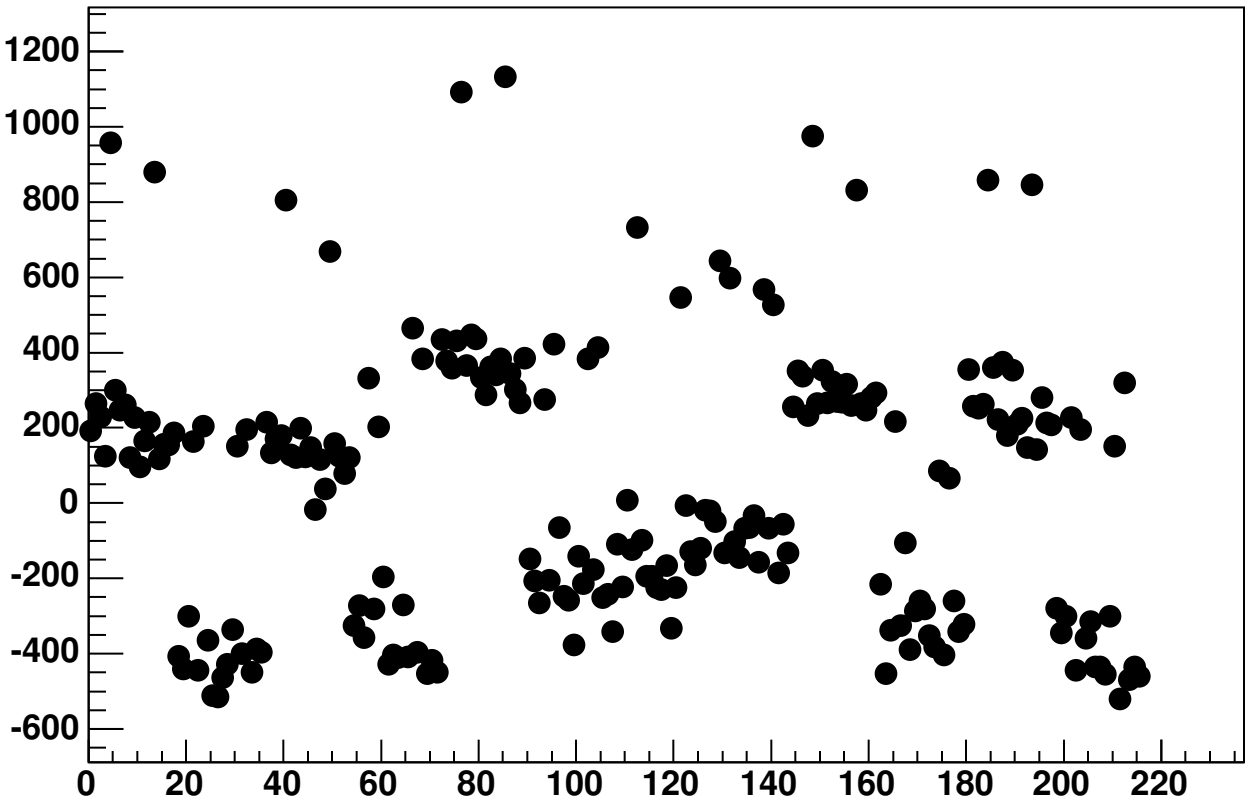
Enable 4, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



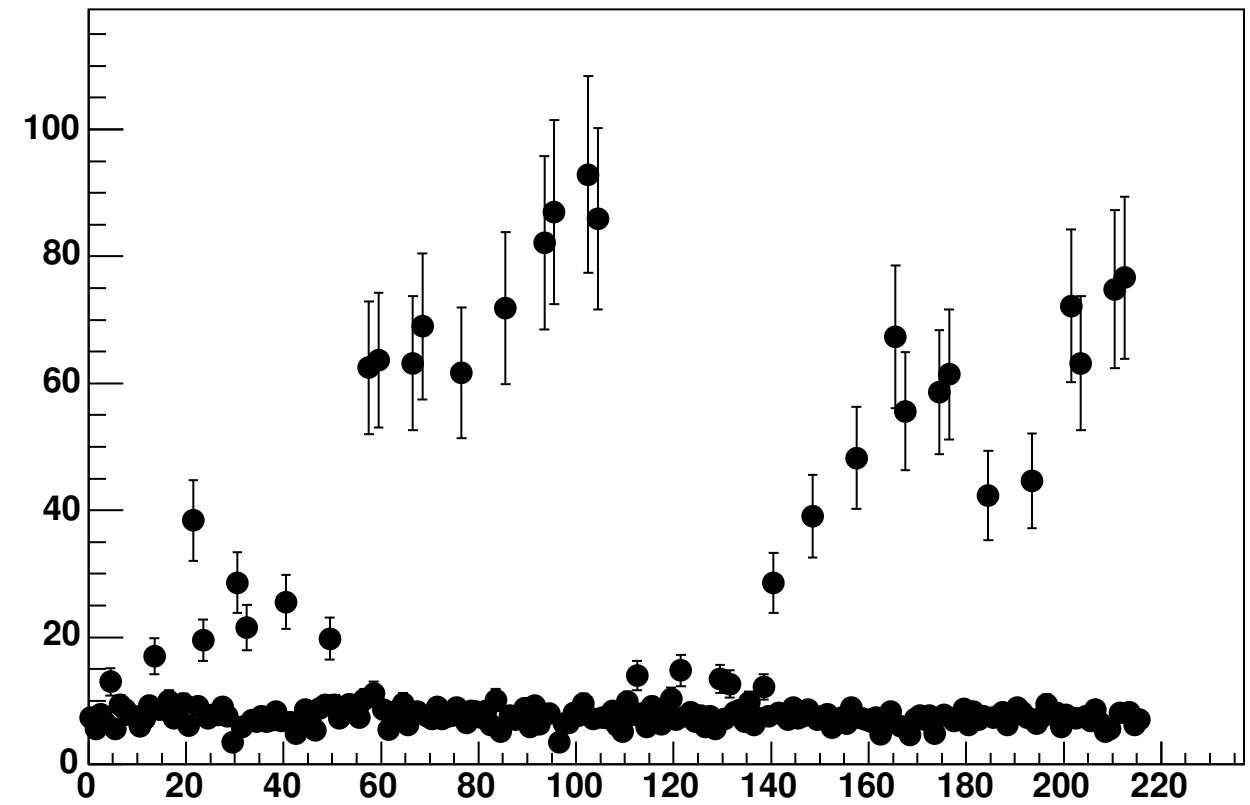
Enable 4, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



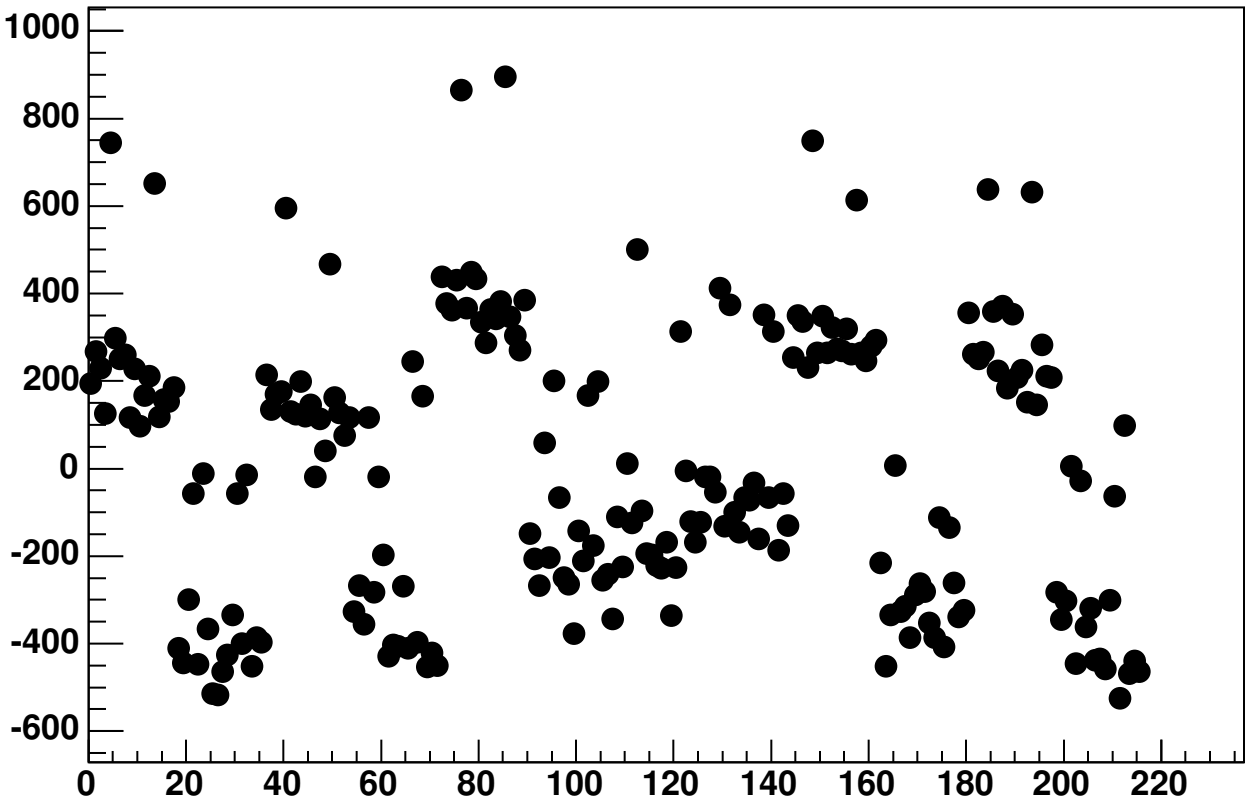
Enable 4, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



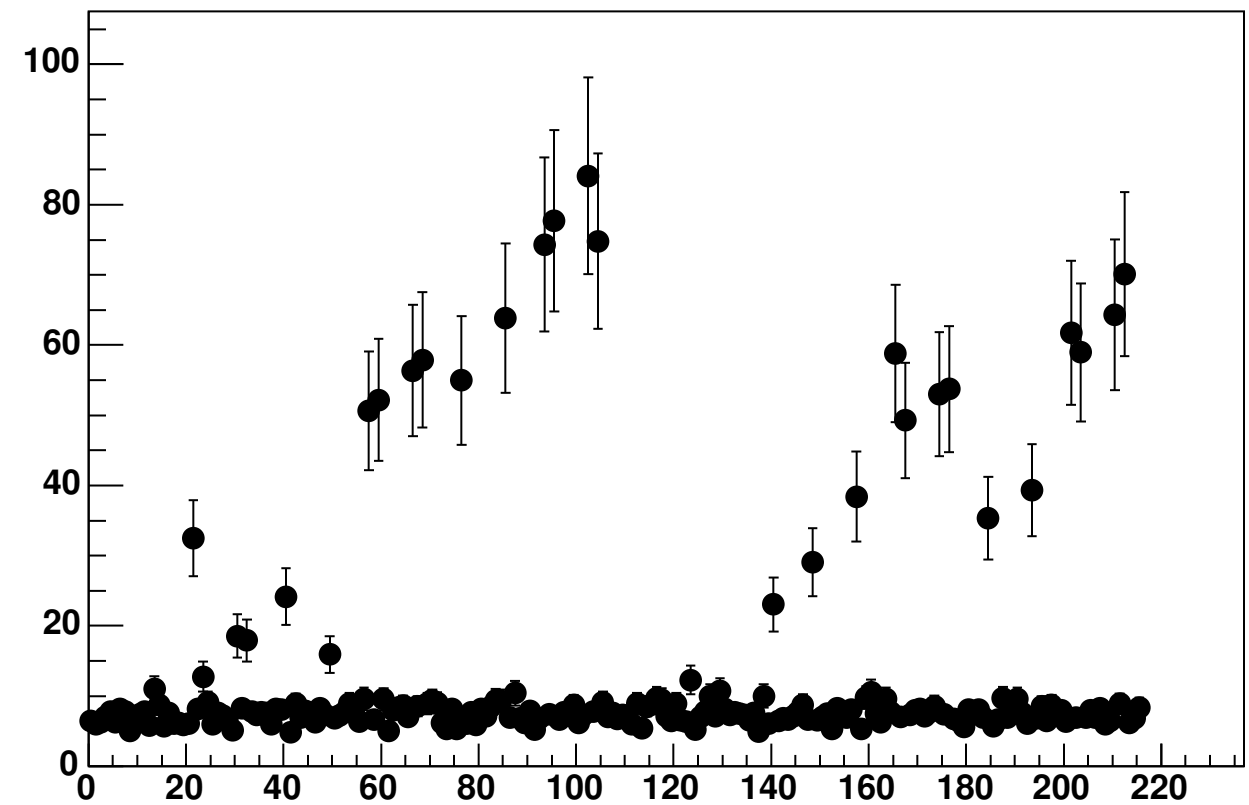
Enable 4, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



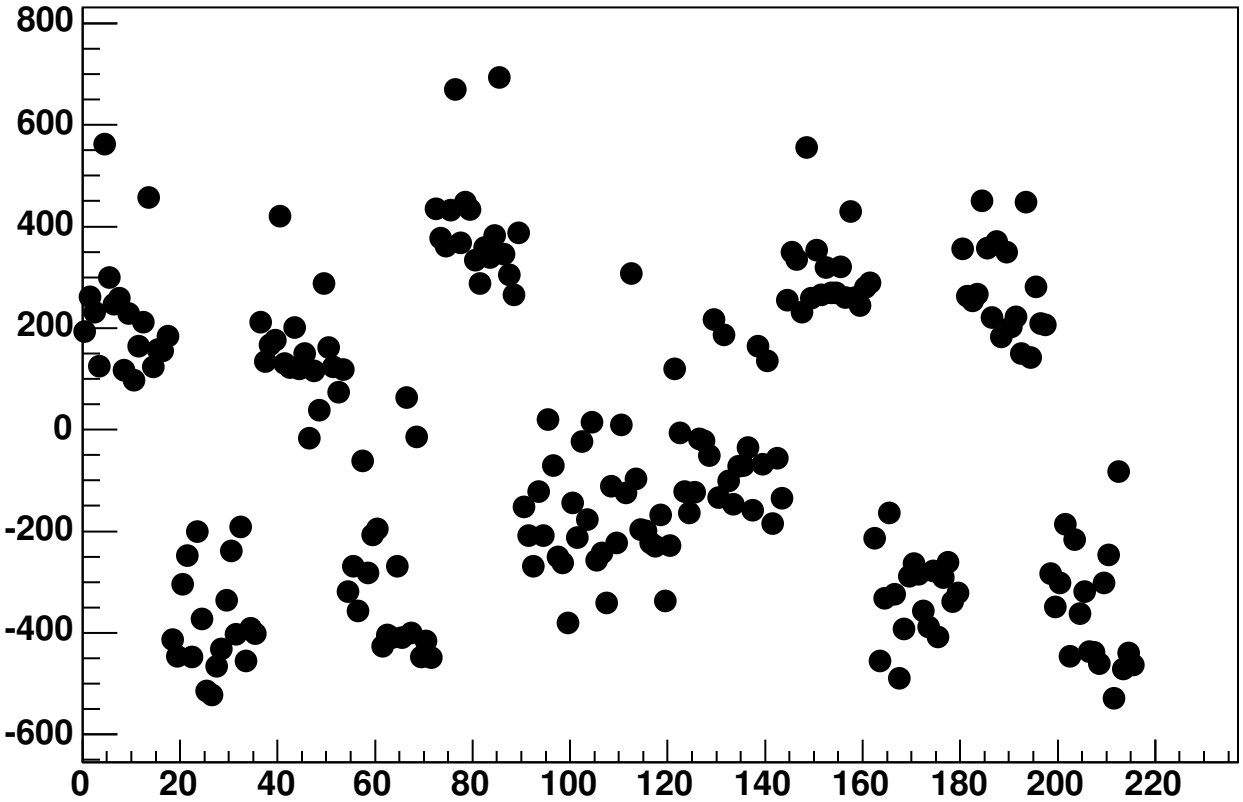
Enable 4, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



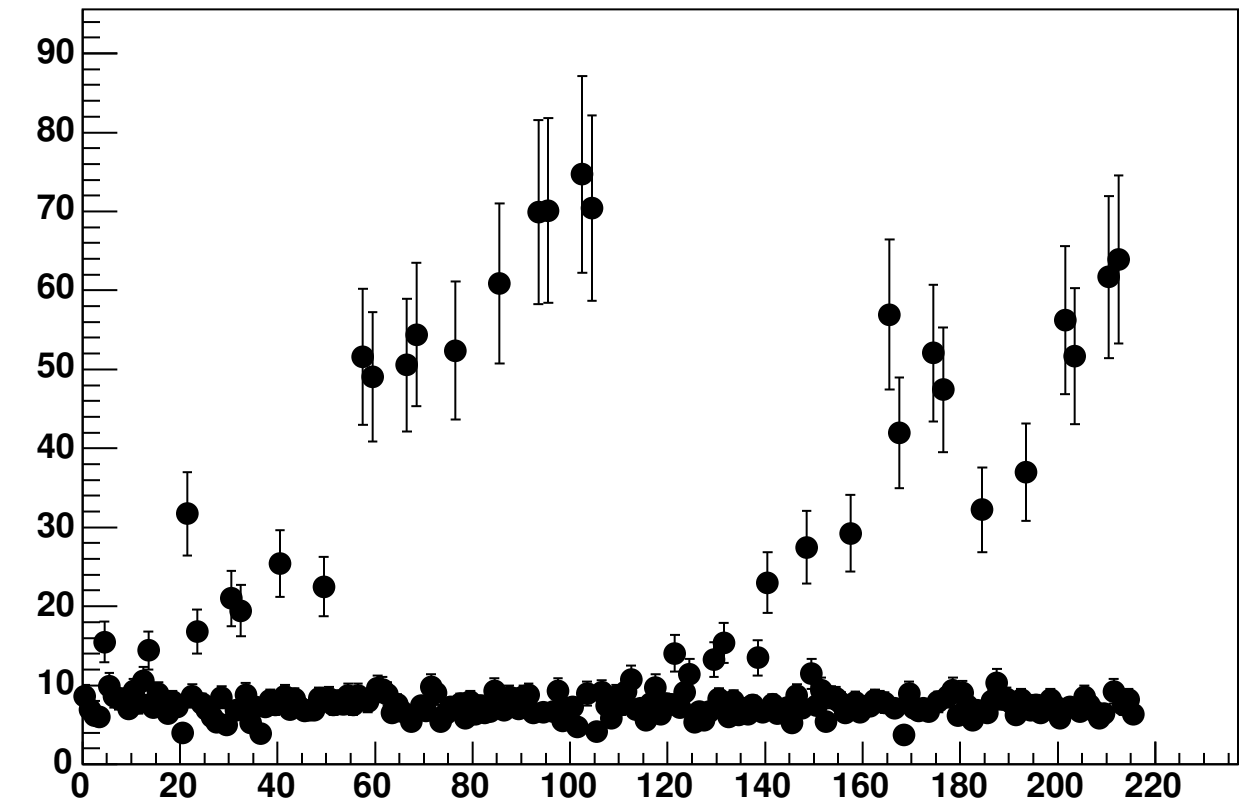
Enable 4, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



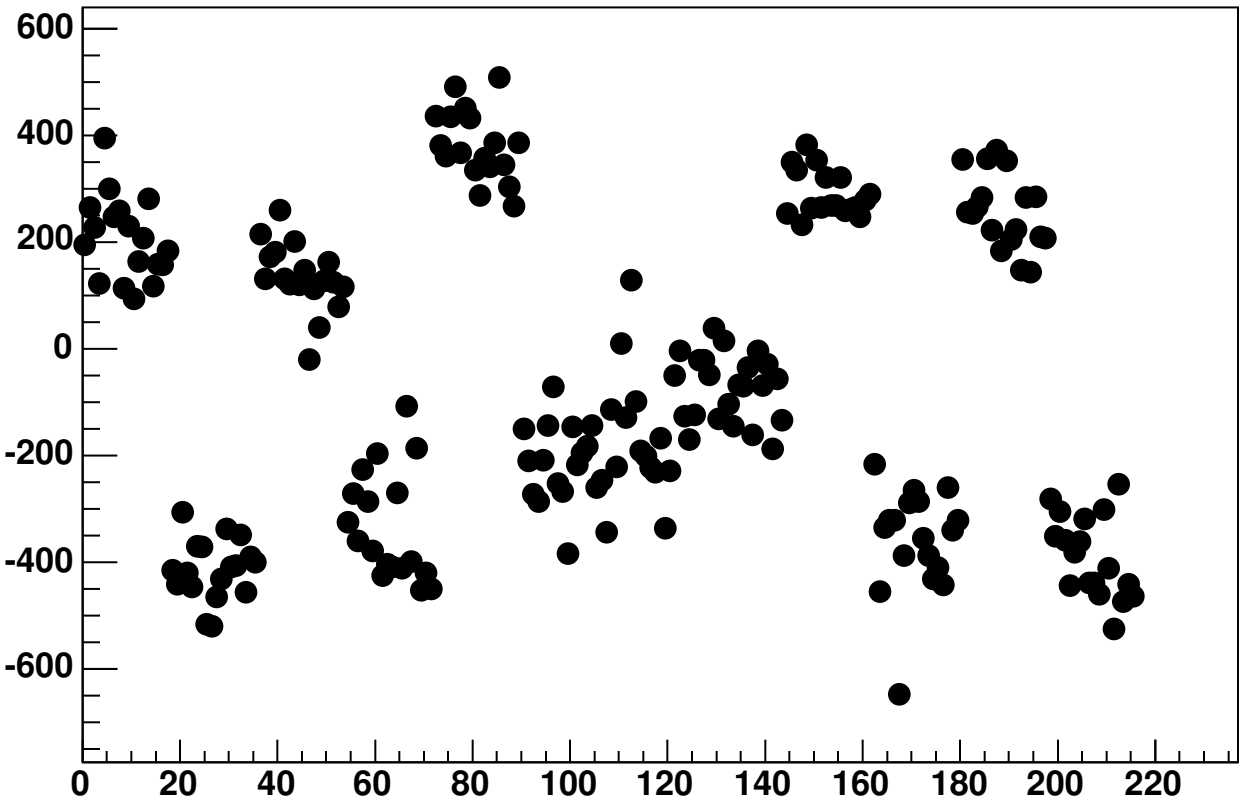
Enable 4, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



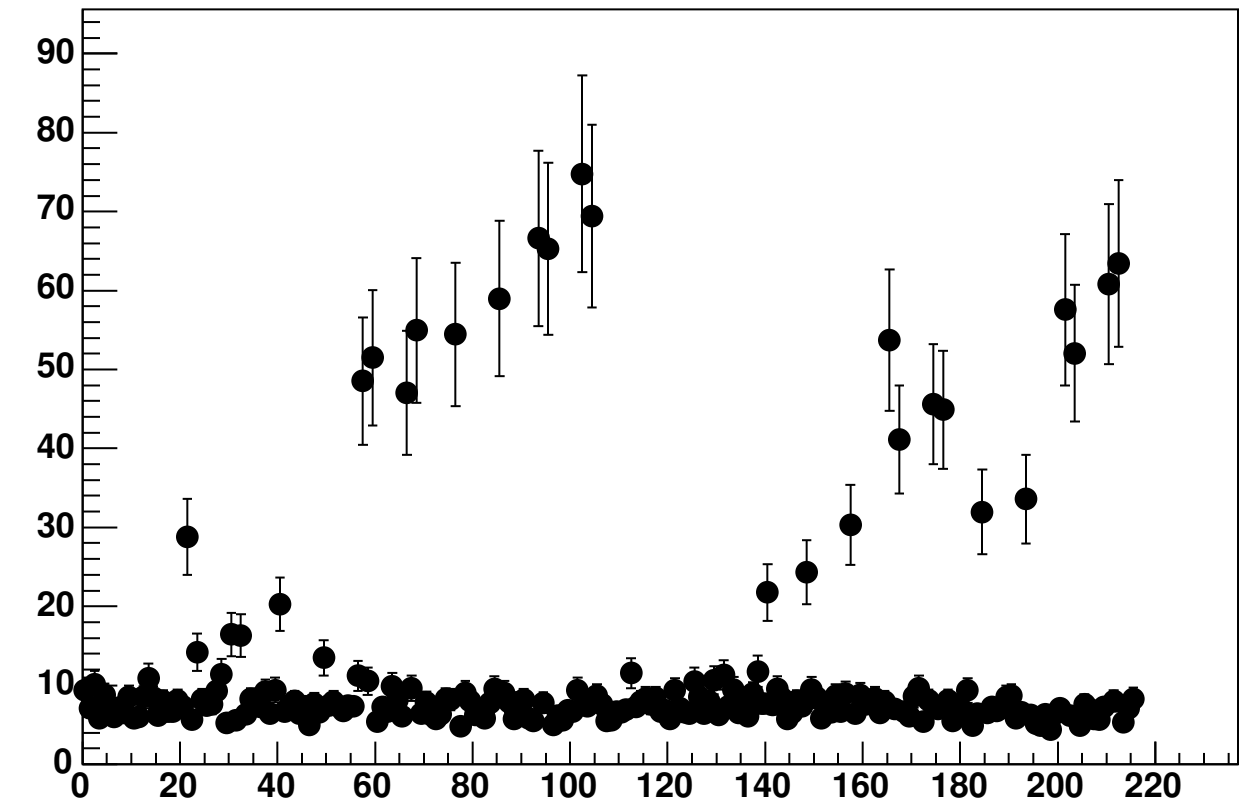
Enable 4, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



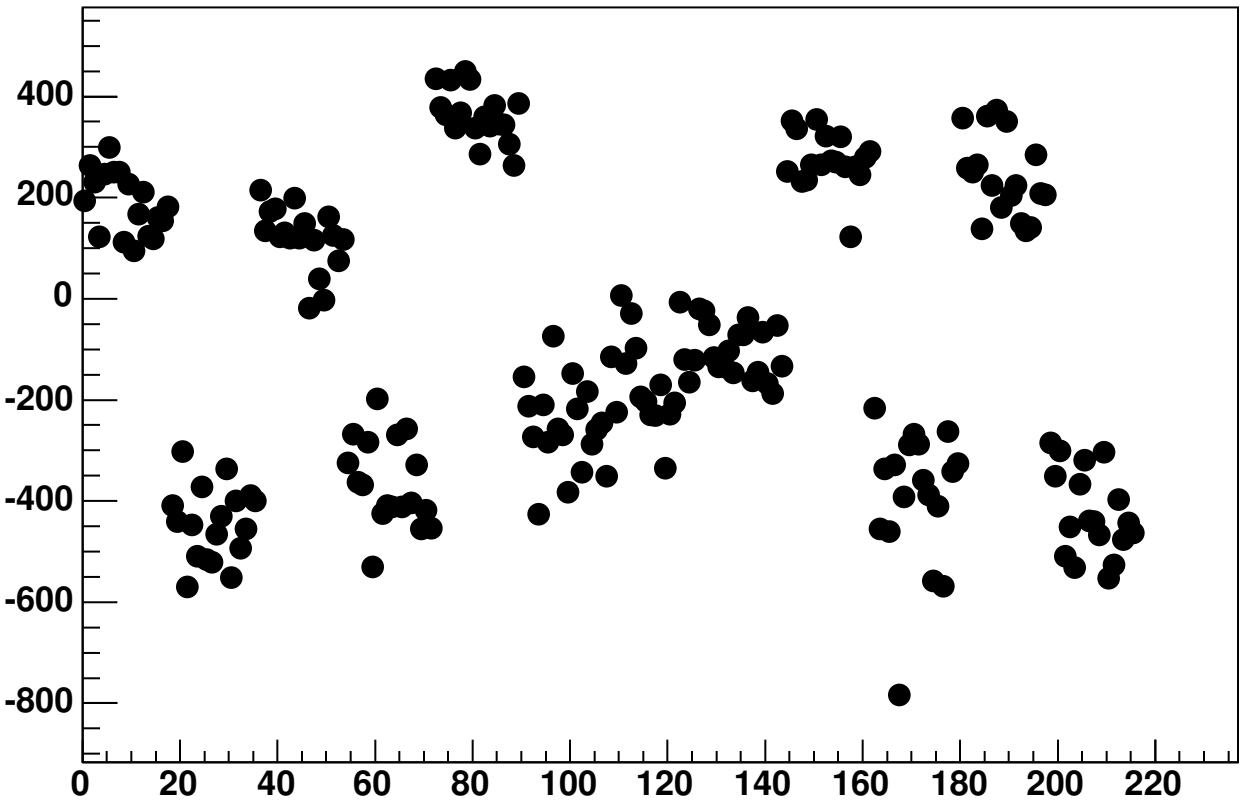
Enable 4, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



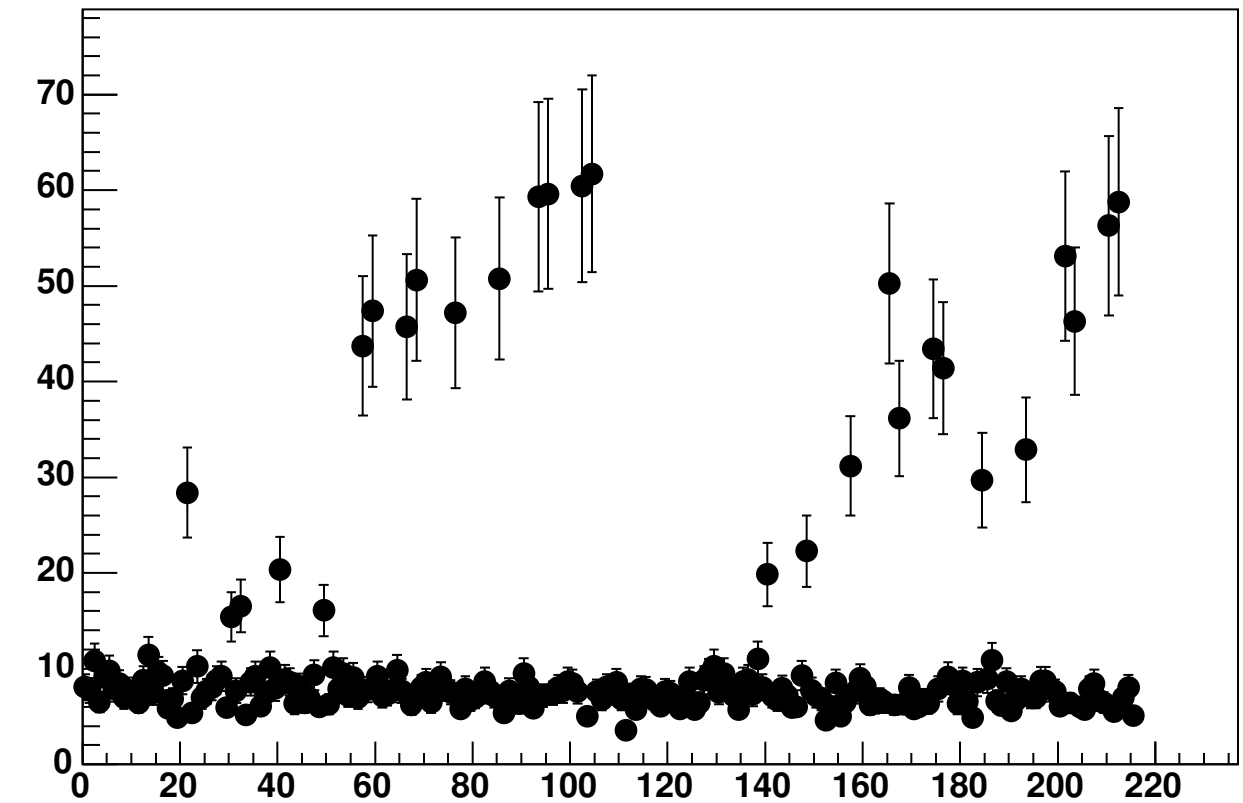
Enable 4, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



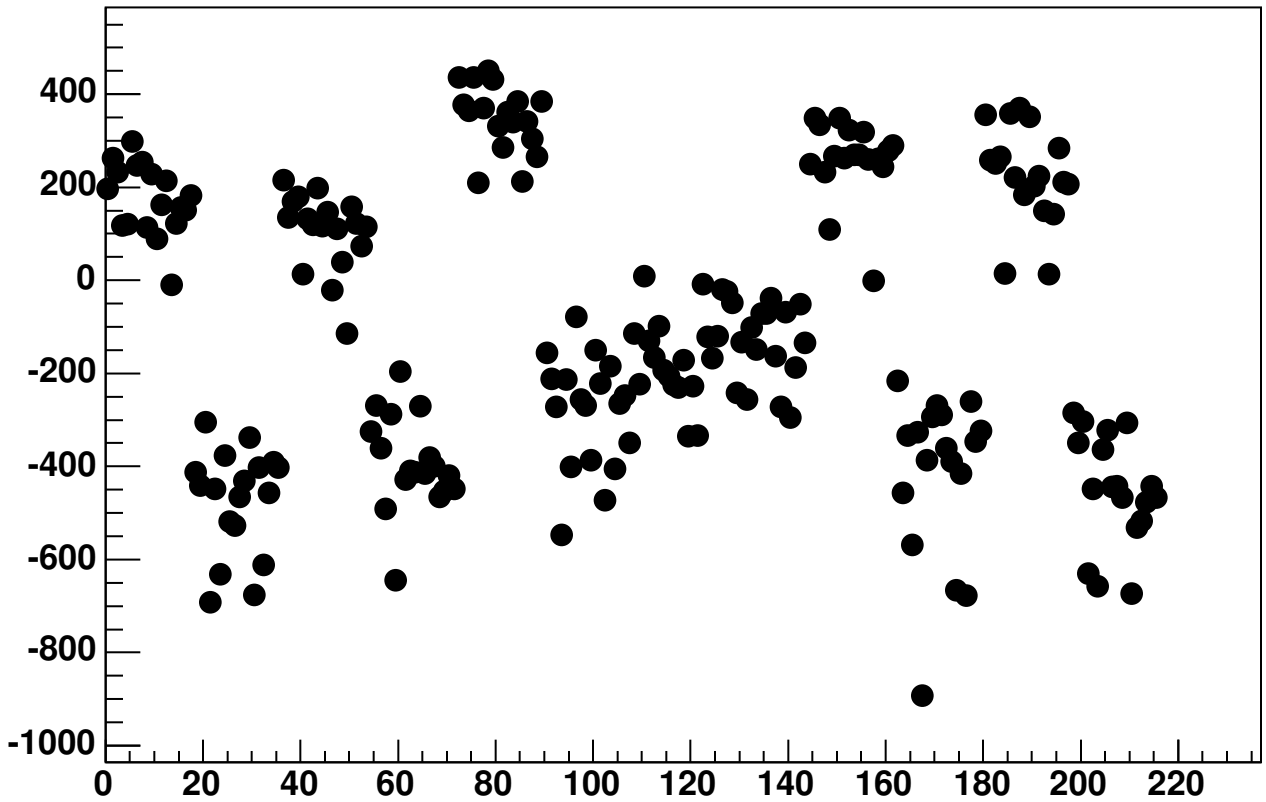
Enable 4, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



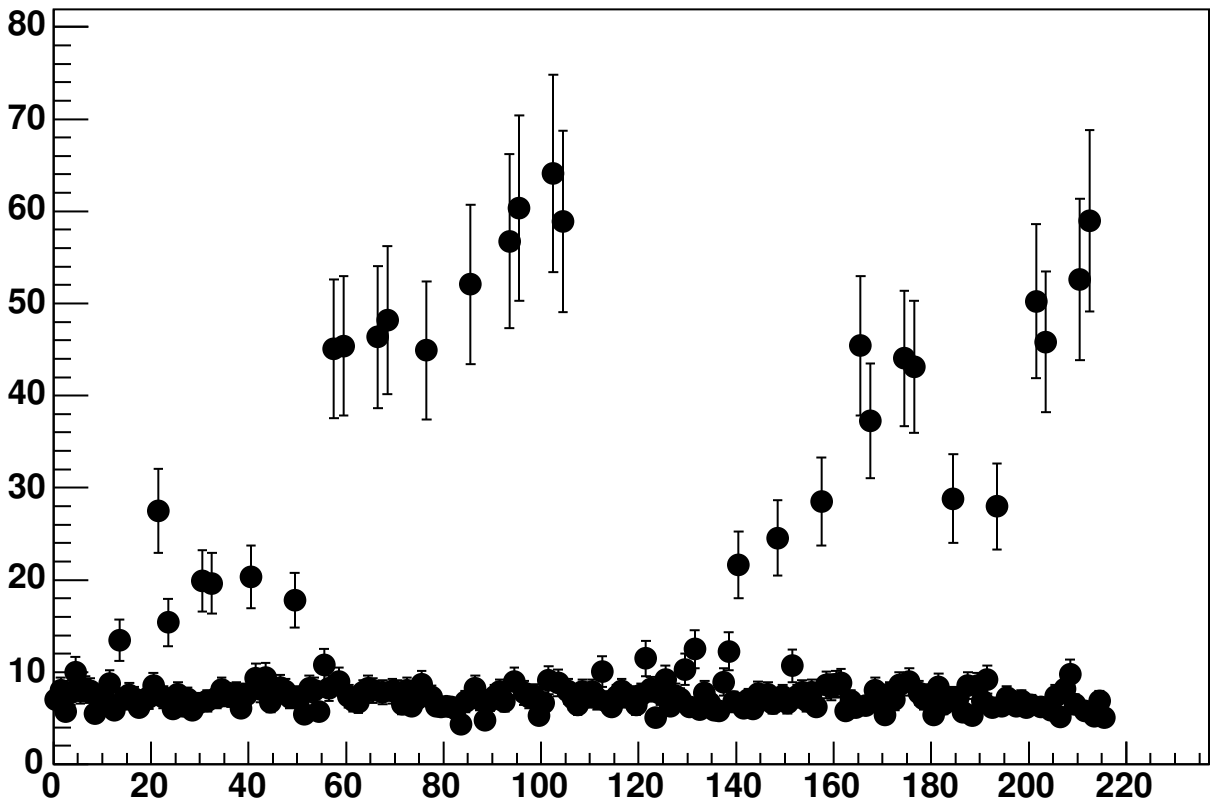
Enable 4, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



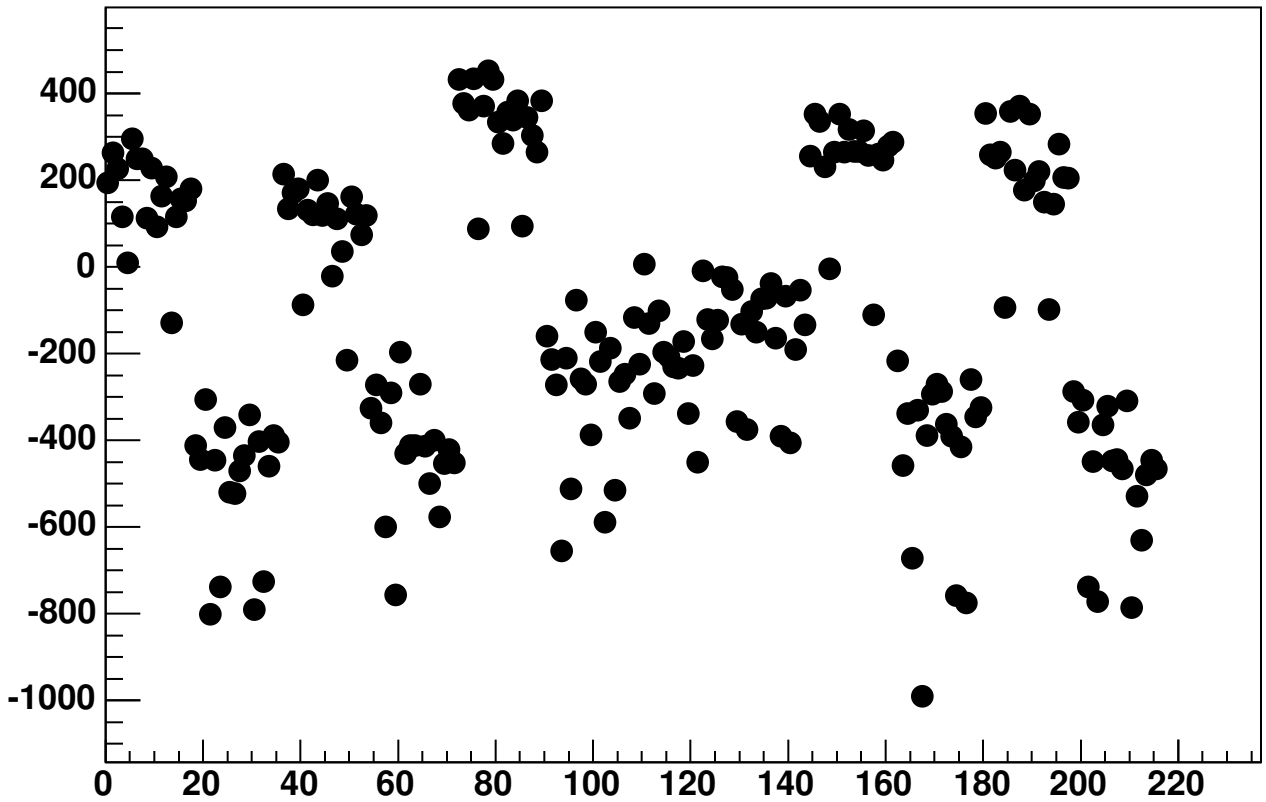
Enable 4, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



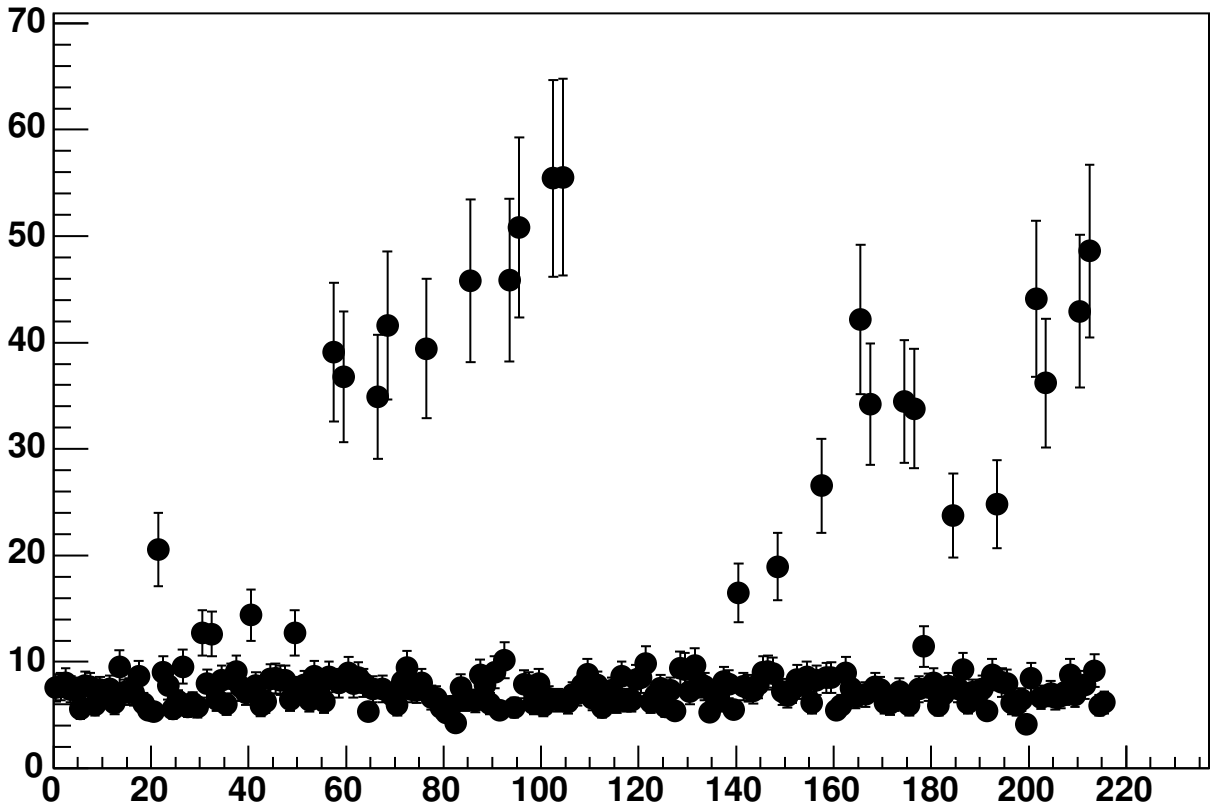
Enable 4, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



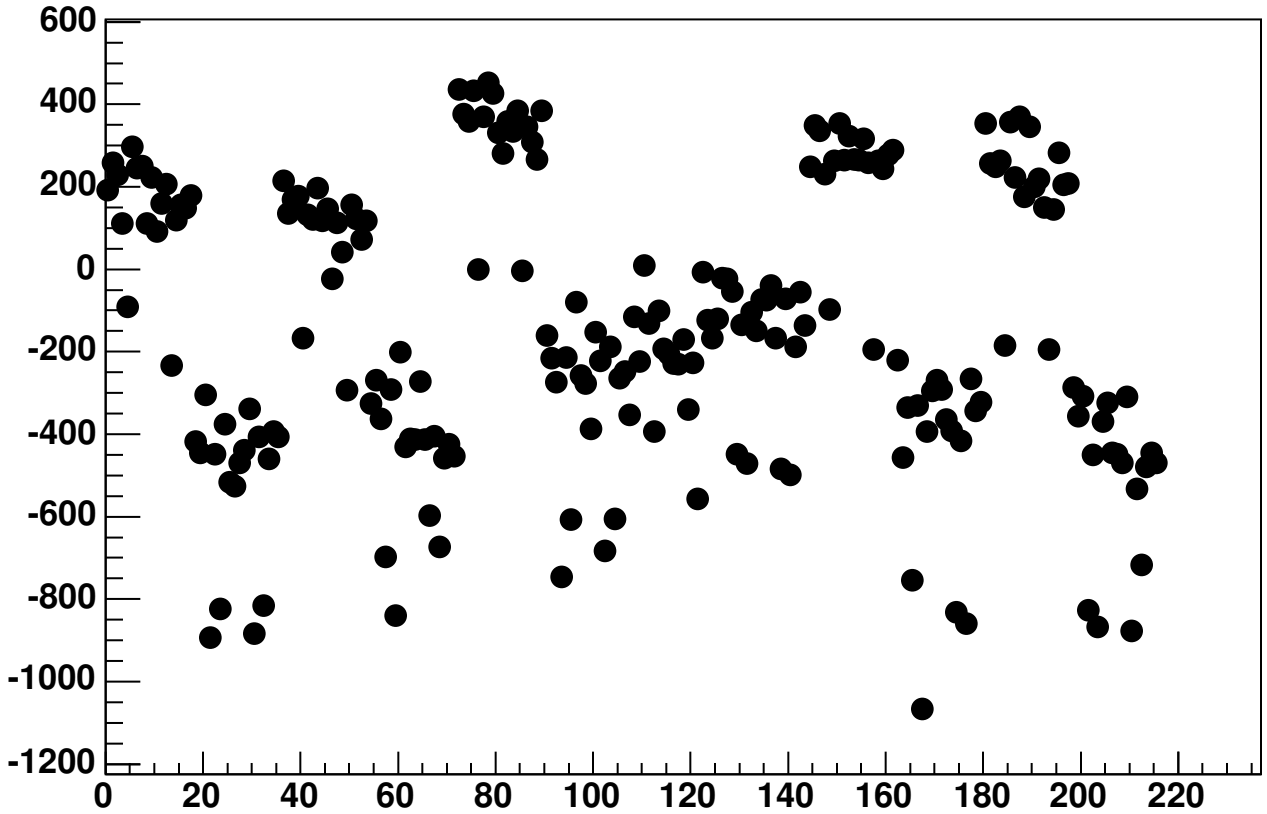
Enable 4, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



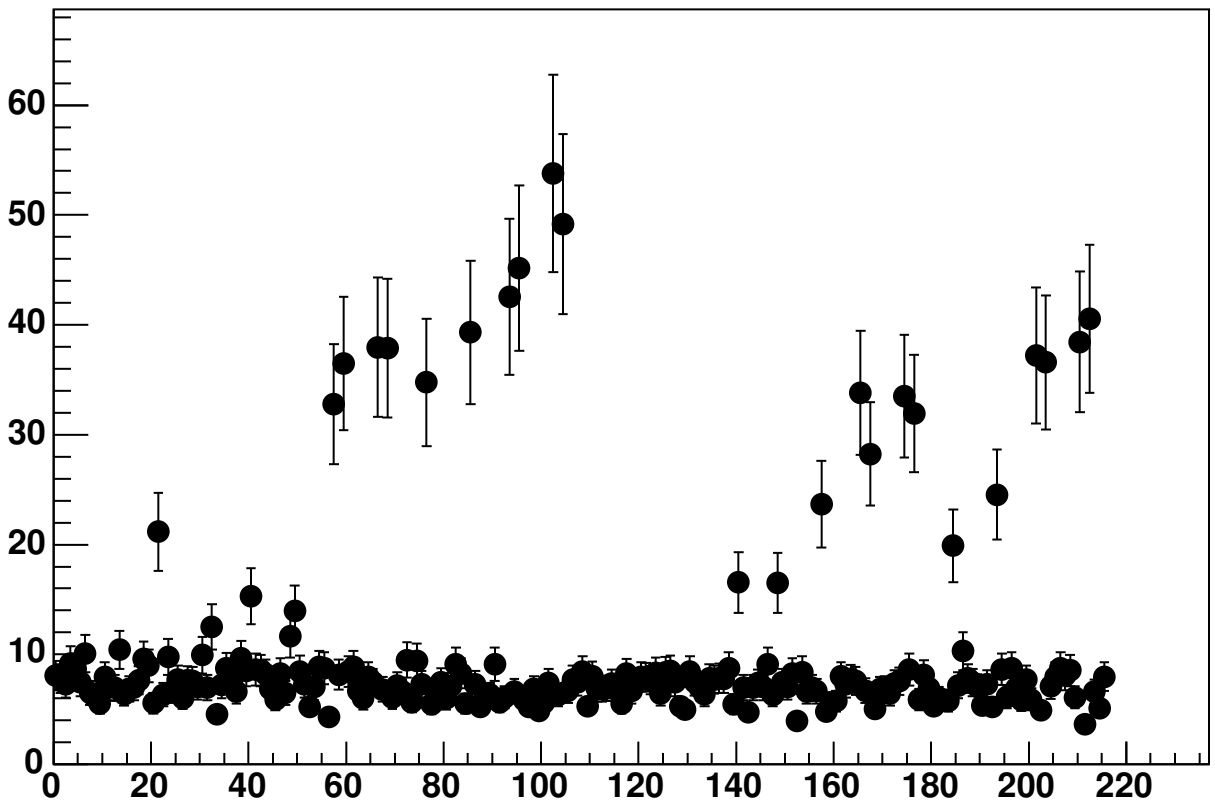
Enable 4, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



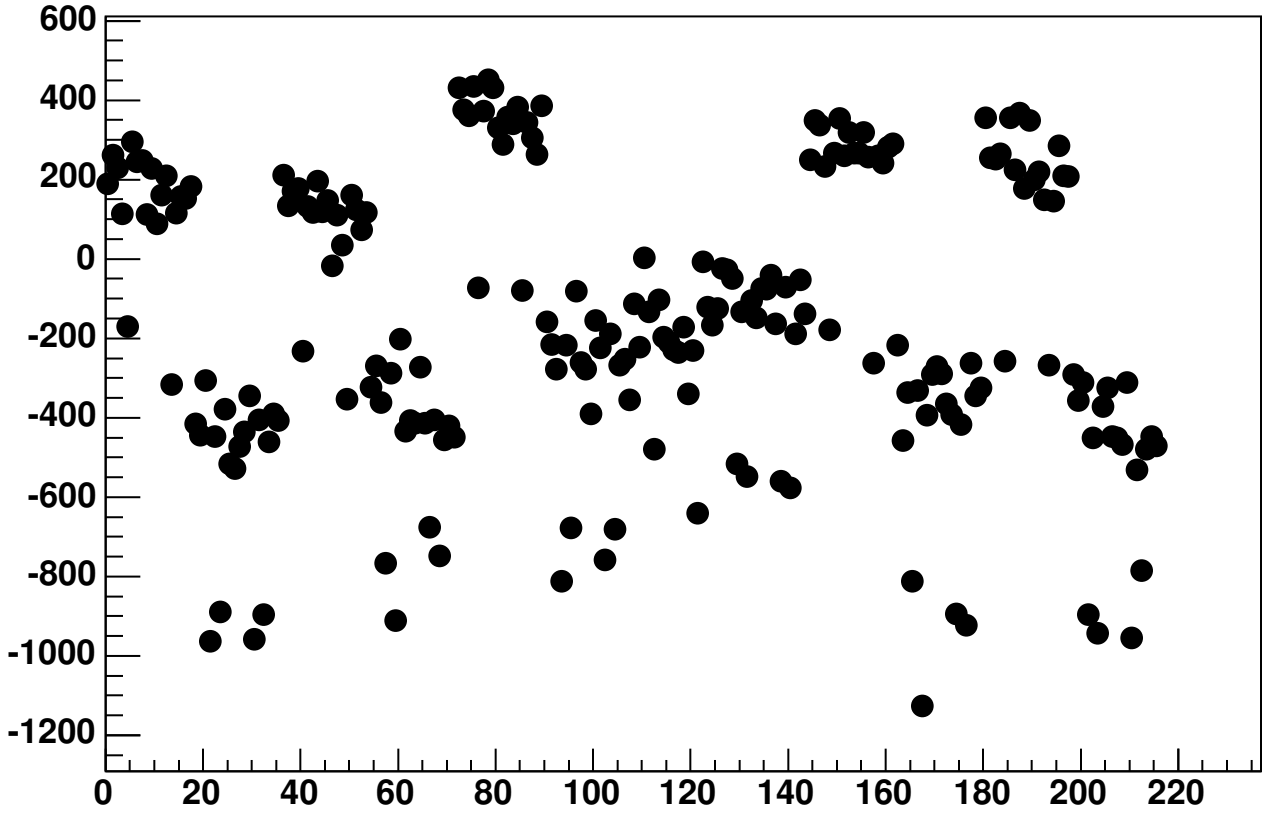
Enable 4, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



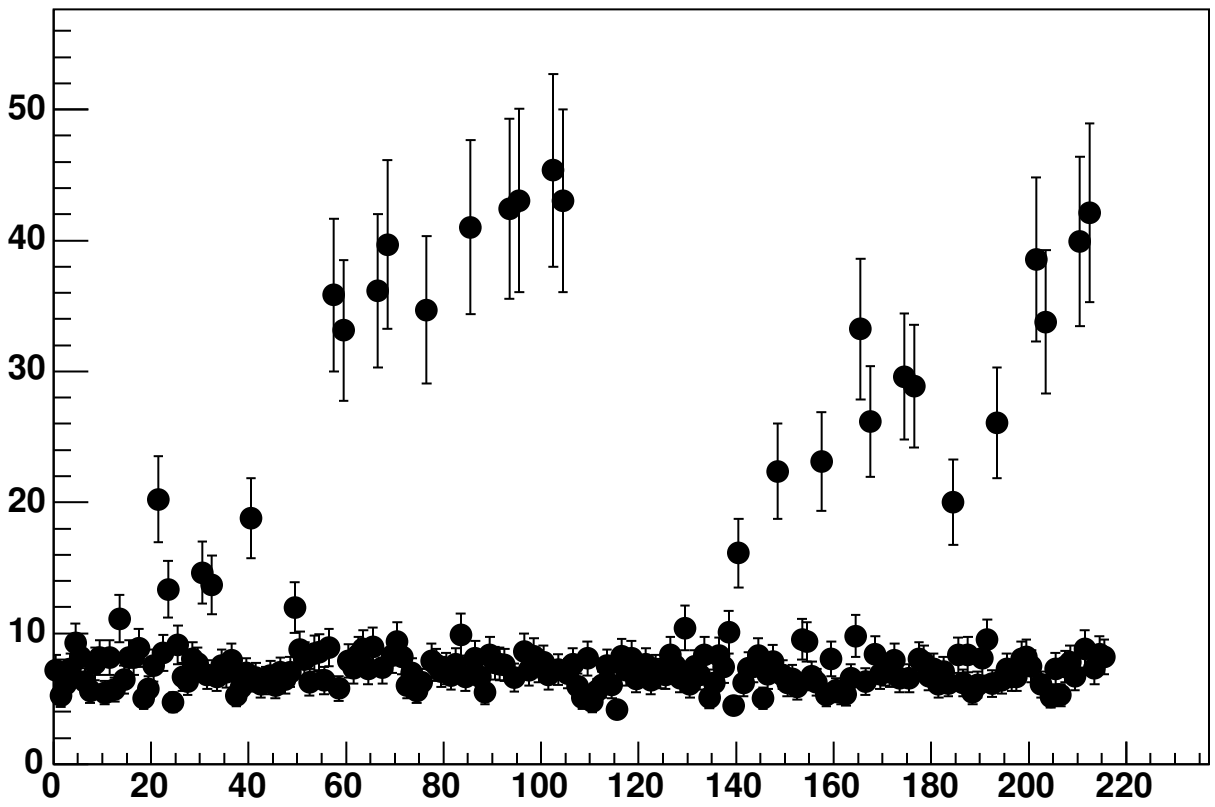
Enable 4, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



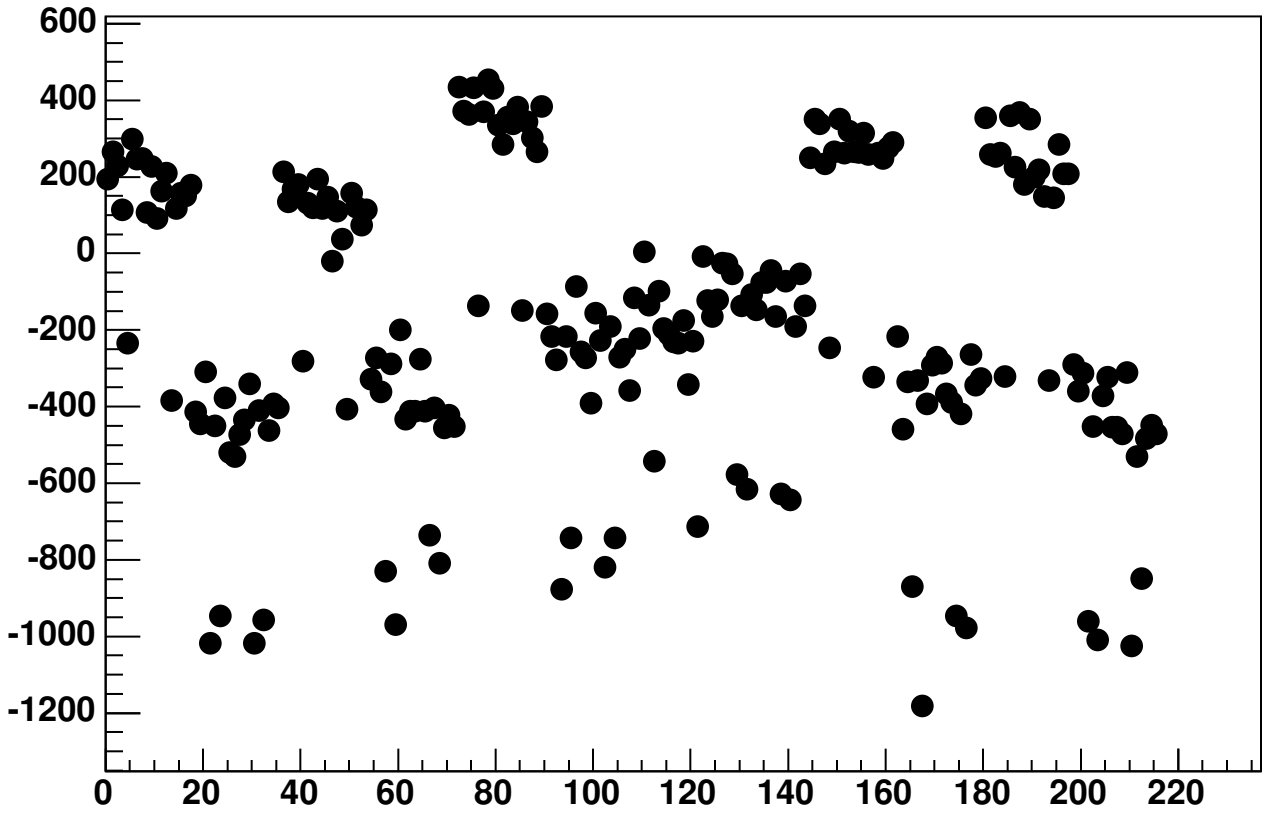
Enable 4, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



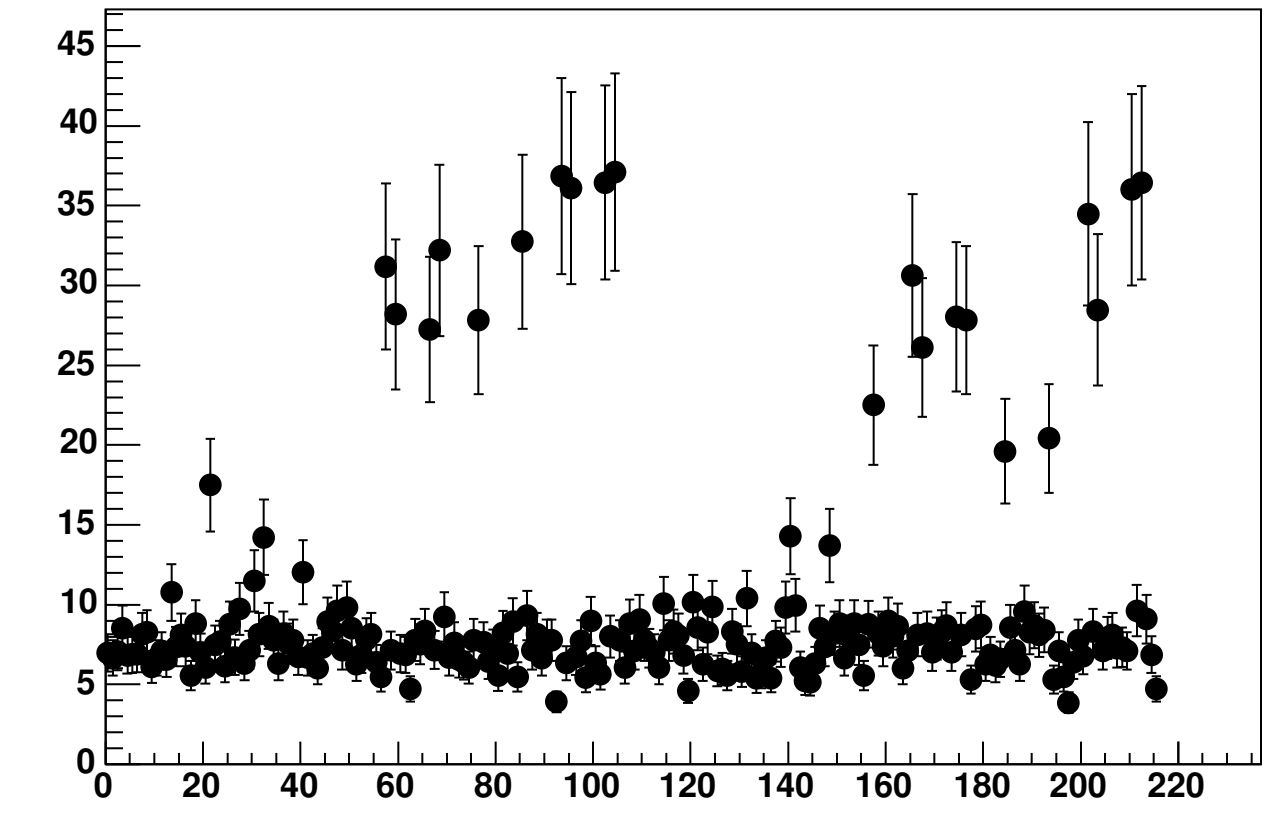
Enable 4, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



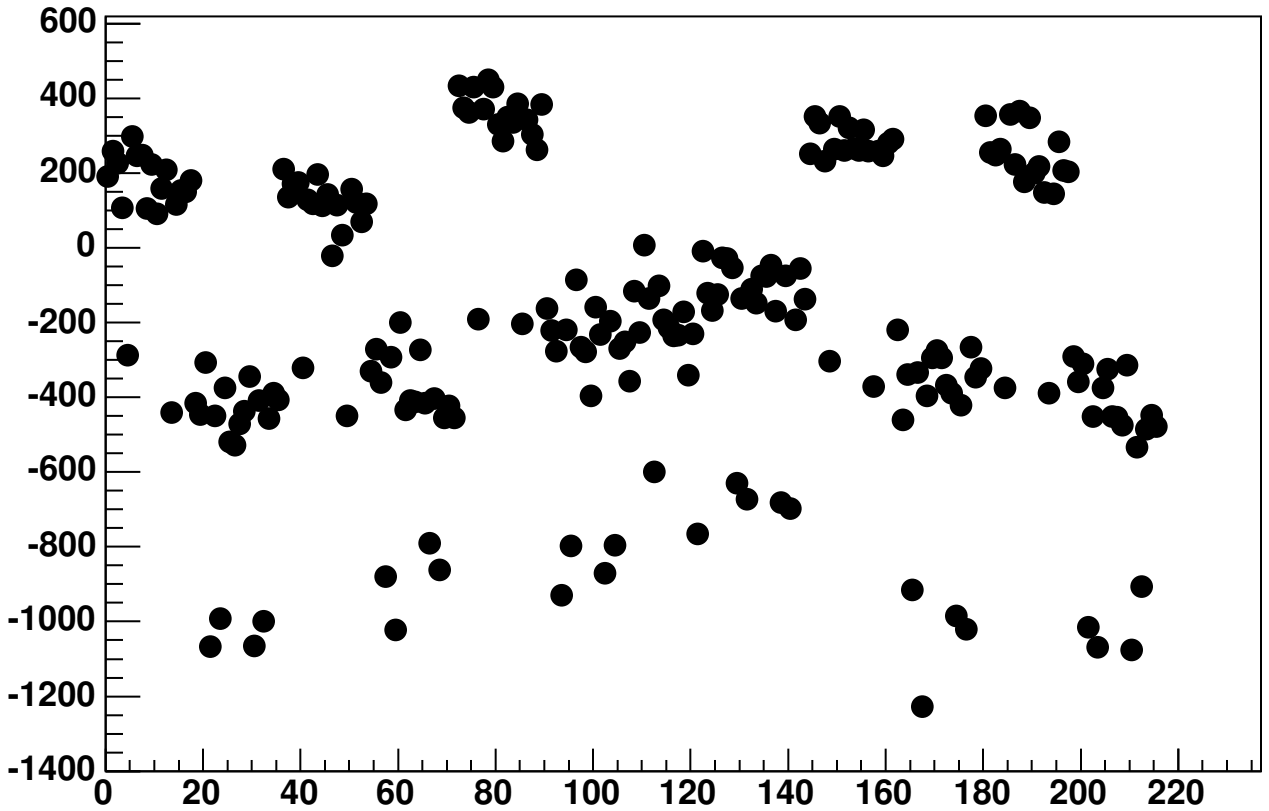
Enable 4, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



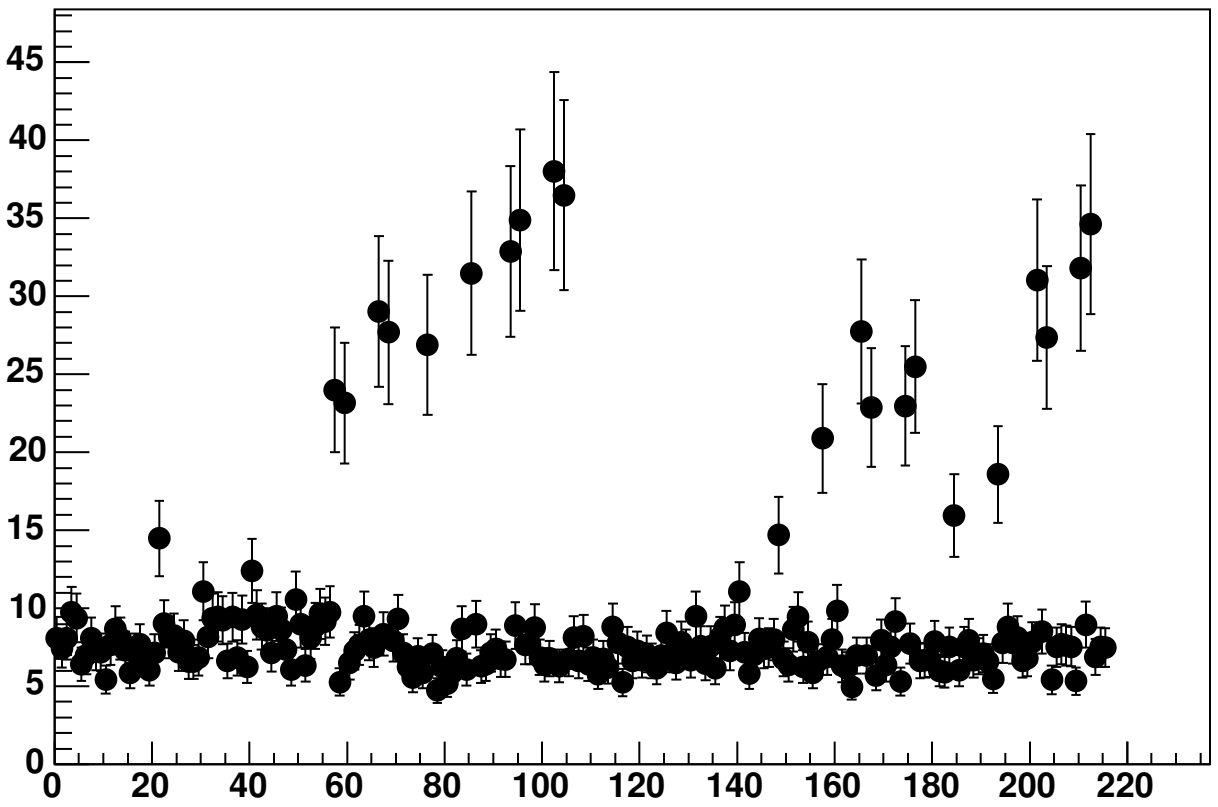
Enable 4, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



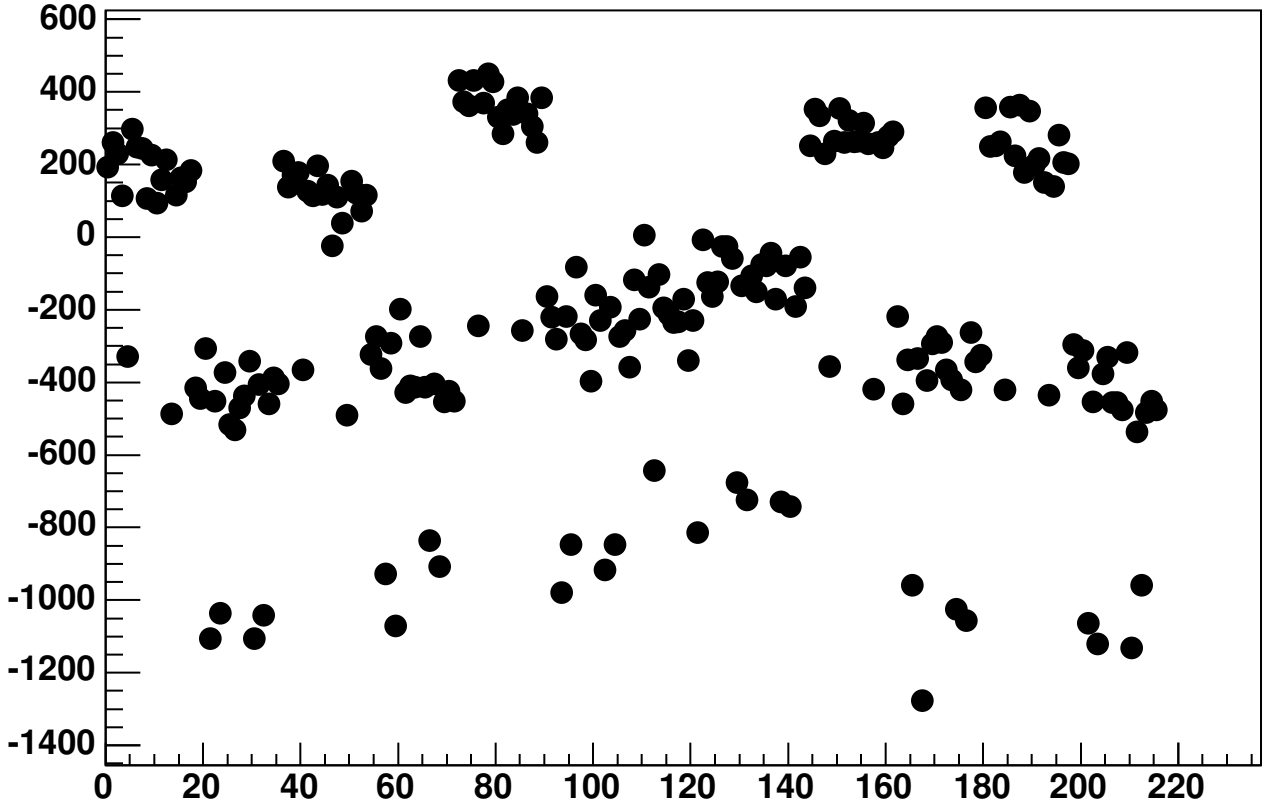
Enable 4, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



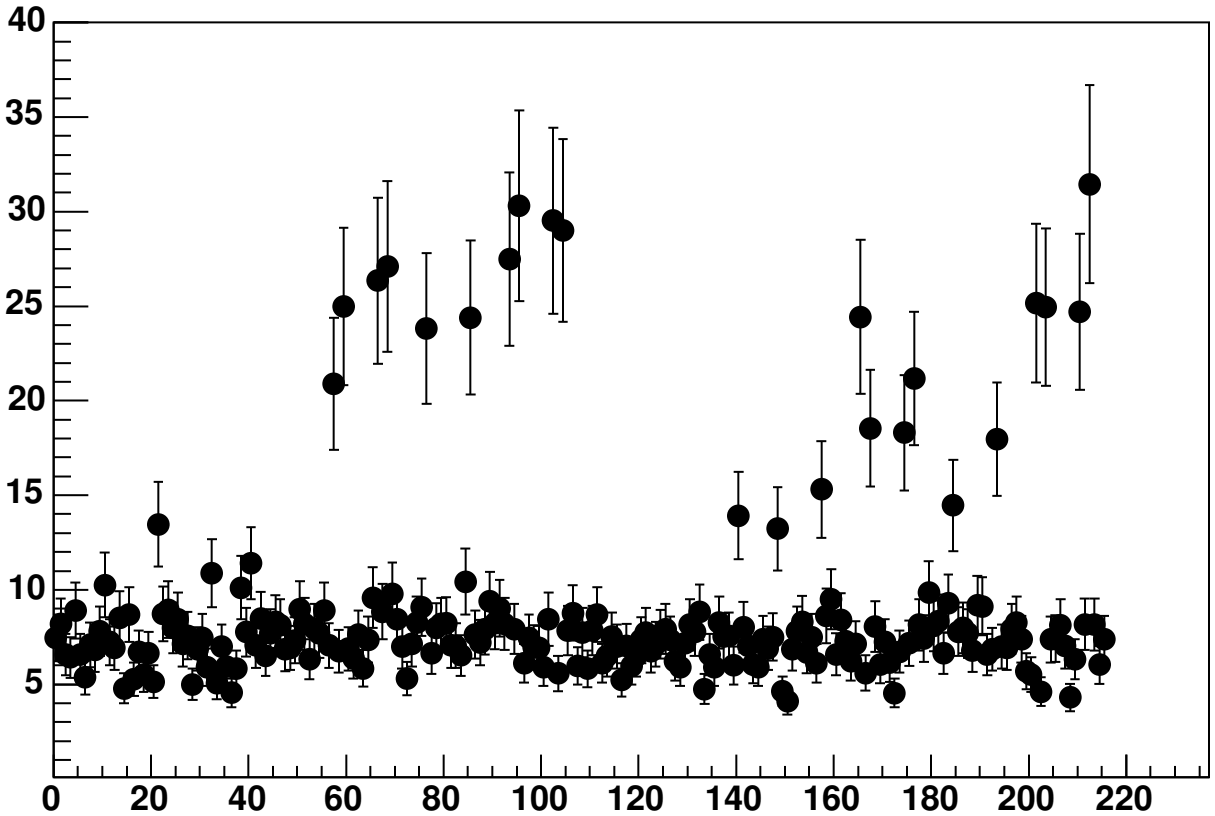
Enable 4, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



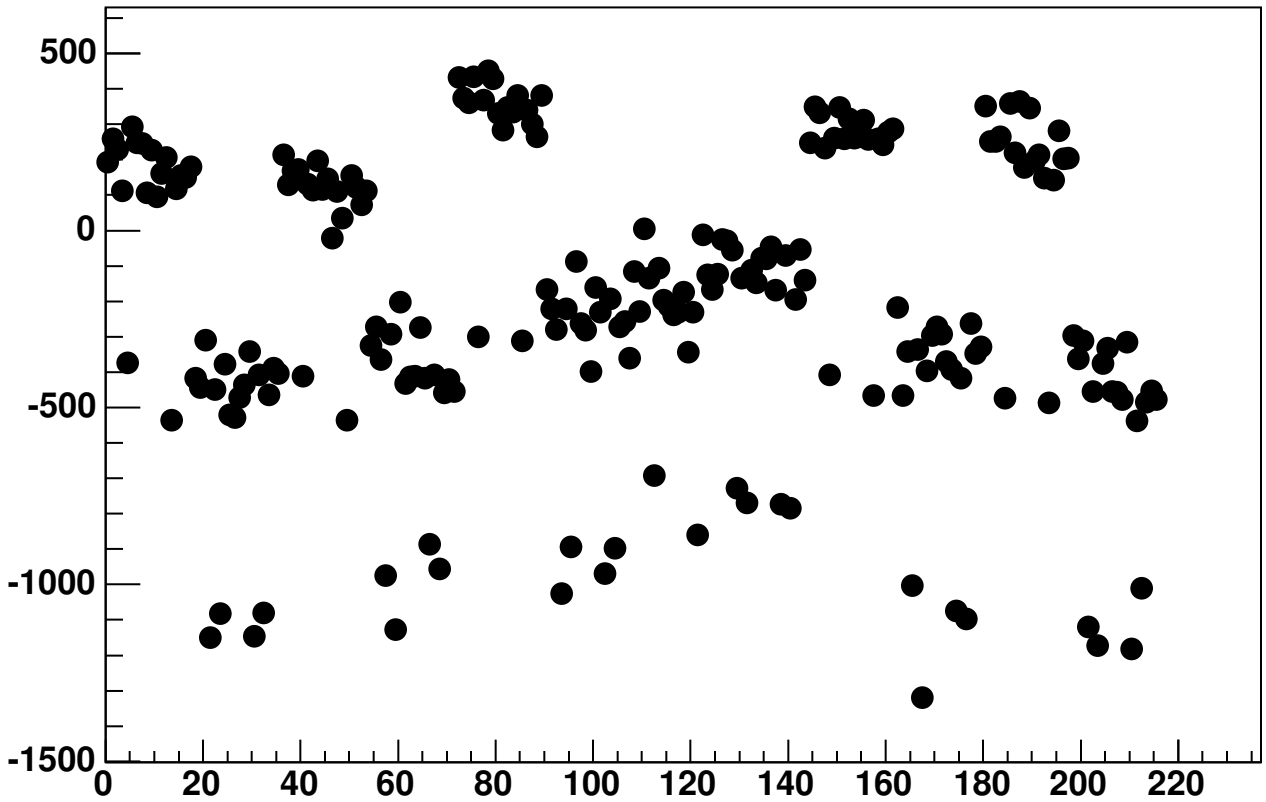
Enable 4, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



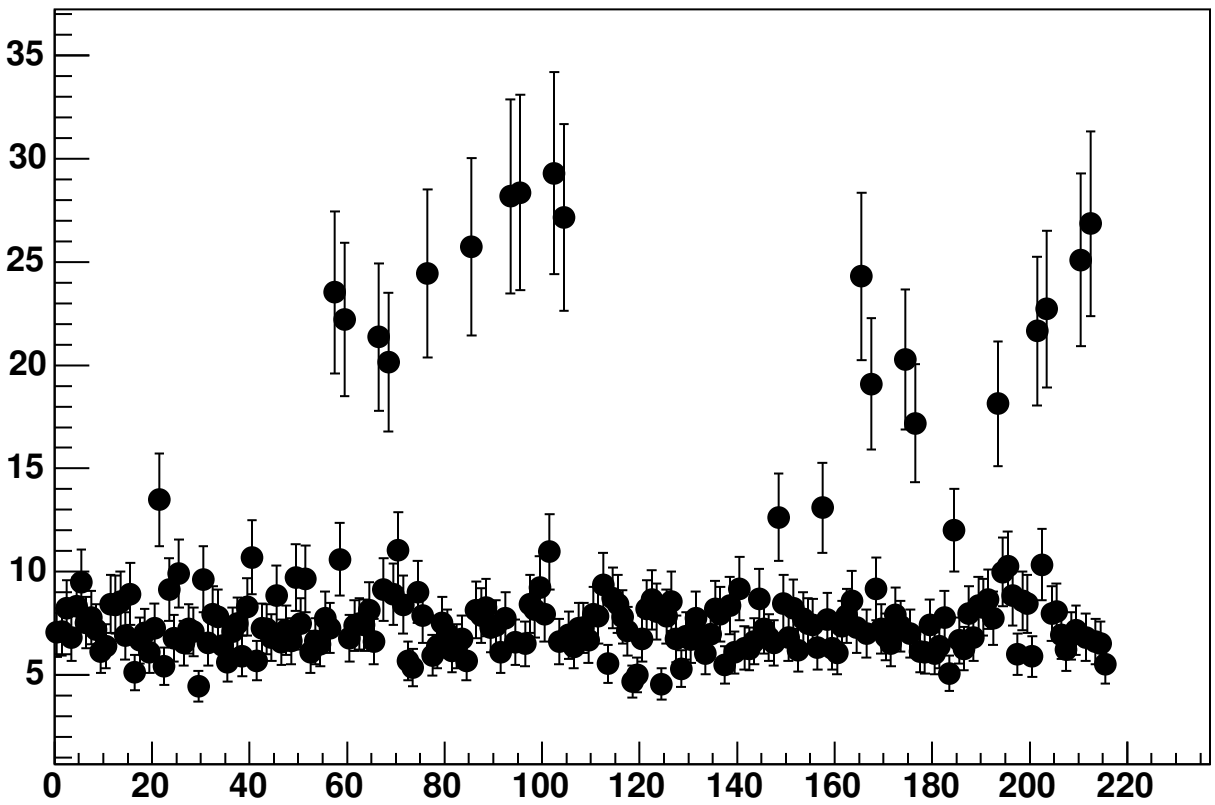
Enable 4, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



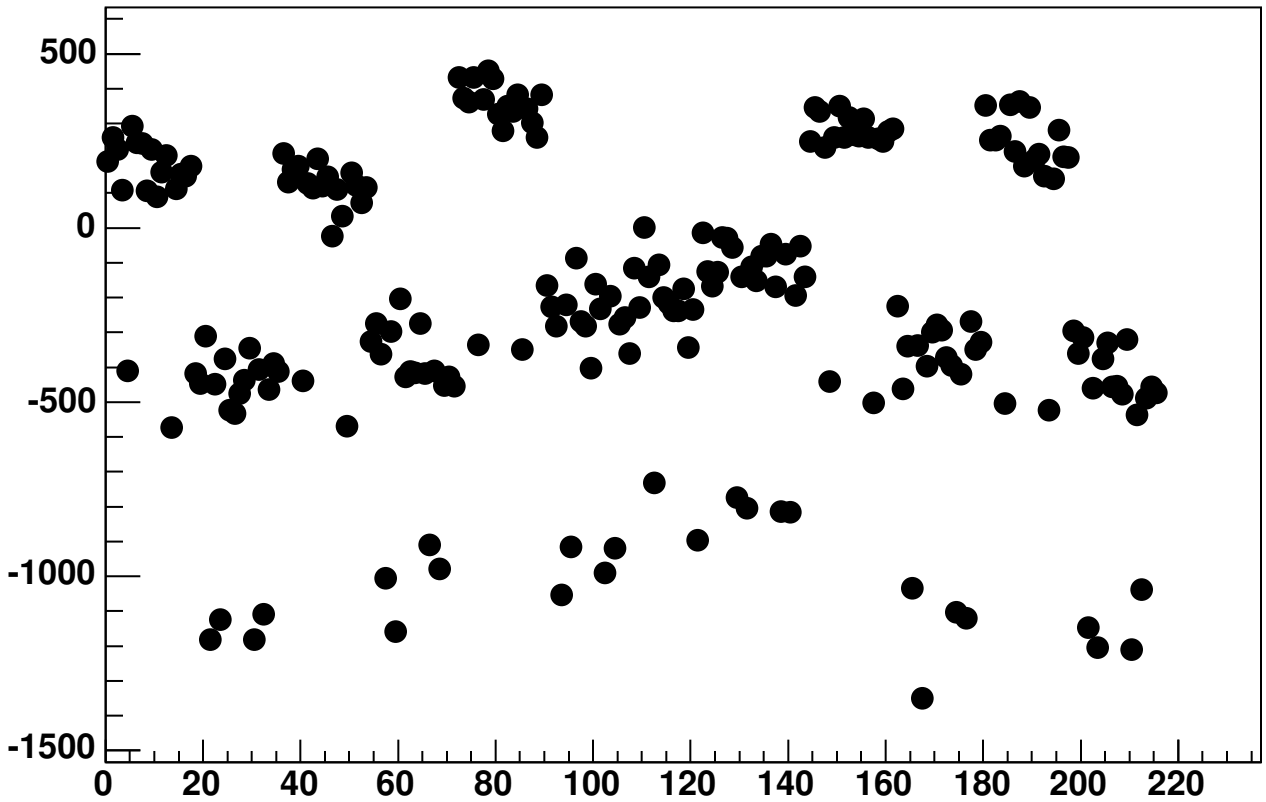
Enable 4, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



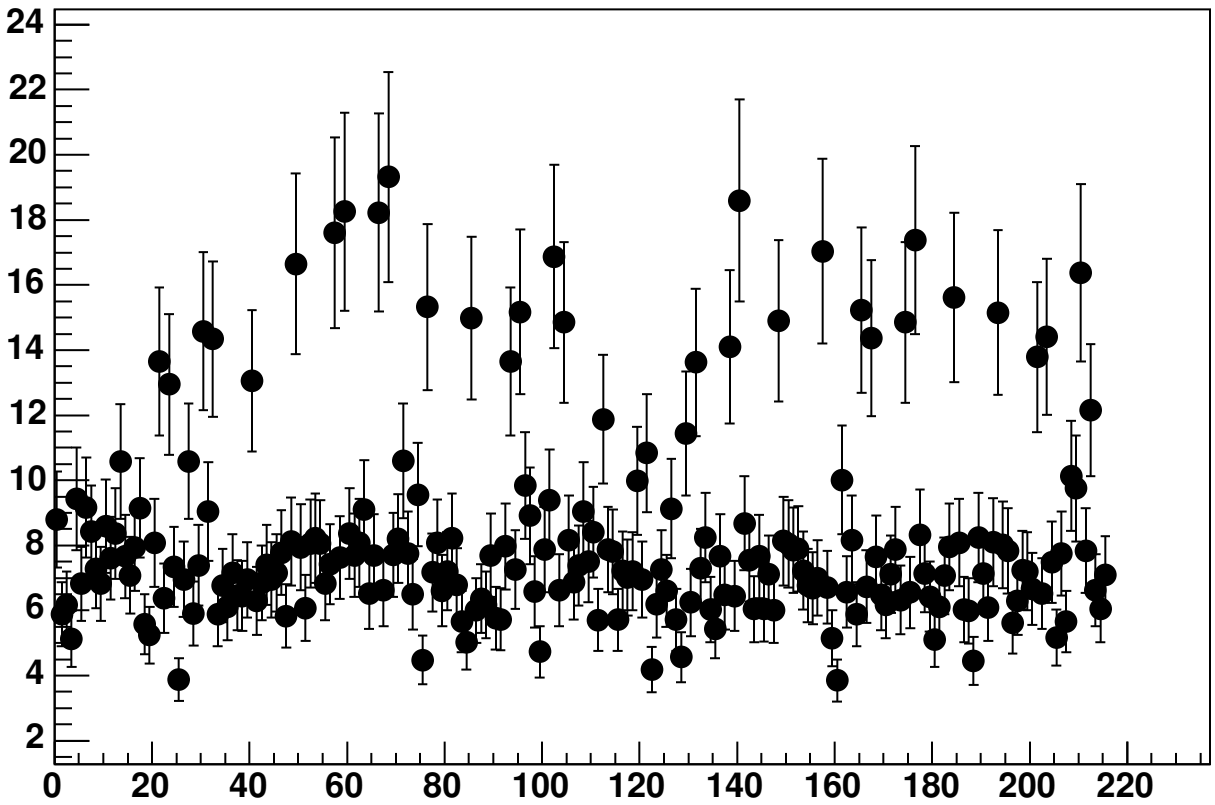
Enable 4, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



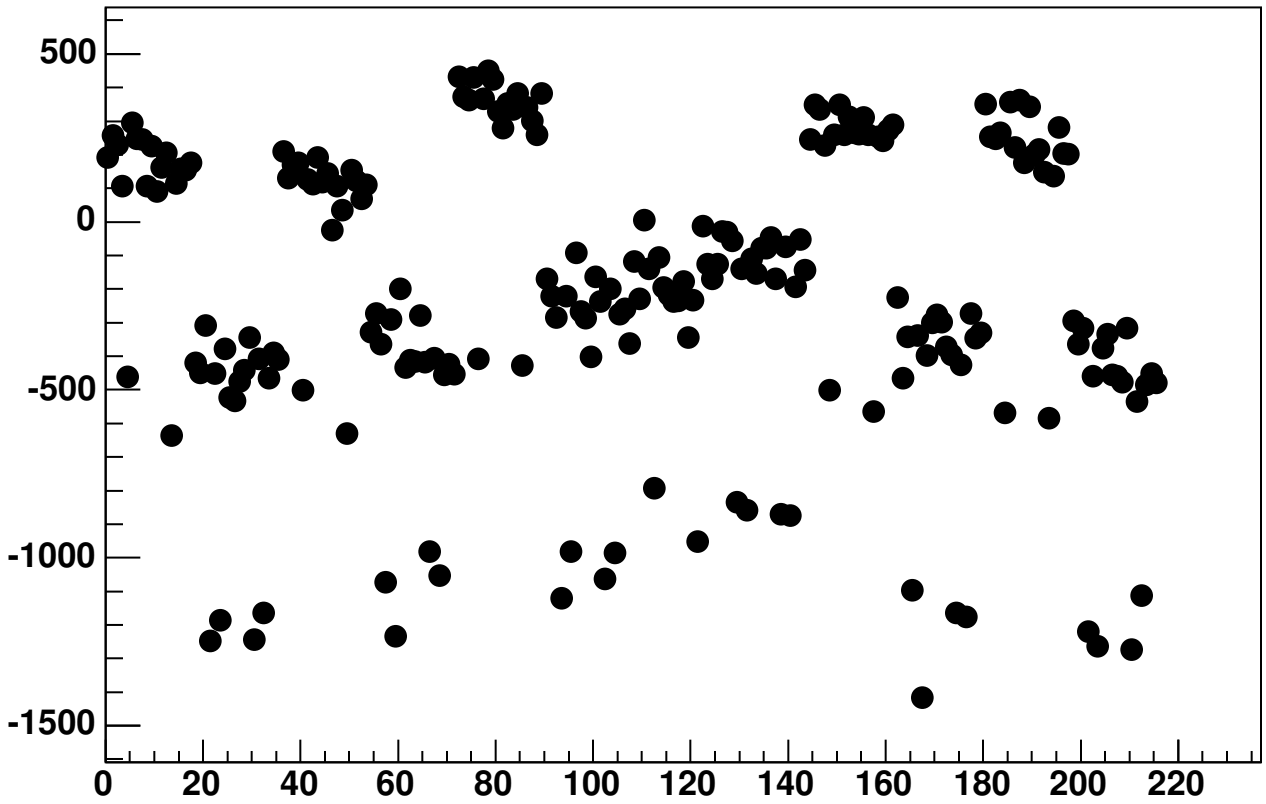
Enable 4, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



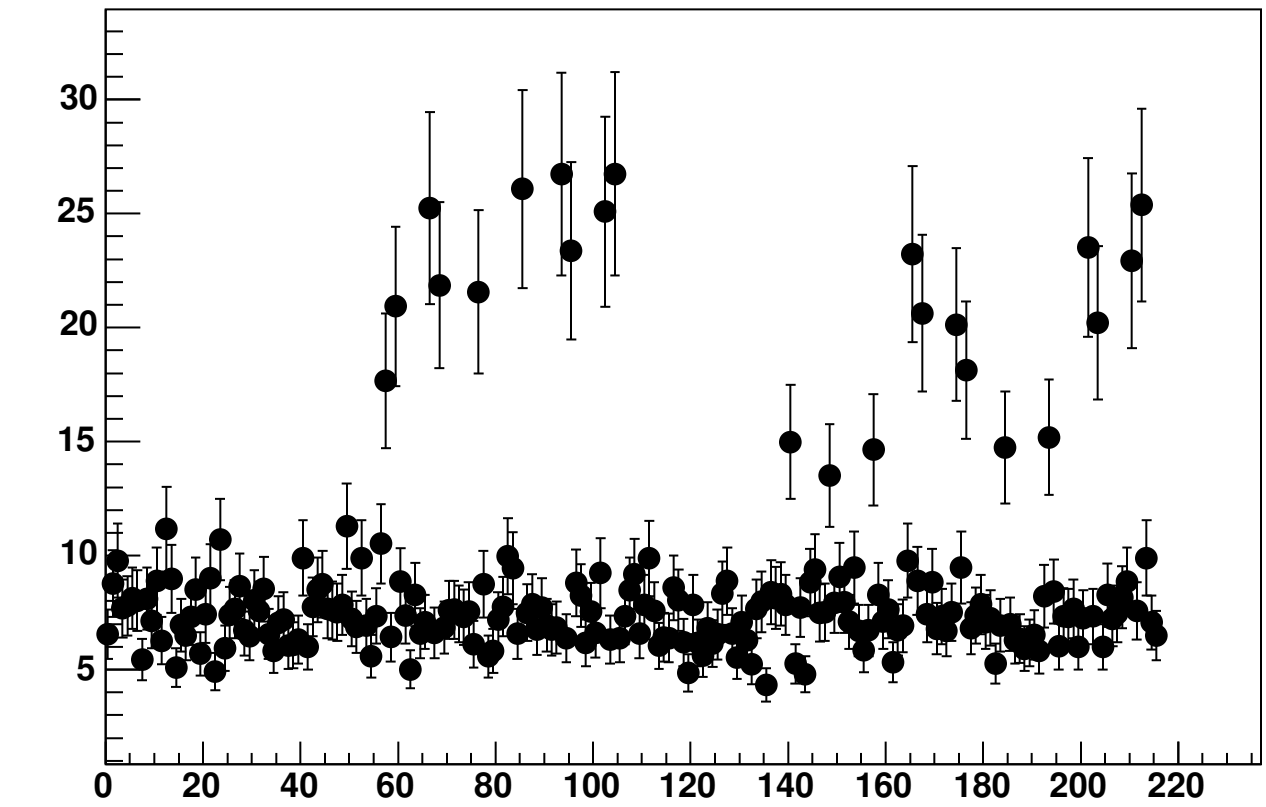
Enable 4, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



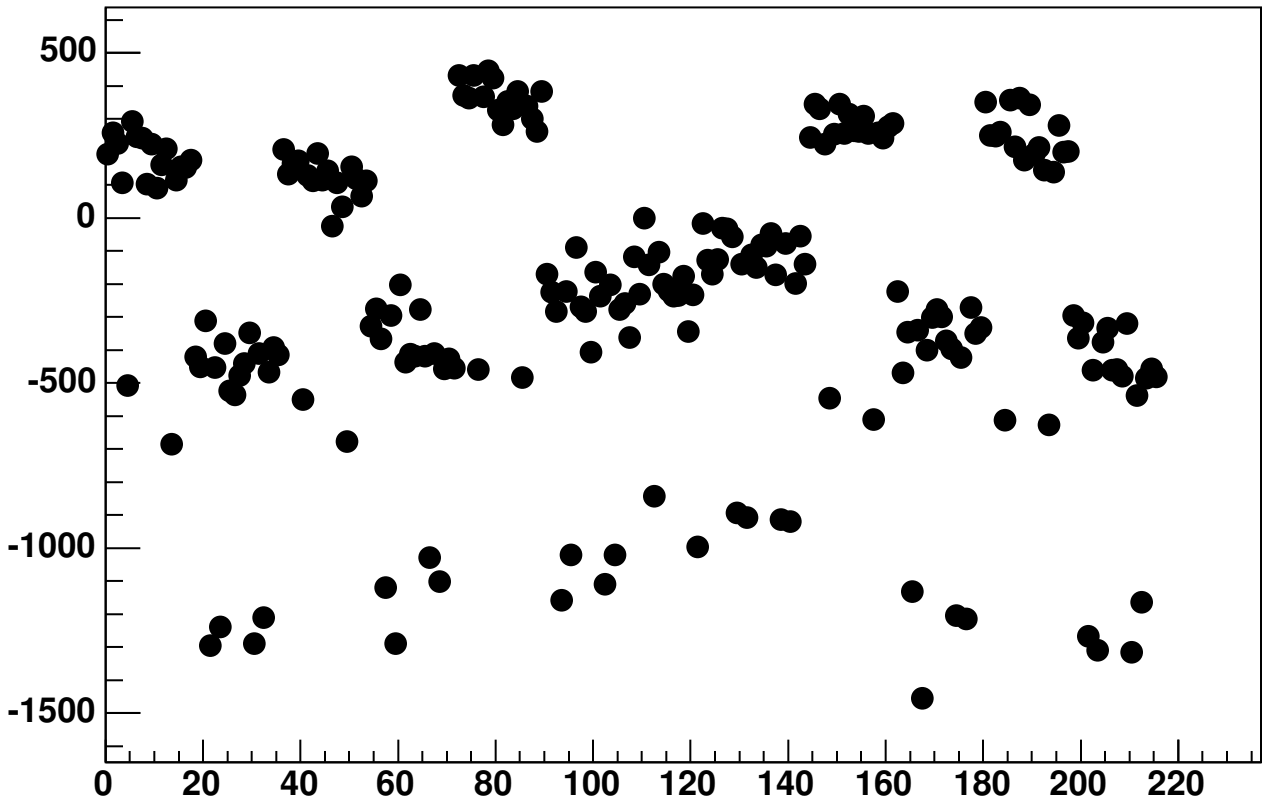
Enable 4, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



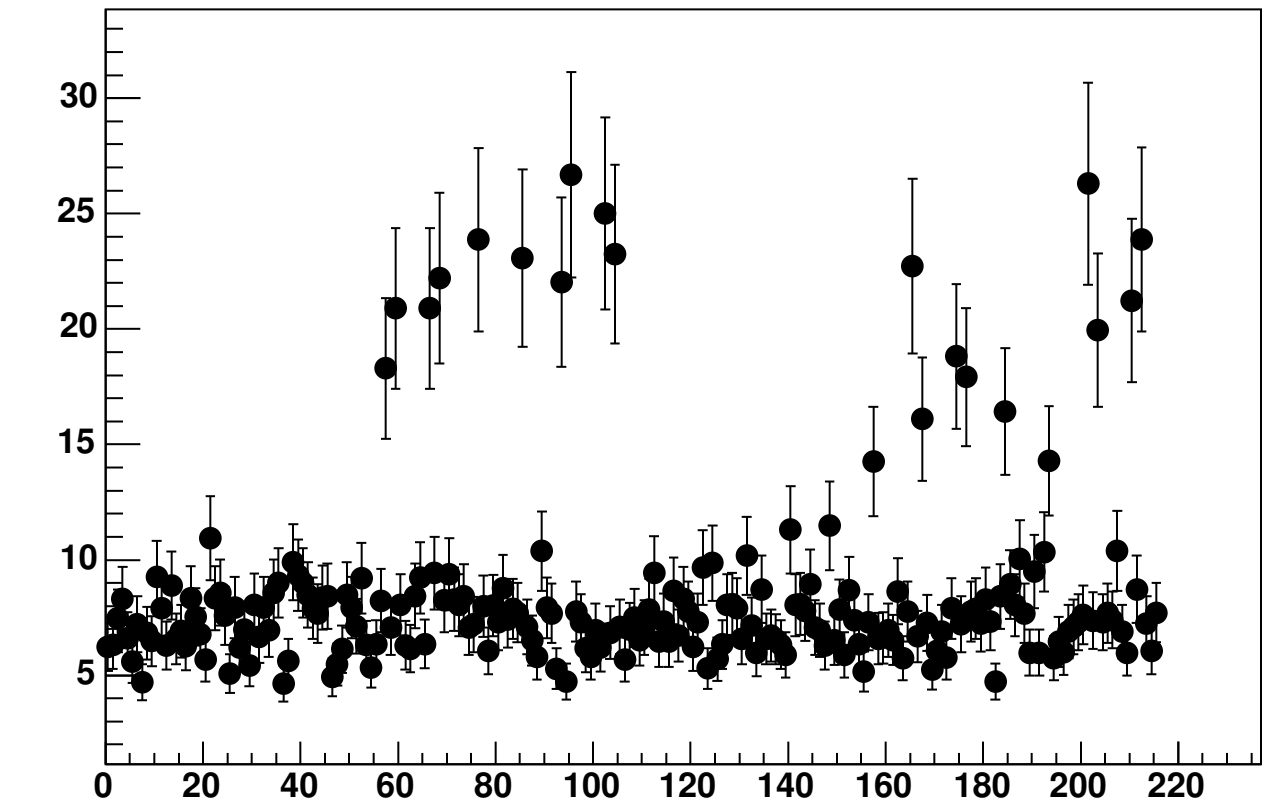
Enable 4, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



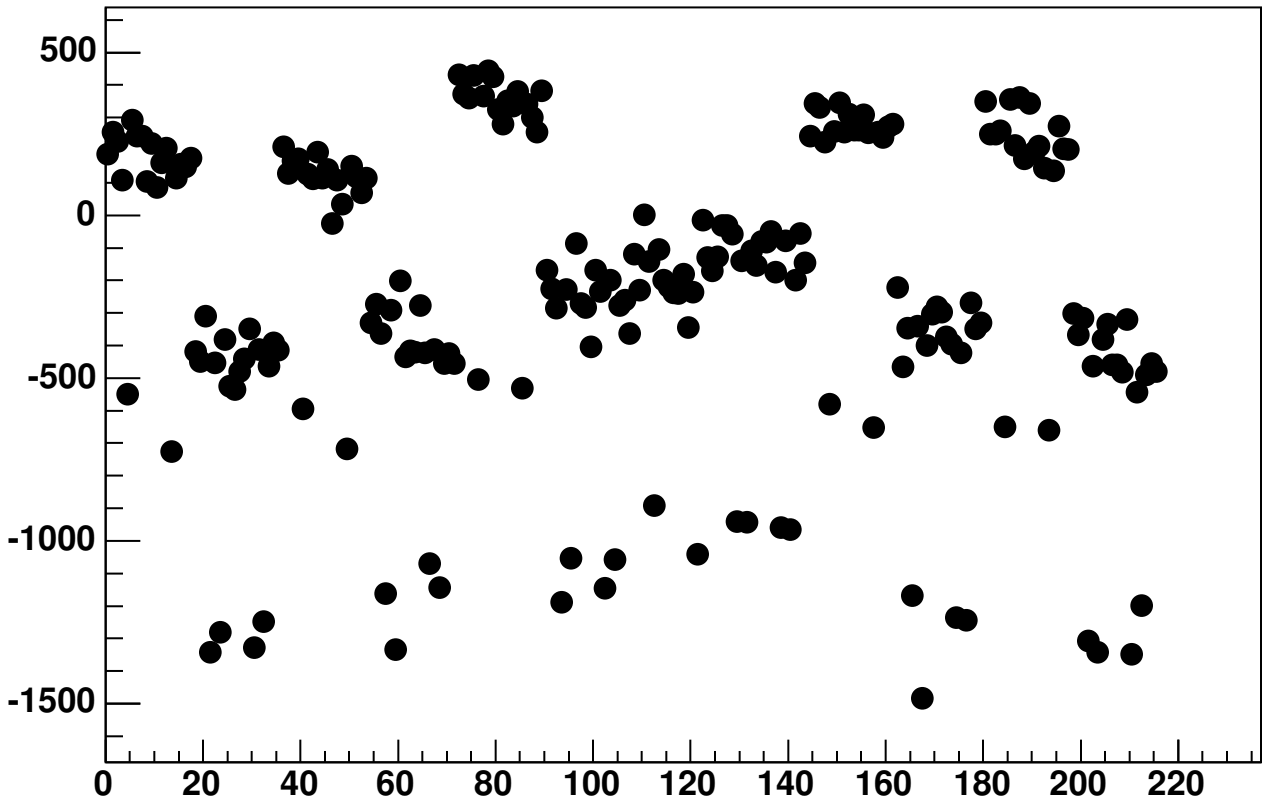
Enable 4, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



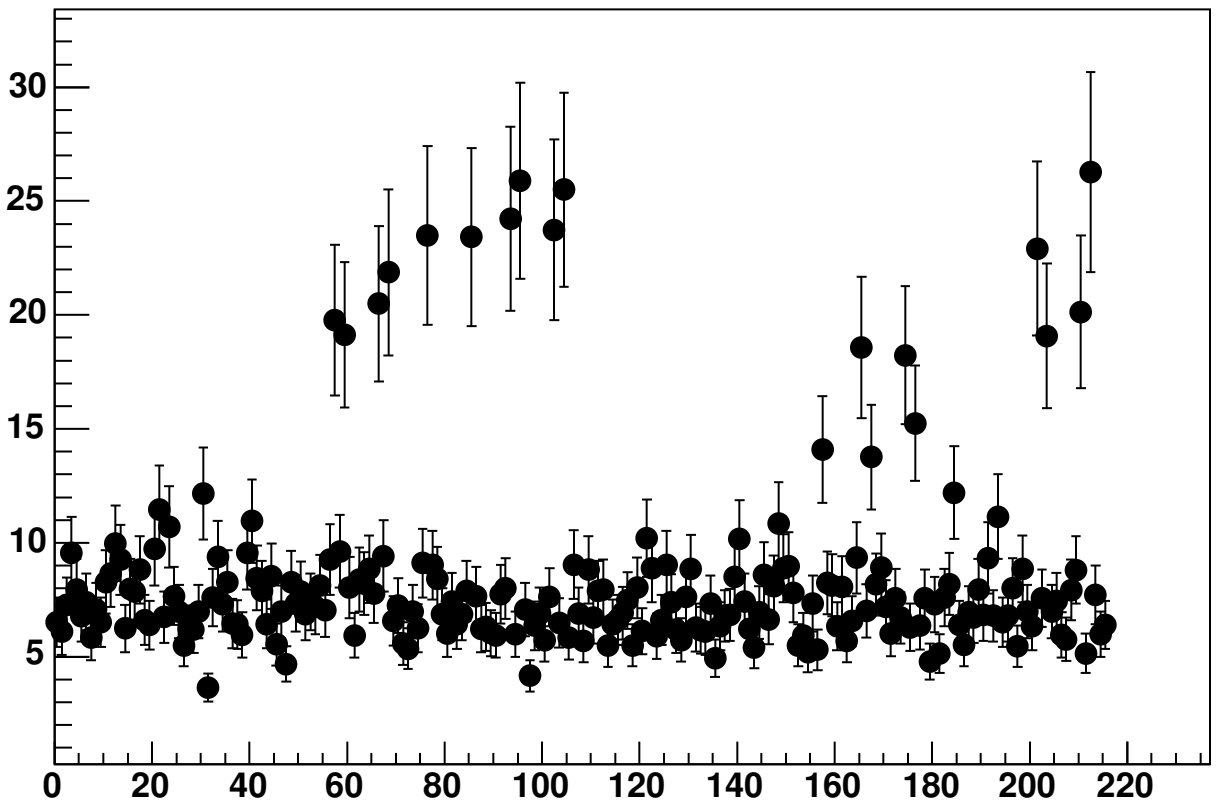
Enable 4, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



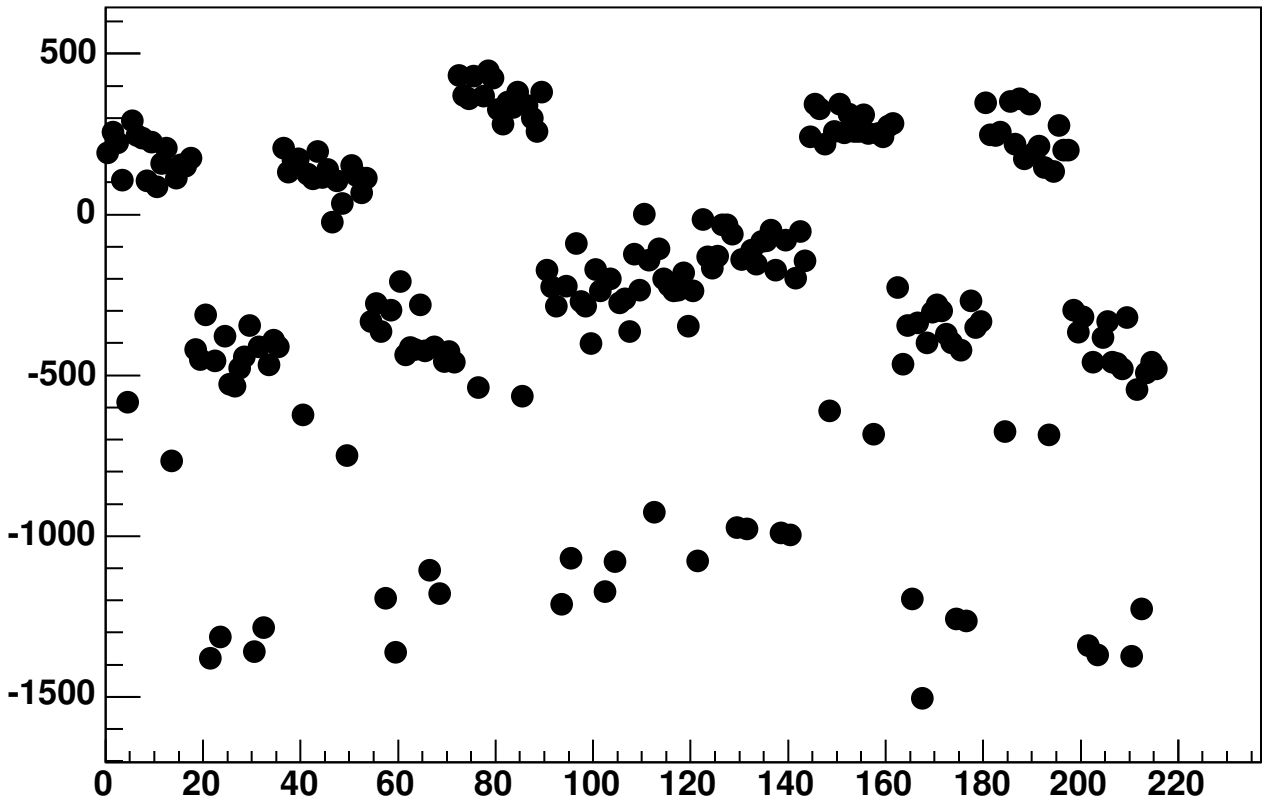
Enable 4, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



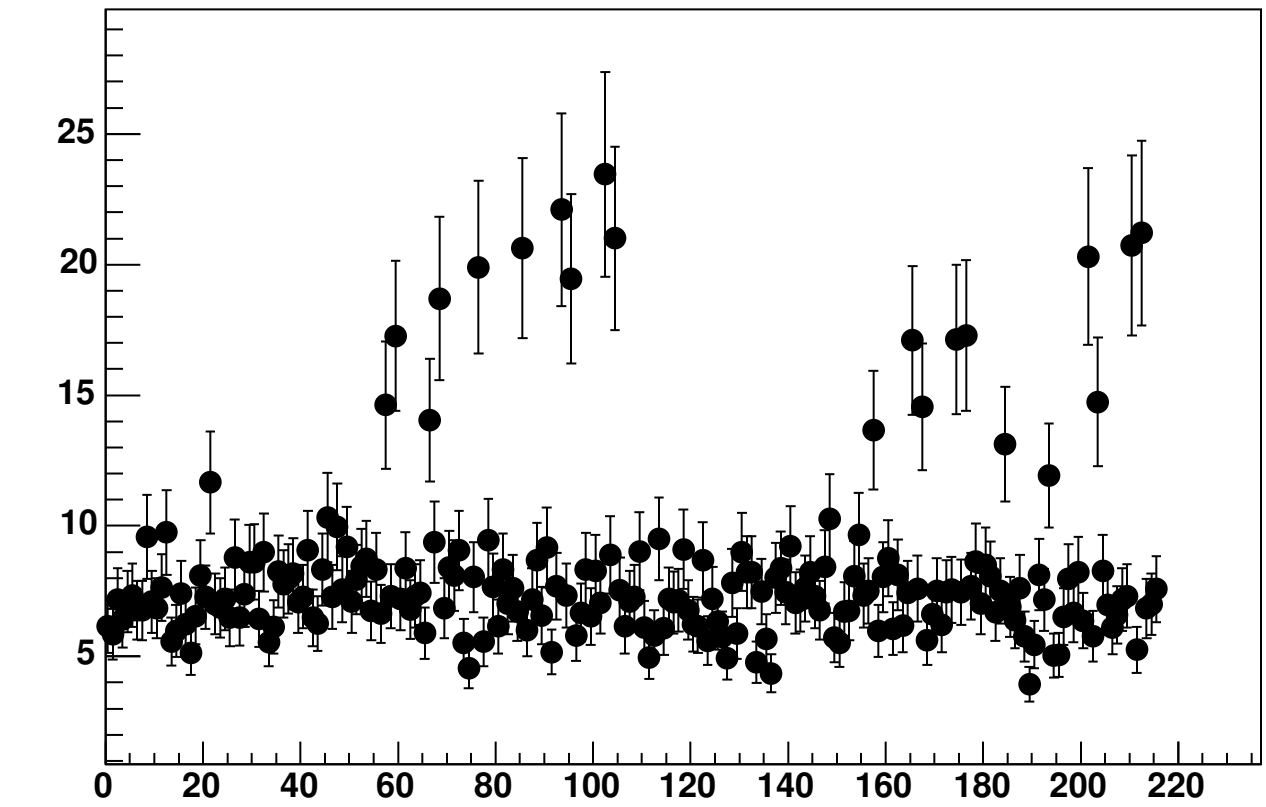
Enable 4, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



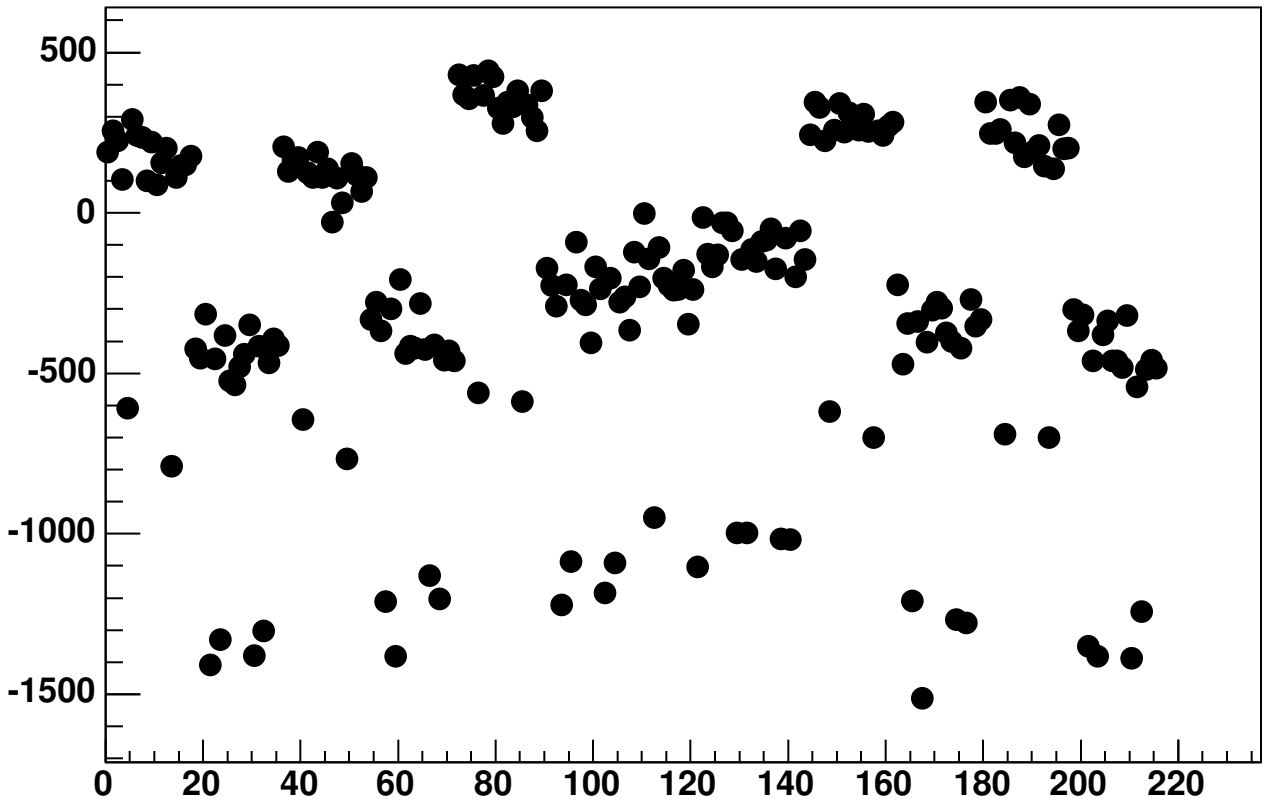
Enable 4, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



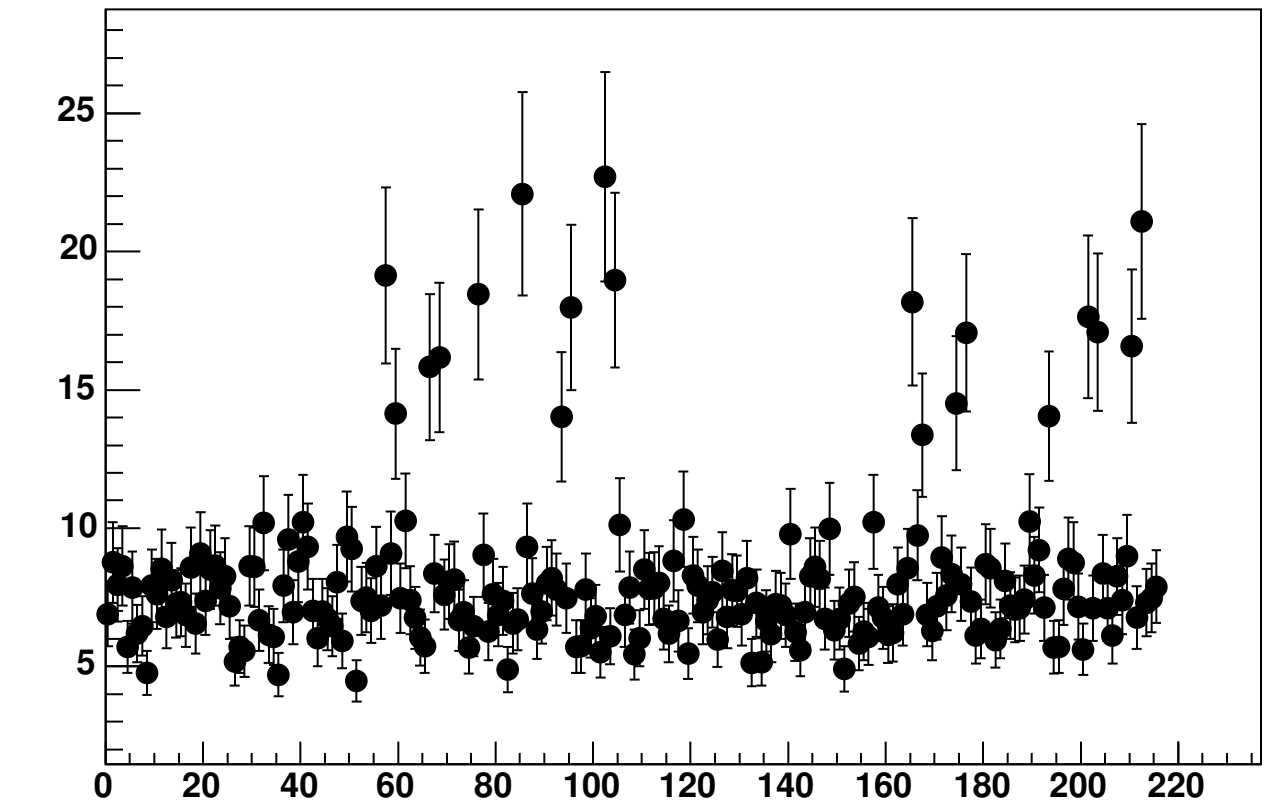
Enable 4, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



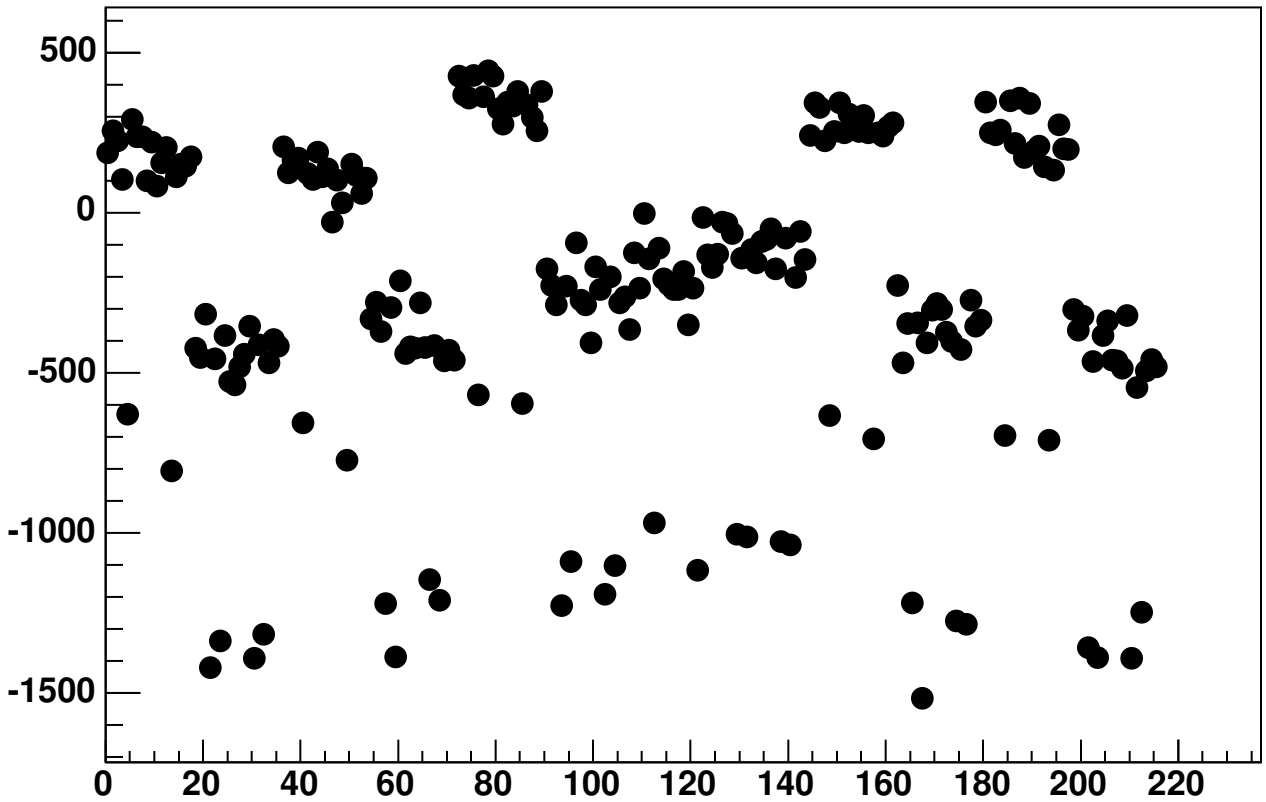
Enable 4, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



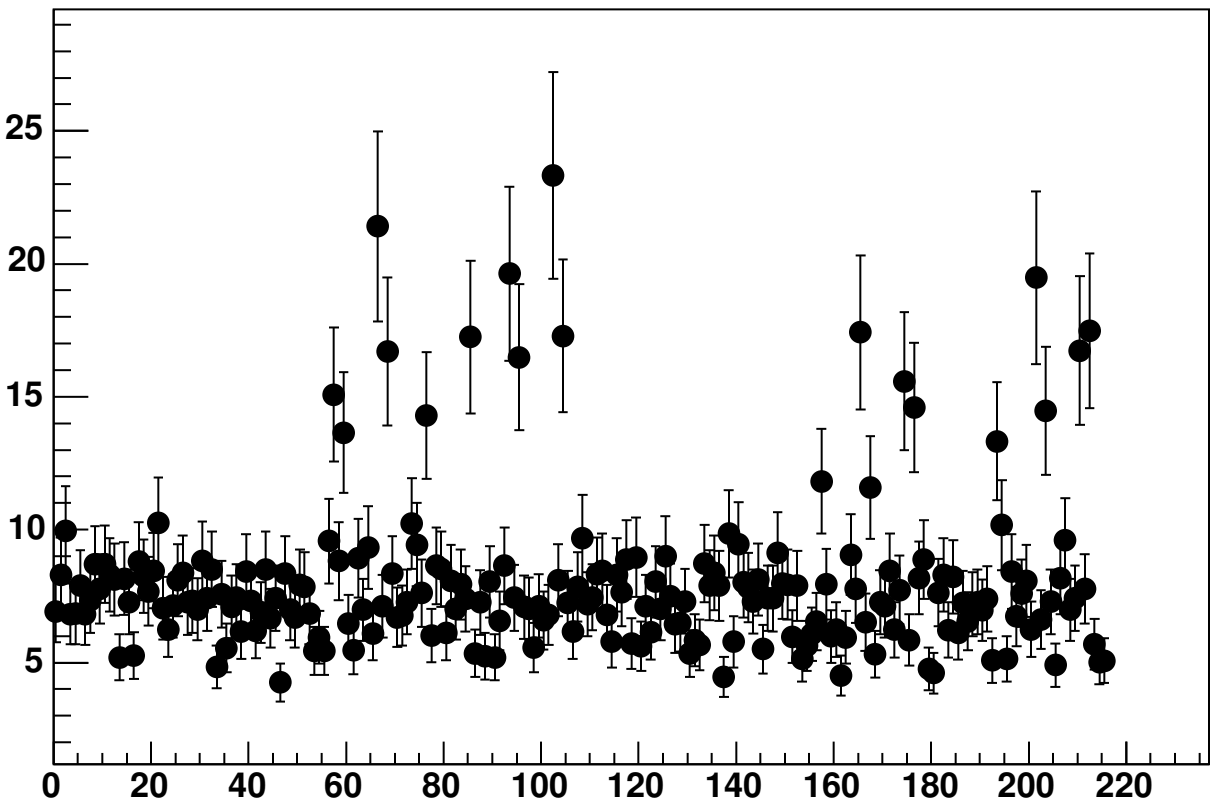
Enable 4, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



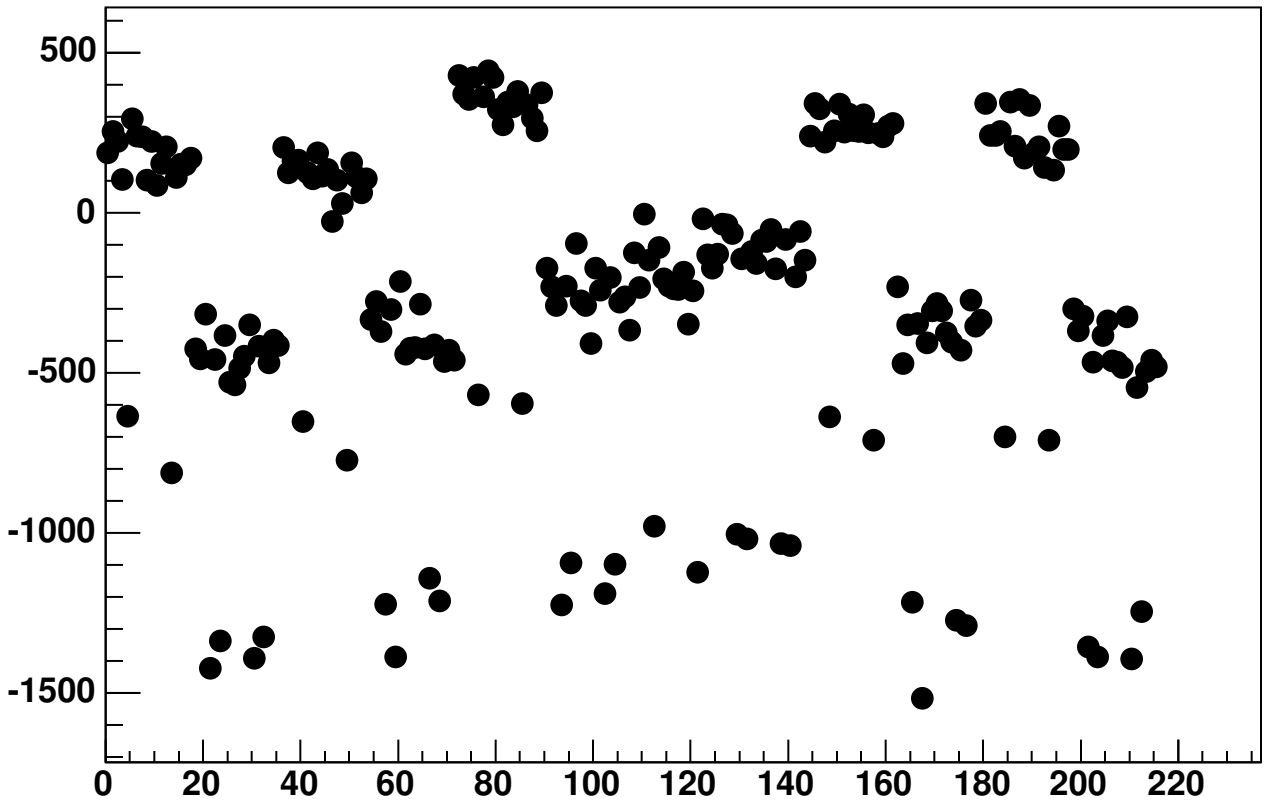
Enable 4, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



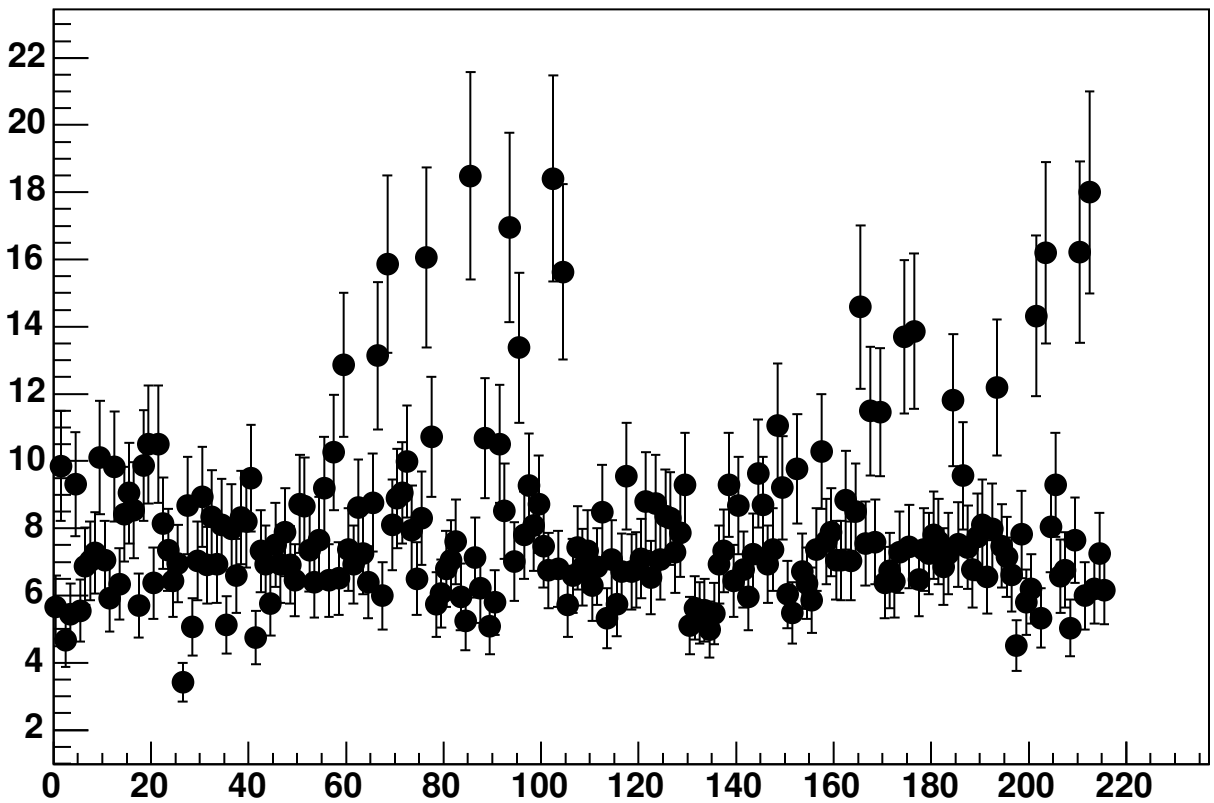
Enable 4, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



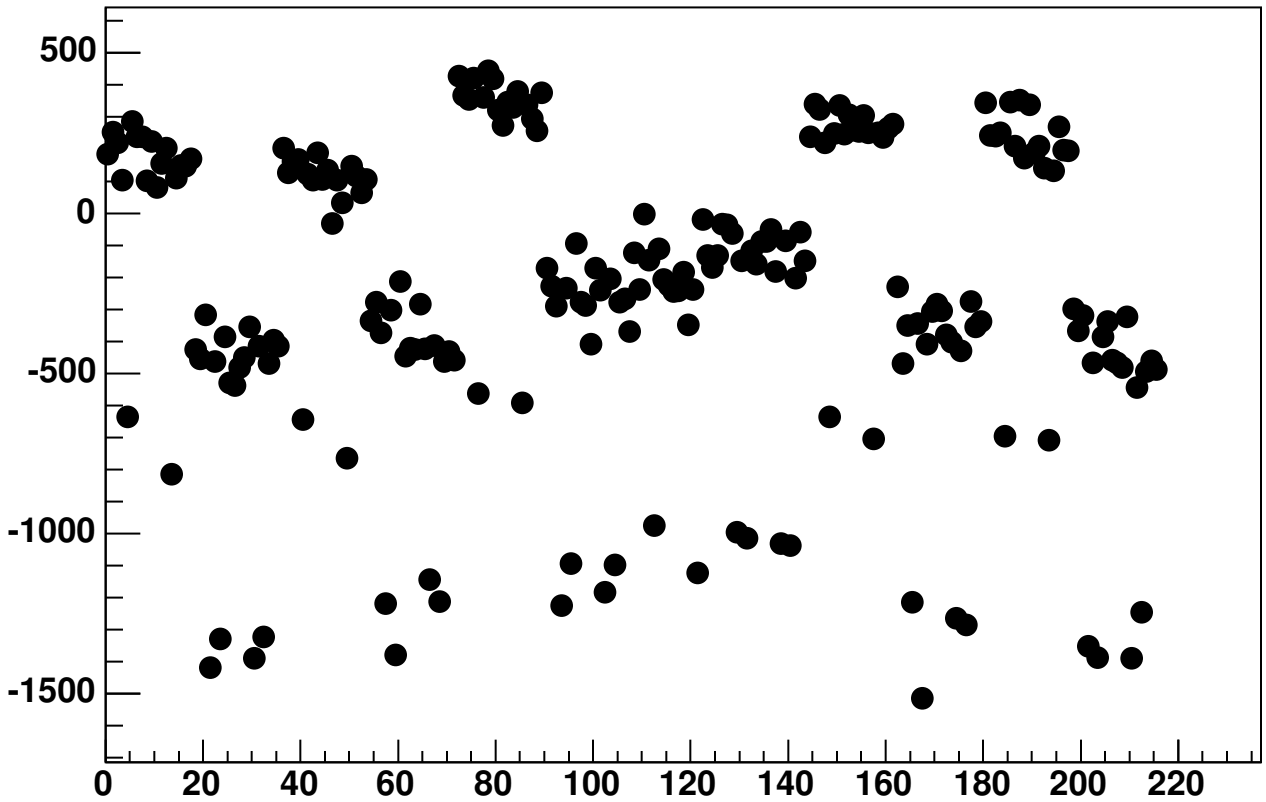
Enable 4, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



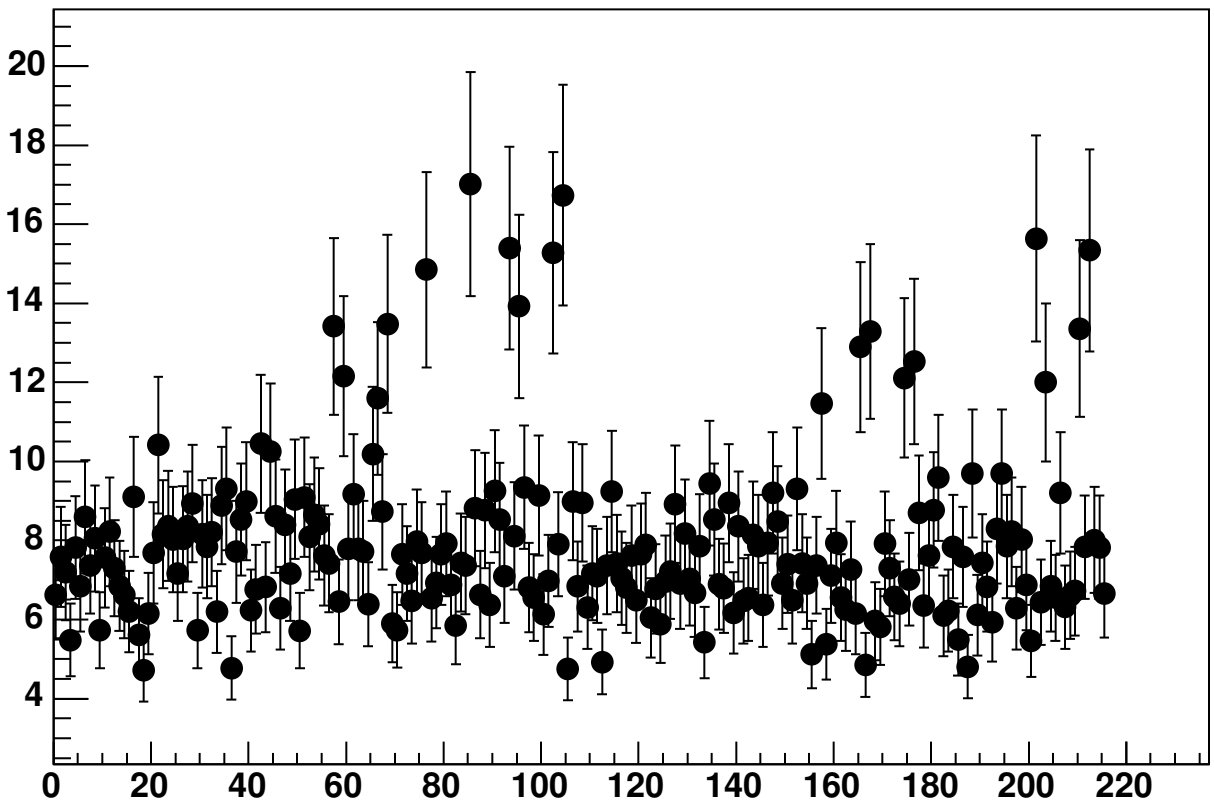
Enable 4, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



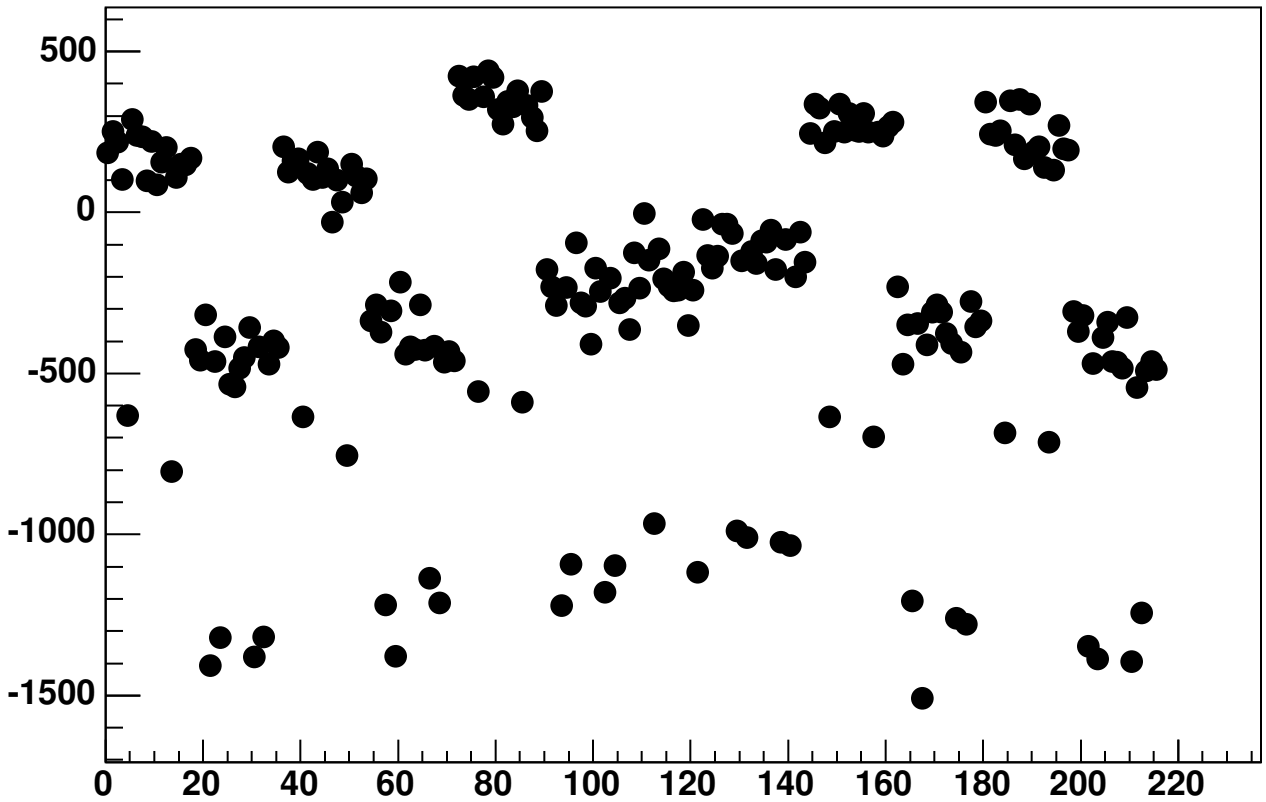
Enable 4, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



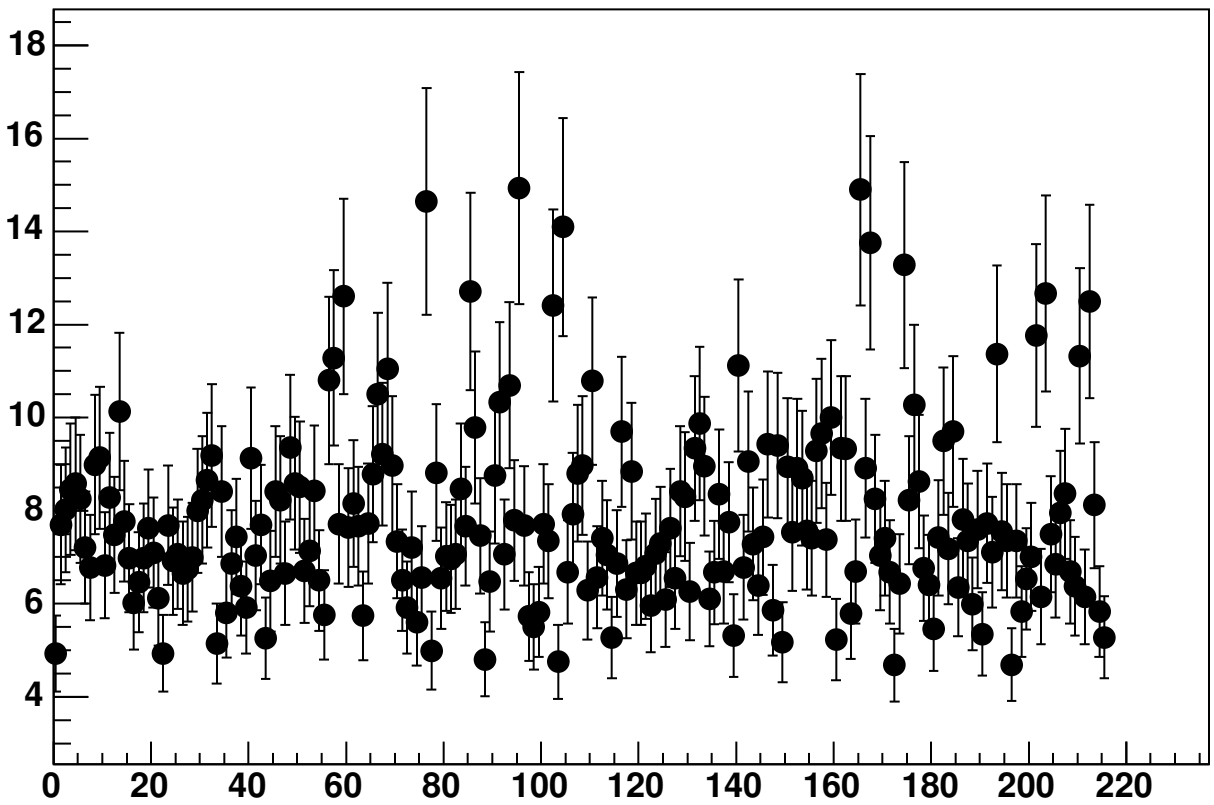
Enable 4, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



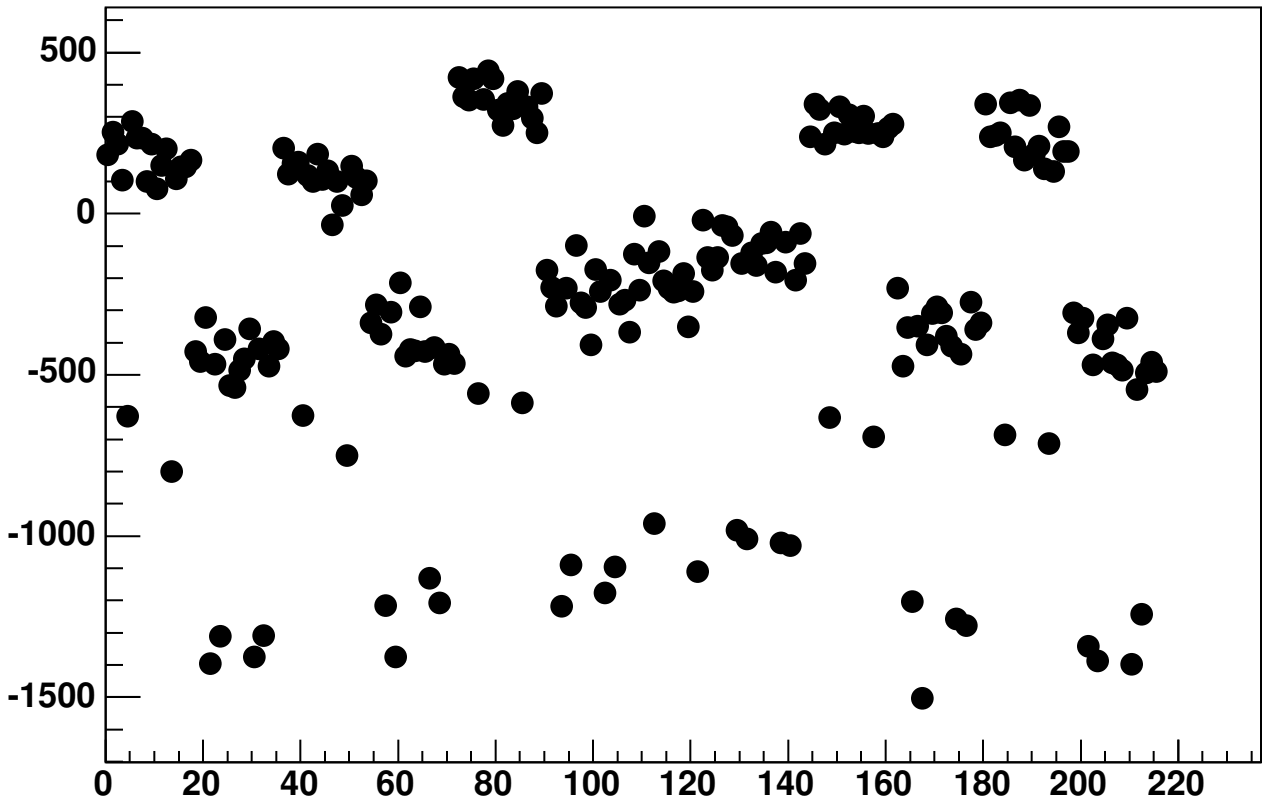
Enable 4, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



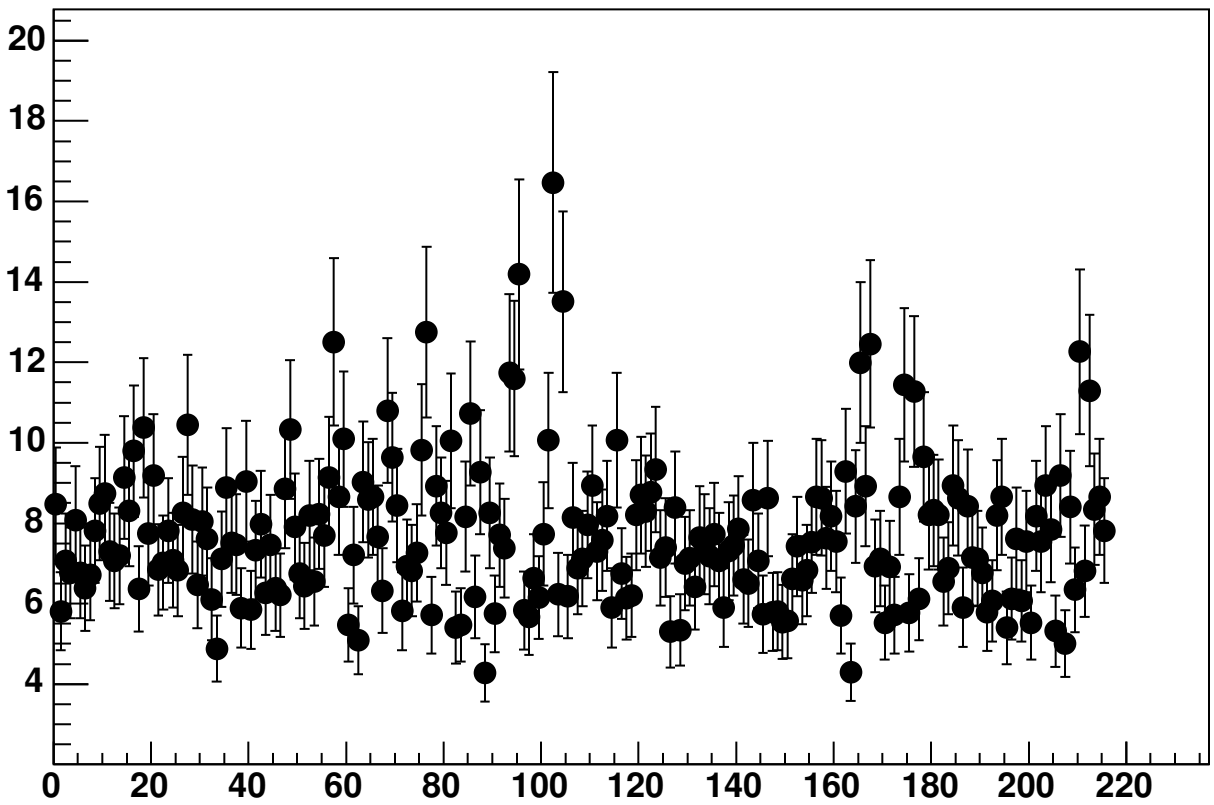
Enable 4, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan



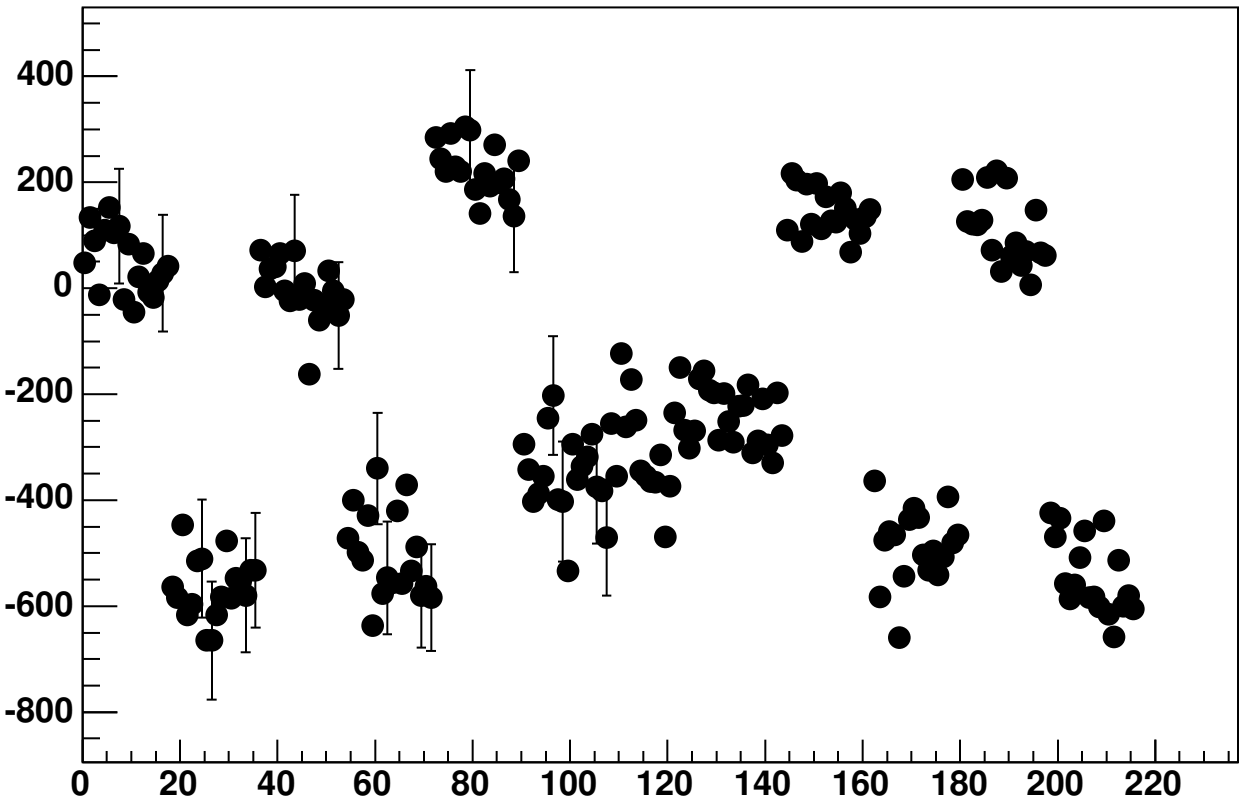
Enable 4, DAC=4000, Hold=245, ADC Mean vs 18*Chip+Chan



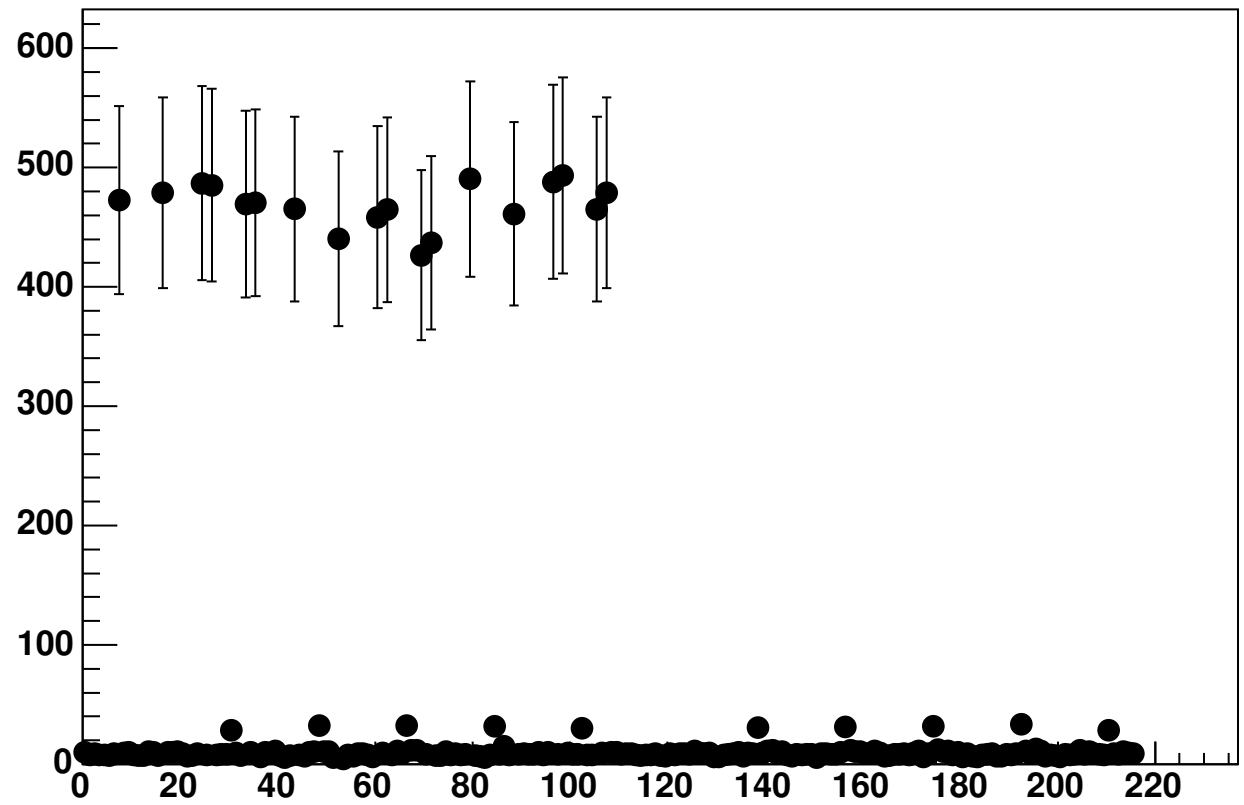
Enable 4, DAC=4000, Hold=245, ADC Noise vs 18*Chip+Chan



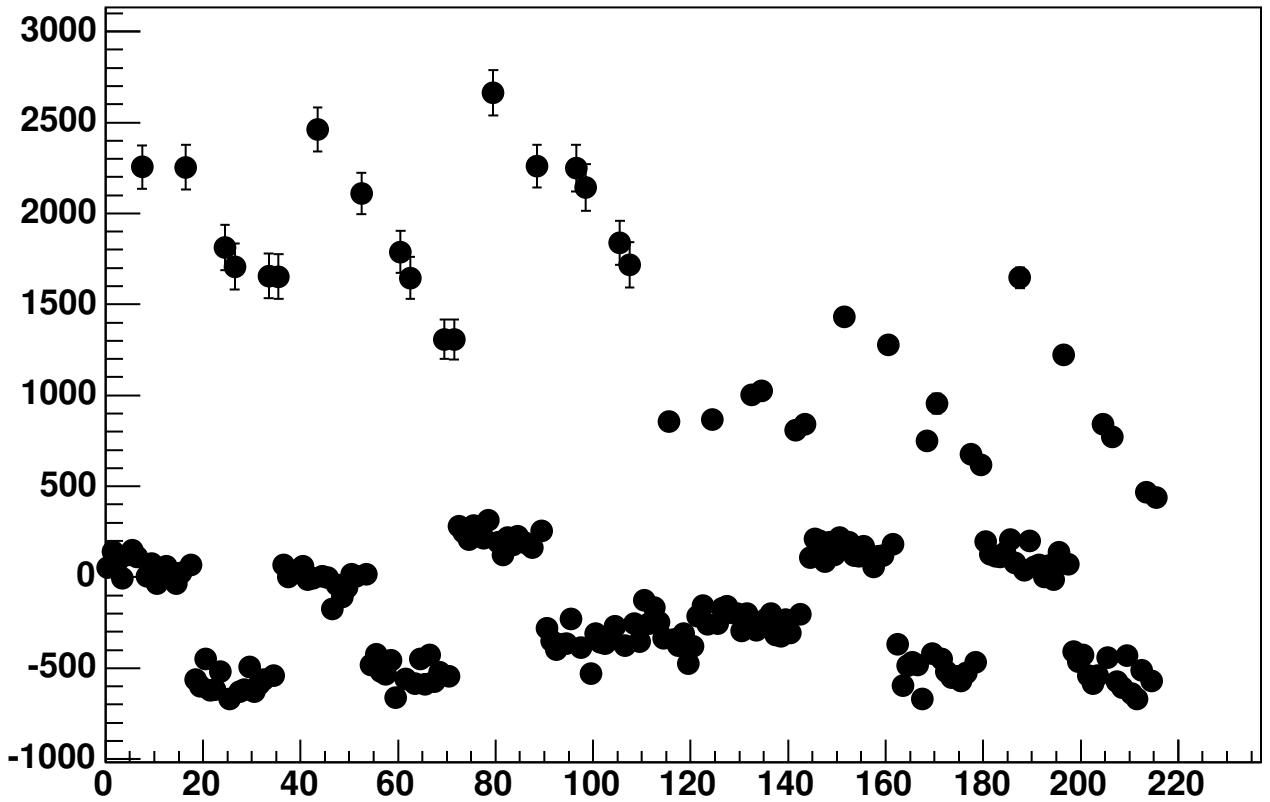
Enable 5, DAC=4000, Hold=0, ADC Mean vs 18*Chip+Chan



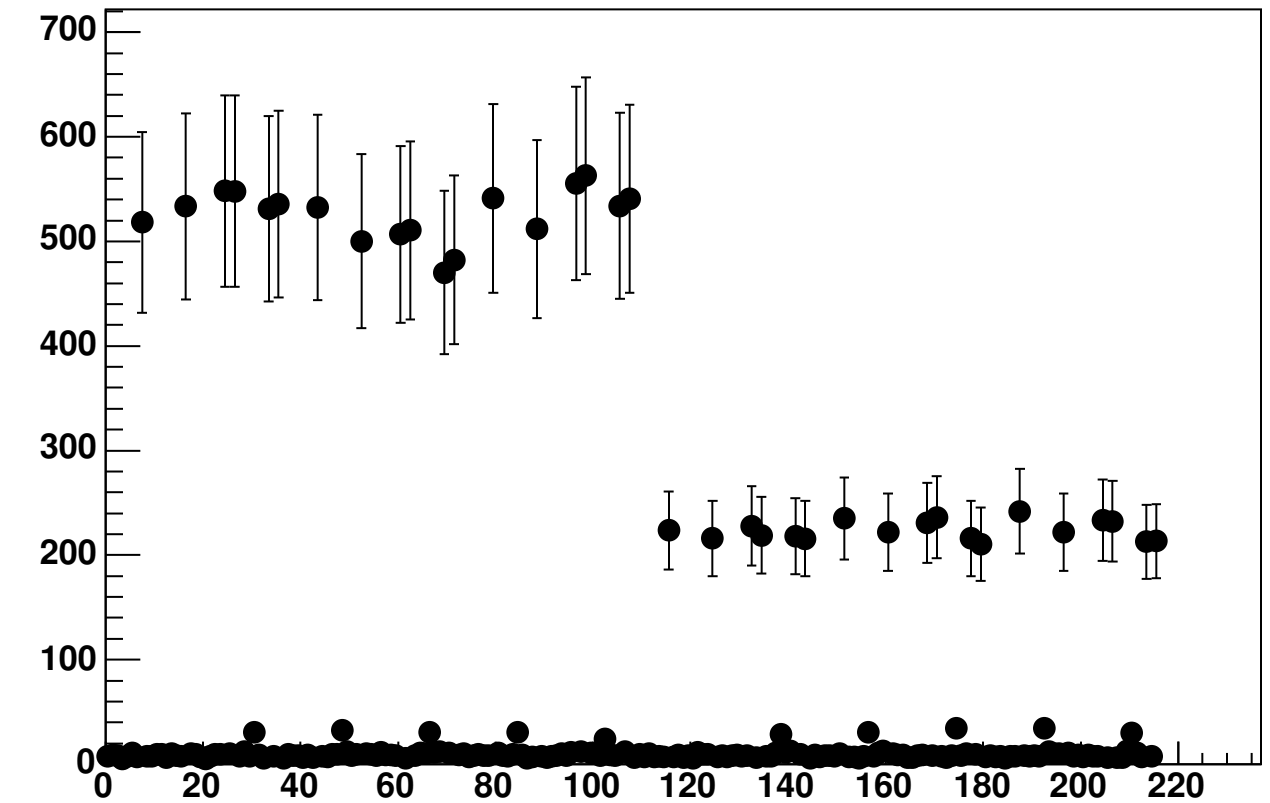
Enable 5, DAC=4000, Hold=0, ADC Noise vs 18*Chip+Chan



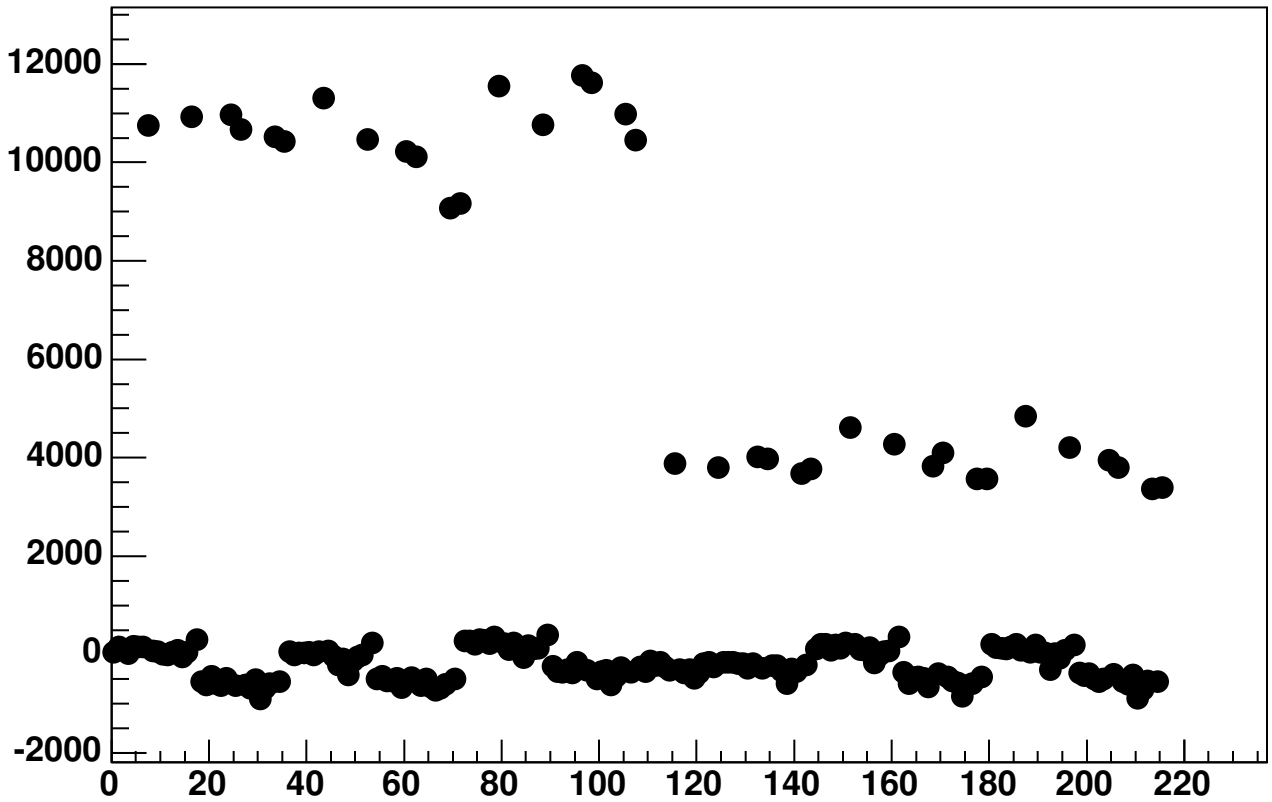
Enable 5, DAC=4000, Hold=5, ADC Mean vs 18*Chip+Chan



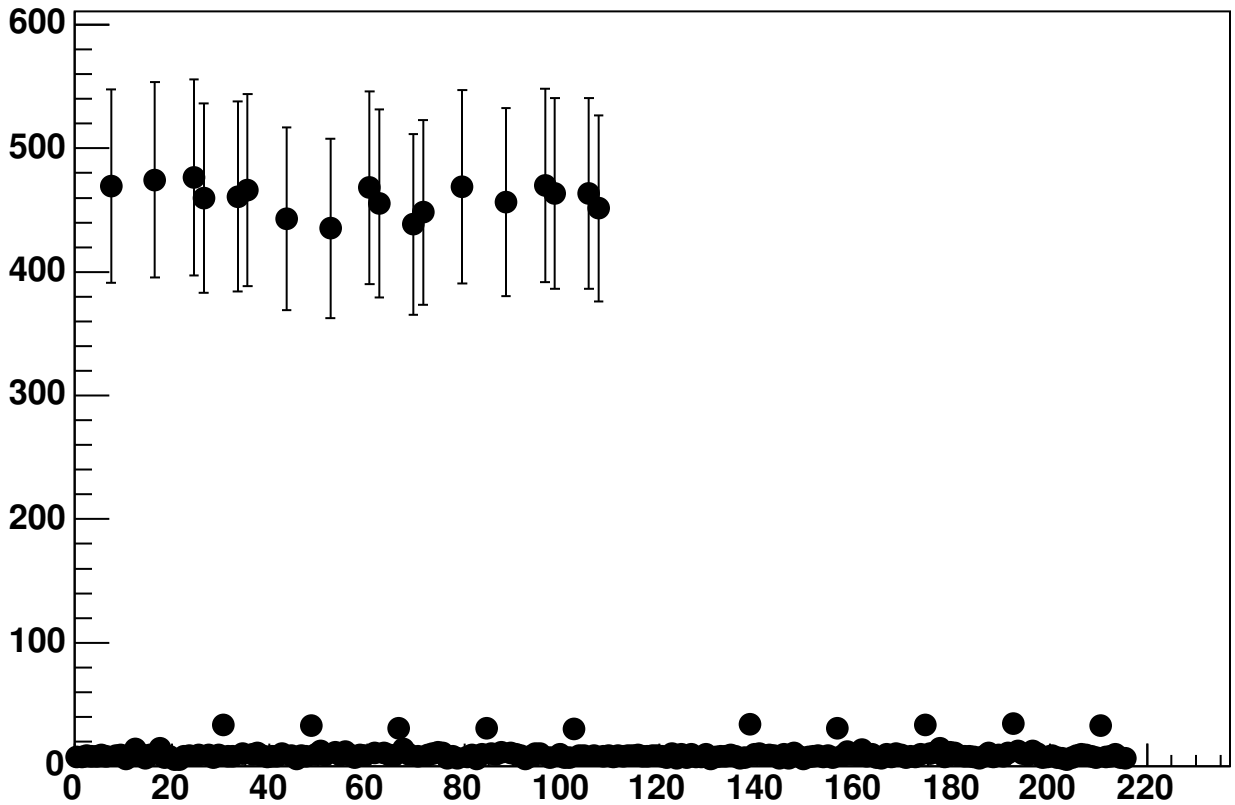
Enable 5, DAC=4000, Hold=5, ADC Noise vs 18*Chip+Chan



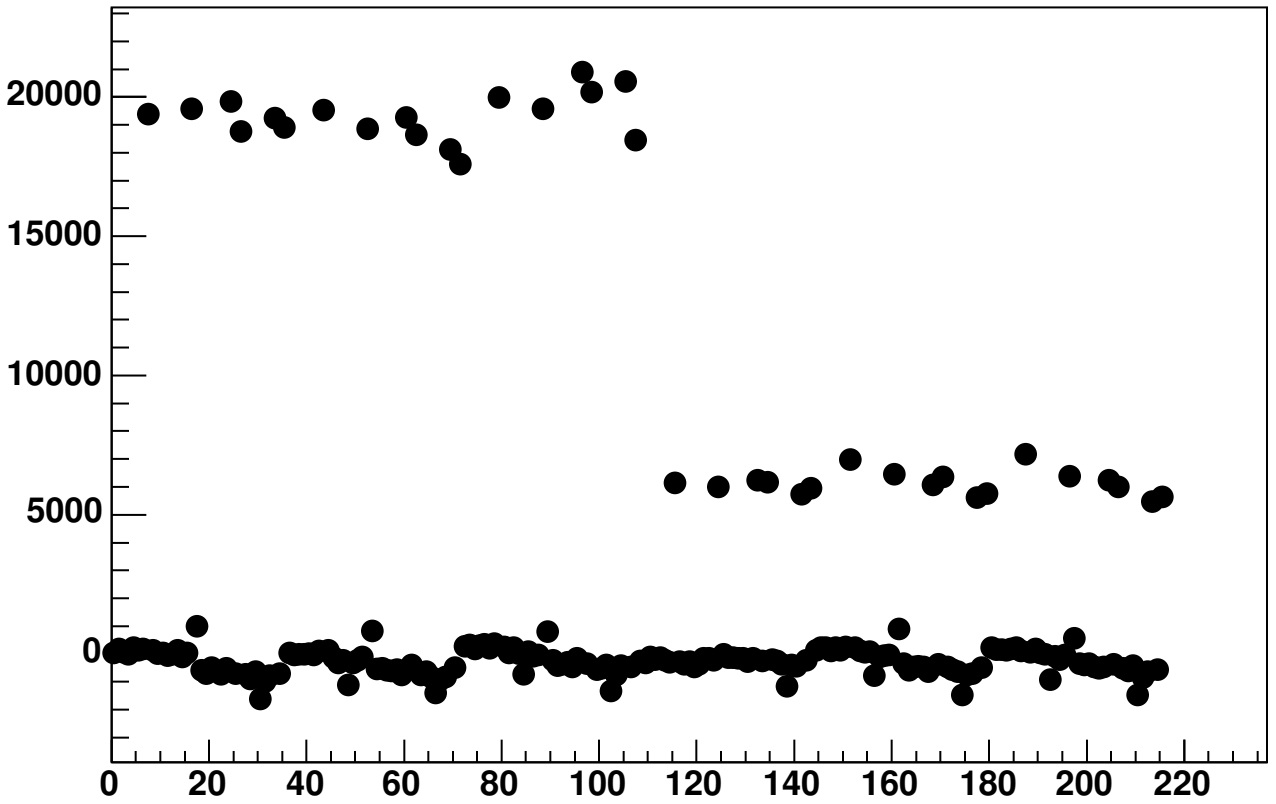
Enable 5, DAC=4000, Hold=10, ADC Mean vs 18*Chip+Chan



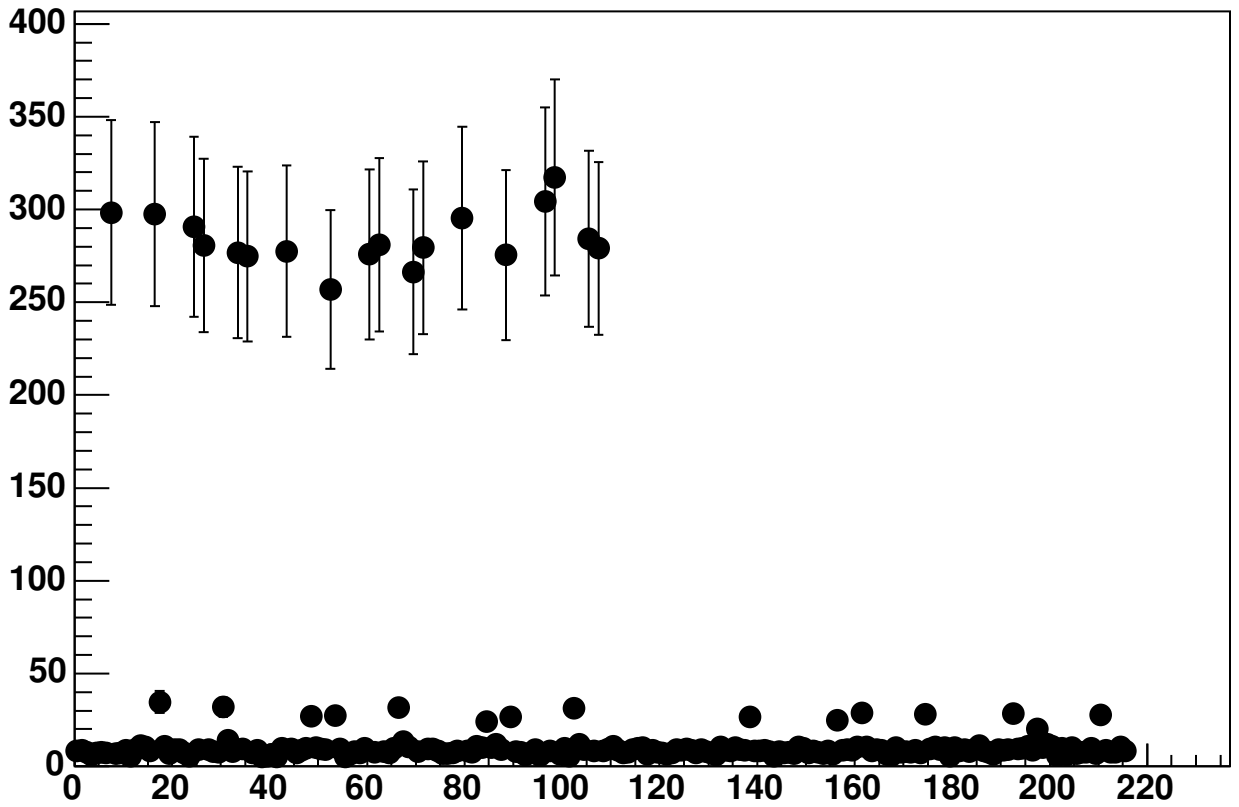
Enable 5, DAC=4000, Hold=10, ADC Noise vs 18*Chip+Chan



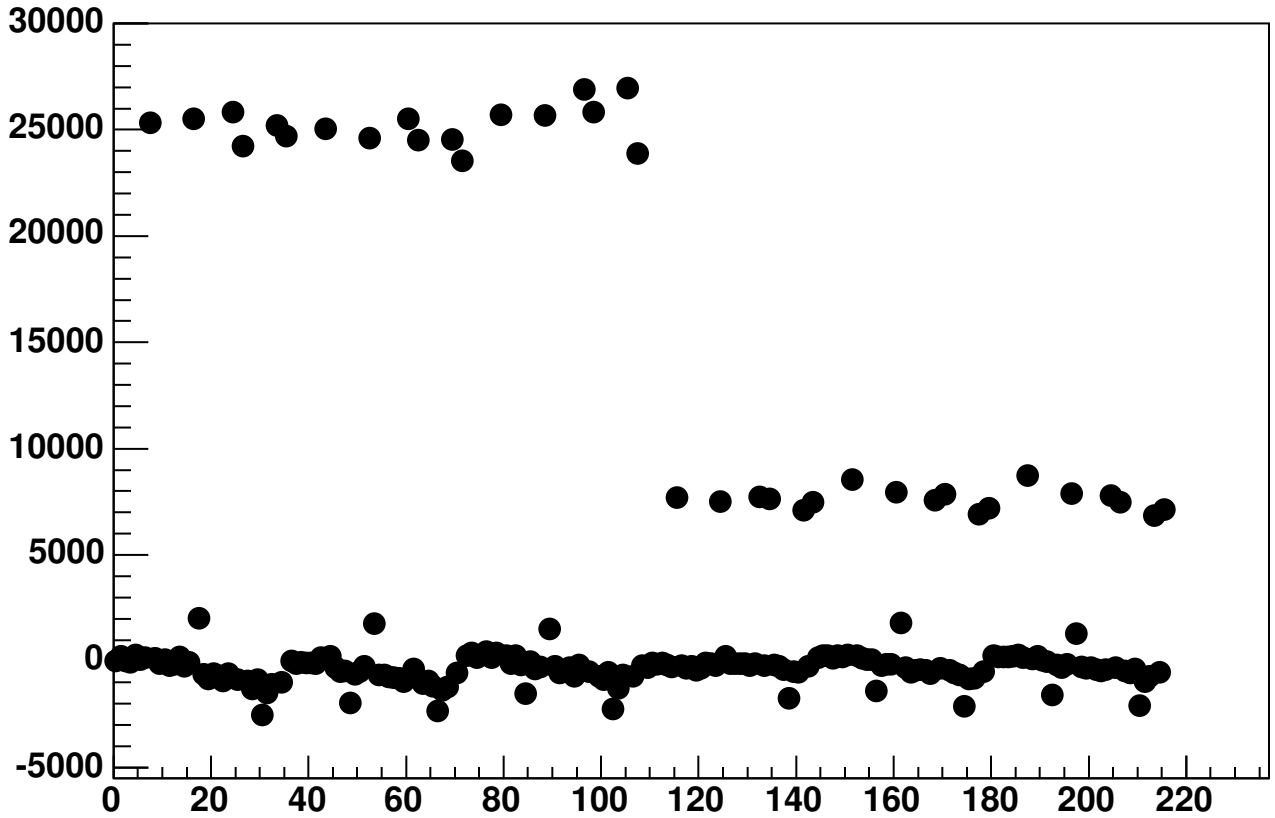
Enable 5, DAC=4000, Hold=15, ADC Mean vs 18*Chip+Chan



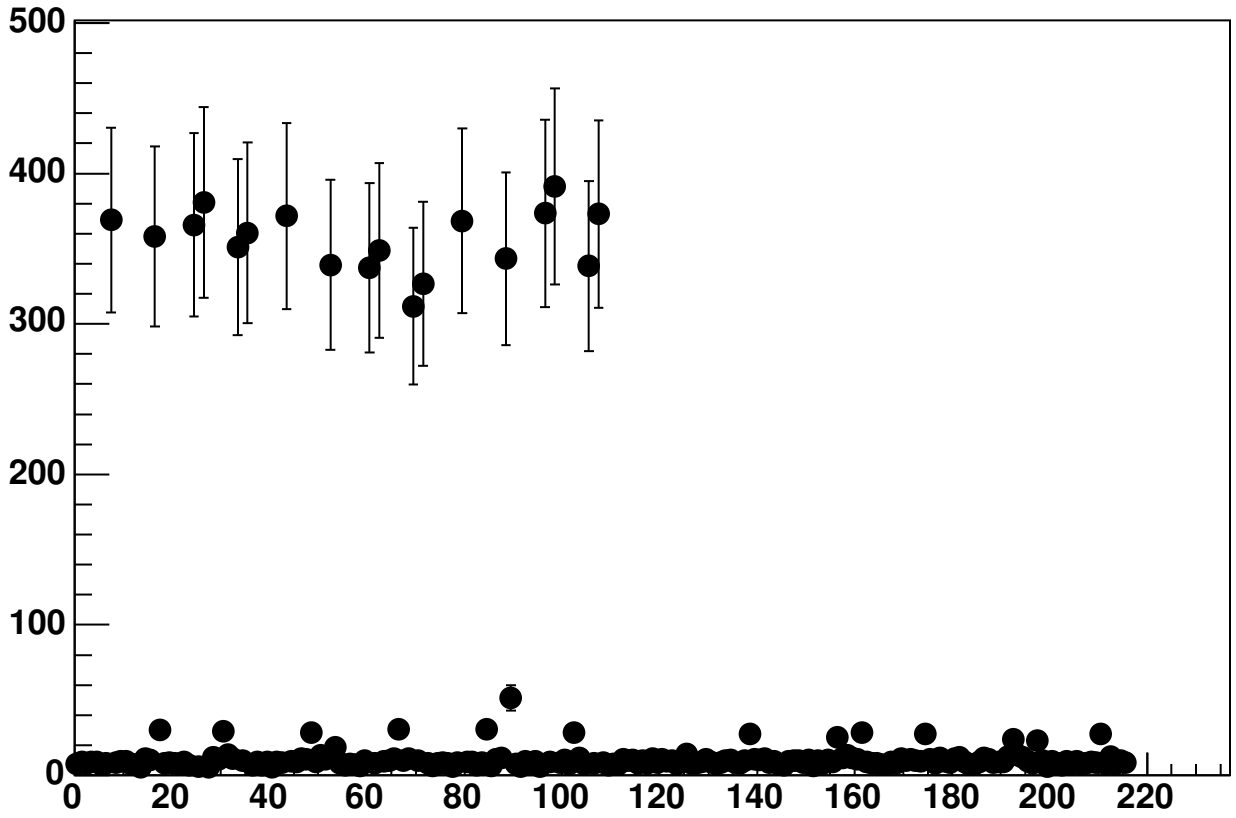
Enable 5, DAC=4000, Hold=15, ADC Noise vs 18*Chip+Chan



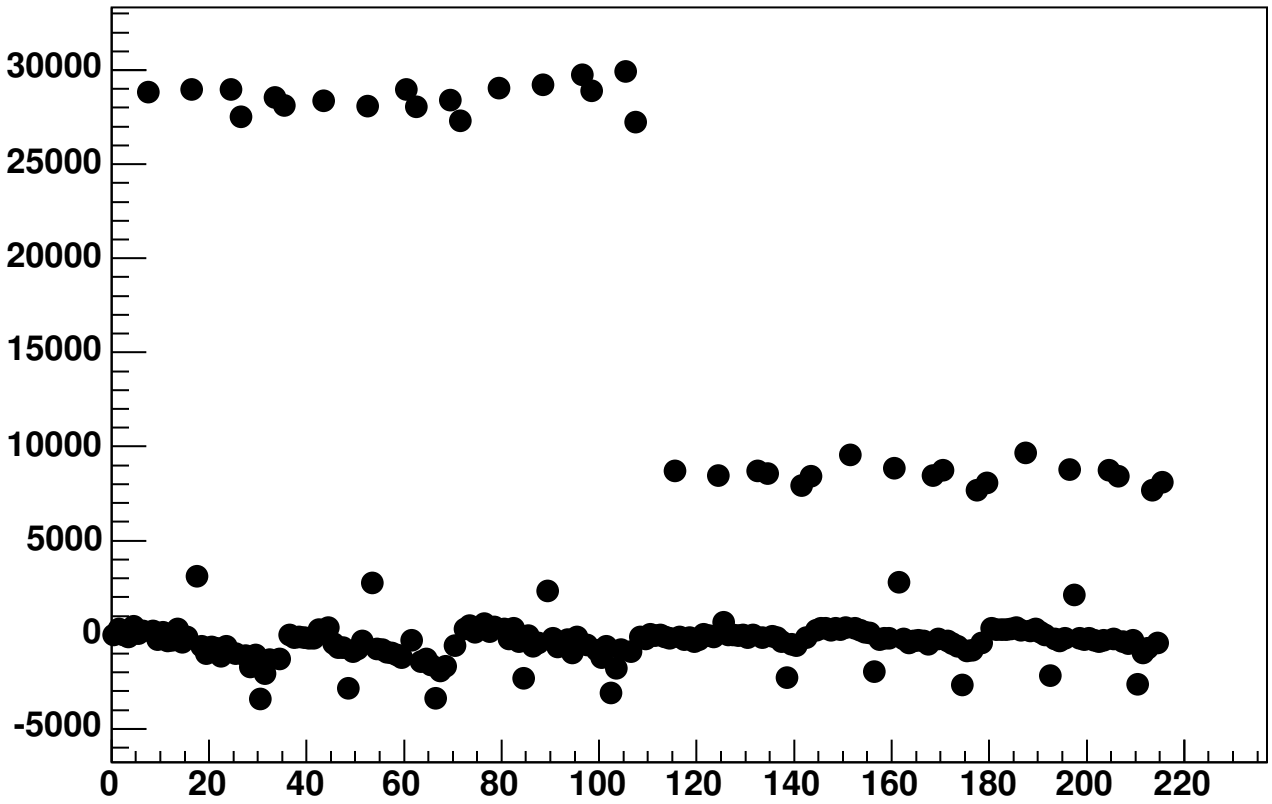
Enable 5, DAC=4000, Hold=20, ADC Mean vs 18*Chip+Chan



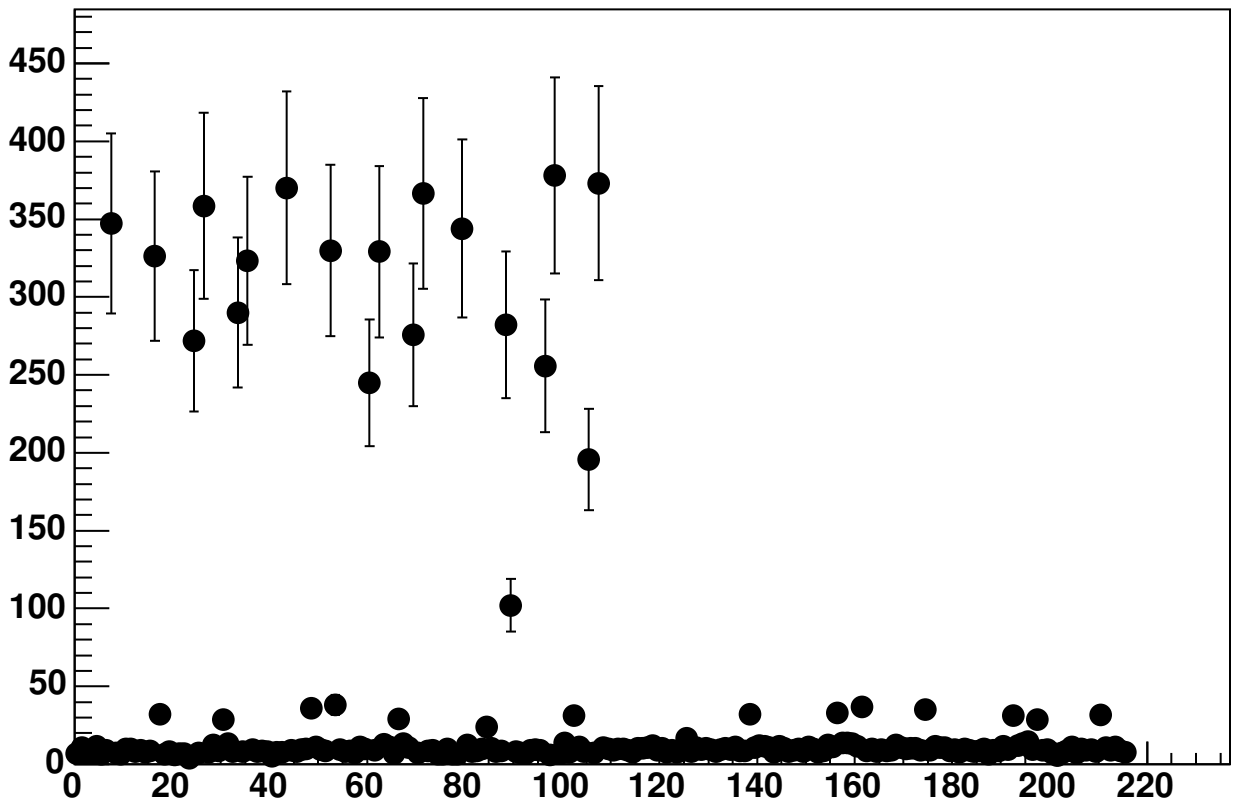
Enable 5, DAC=4000, Hold=20, ADC Noise vs 18*Chip+Chan



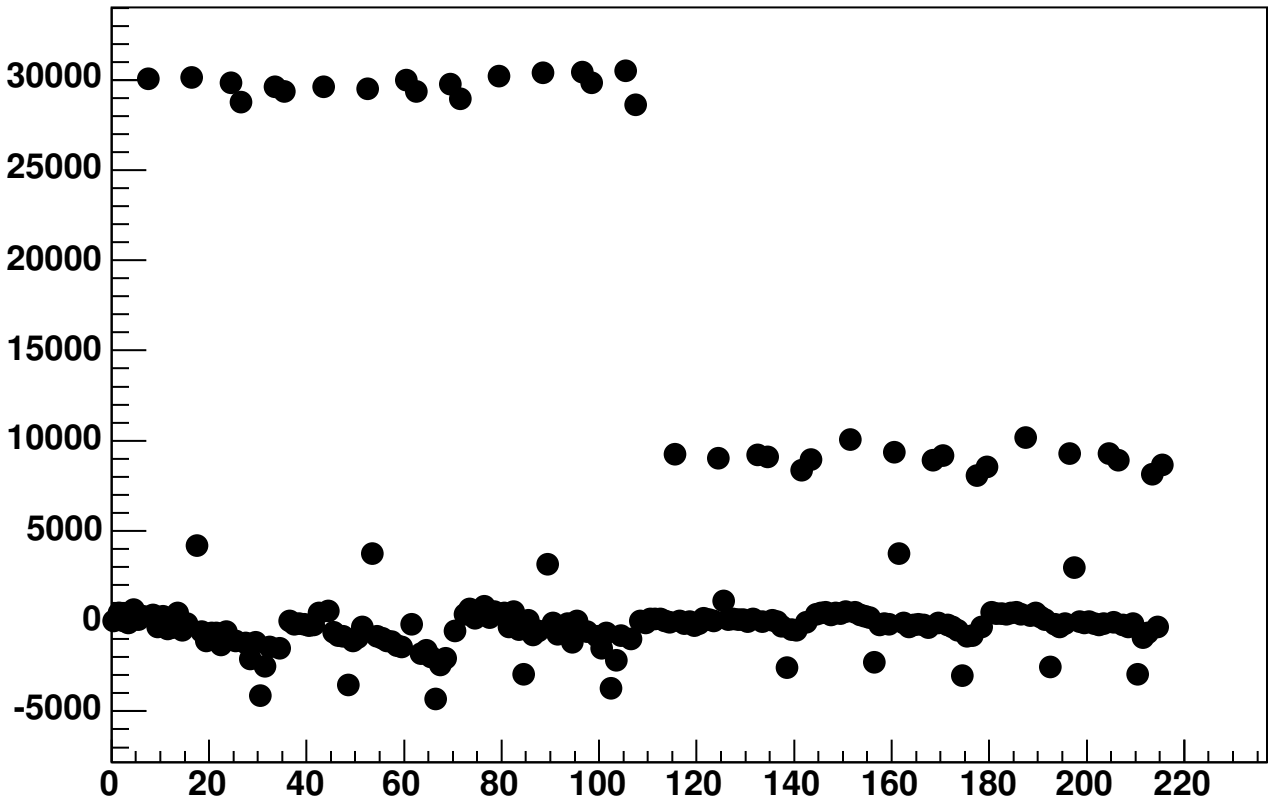
Enable 5, DAC=4000, Hold=25, ADC Mean vs 18*Chip+Chan



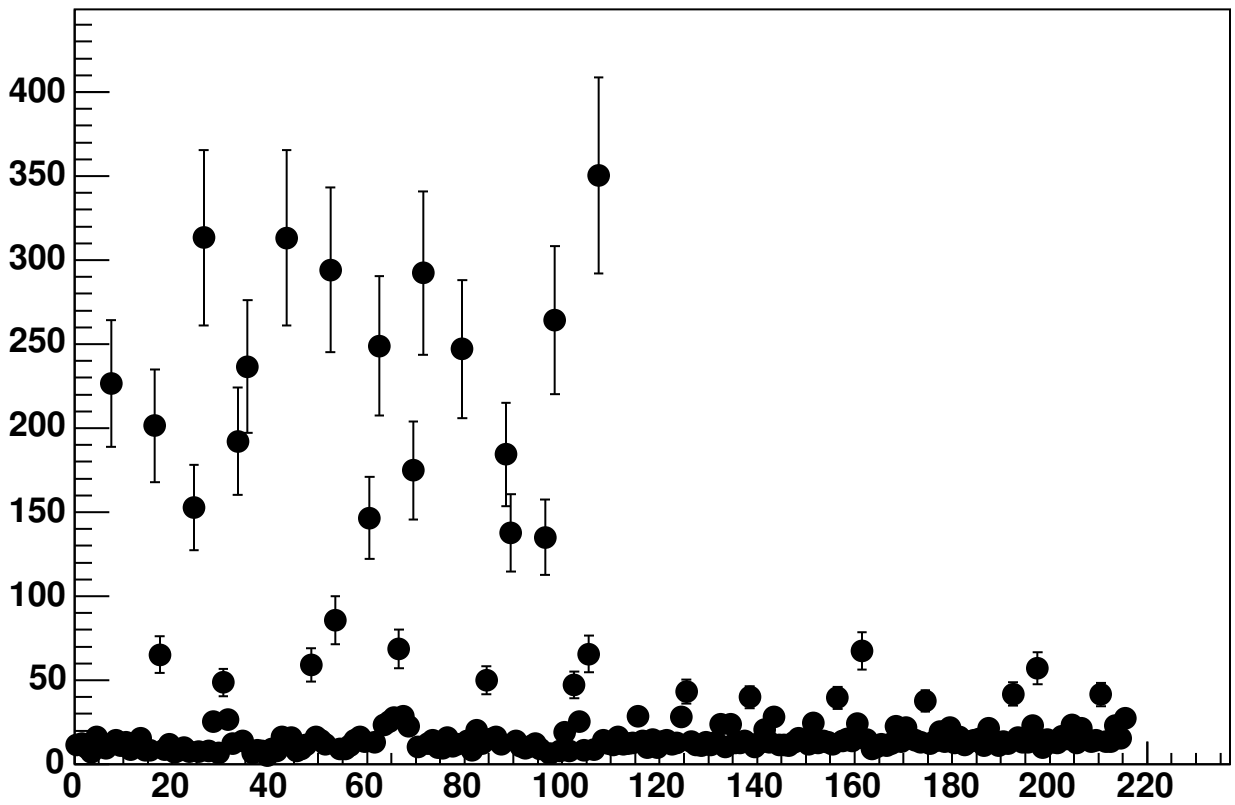
Enable 5, DAC=4000, Hold=25, ADC Noise vs 18*Chip+Chan



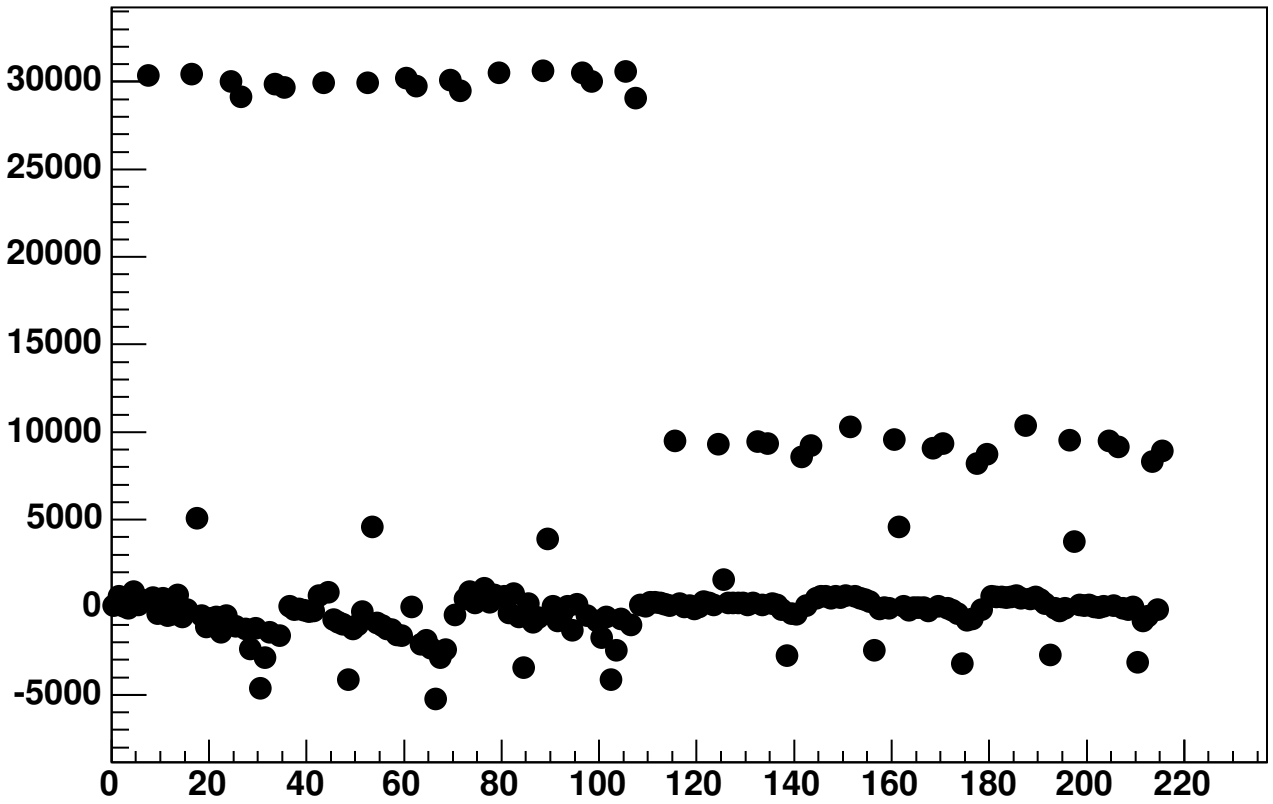
Enable 5, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



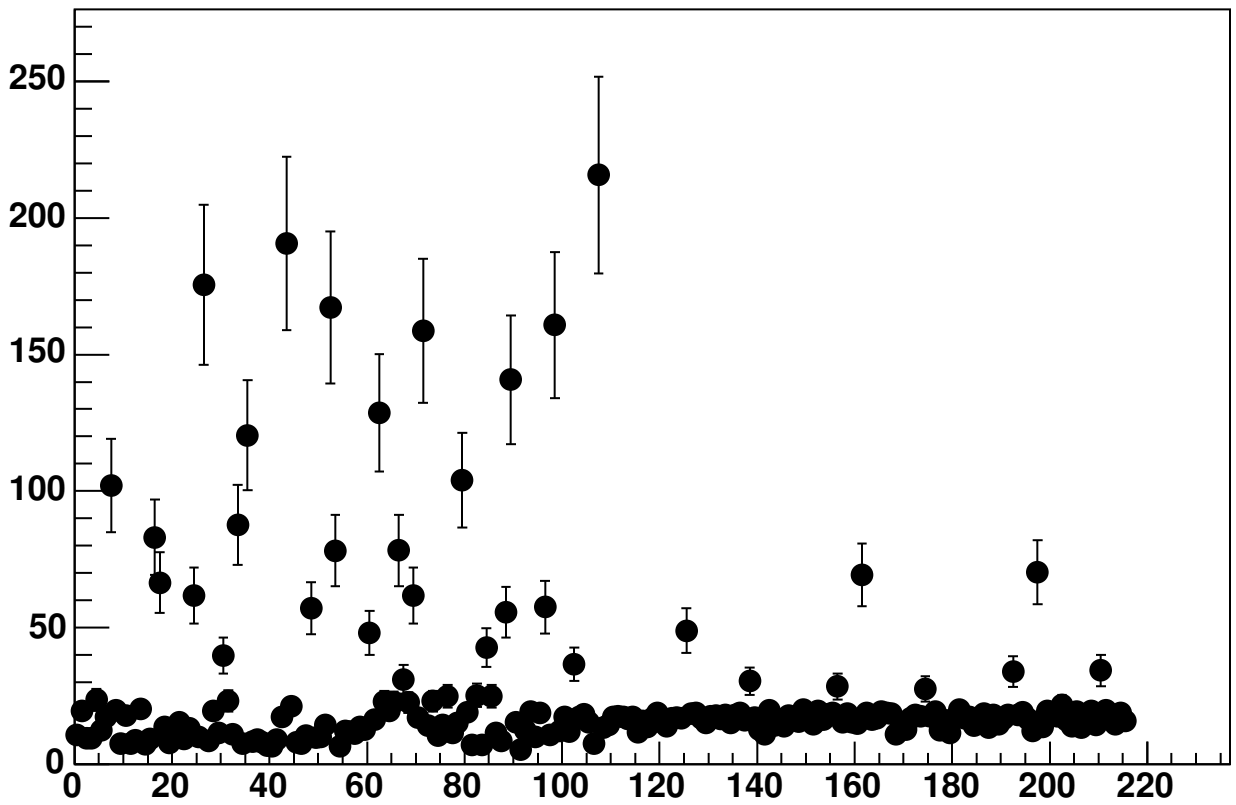
Enable 5, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



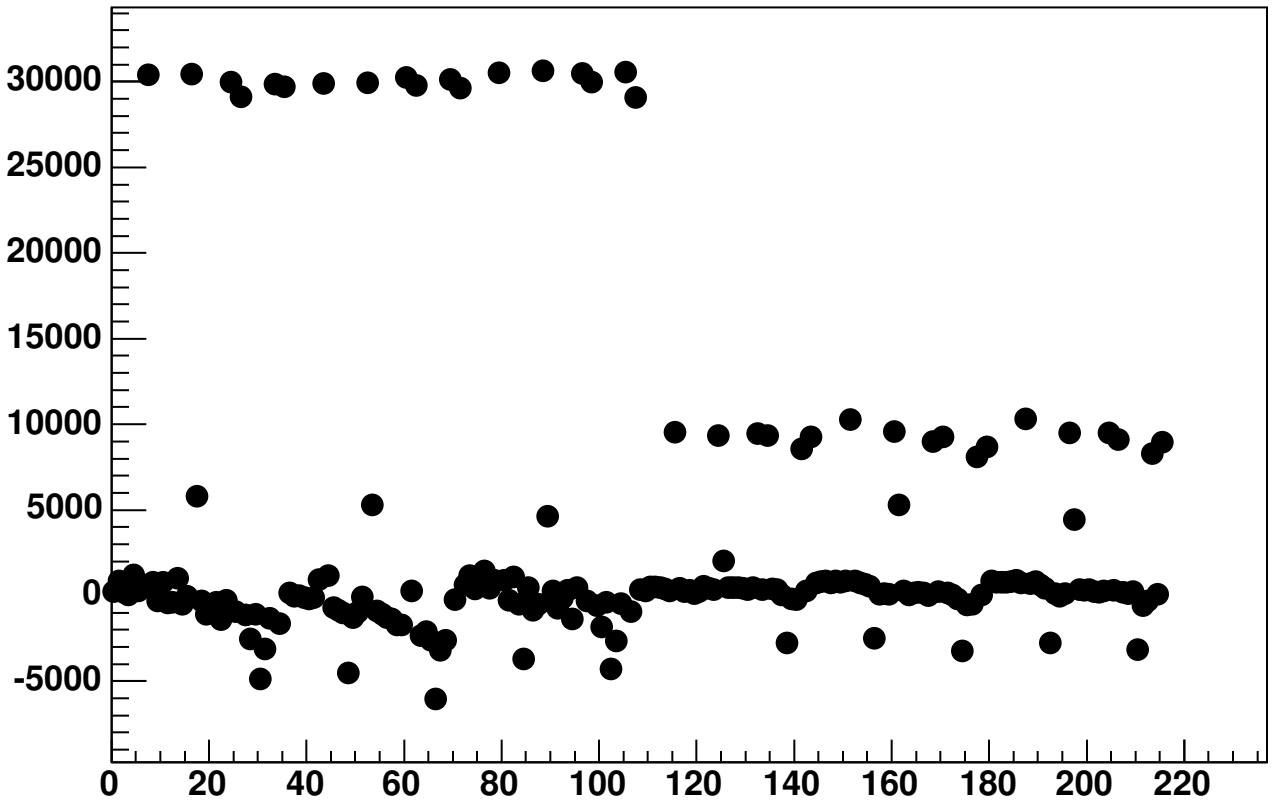
Enable 5, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



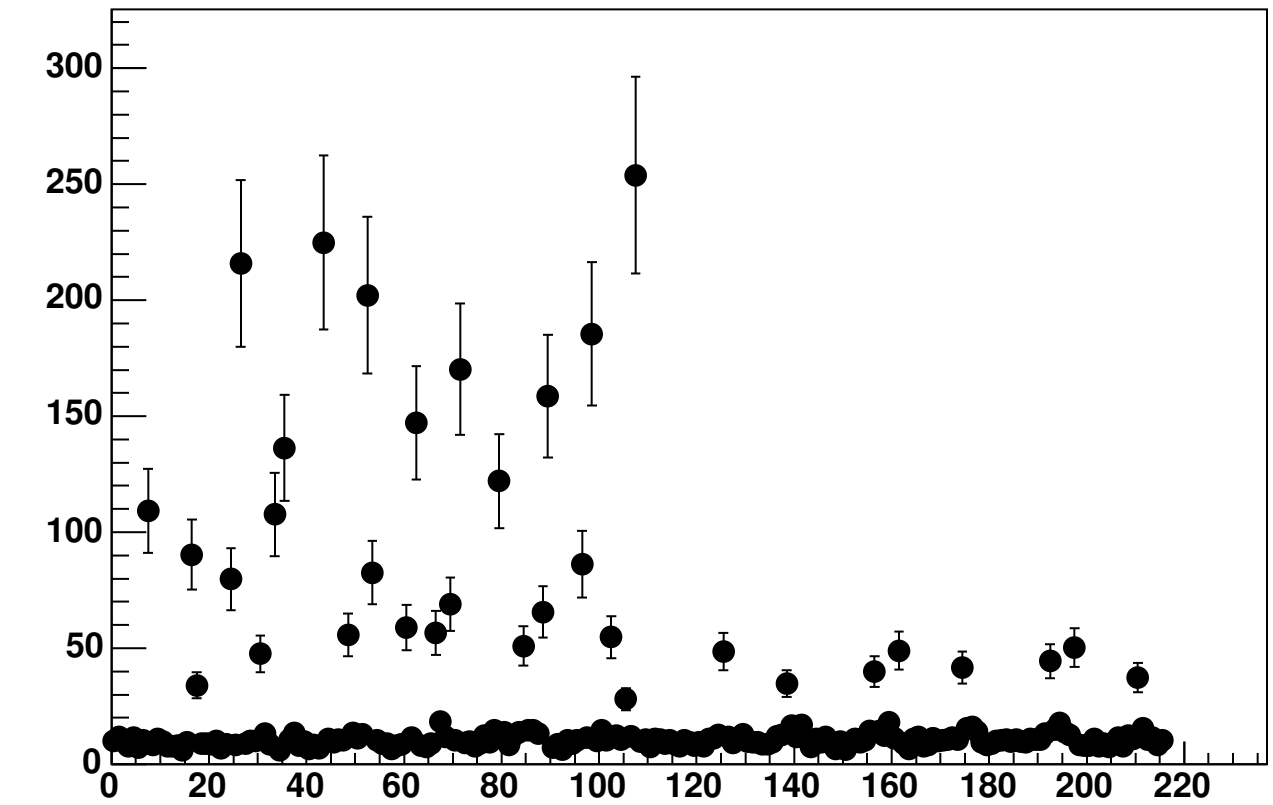
Enable 5, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



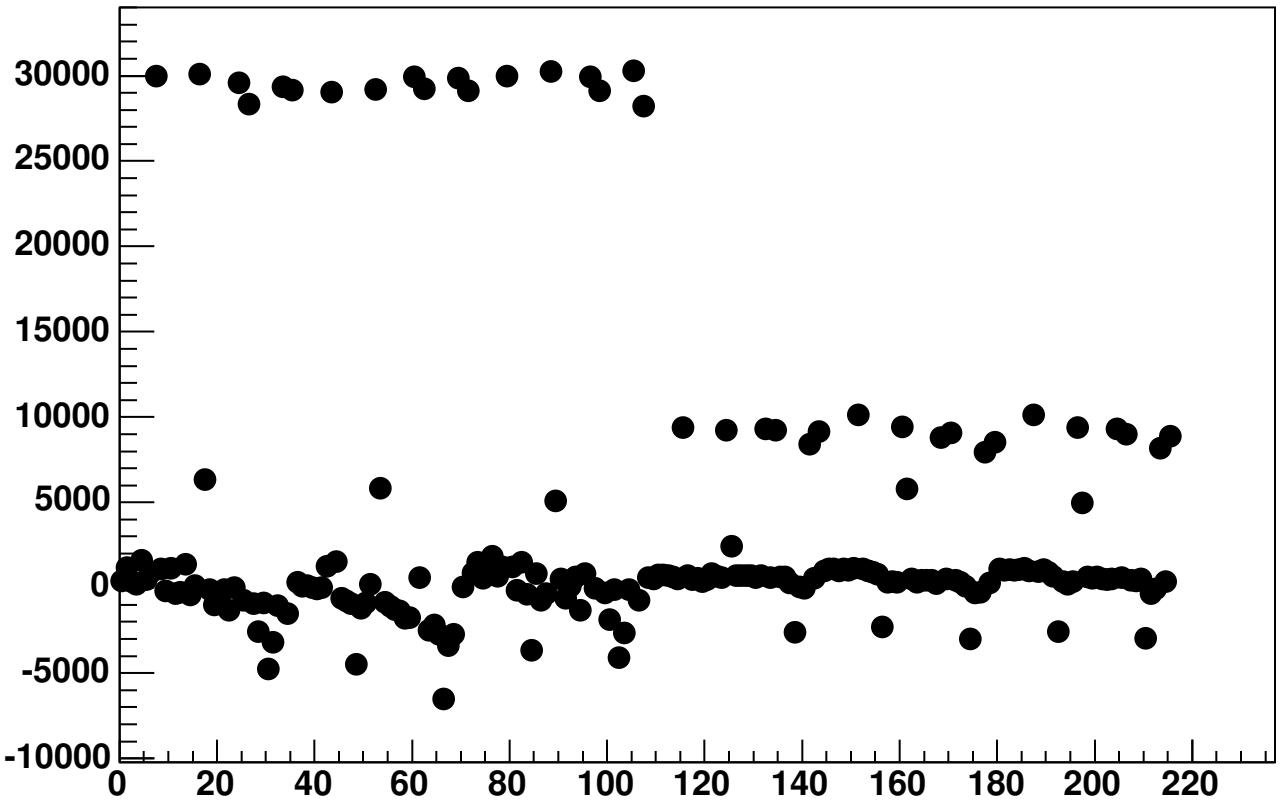
Enable 5, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



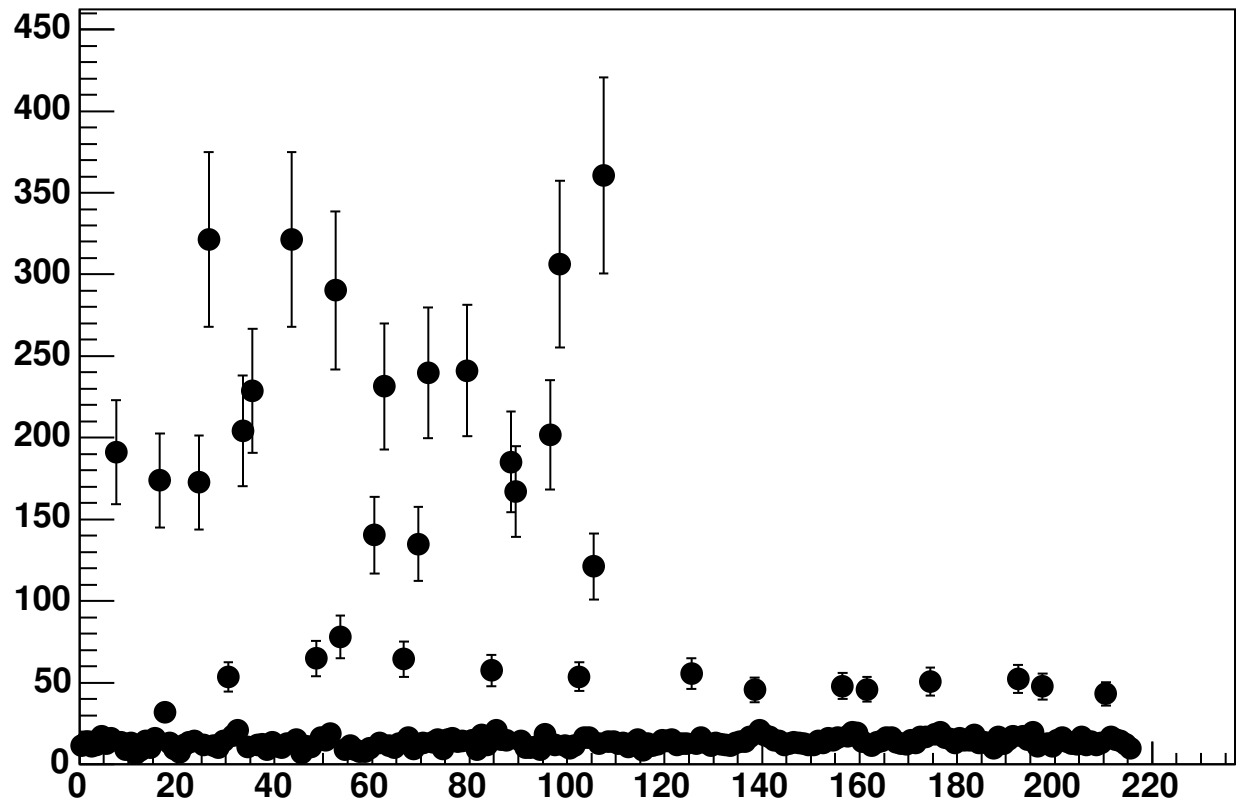
Enable 5, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



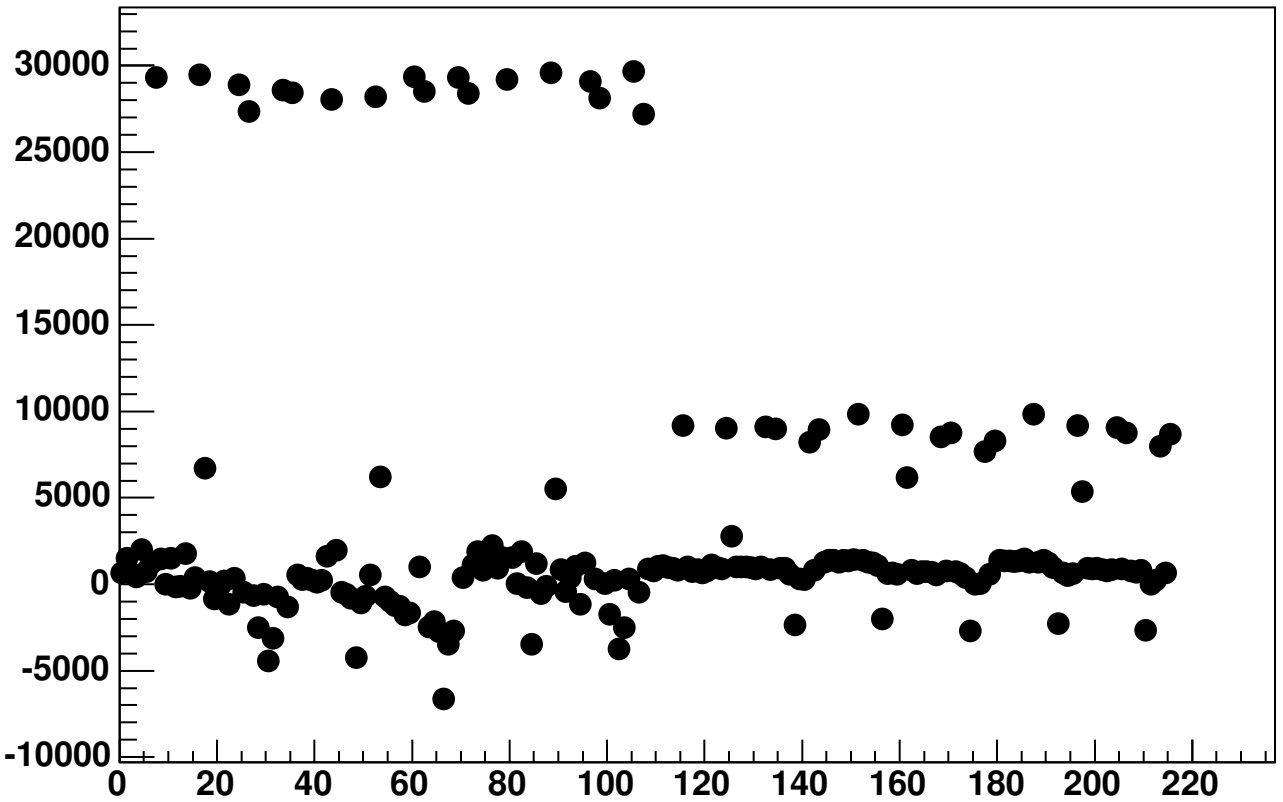
Enable 5, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



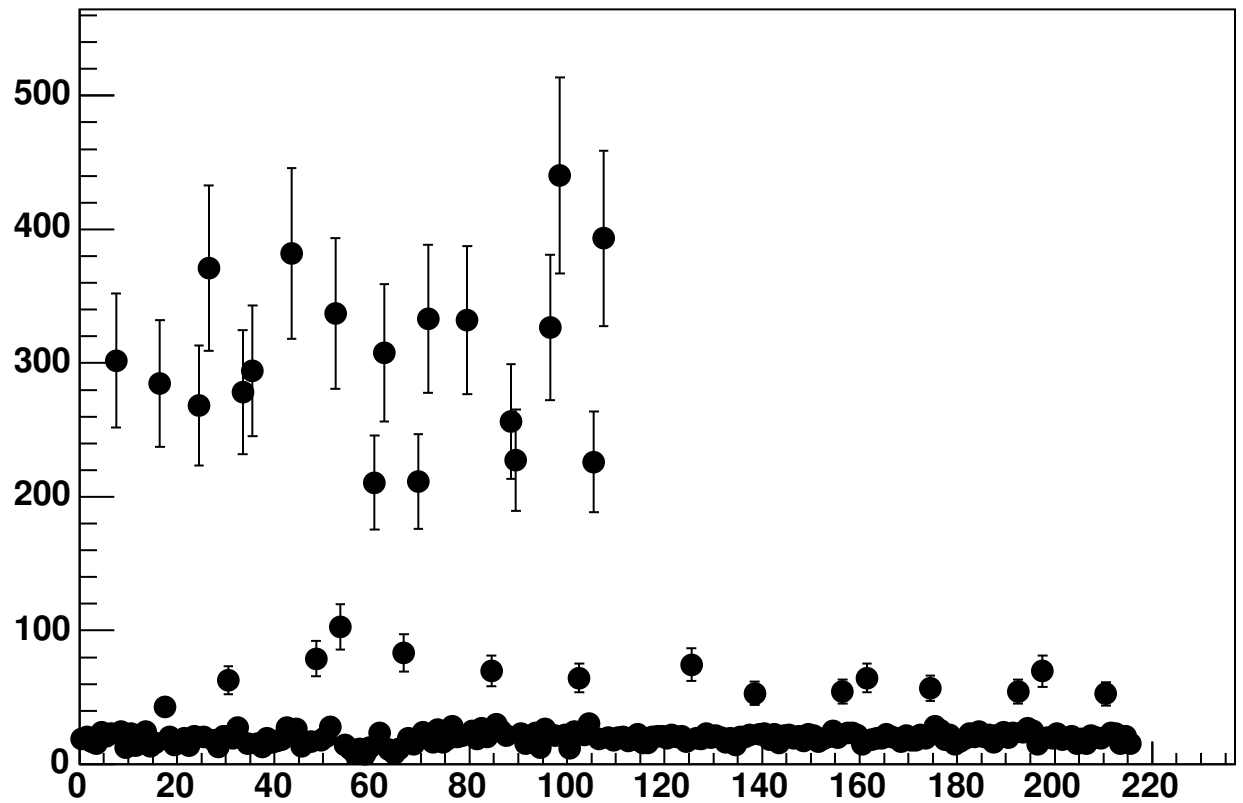
Enable 5, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



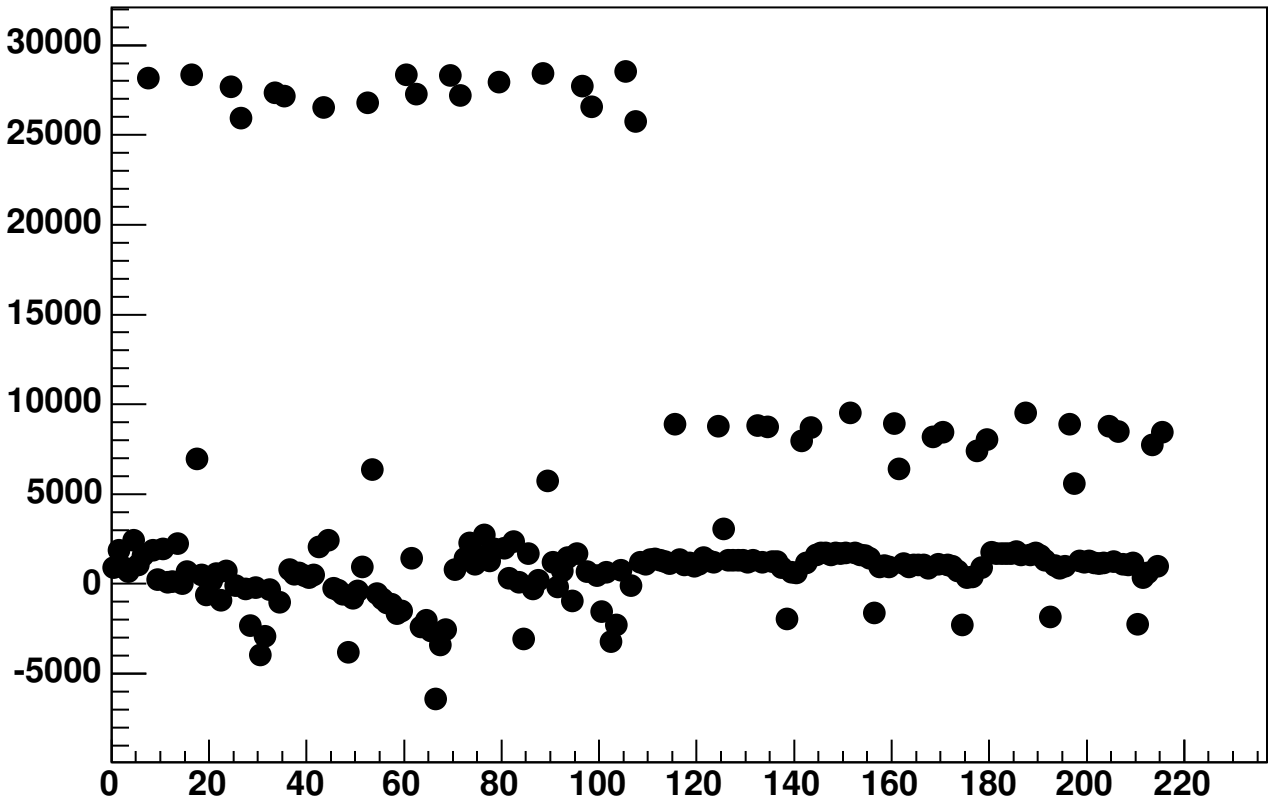
Enable 5, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



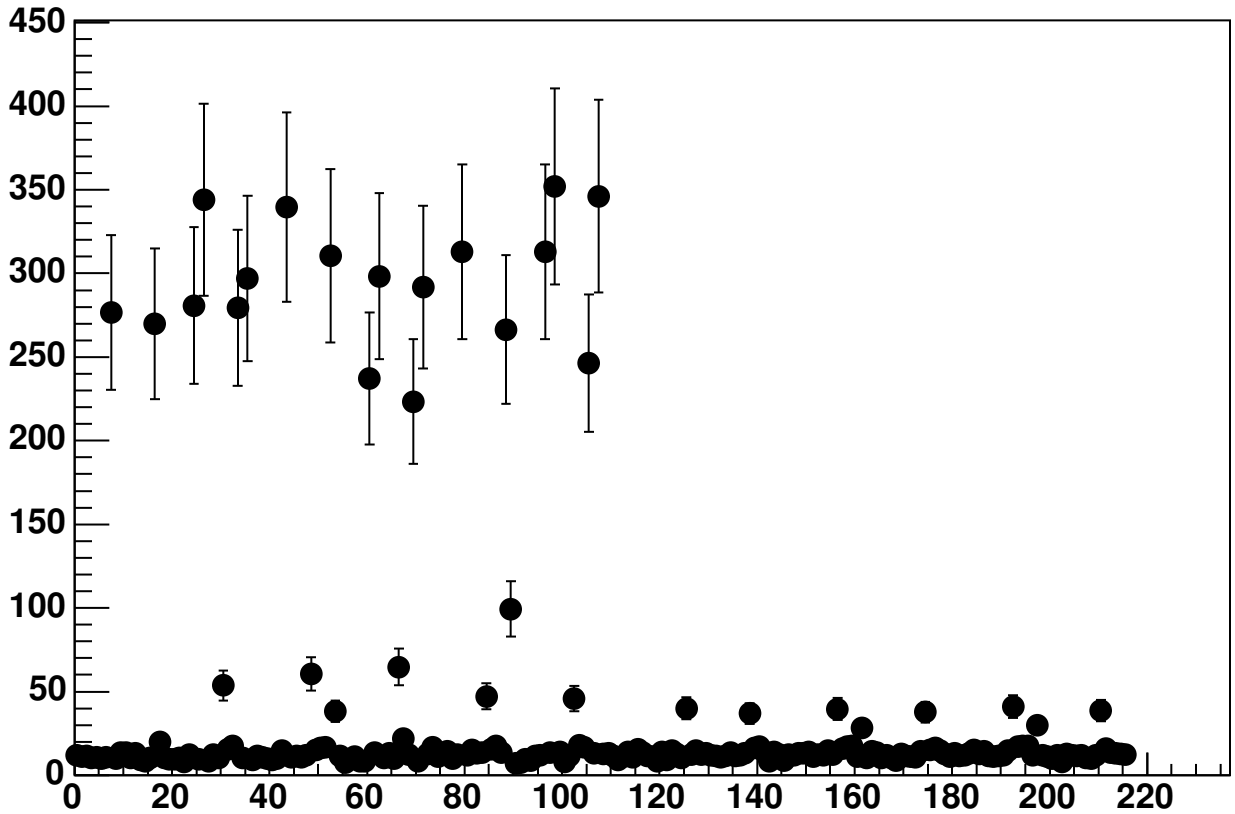
Enable 5, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



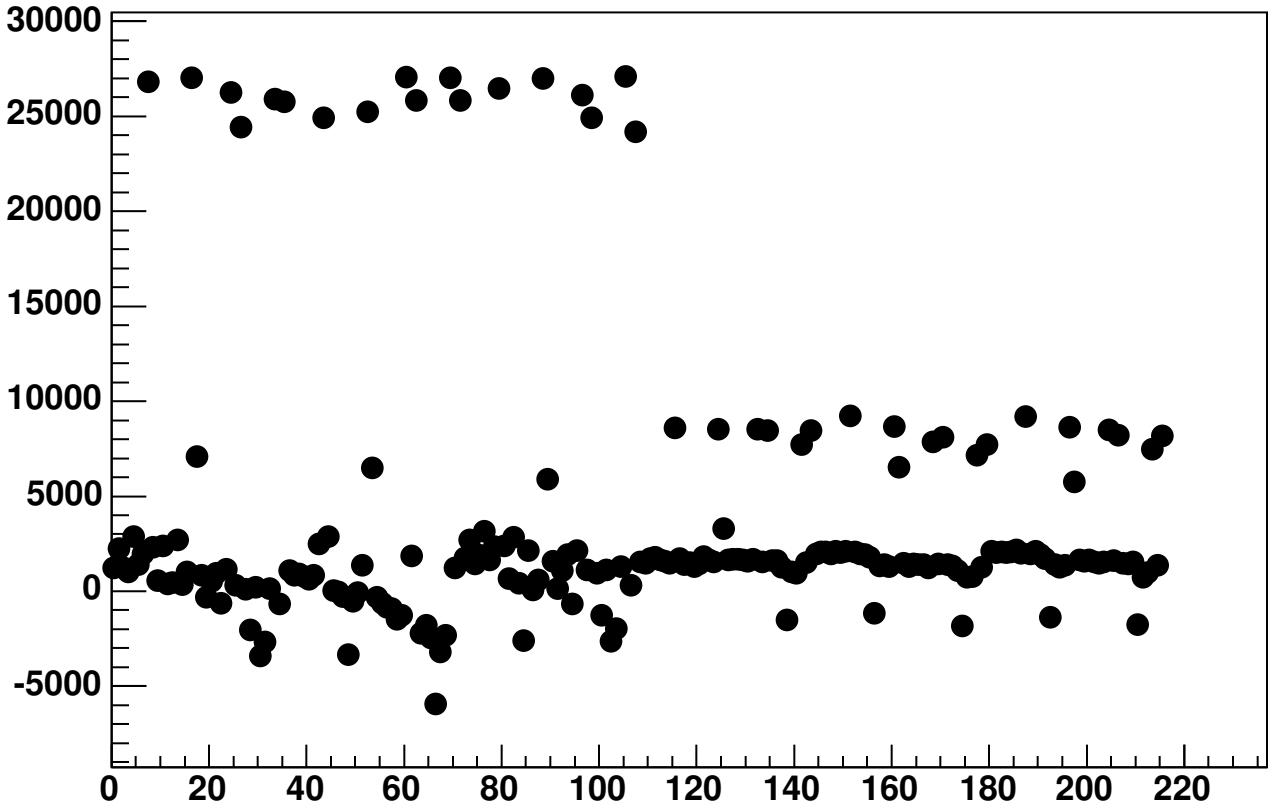
Enable 5, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



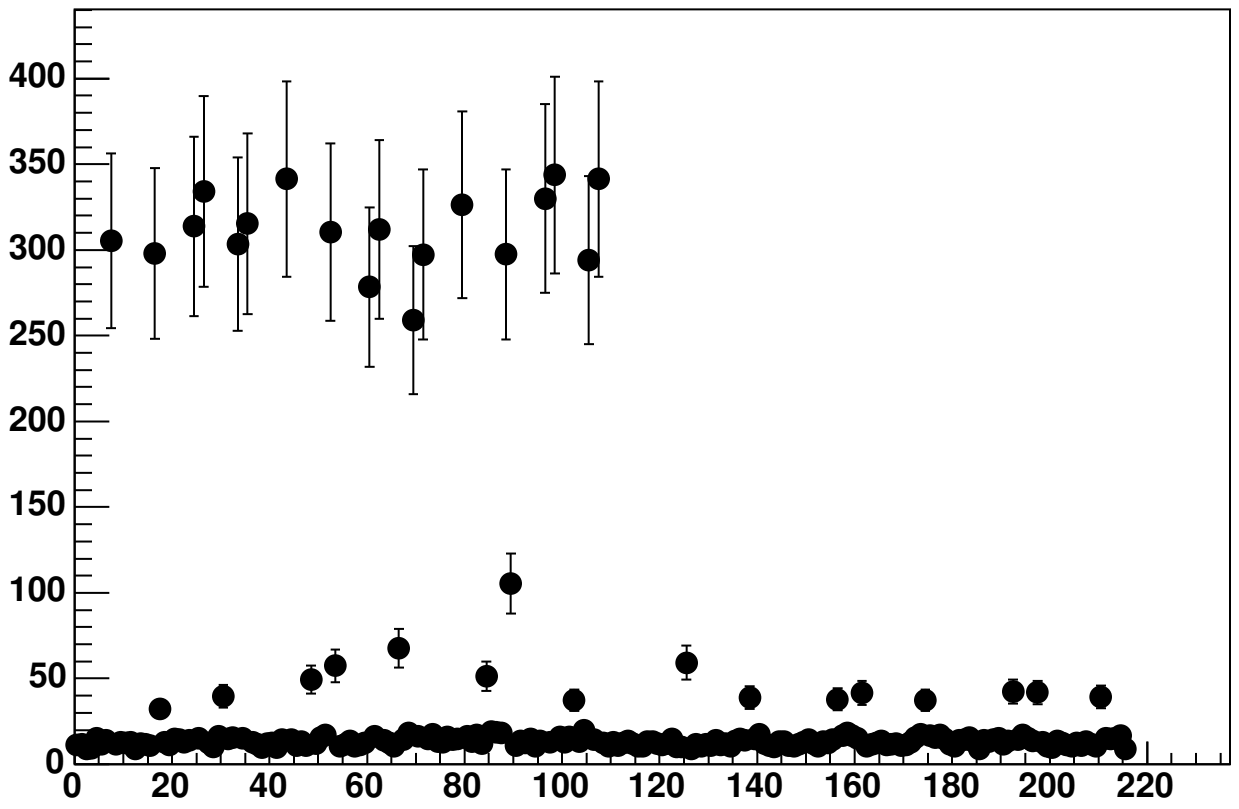
Enable 5, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



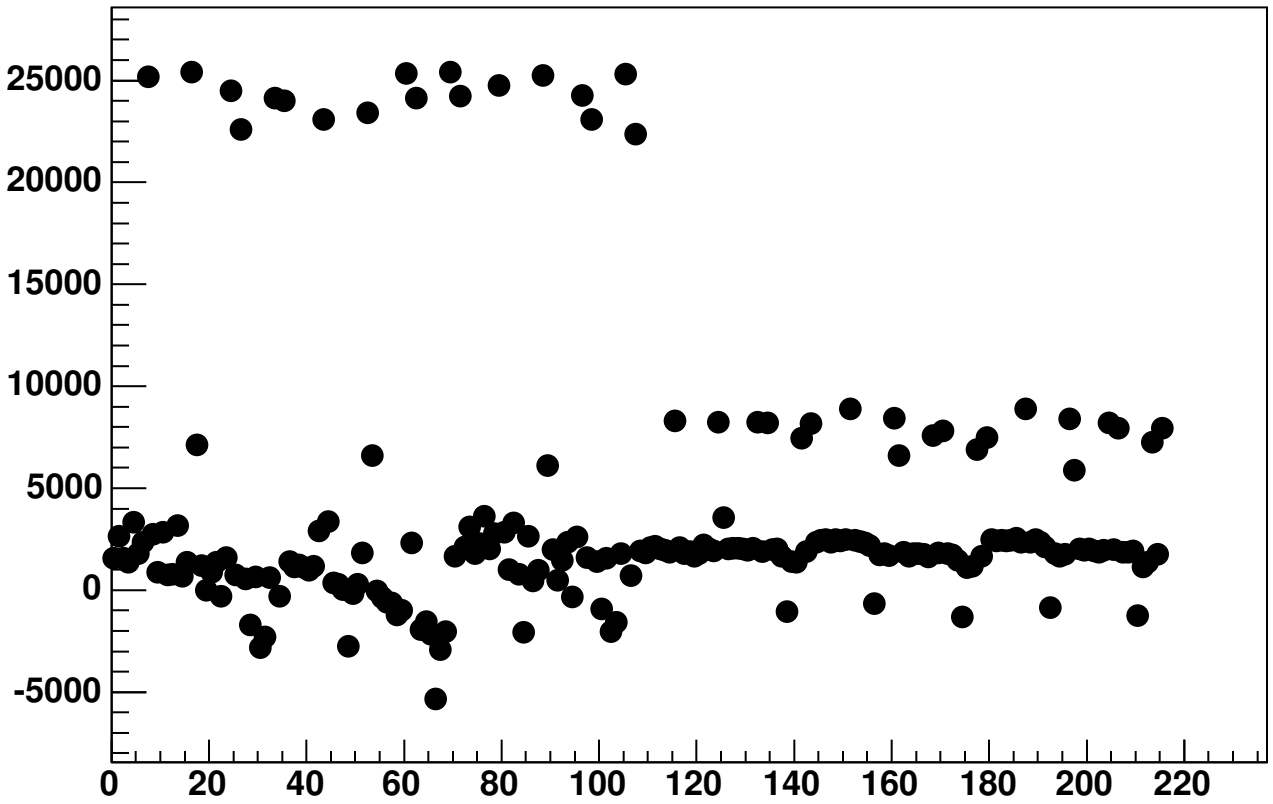
Enable 5, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



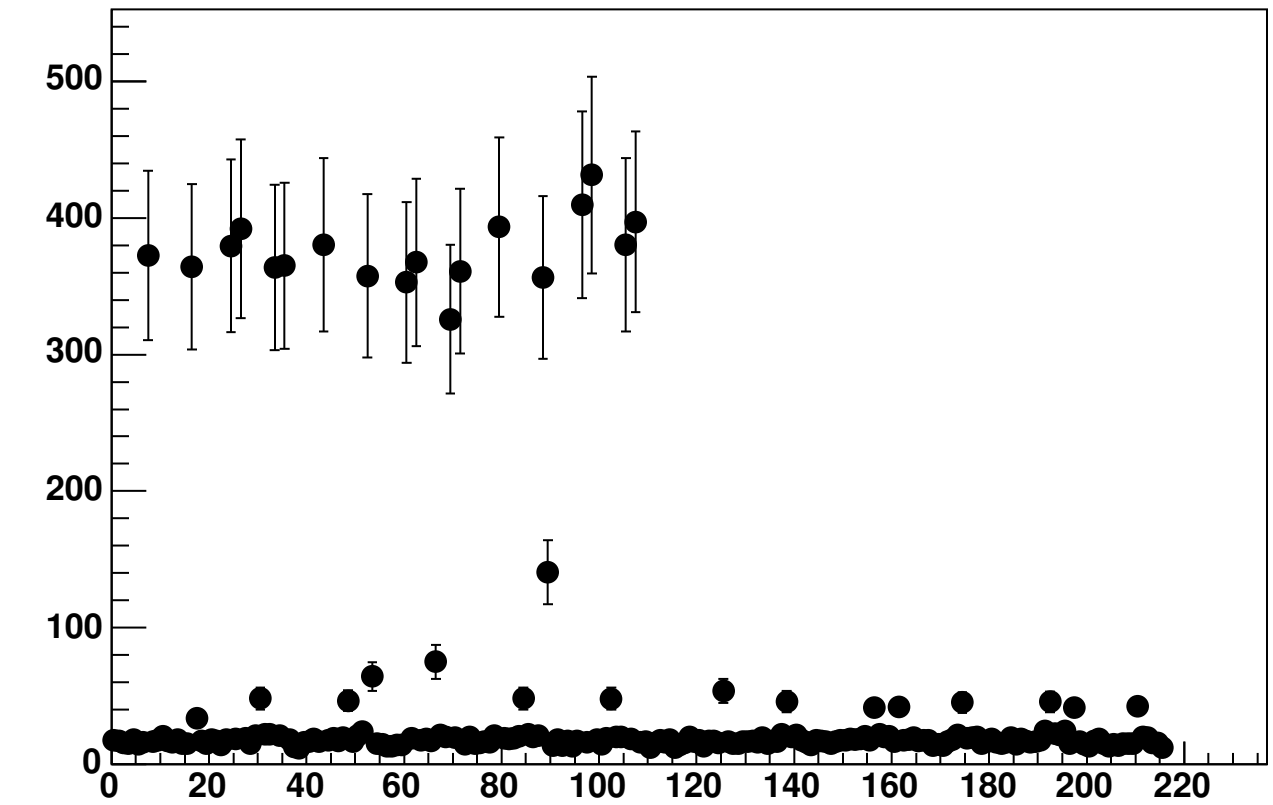
Enable 5, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



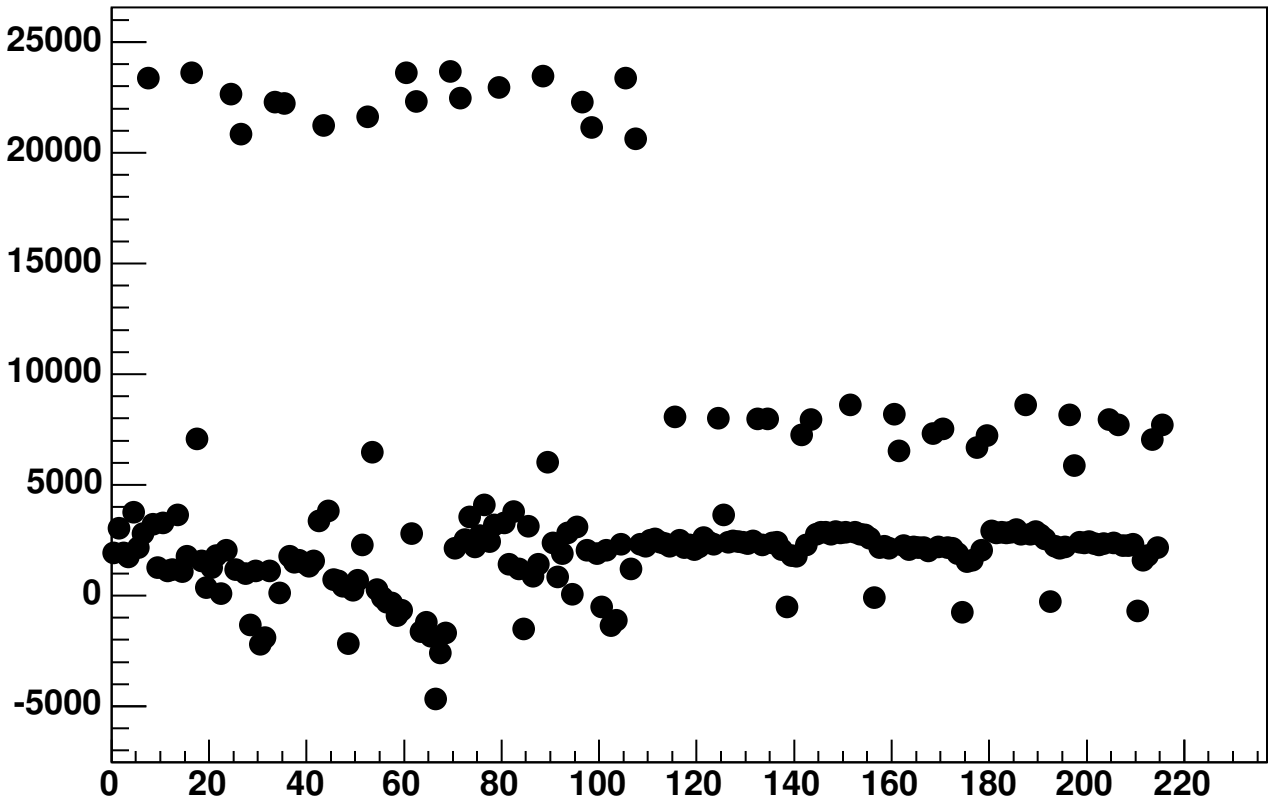
Enable 5, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



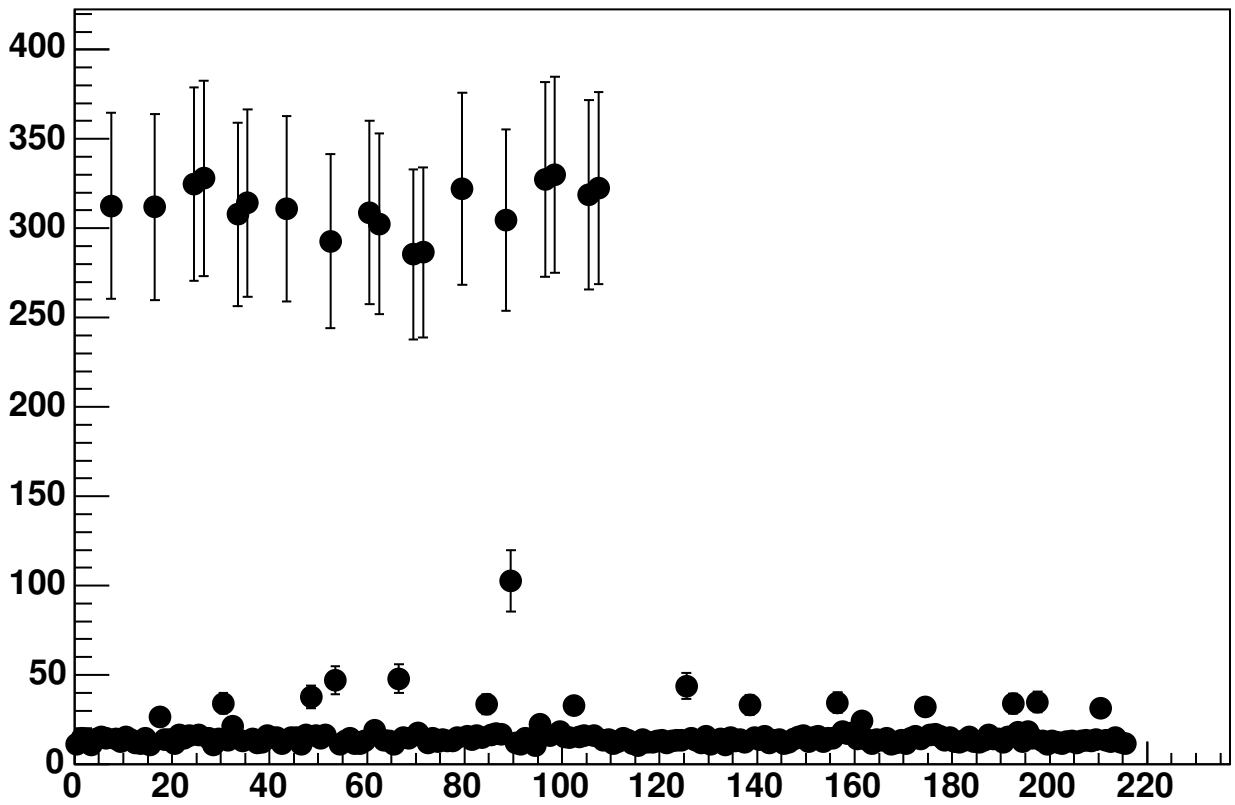
Enable 5, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



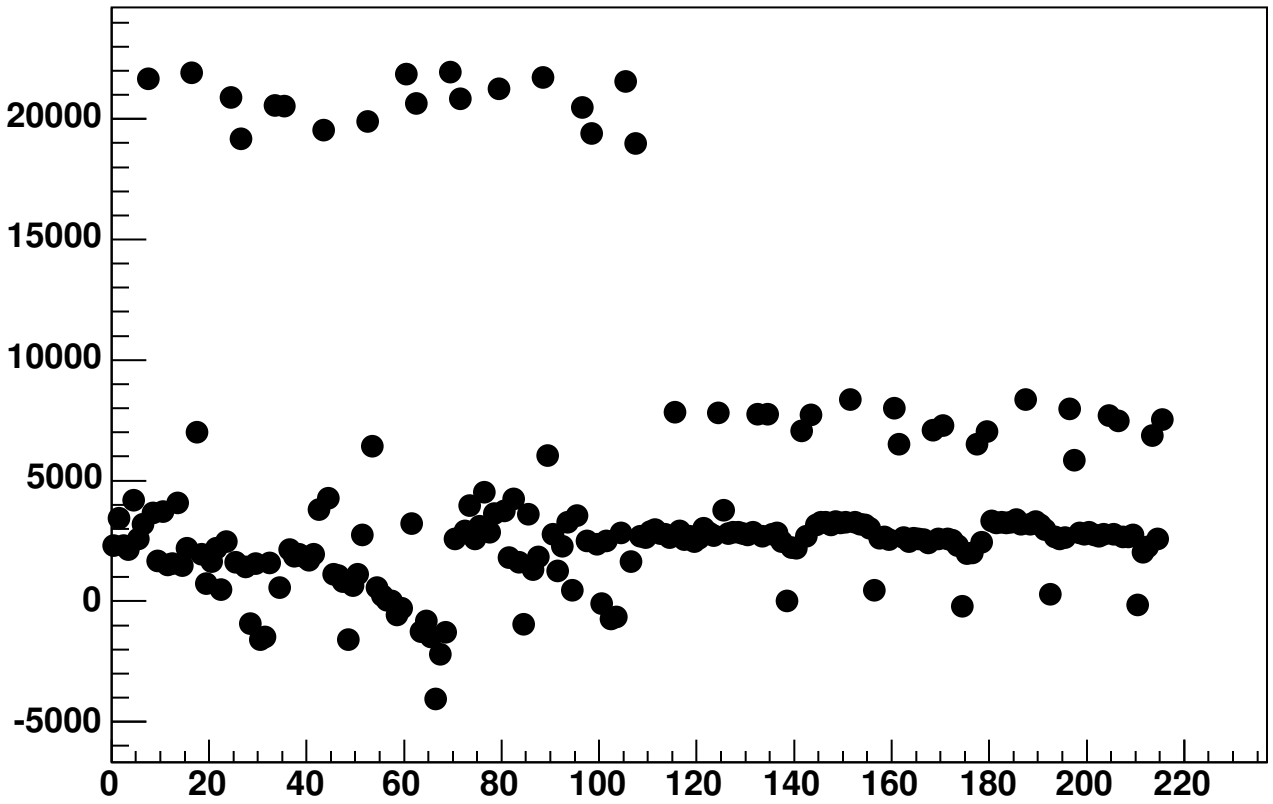
Enable 5, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



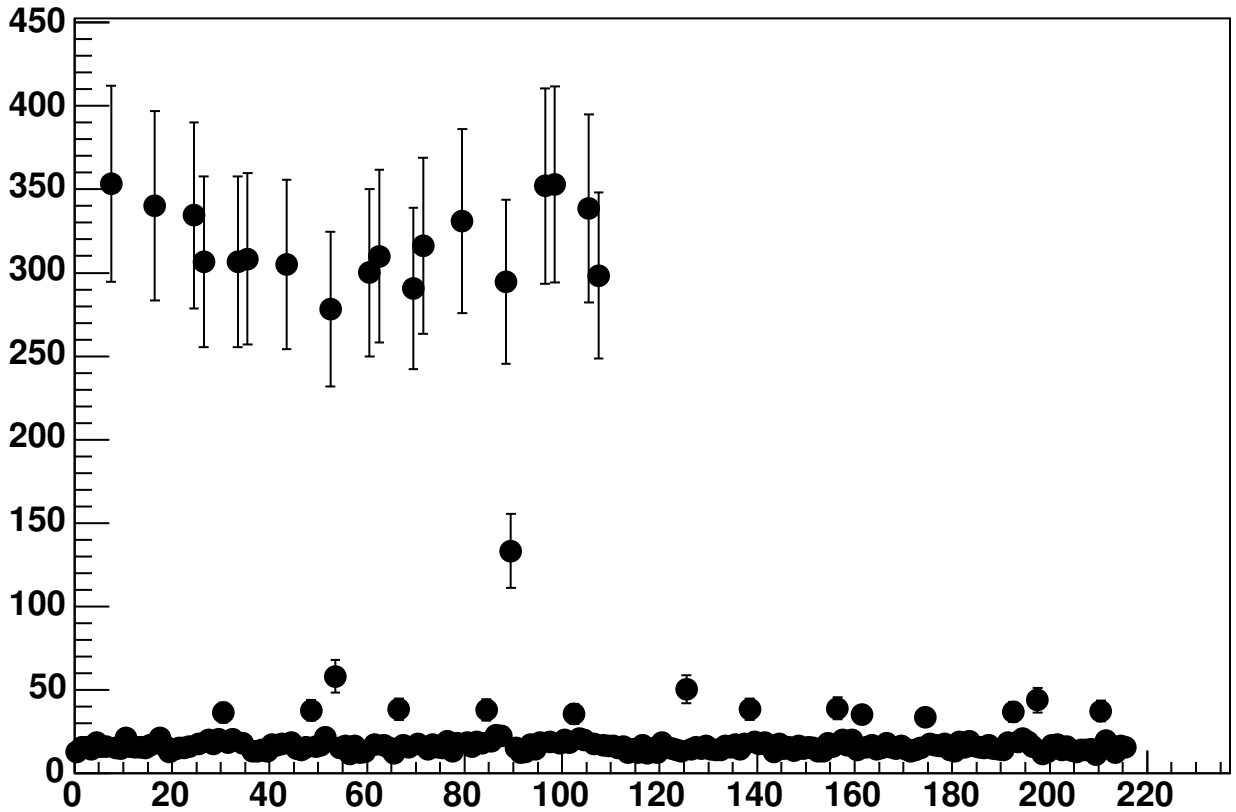
Enable 5, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



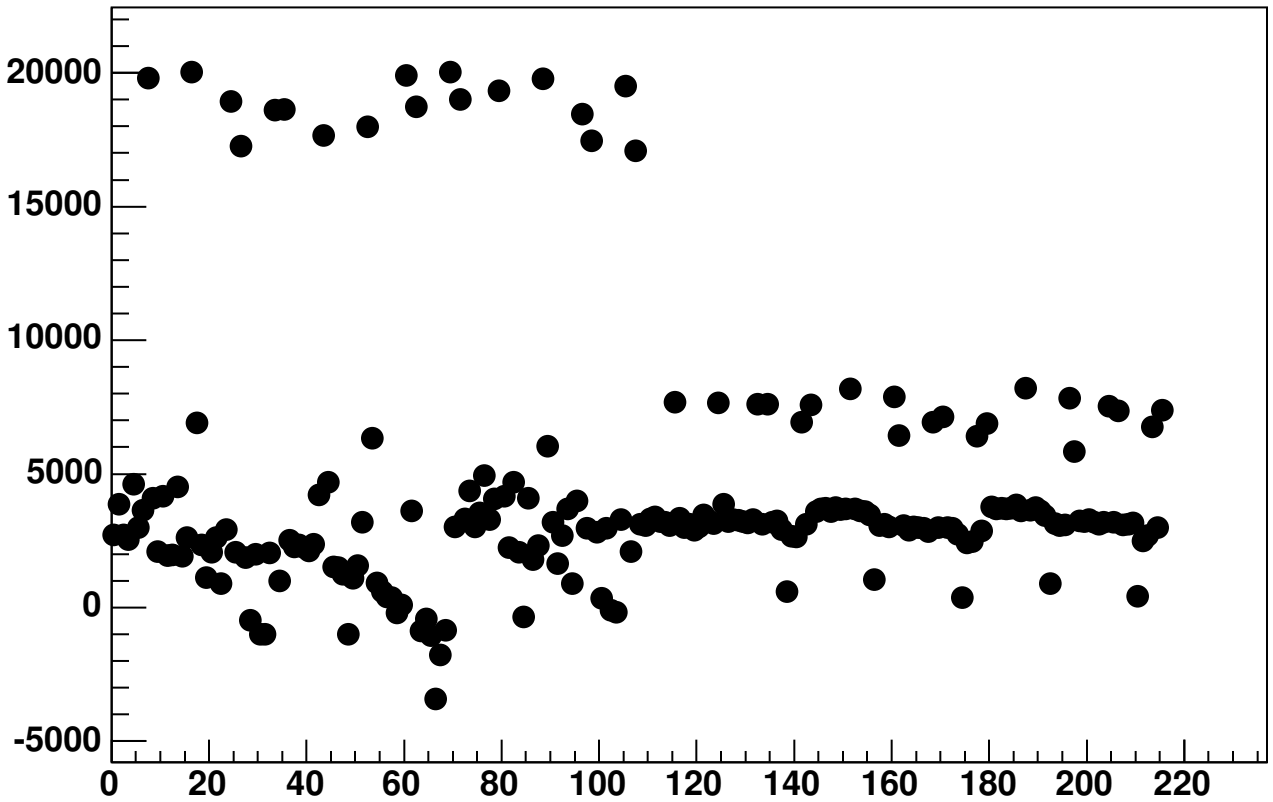
Enable 5, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



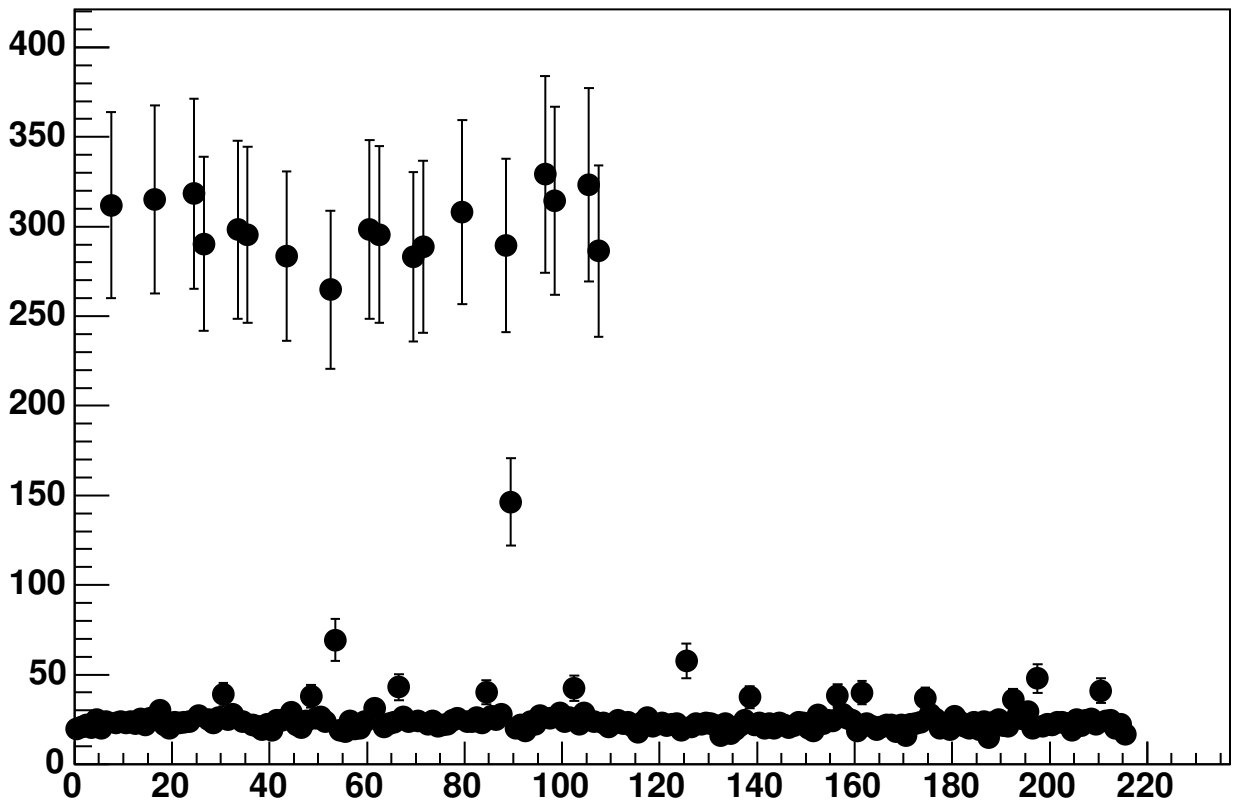
Enable 5, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



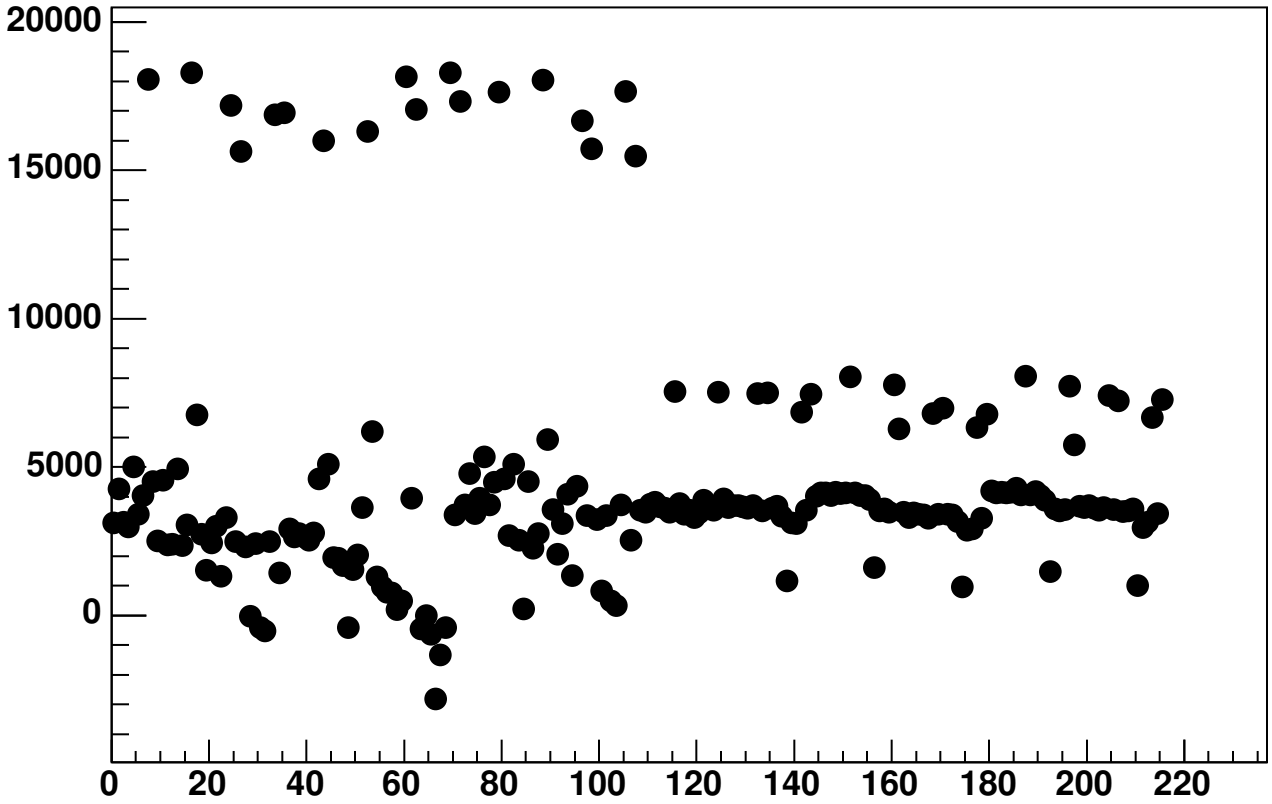
Enable 5, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



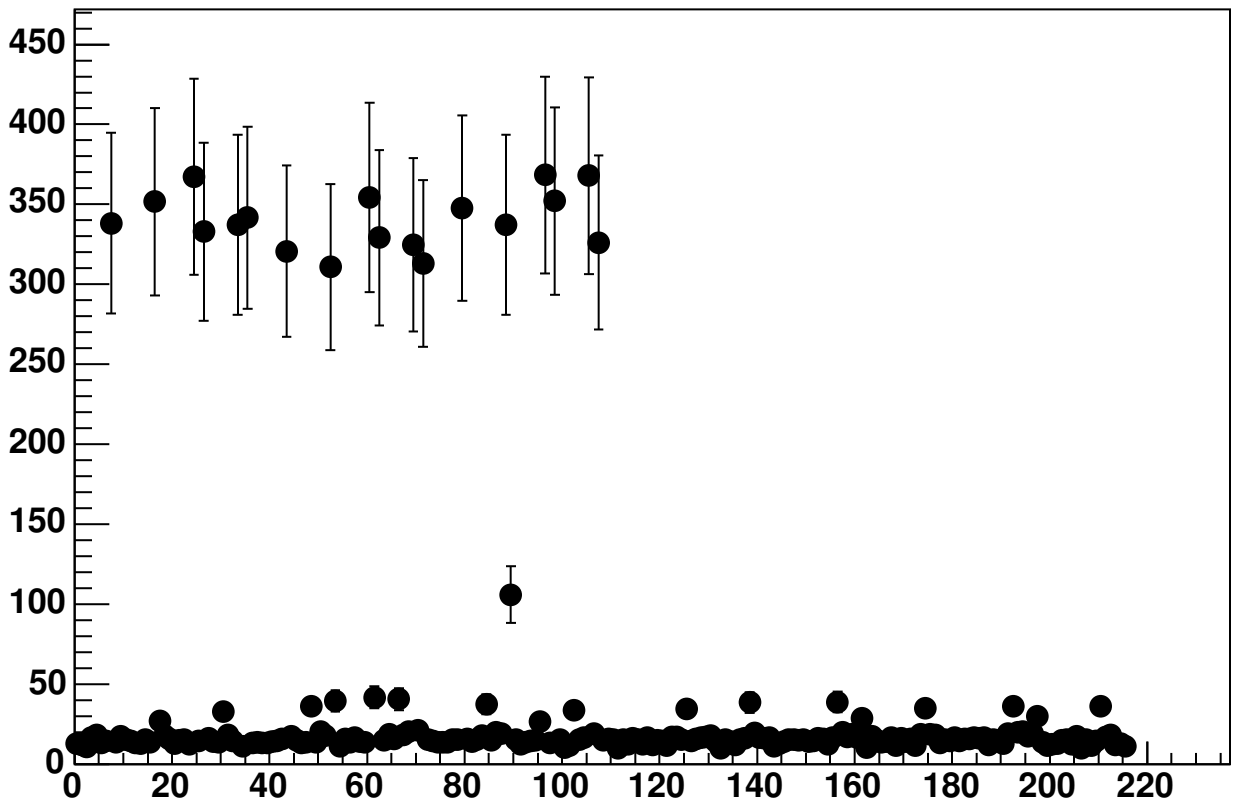
Enable 5, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



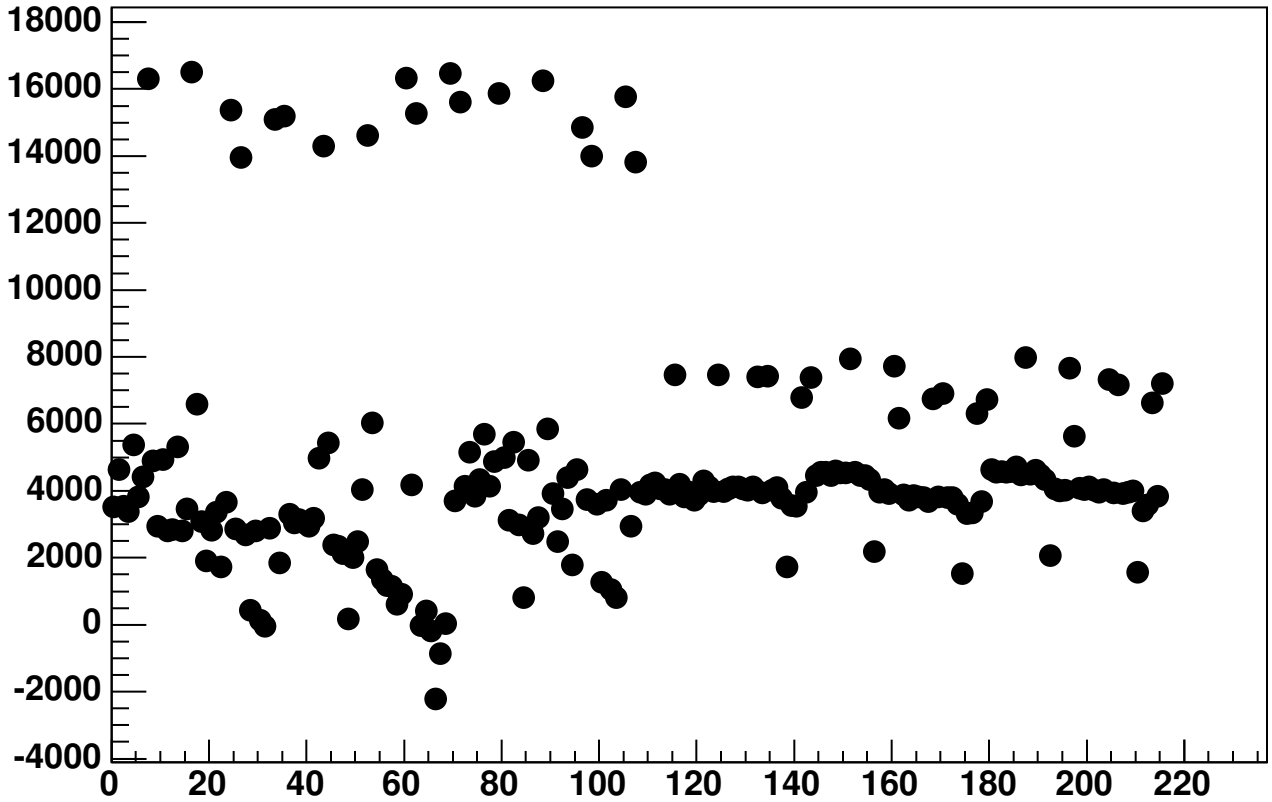
Enable 5, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



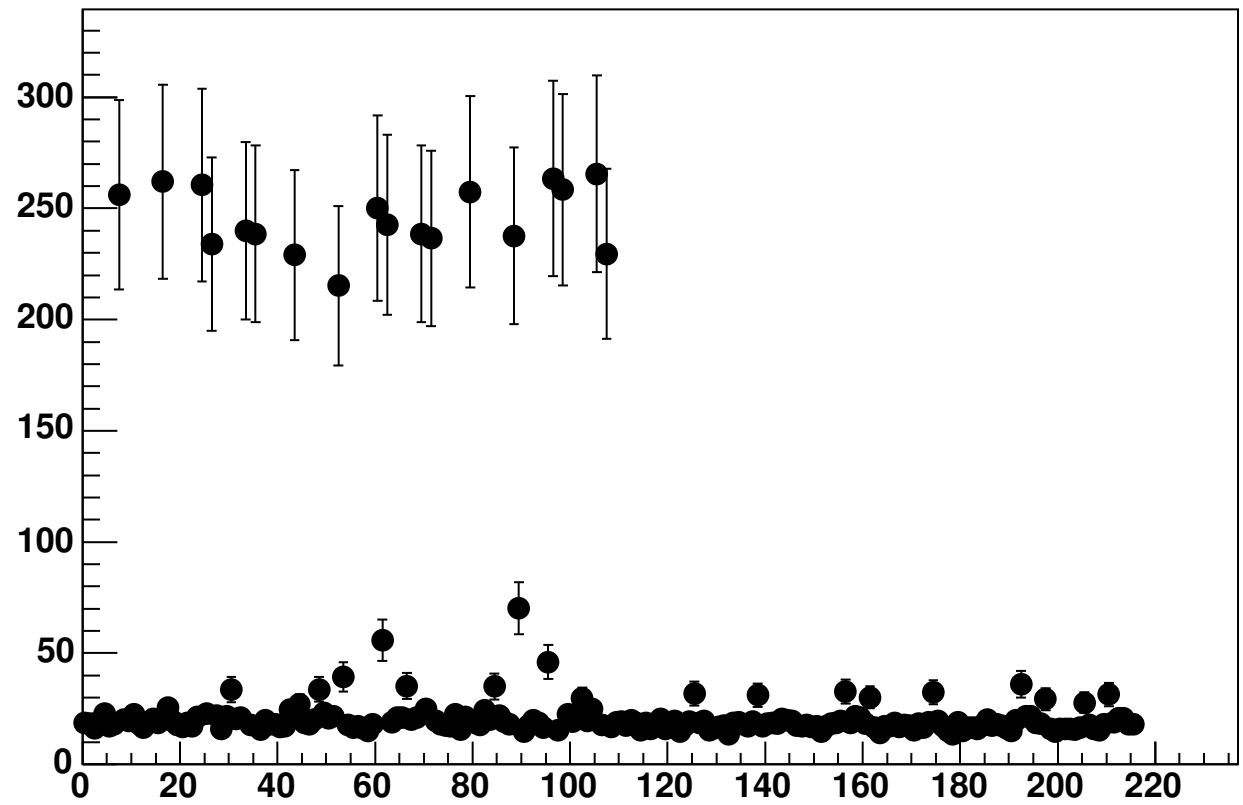
Enable 5, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



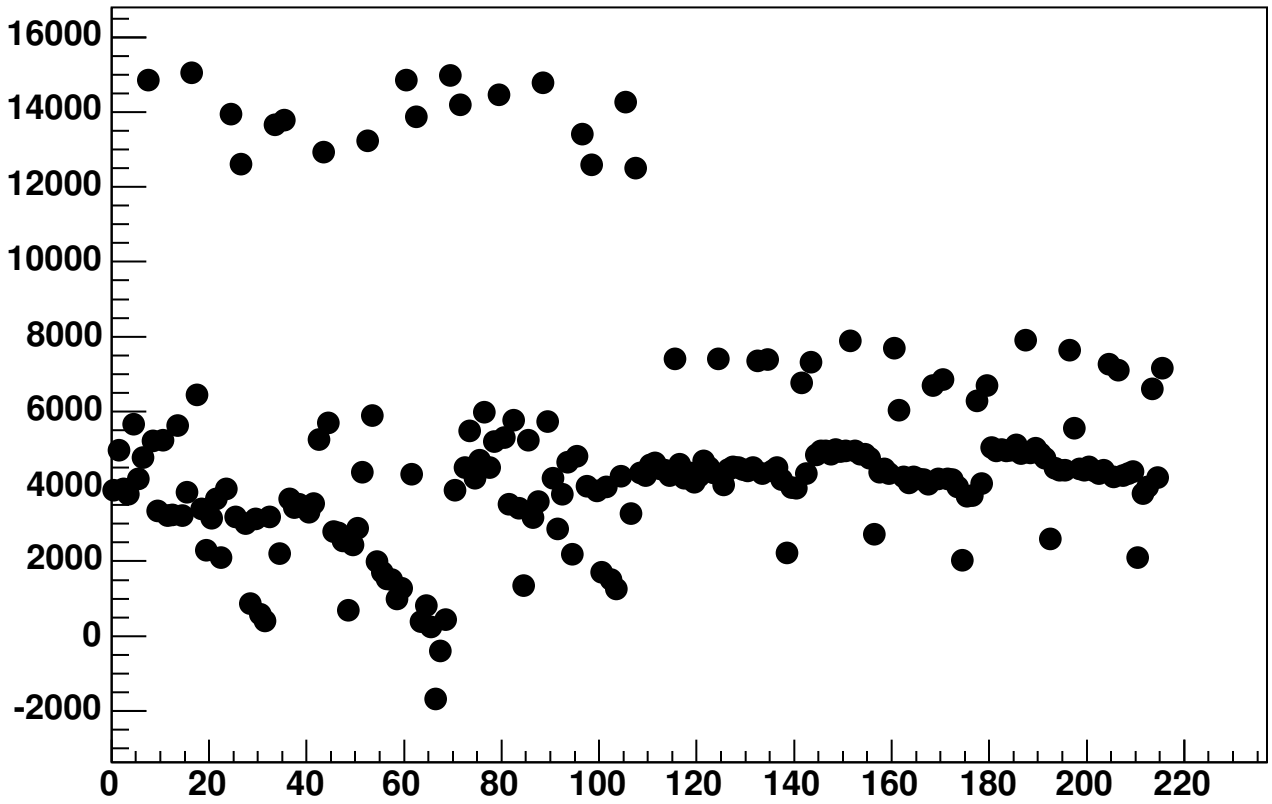
Enable 5, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



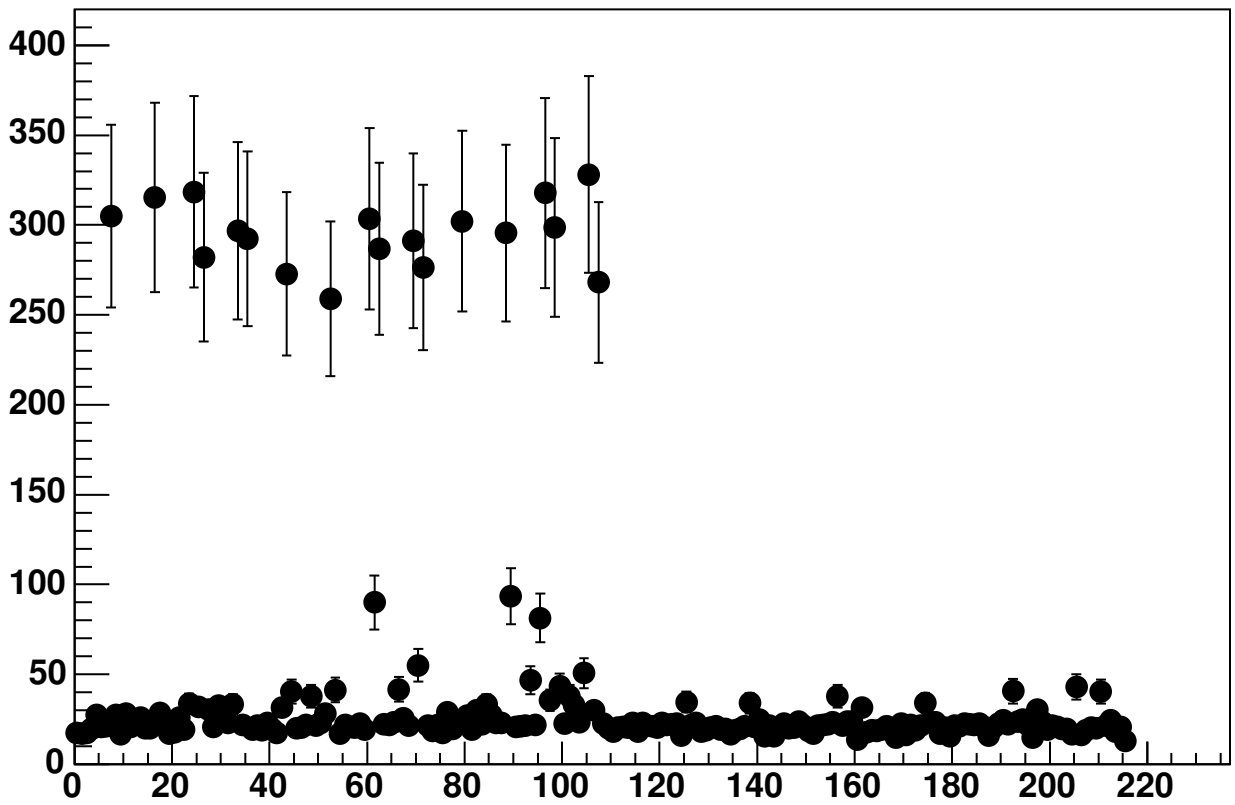
Enable 5, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



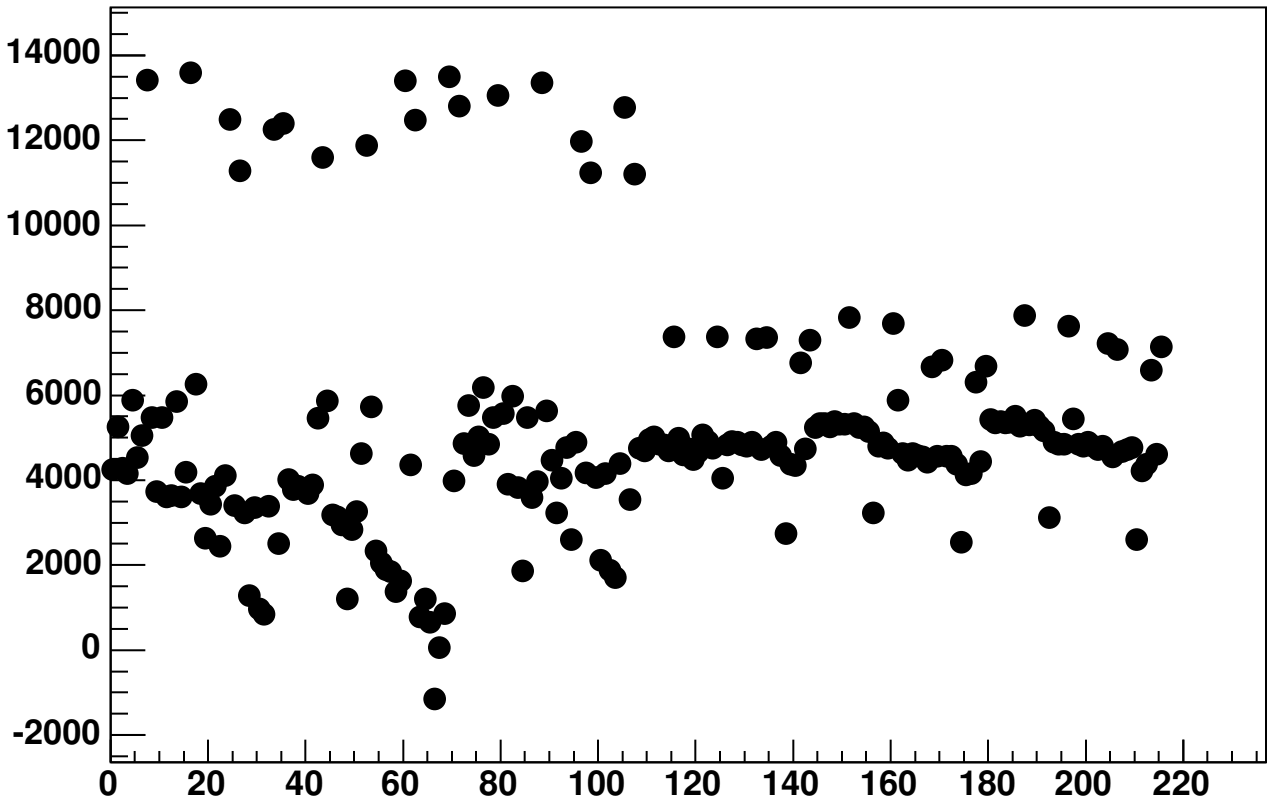
Enable 5, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



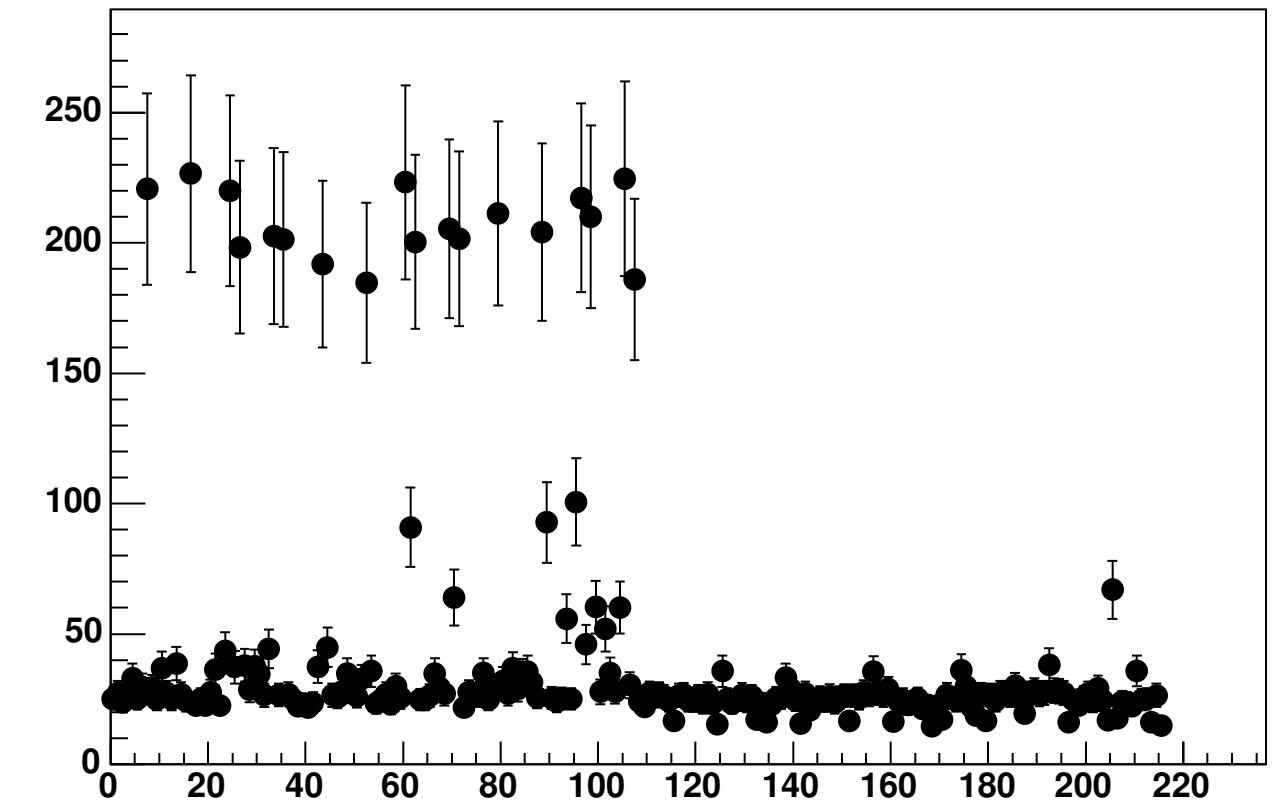
Enable 5, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



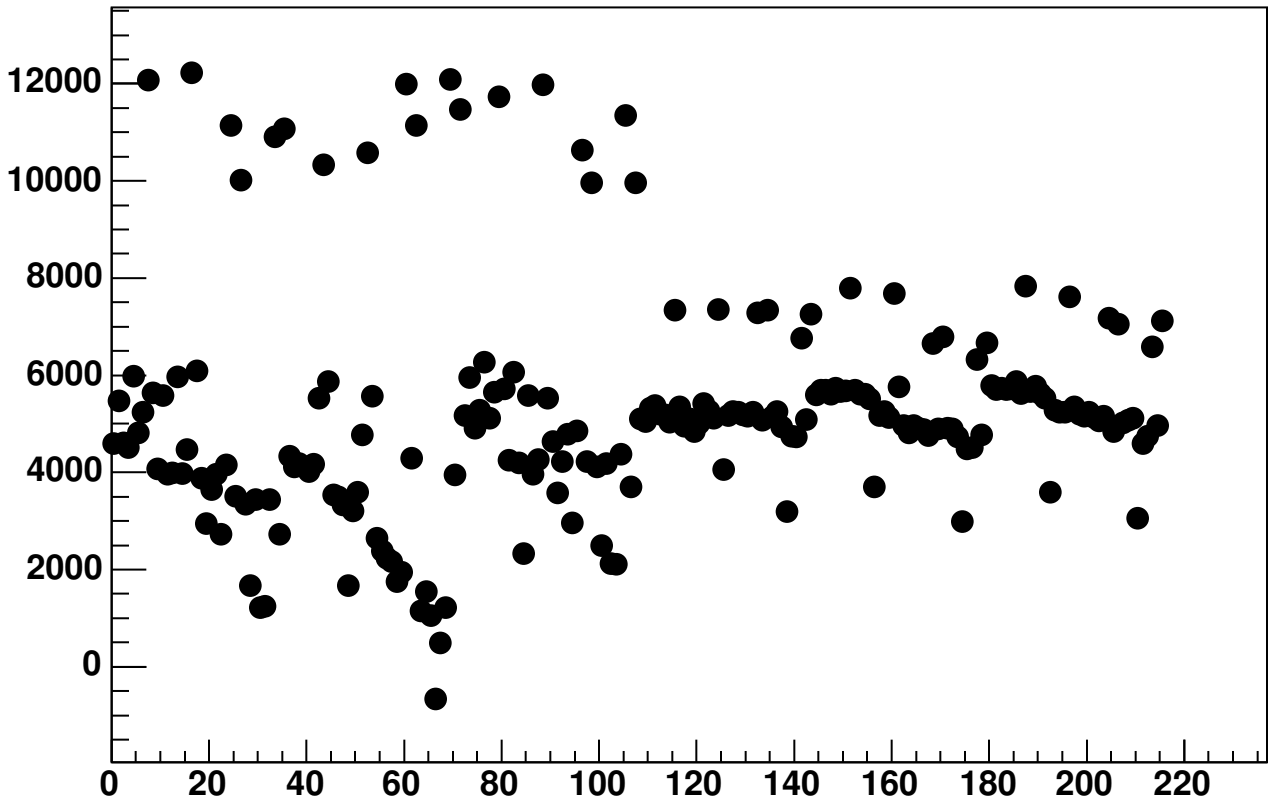
Enable 5, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



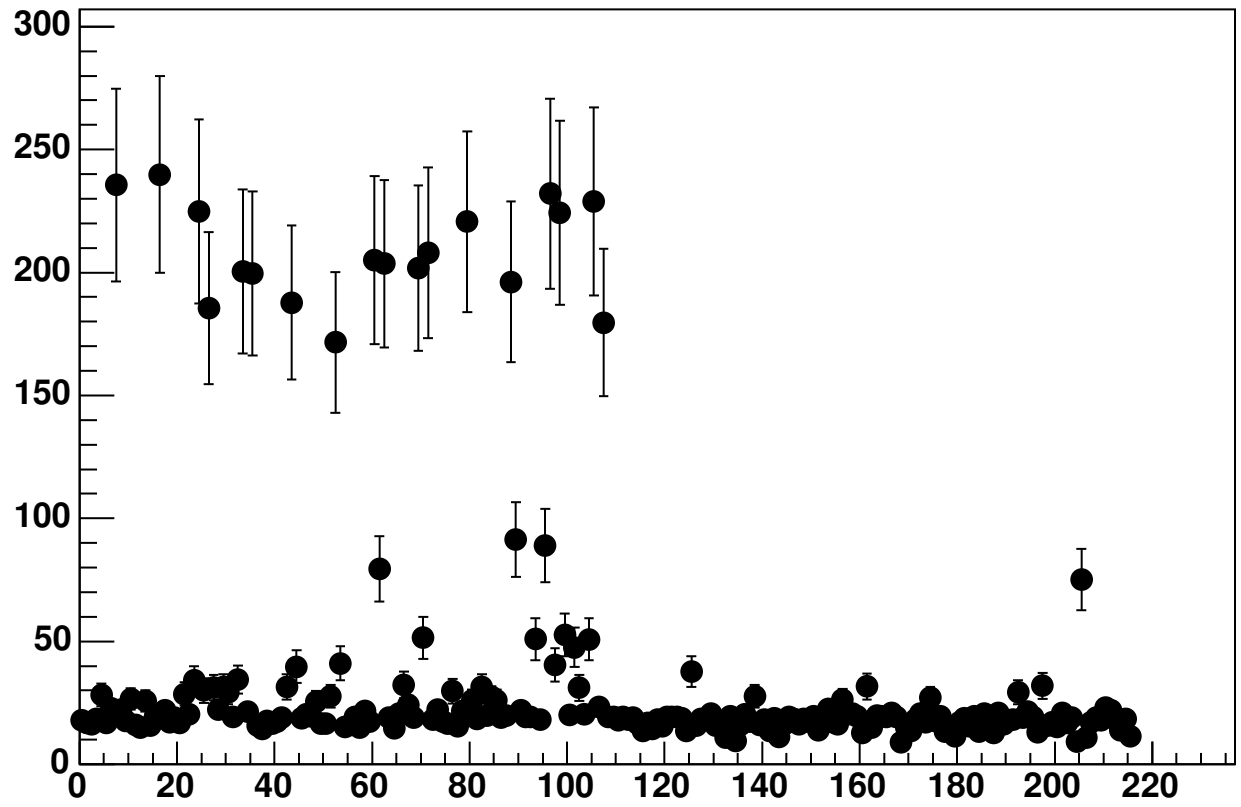
Enable 5, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



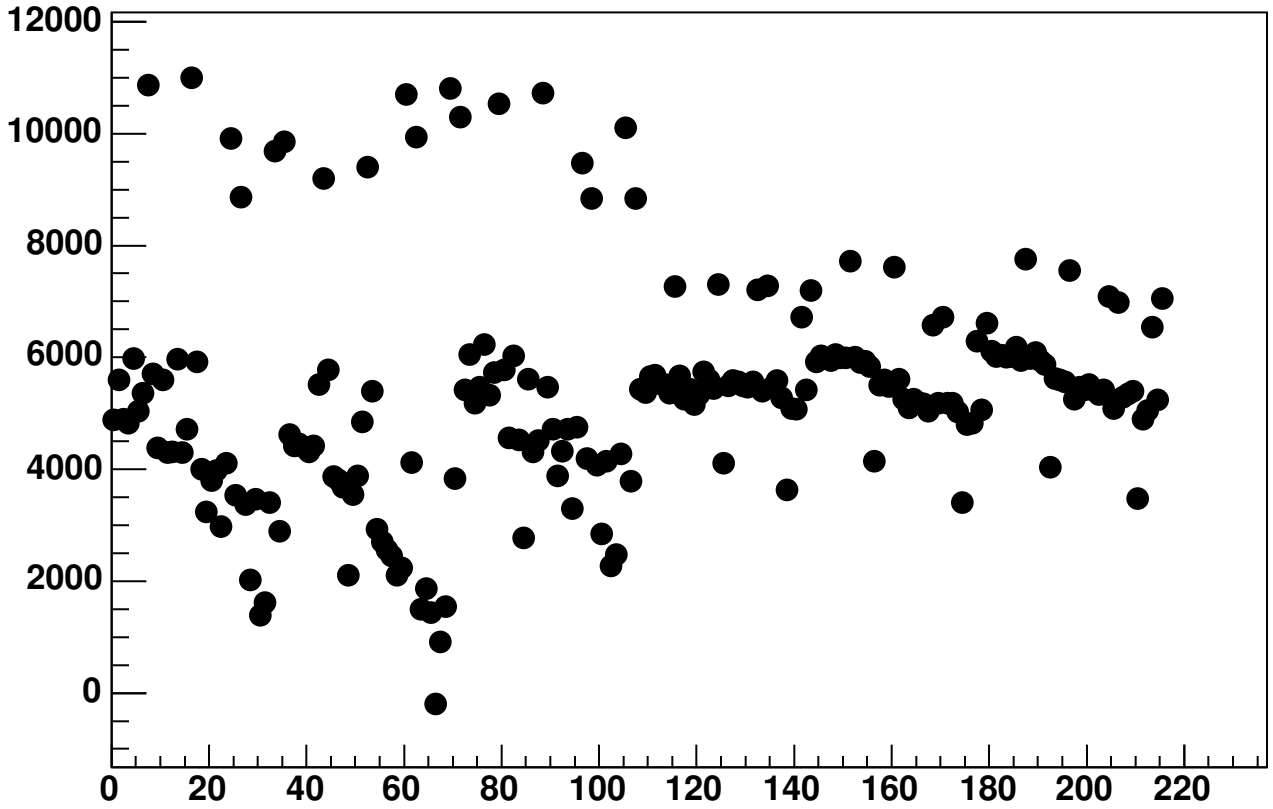
Enable 5, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



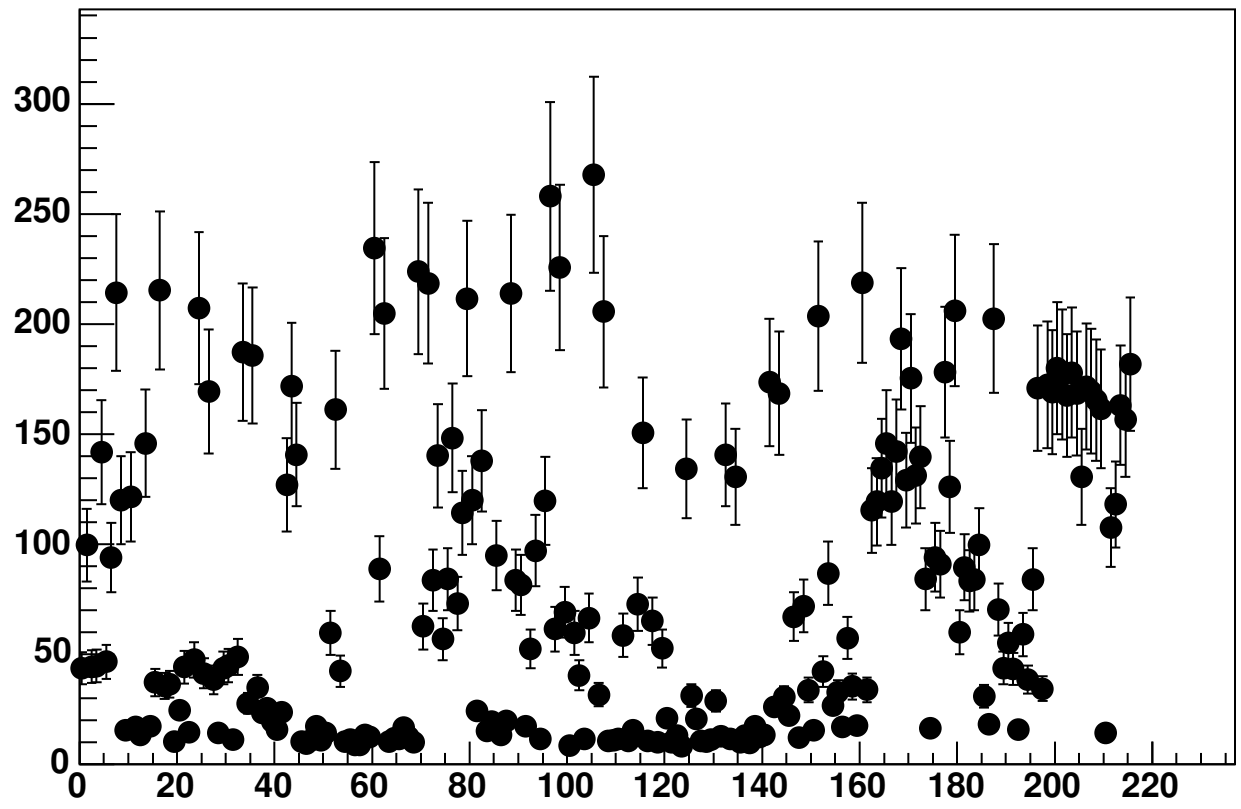
Enable 5, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



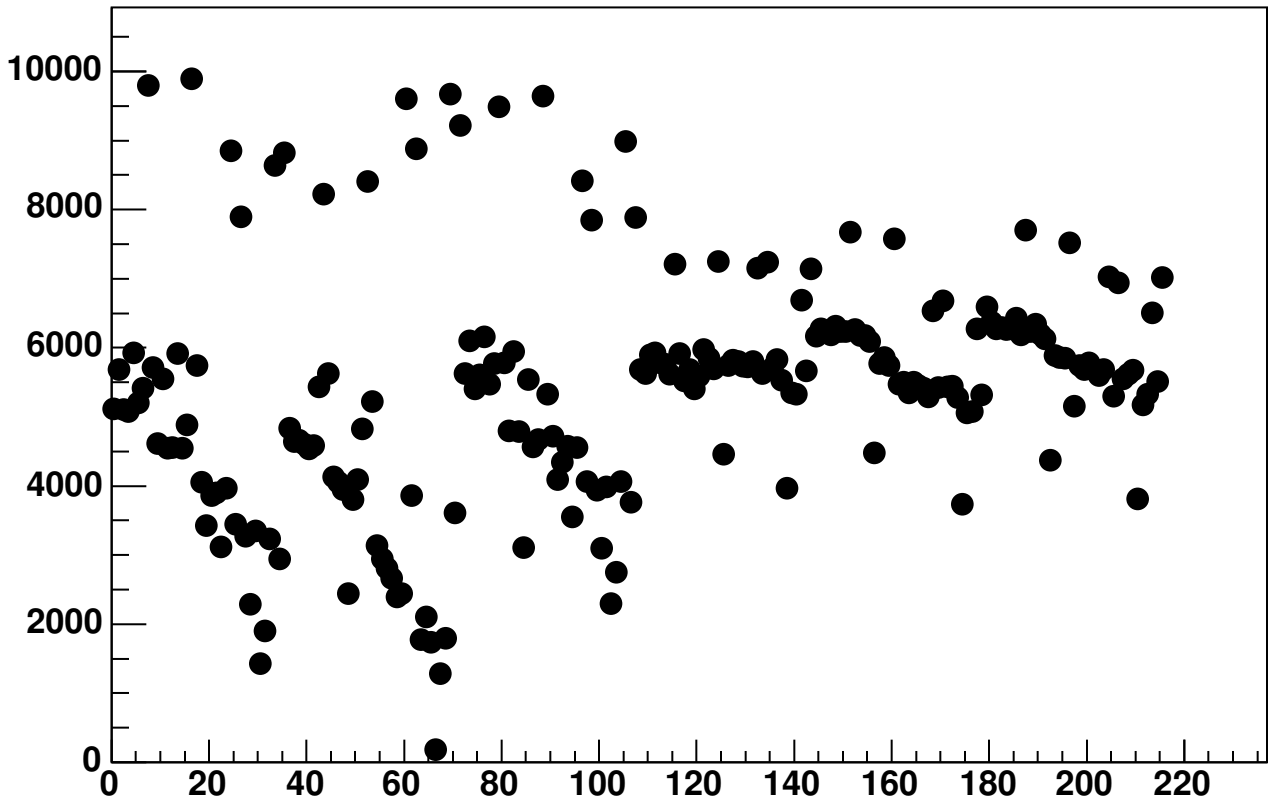
Enable 5, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



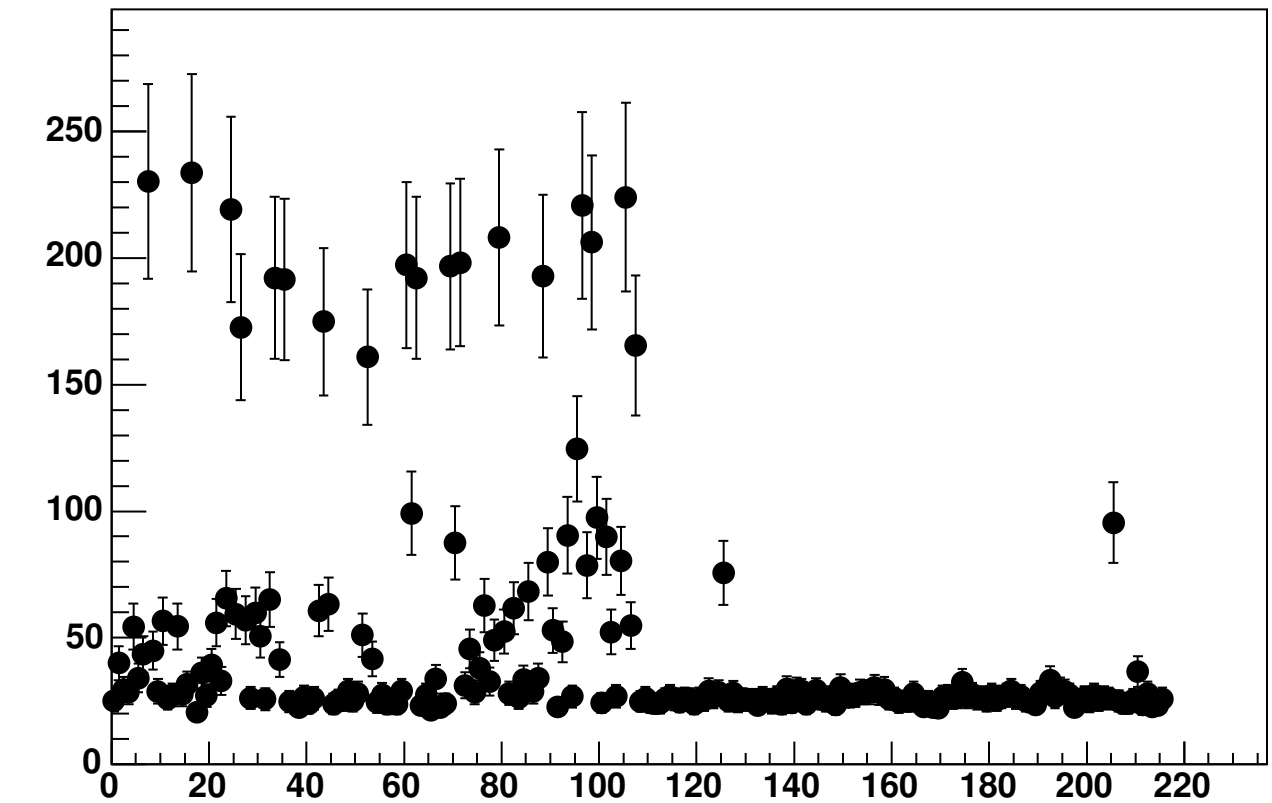
Enable 5, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



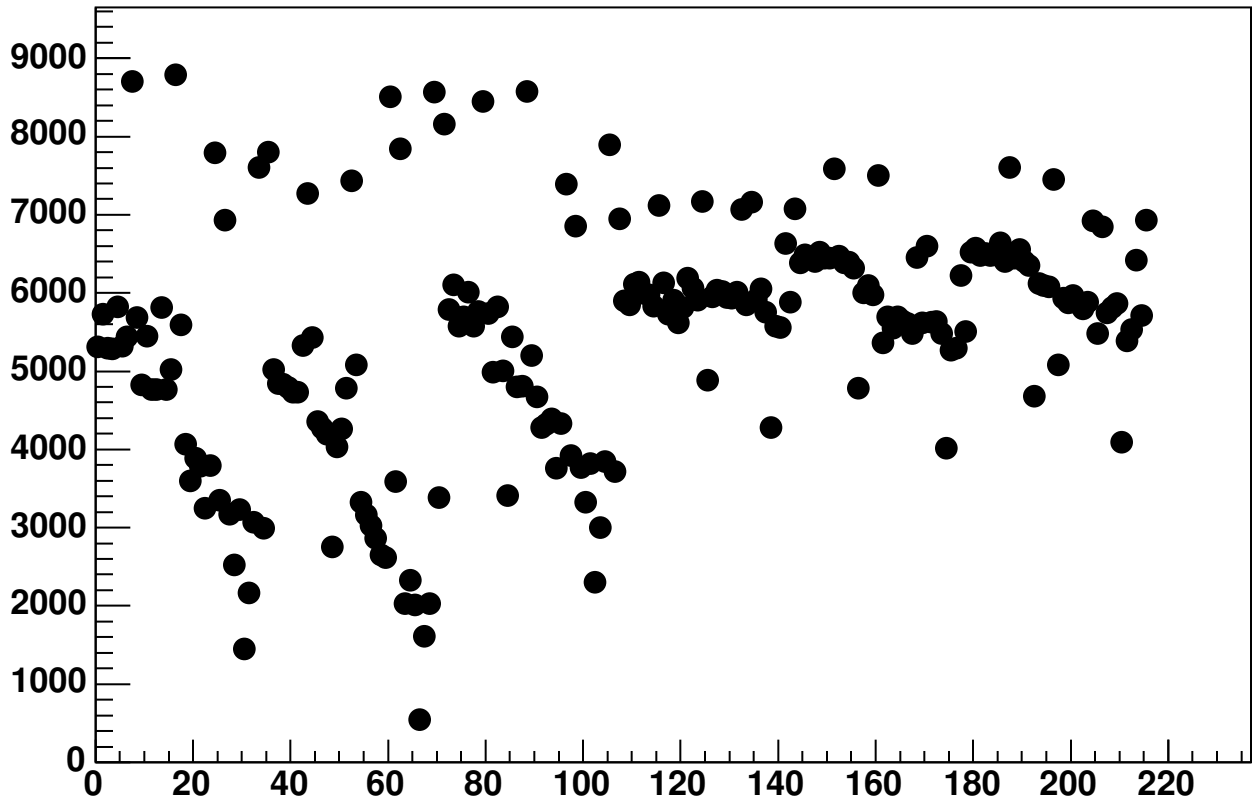
Enable 5, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



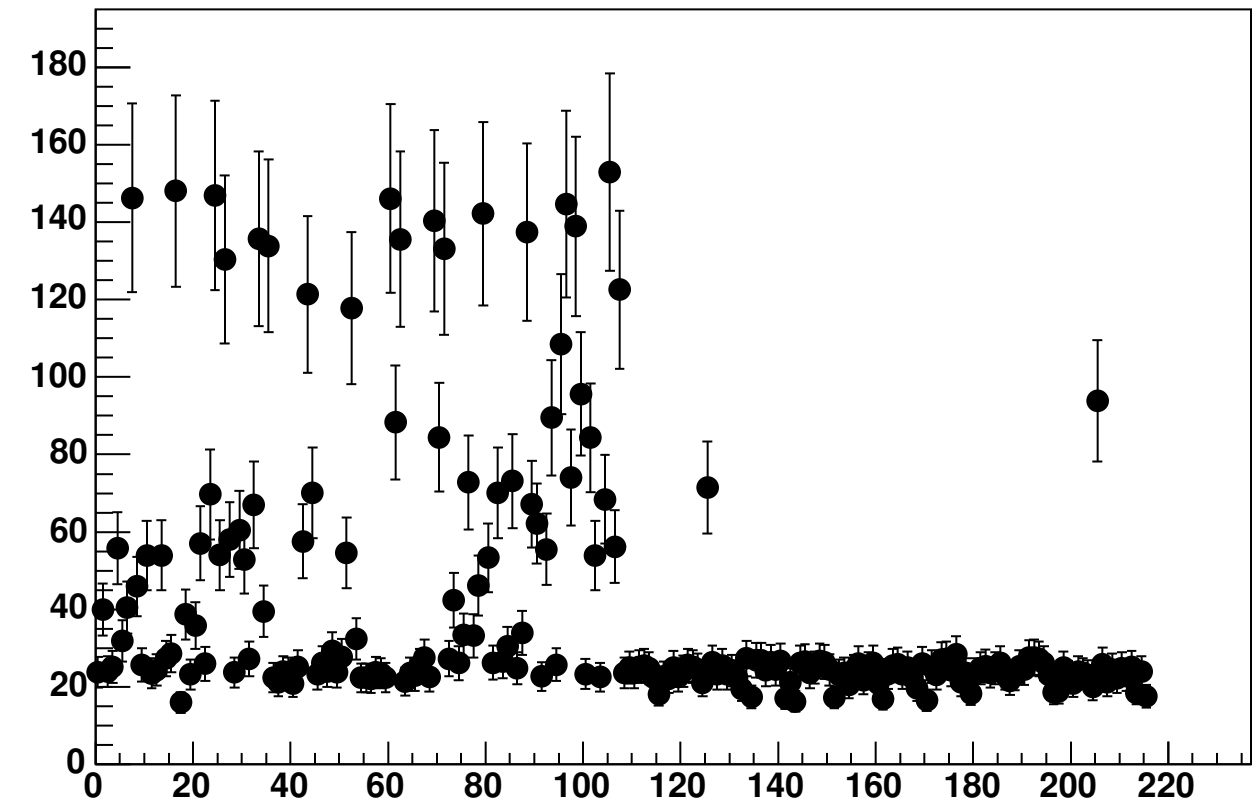
Enable 5, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



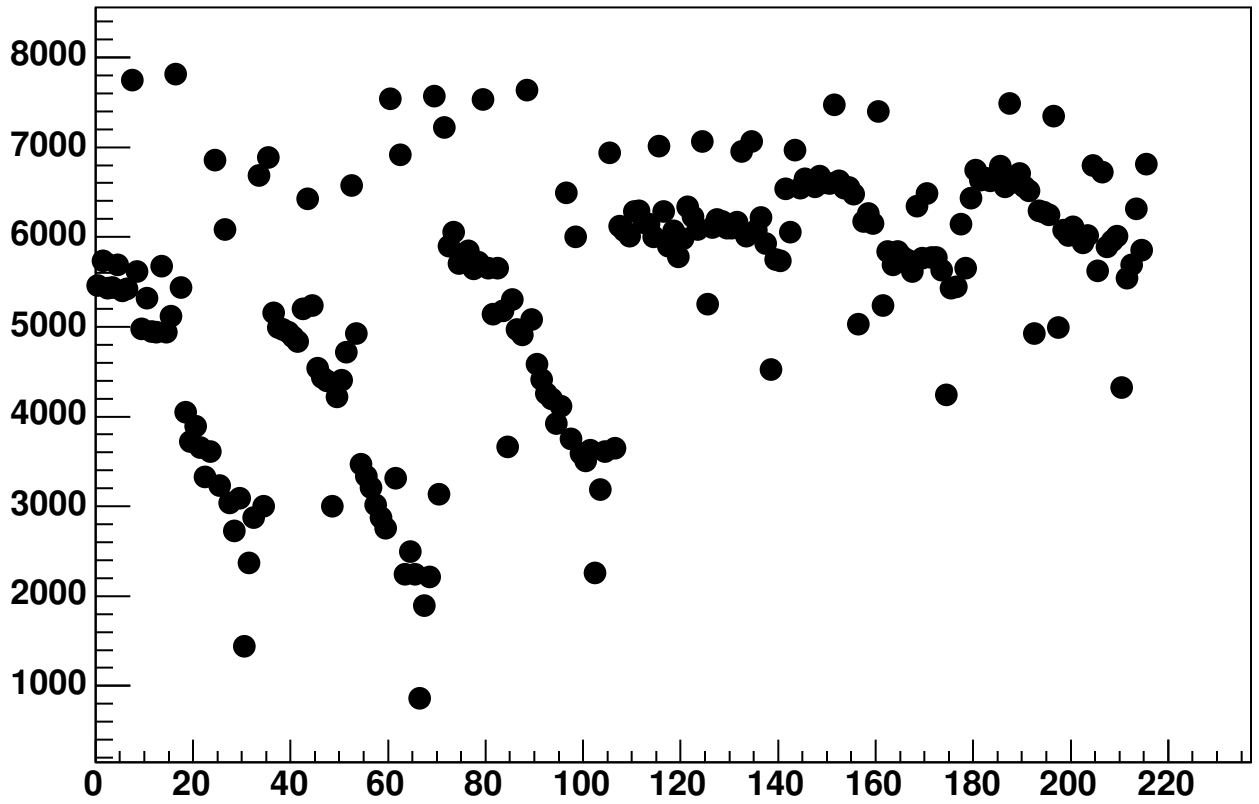
Enable 5, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



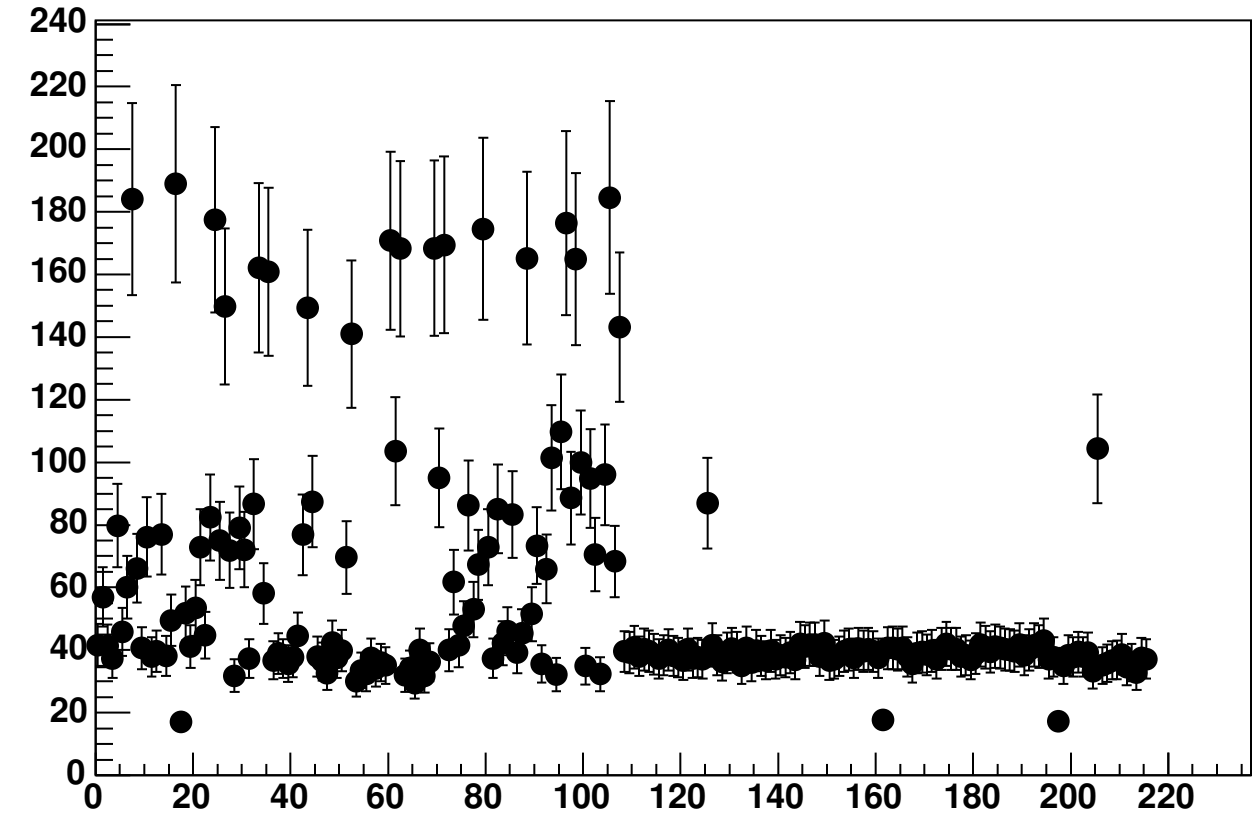
Enable 5, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



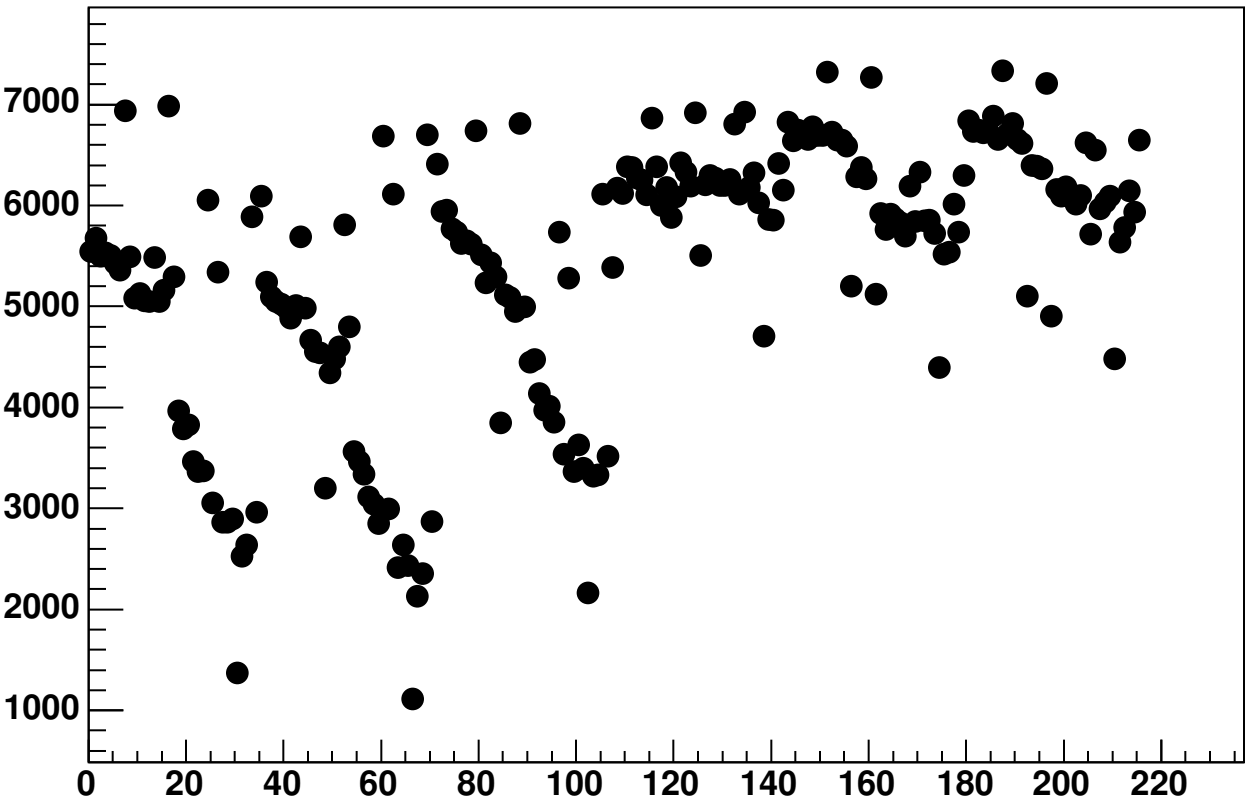
Enable 5, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



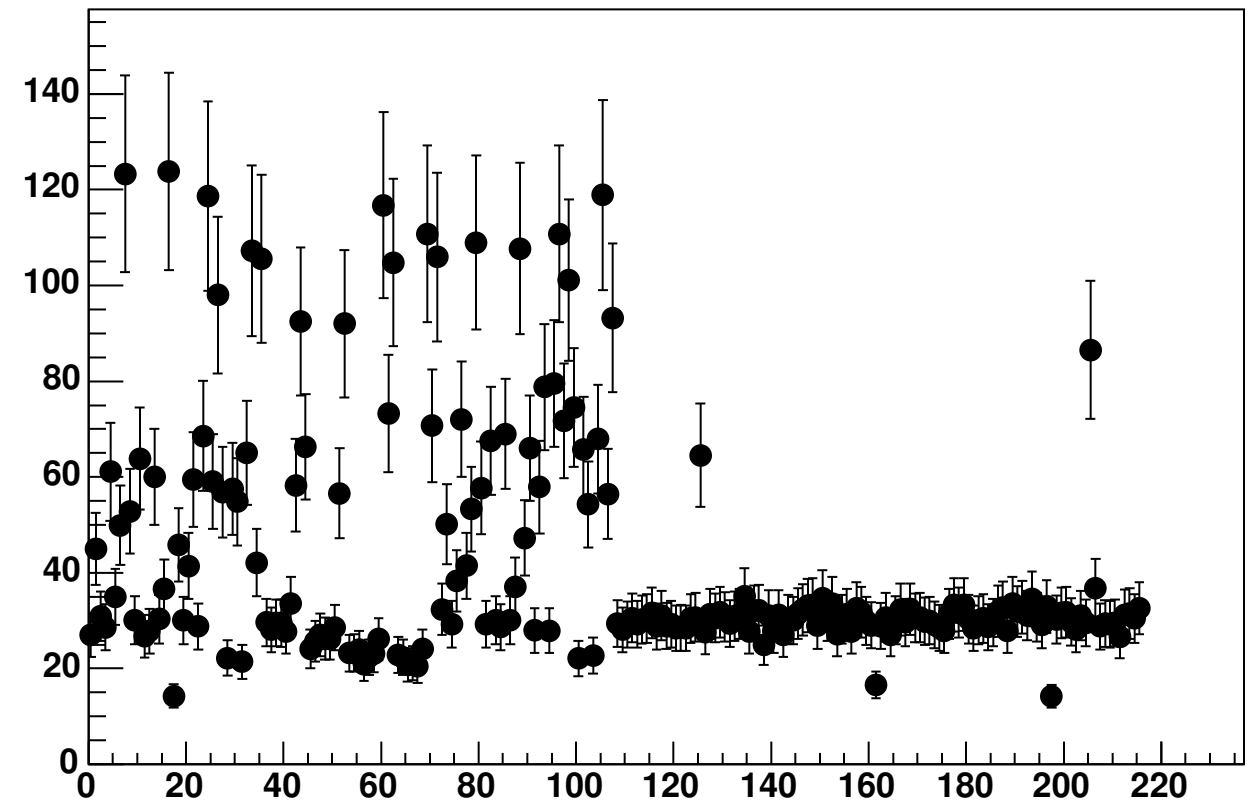
Enable 5, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



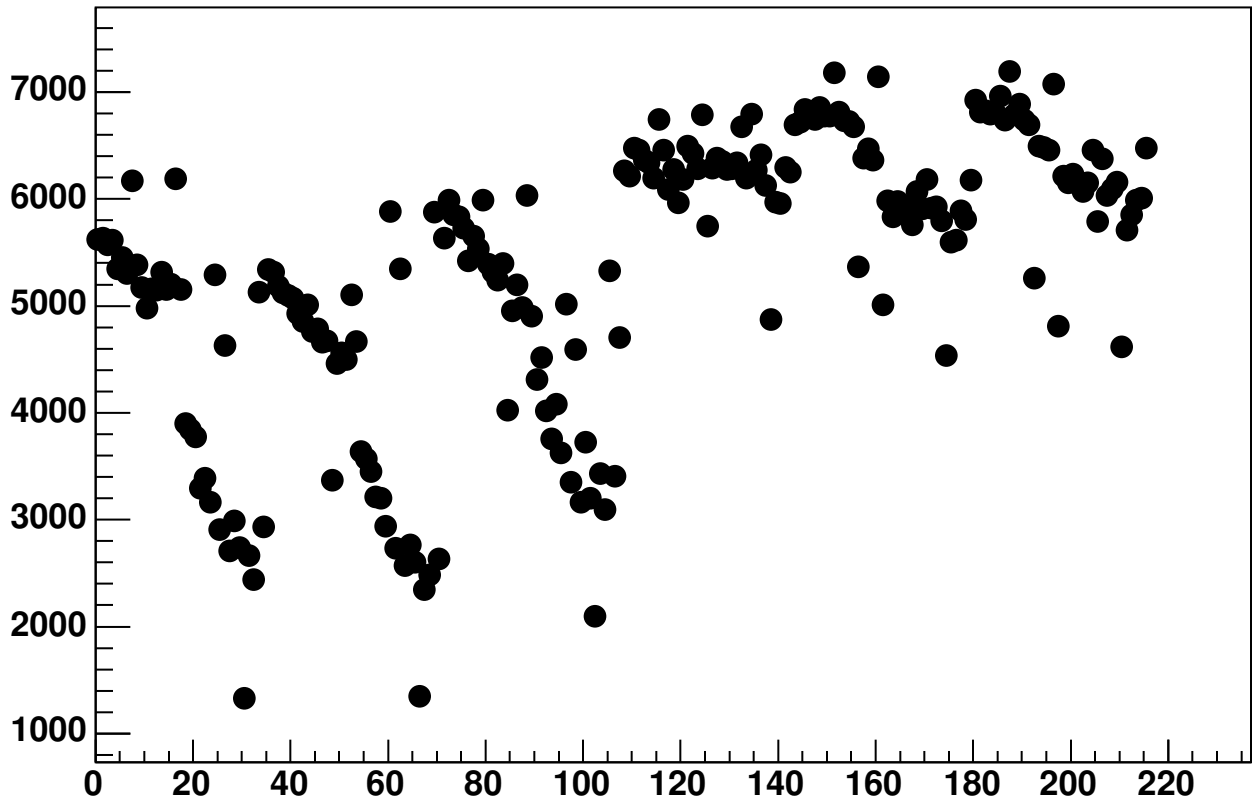
Enable 5, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



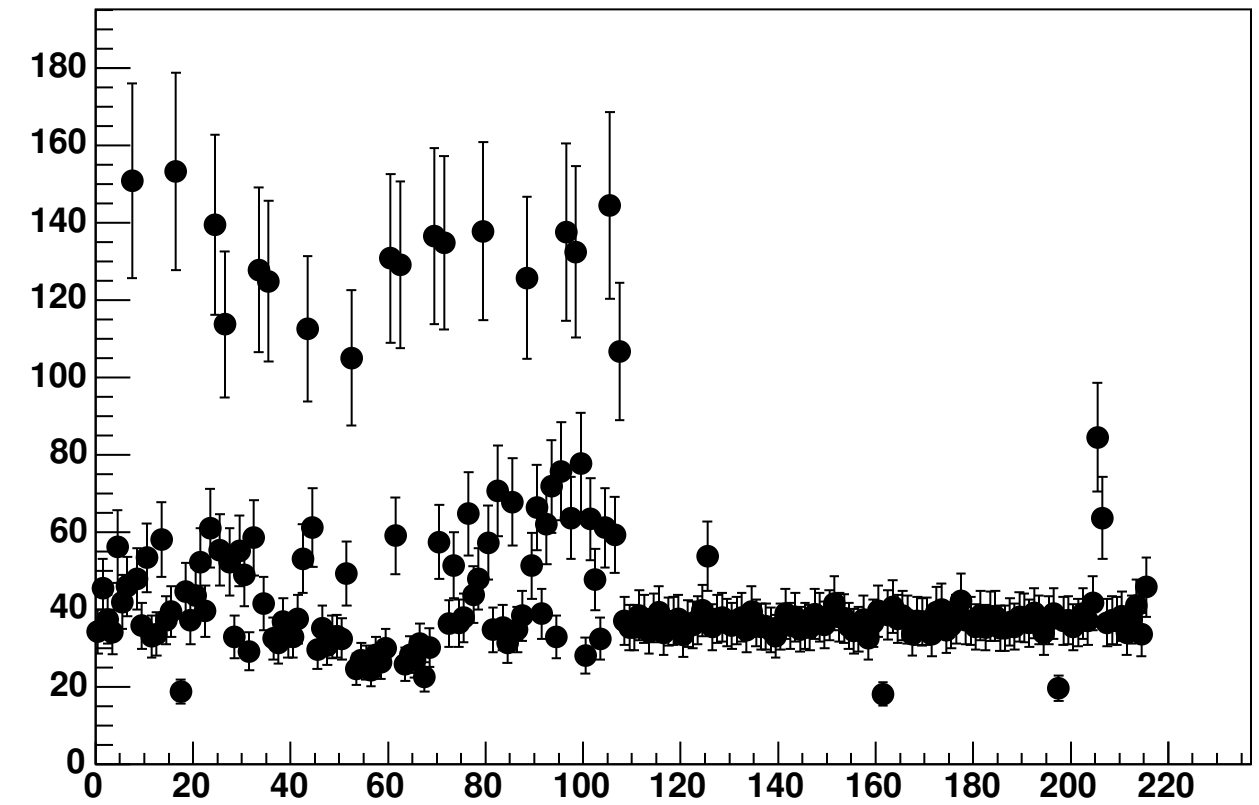
Enable 5, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



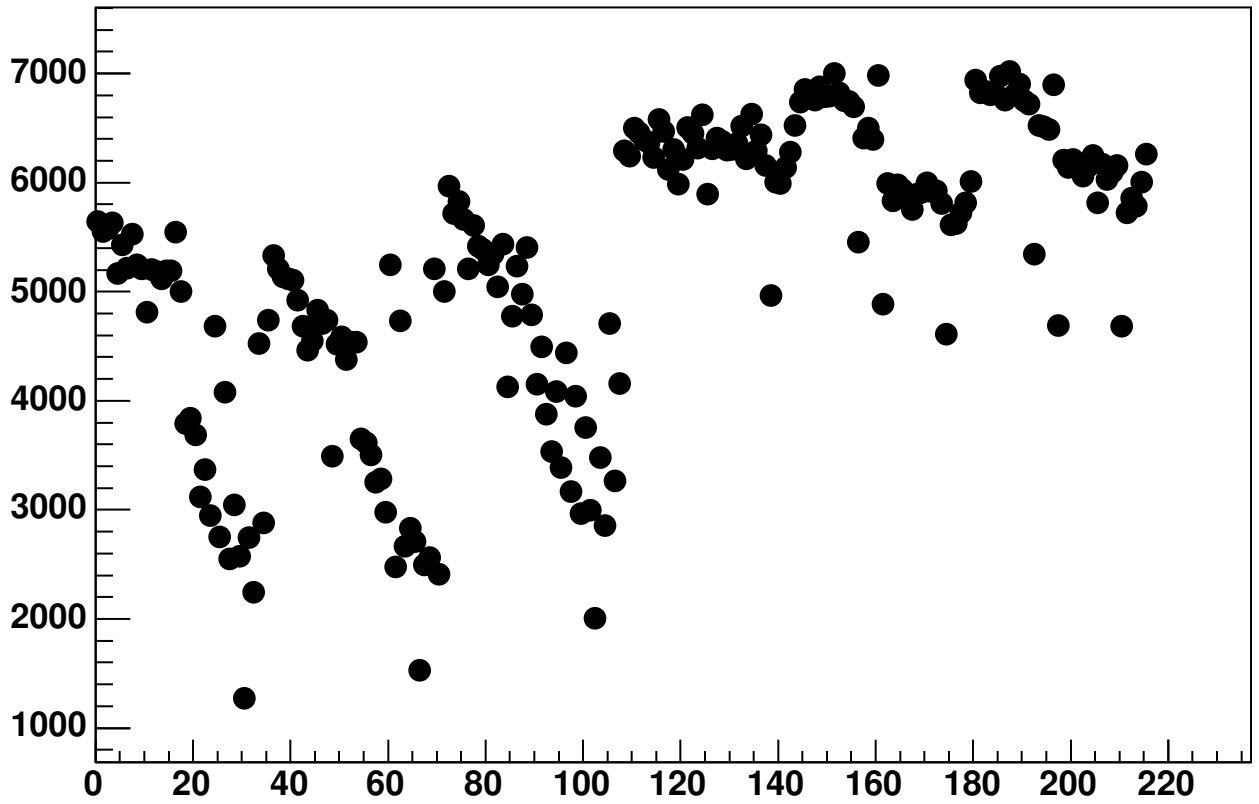
Enable 5, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



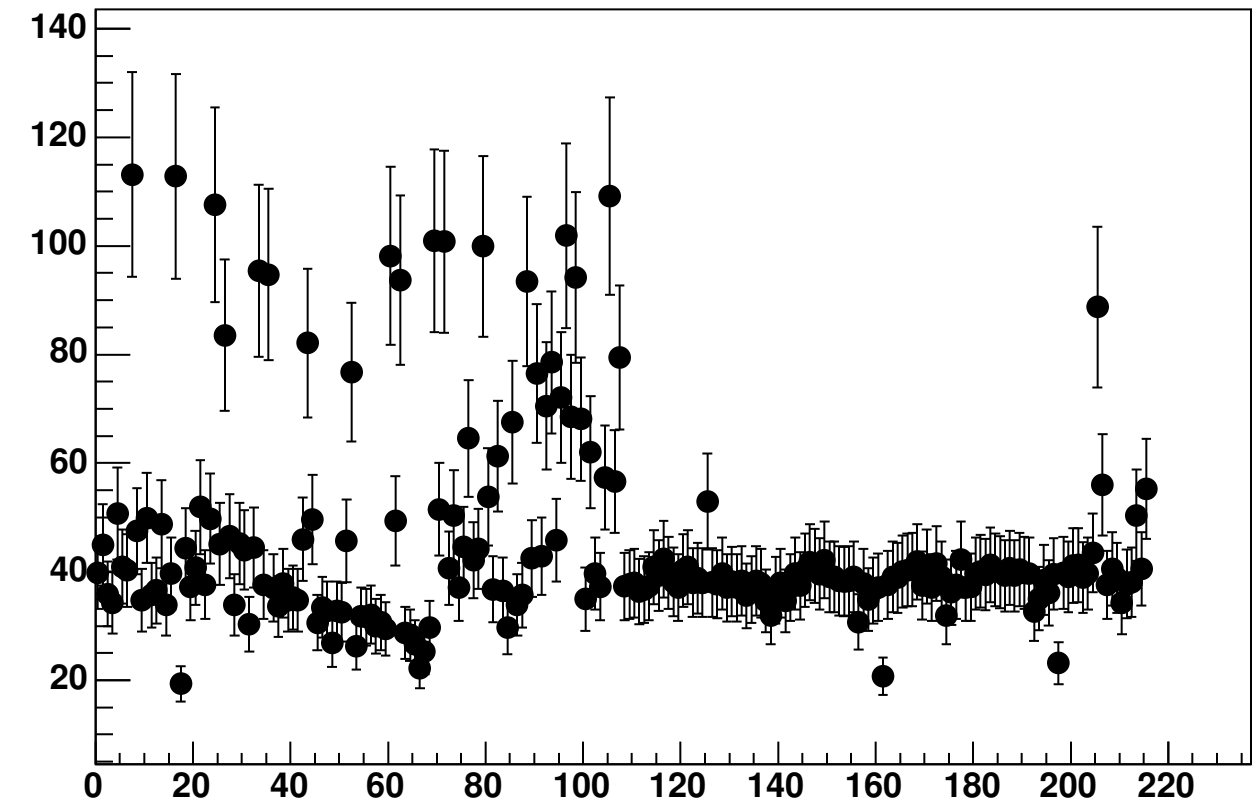
Enable 5, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



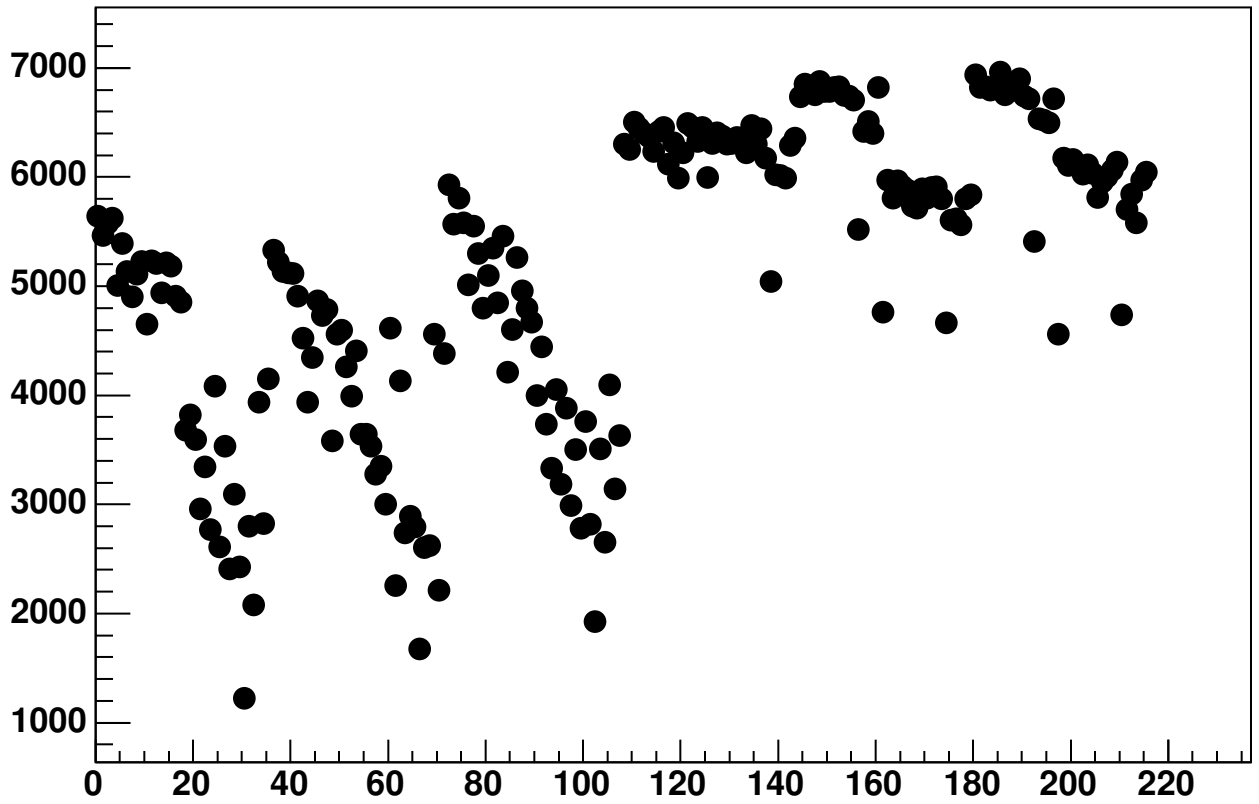
Enable 5, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



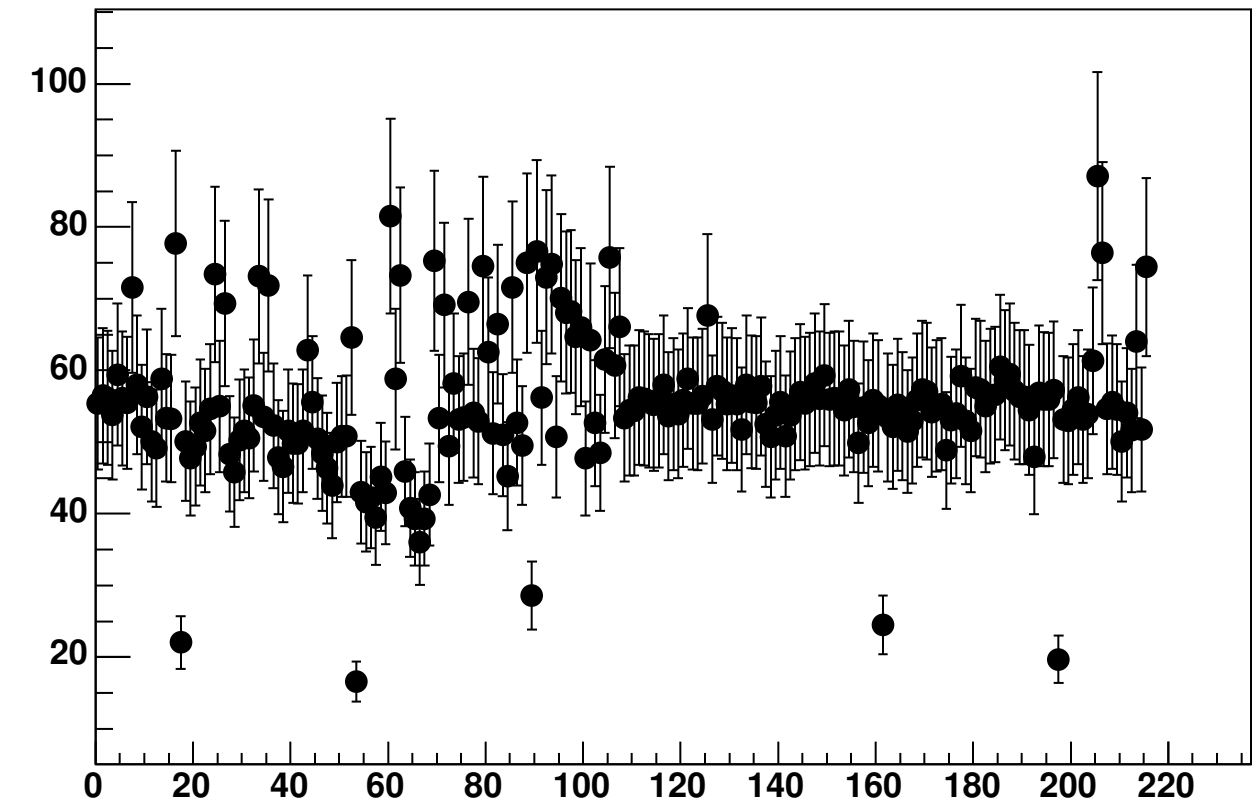
Enable 5, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



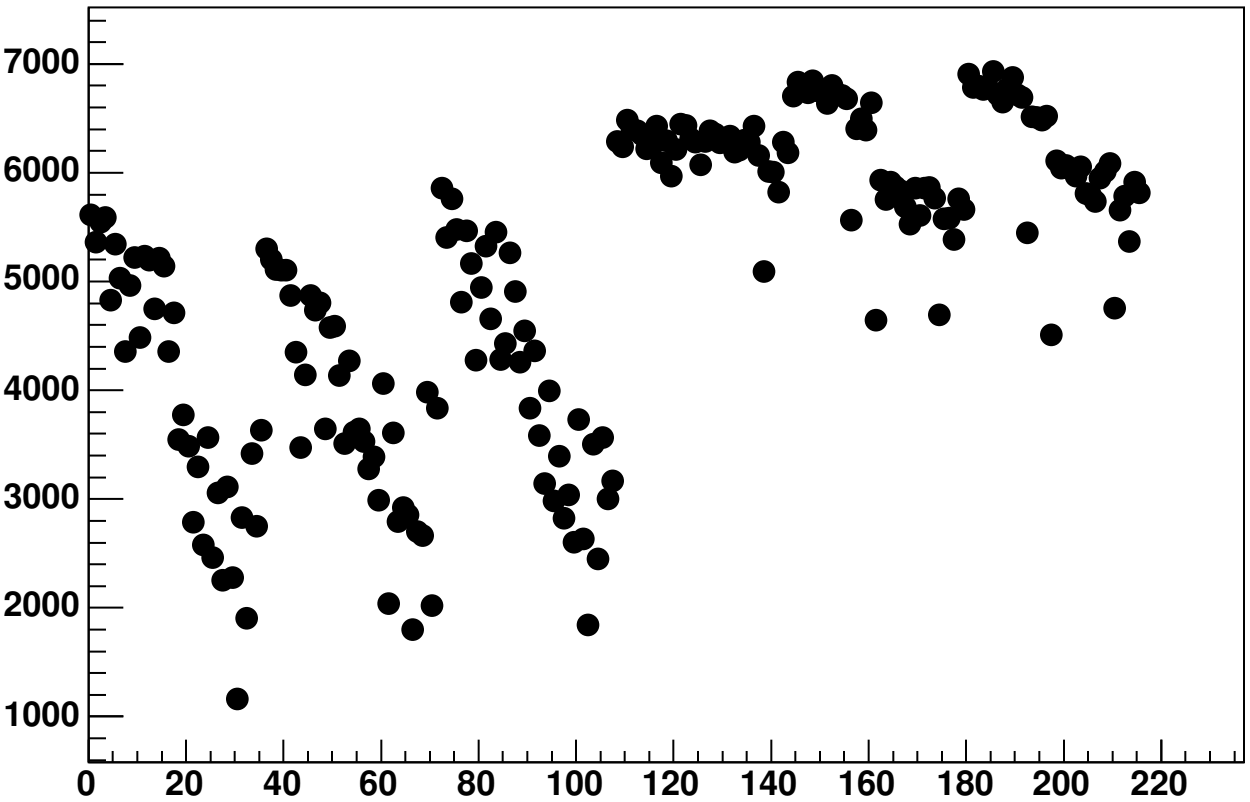
Enable 5, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



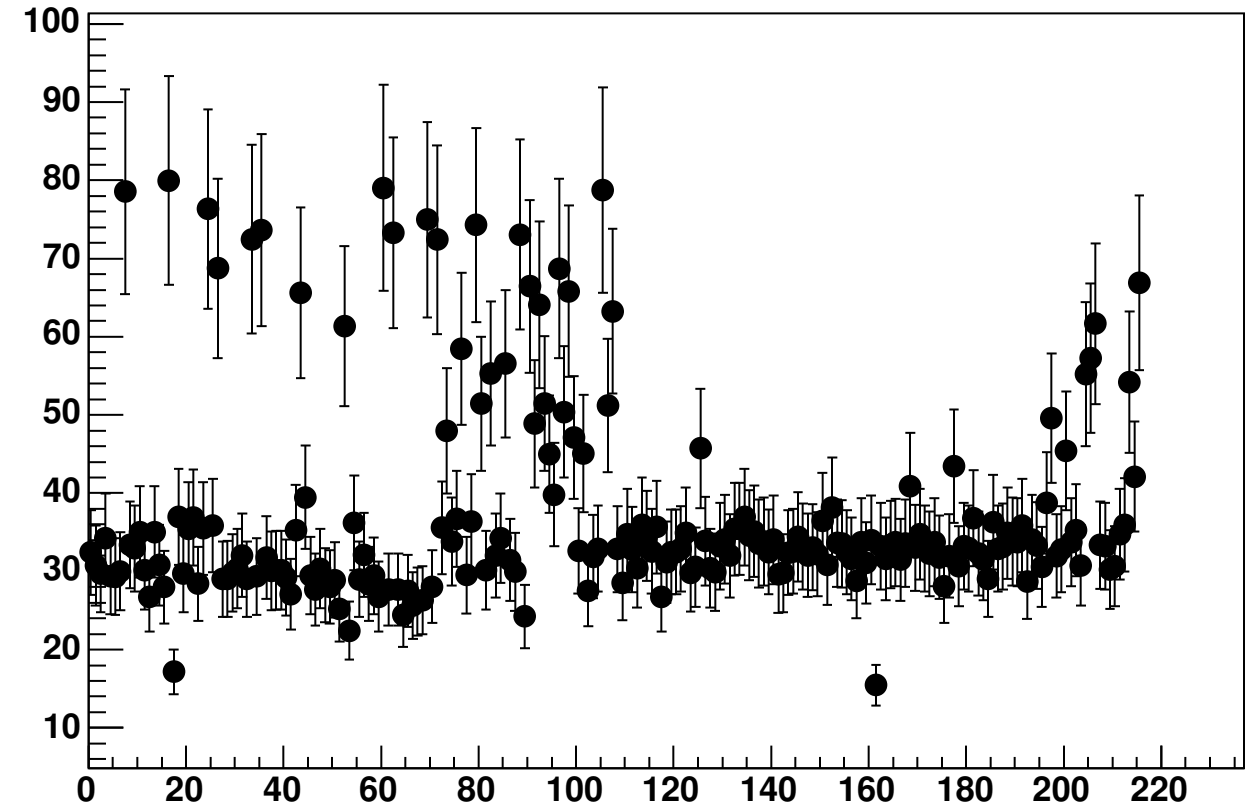
Enable 5, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



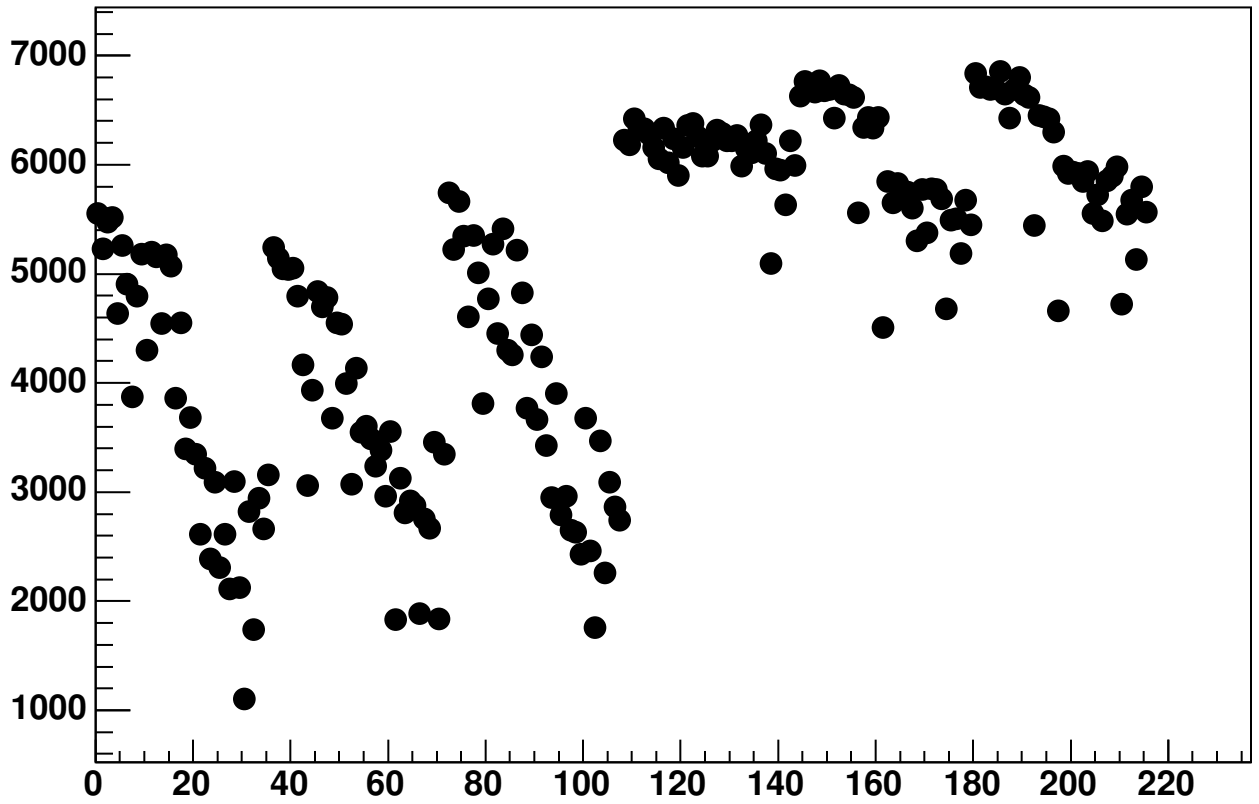
Enable 5, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



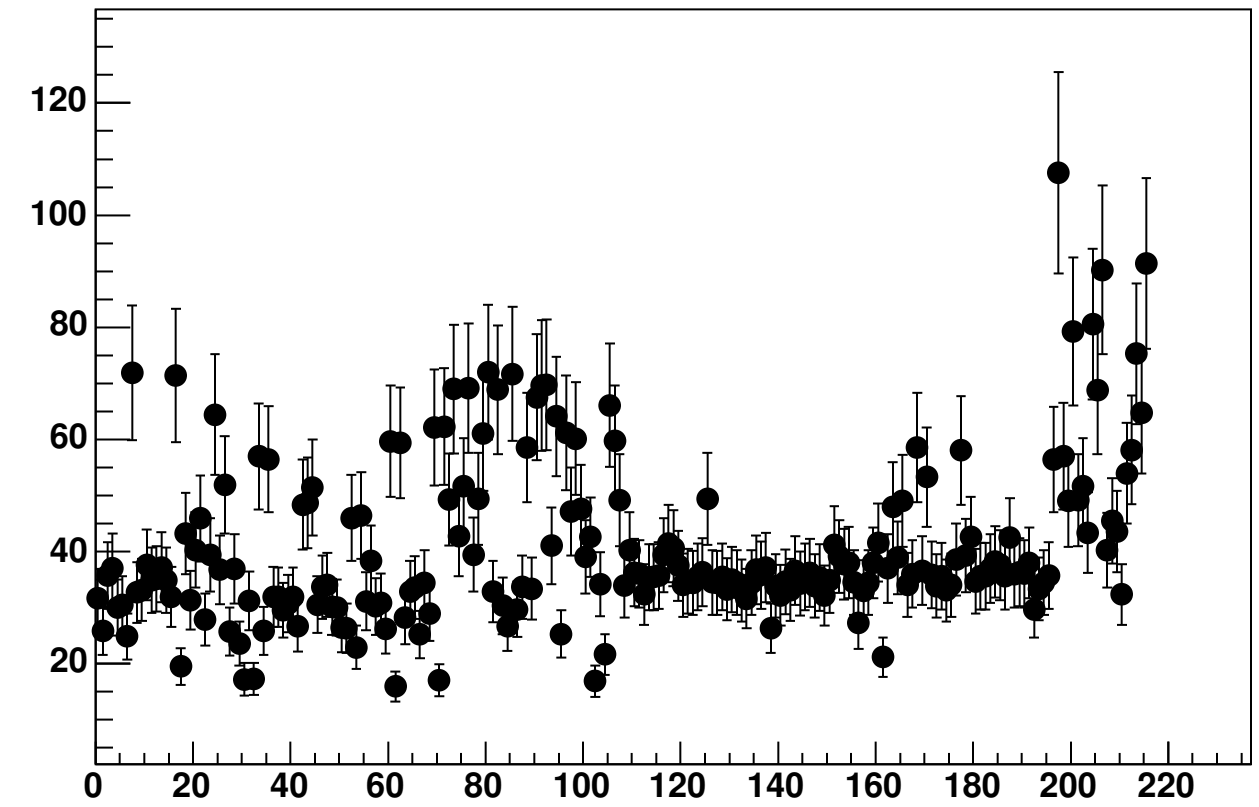
Enable 5, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



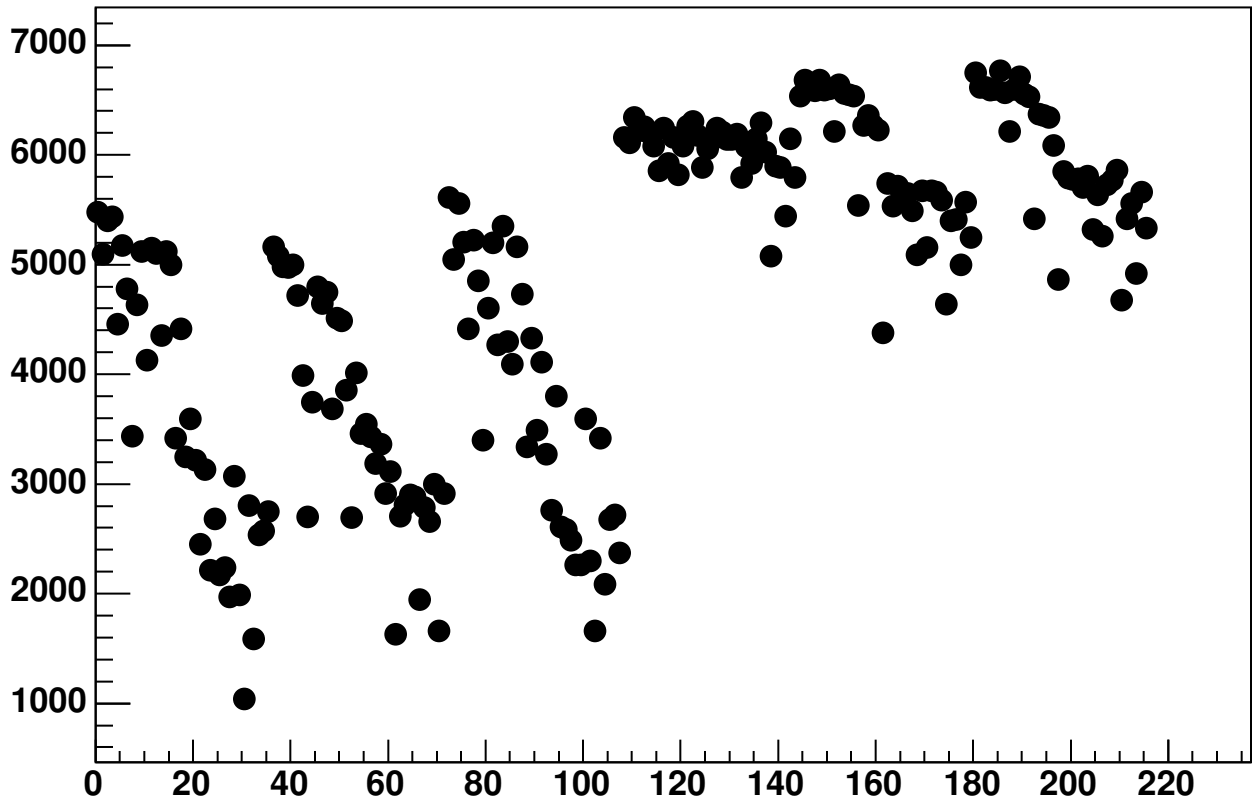
Enable 5, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



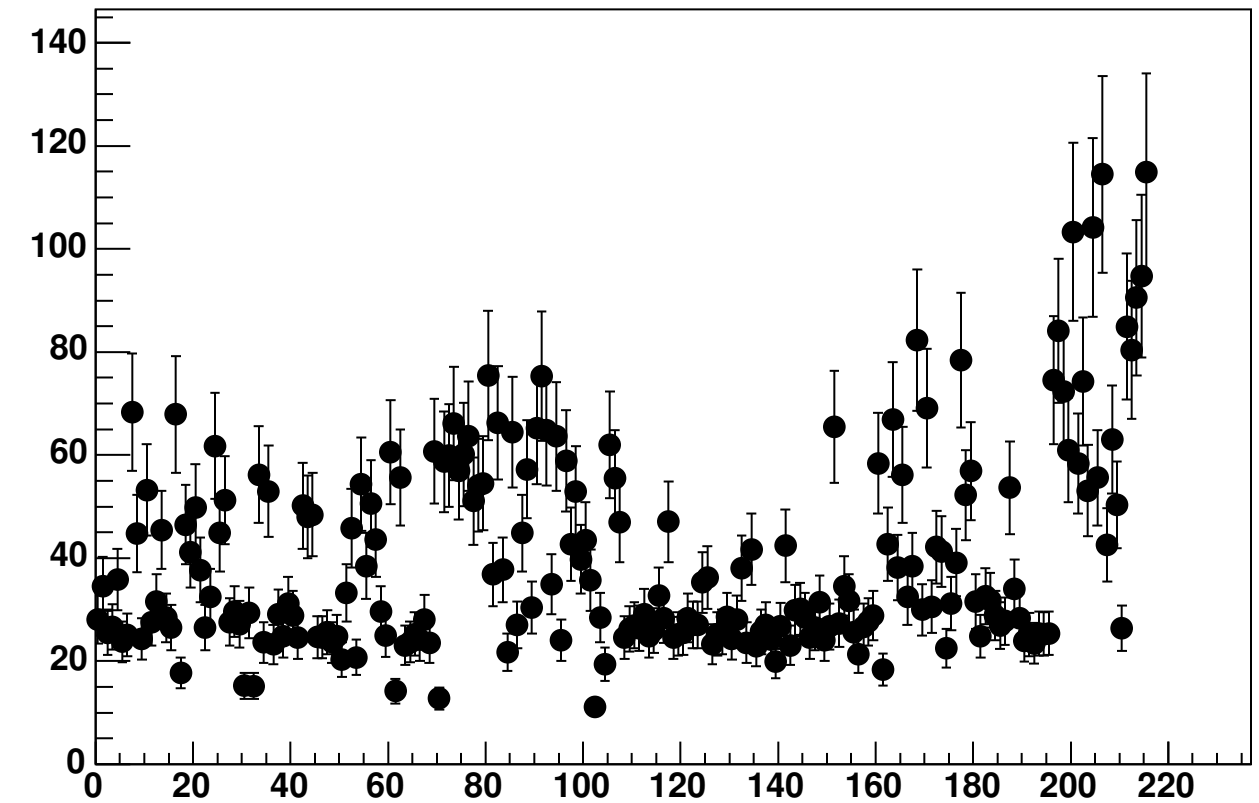
Enable 5, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



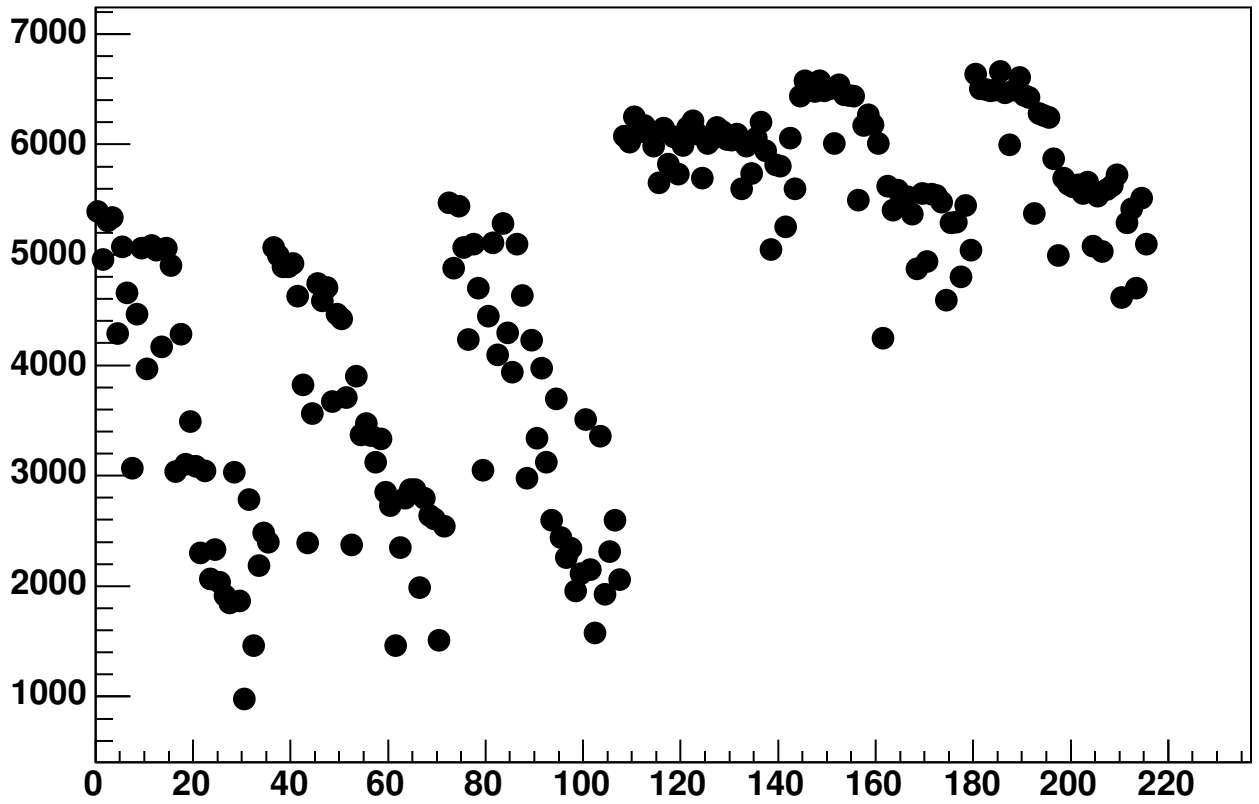
Enable 5, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



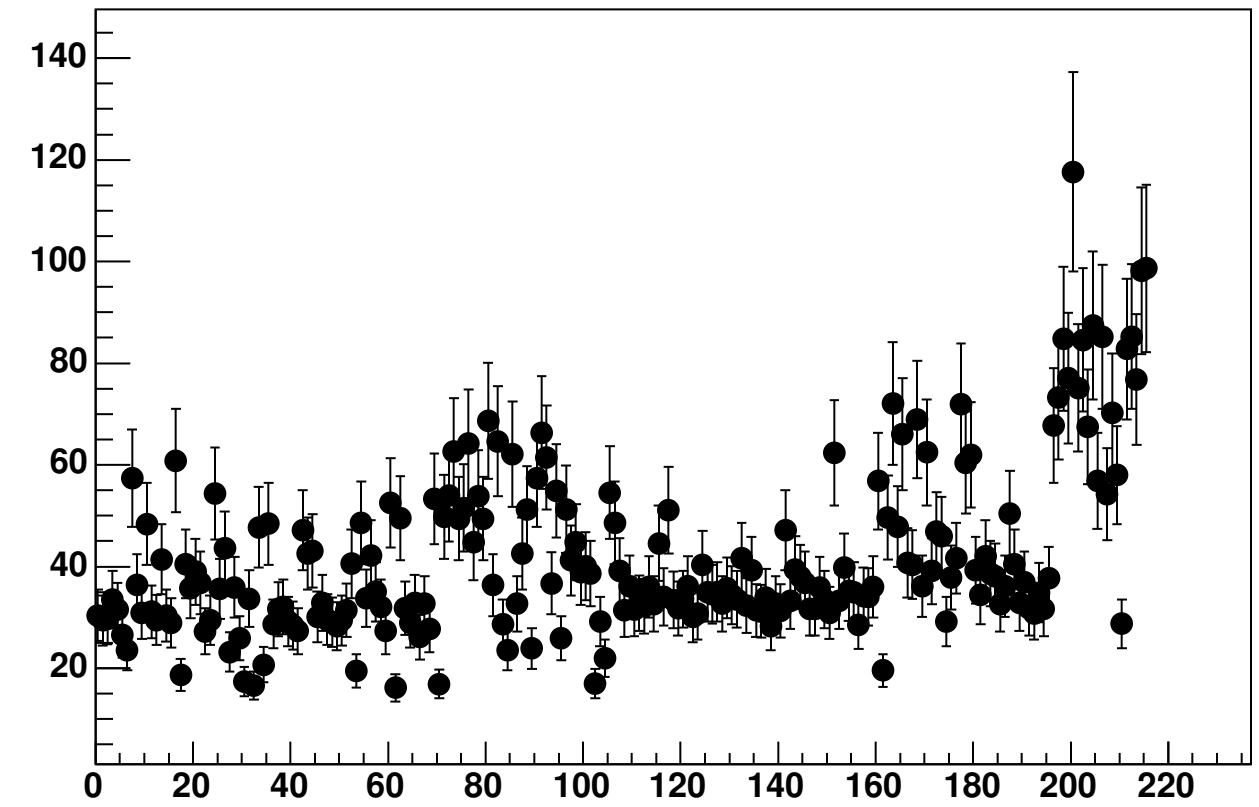
Enable 5, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



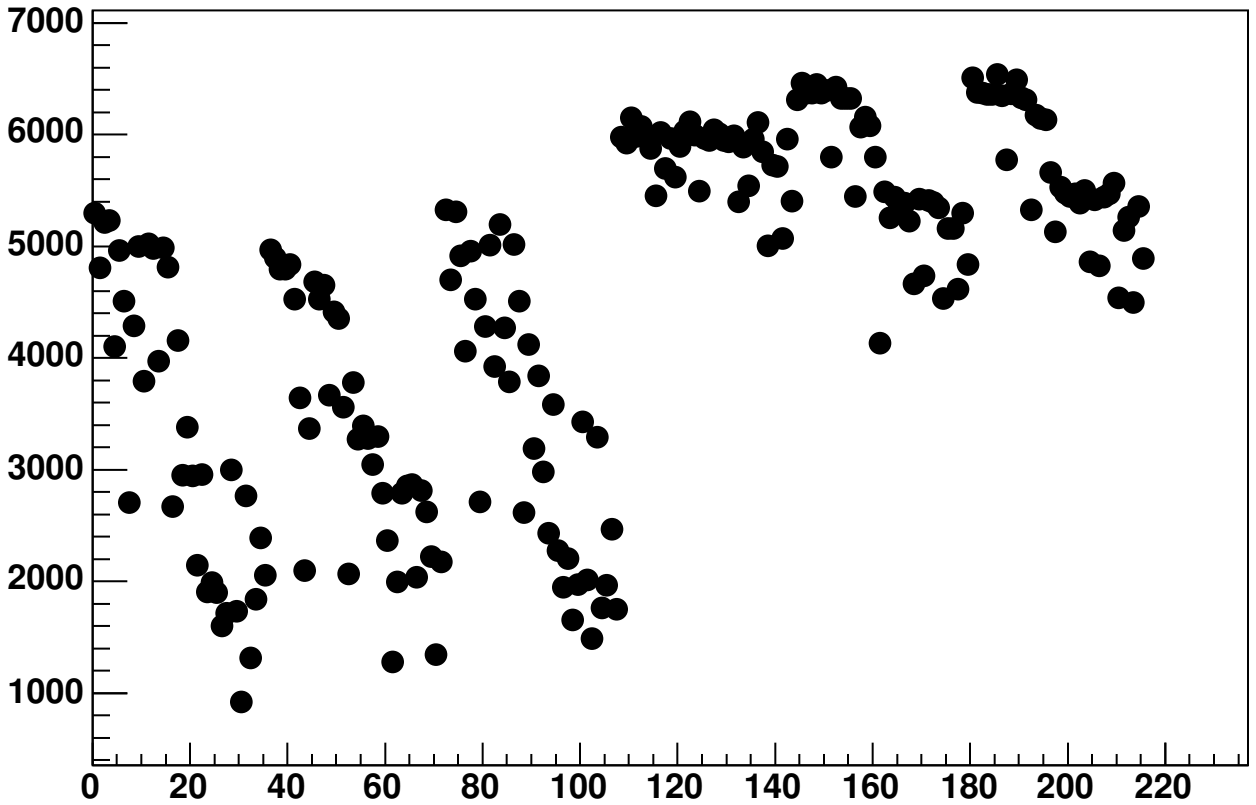
Enable 5, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



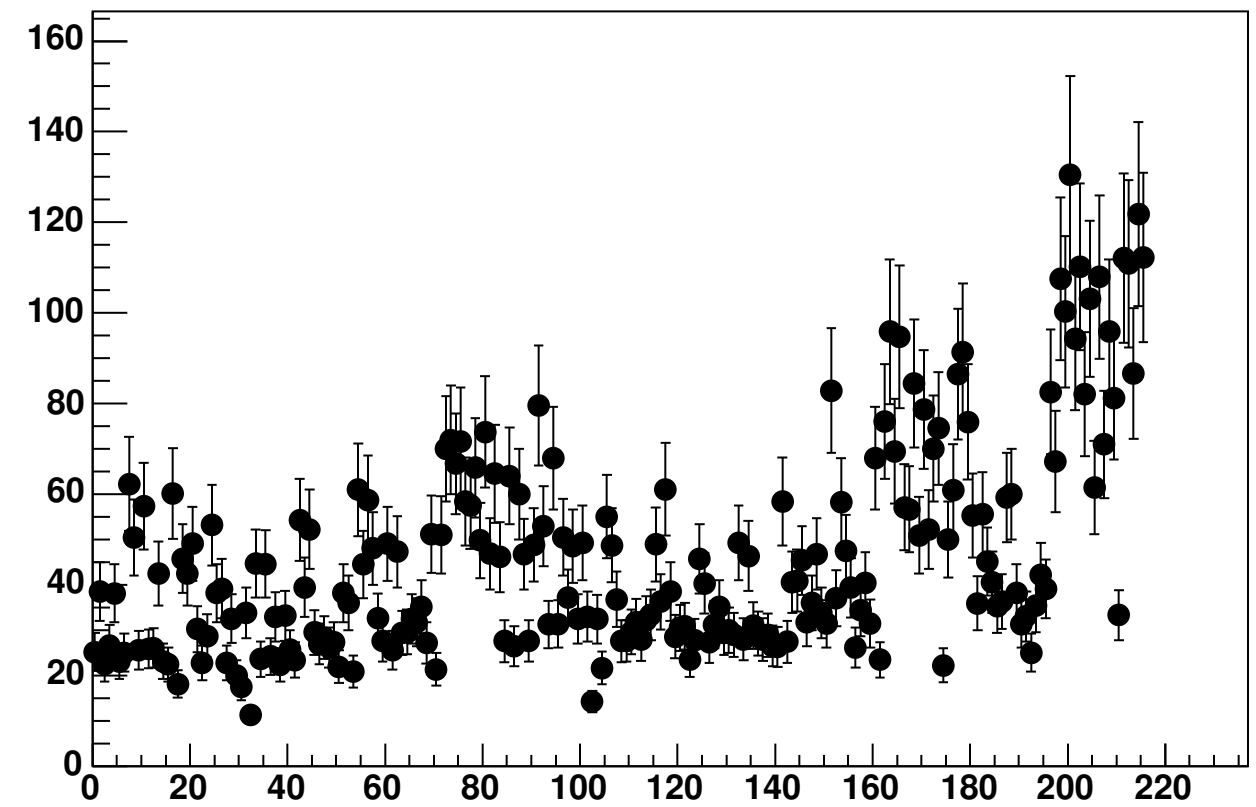
Enable 5, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



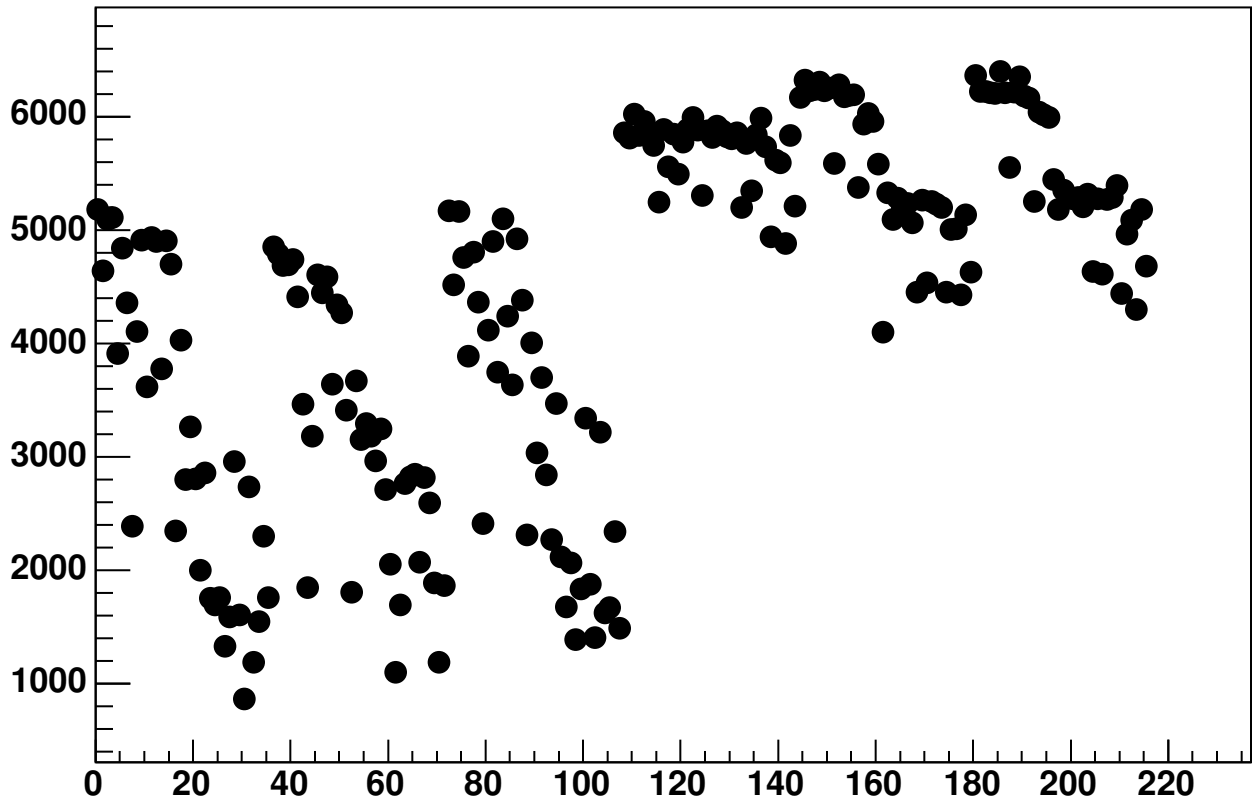
Enable 5, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



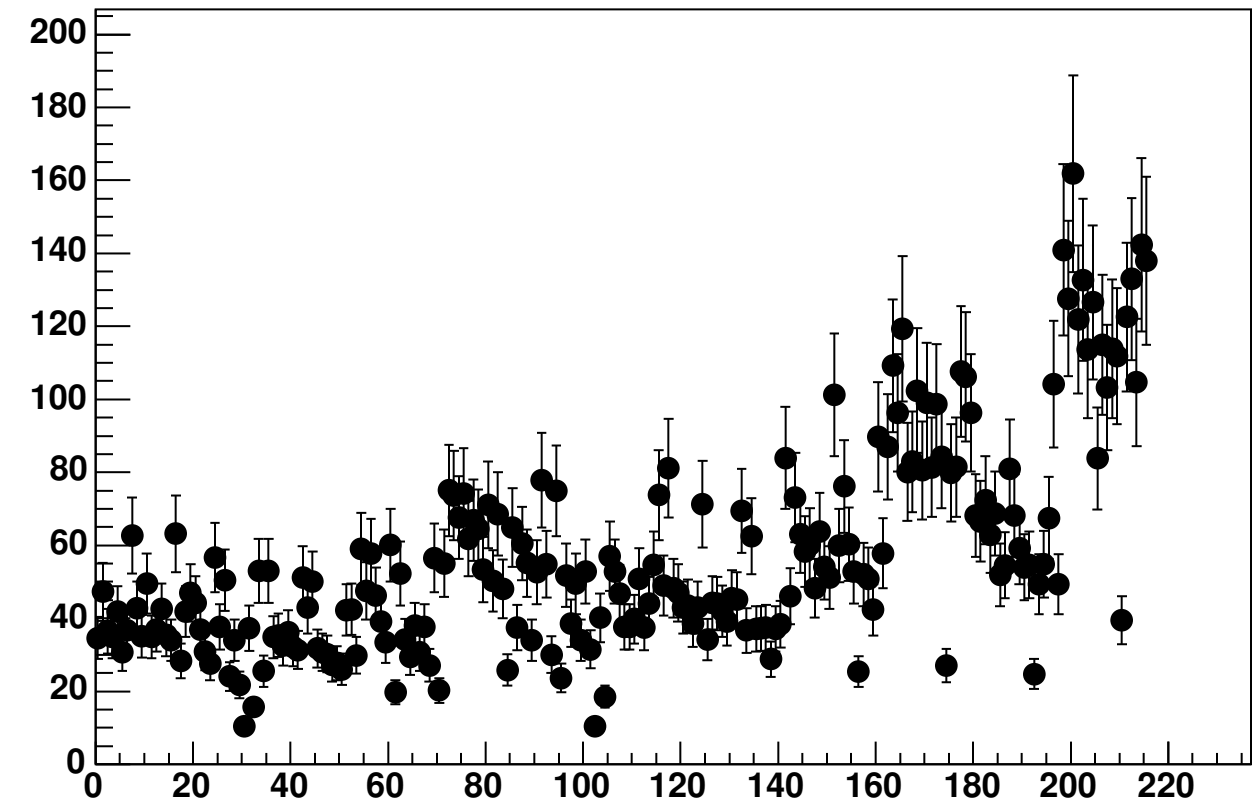
Enable 5, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



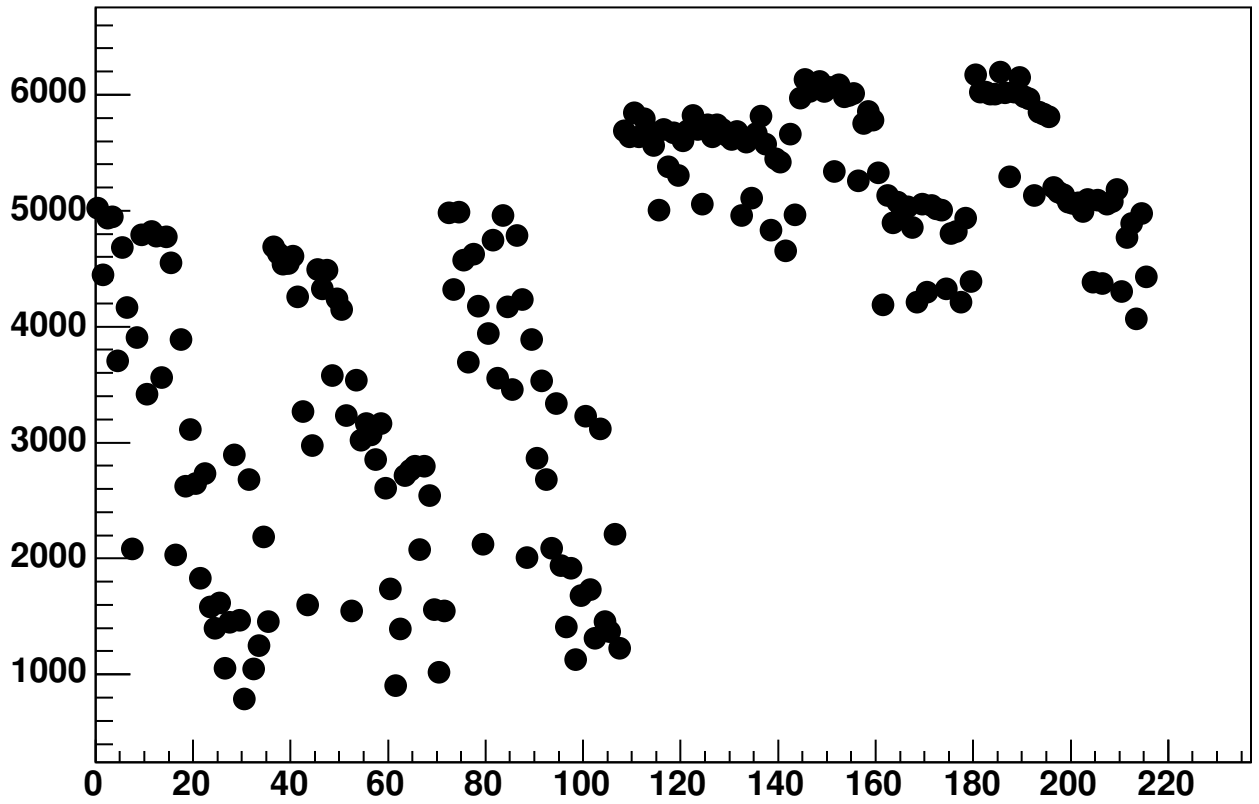
Enable 5, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



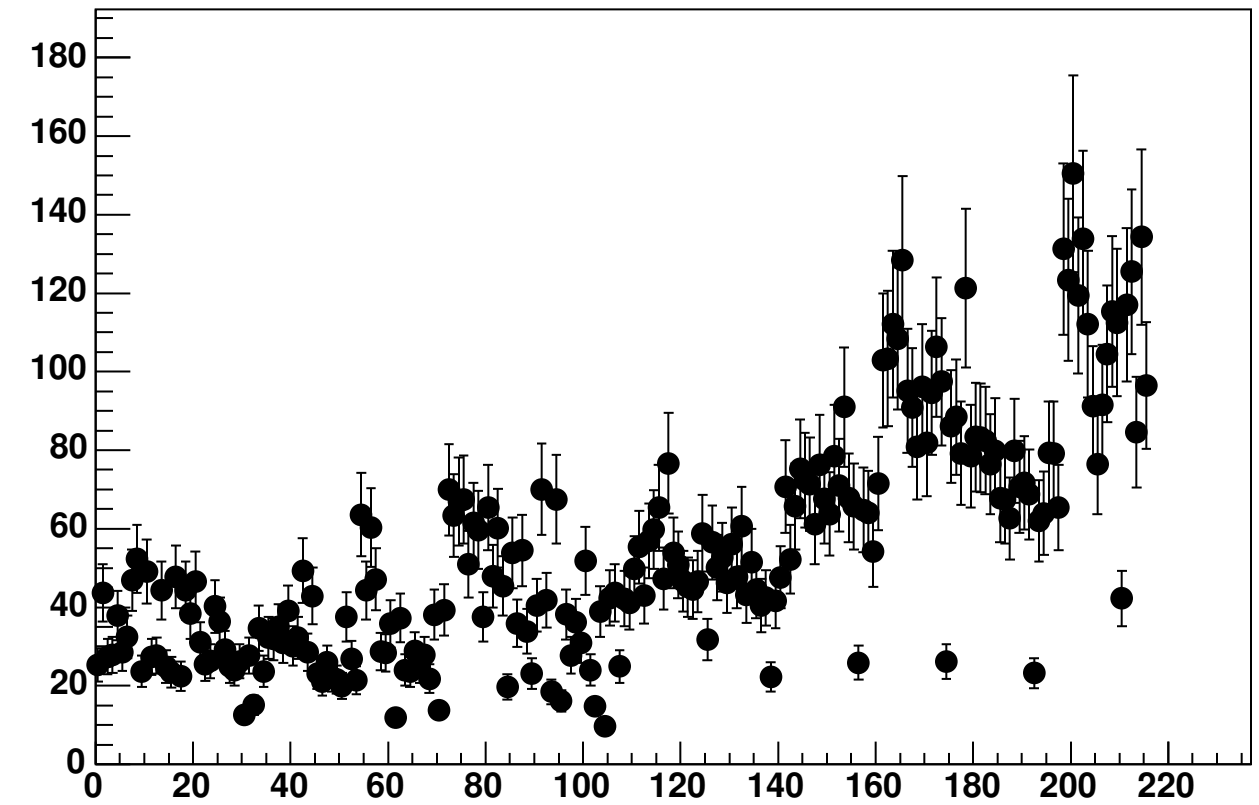
Enable 5, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



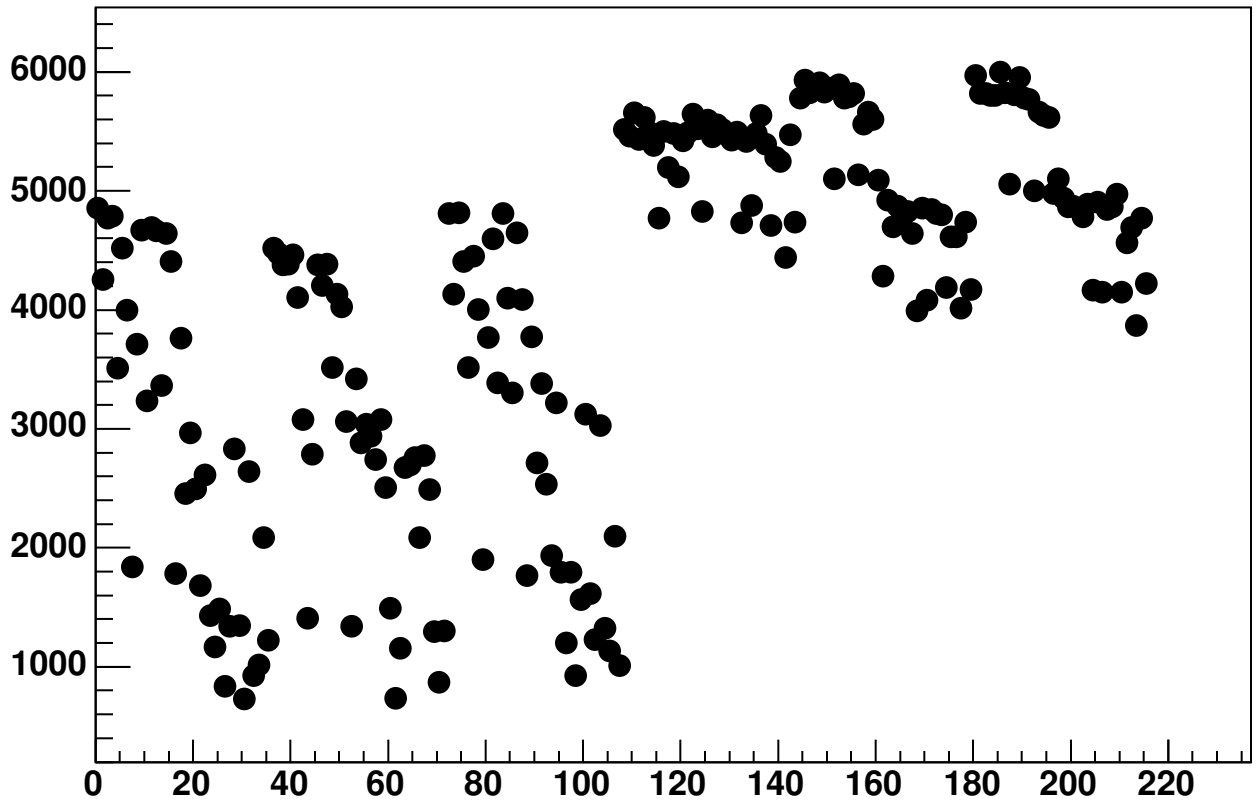
Enable 5, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



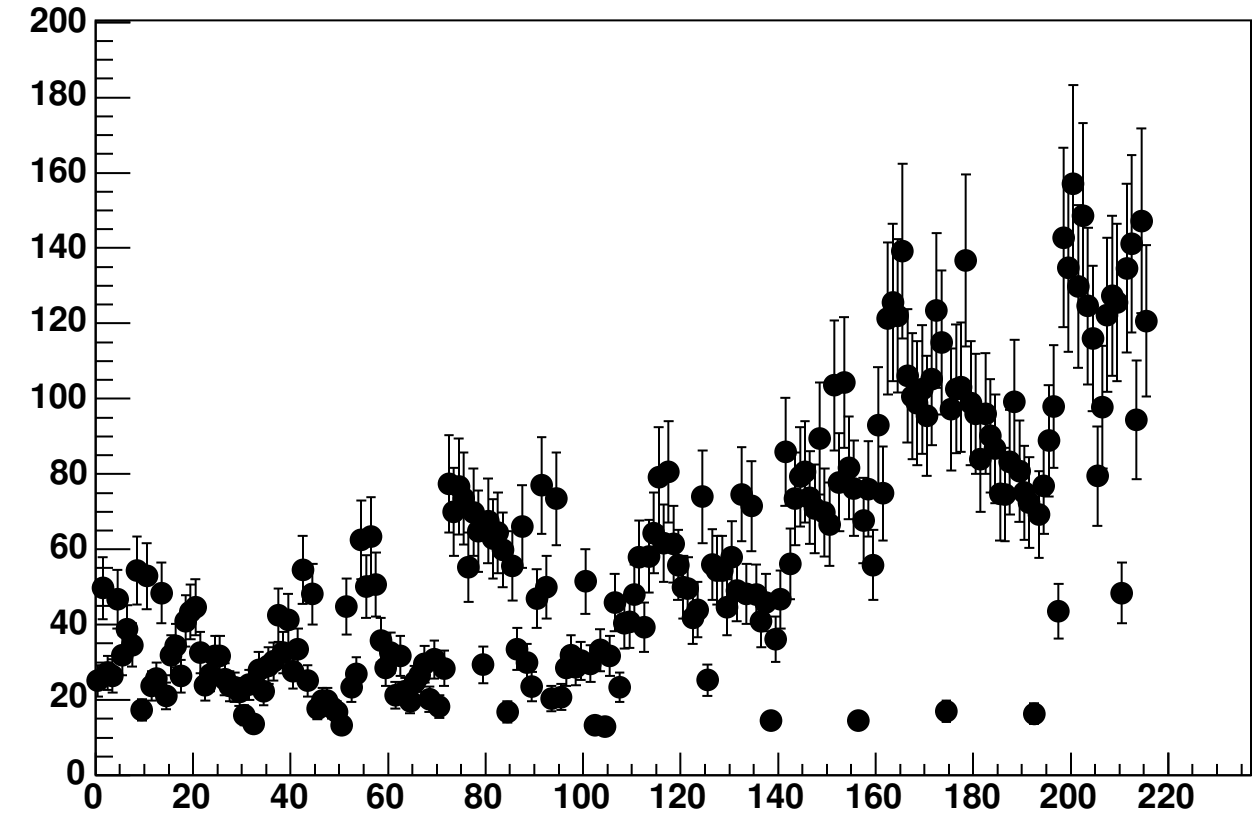
Enable 5, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



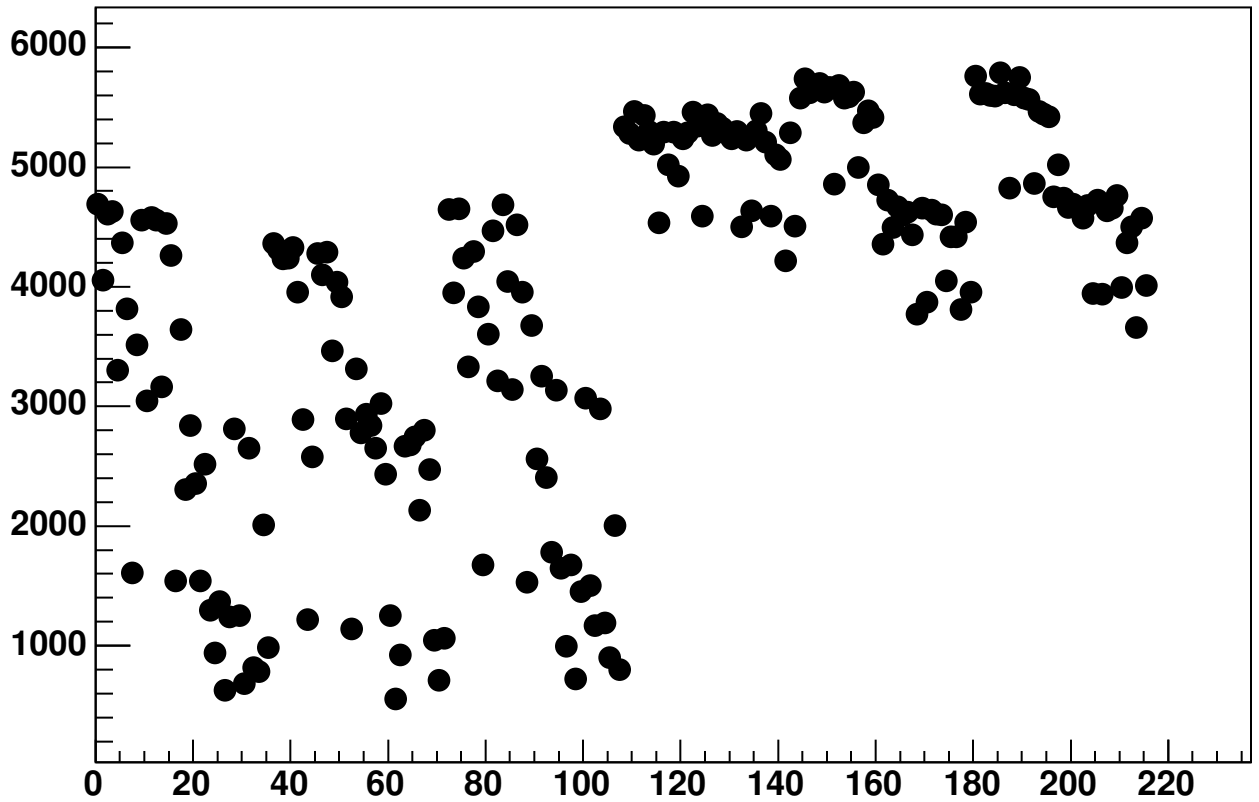
Enable 5, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



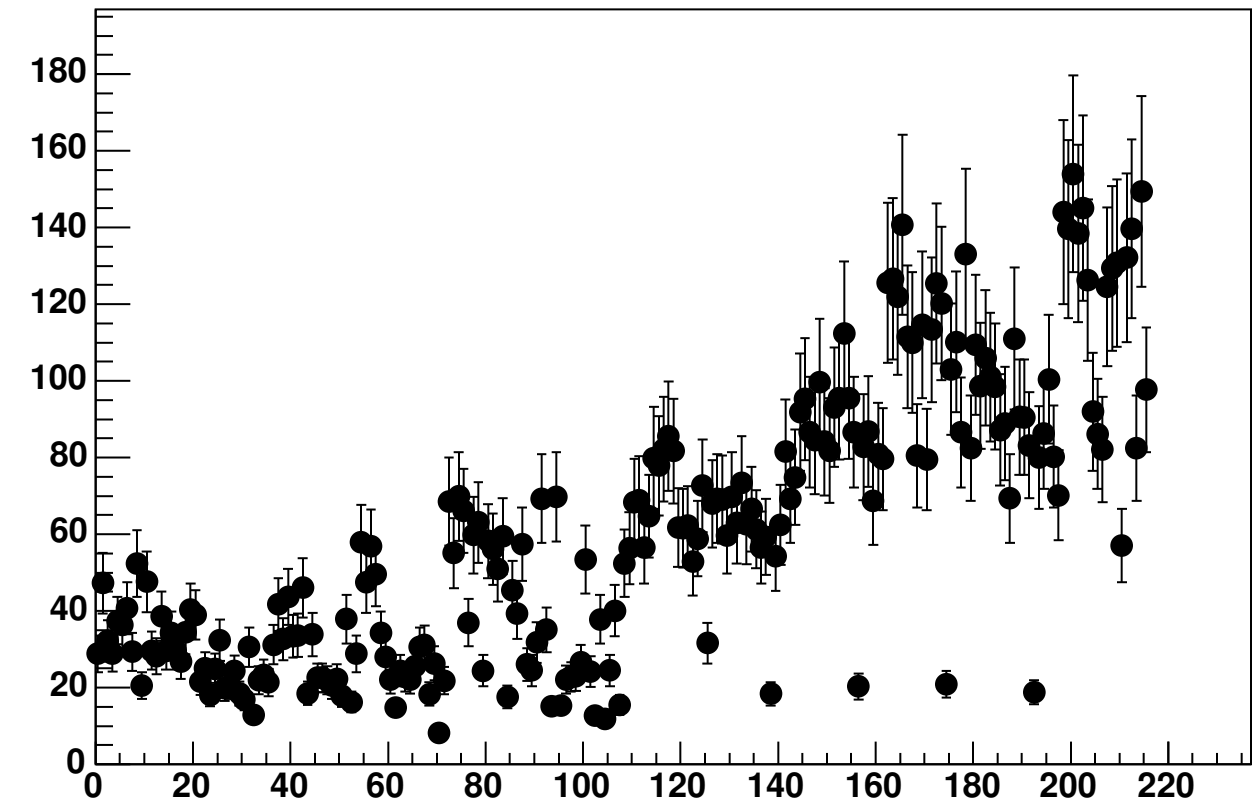
Enable 5, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



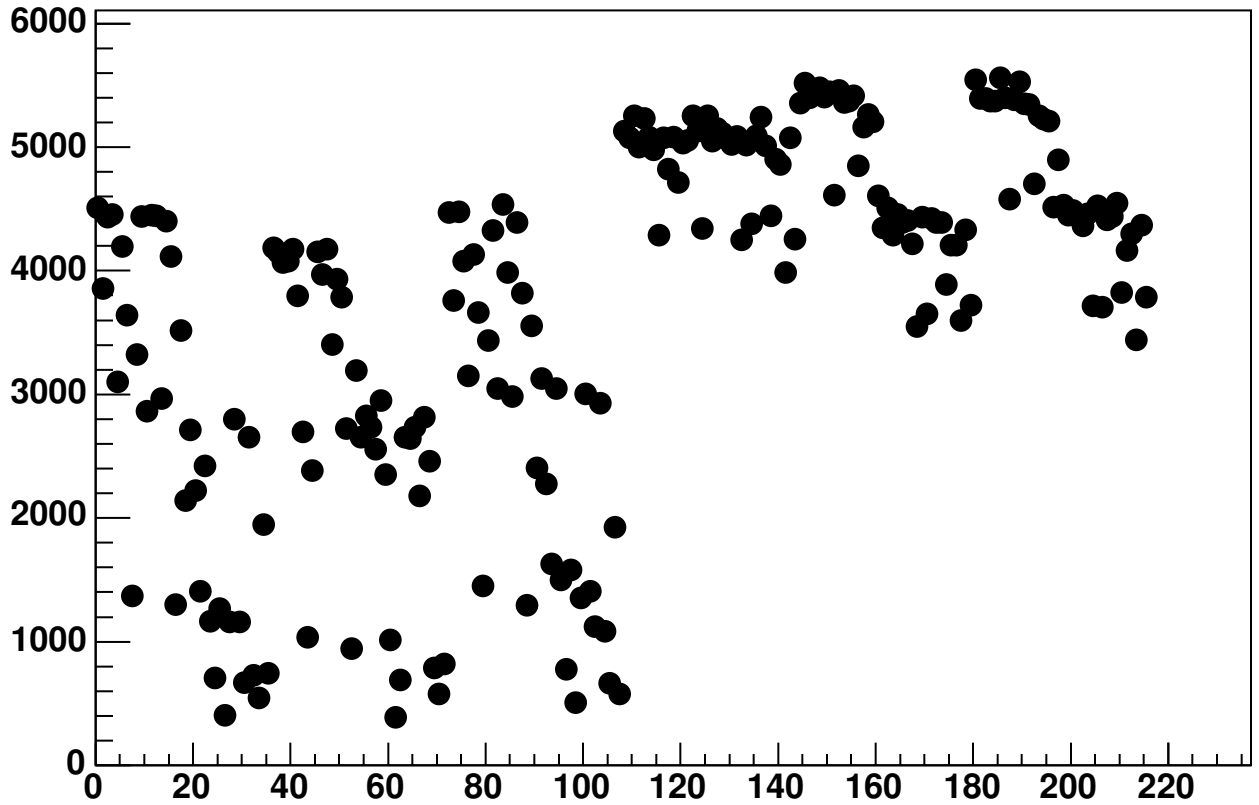
Enable 5, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



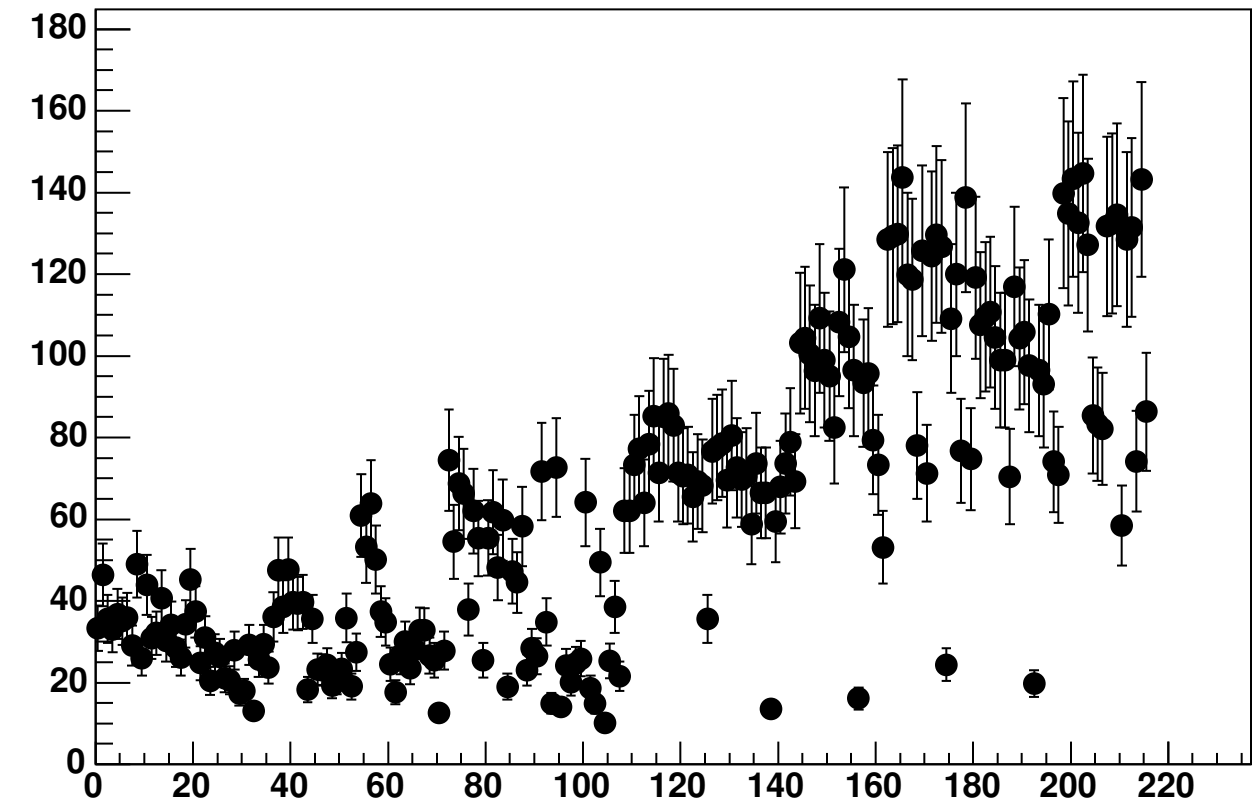
Enable 5, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



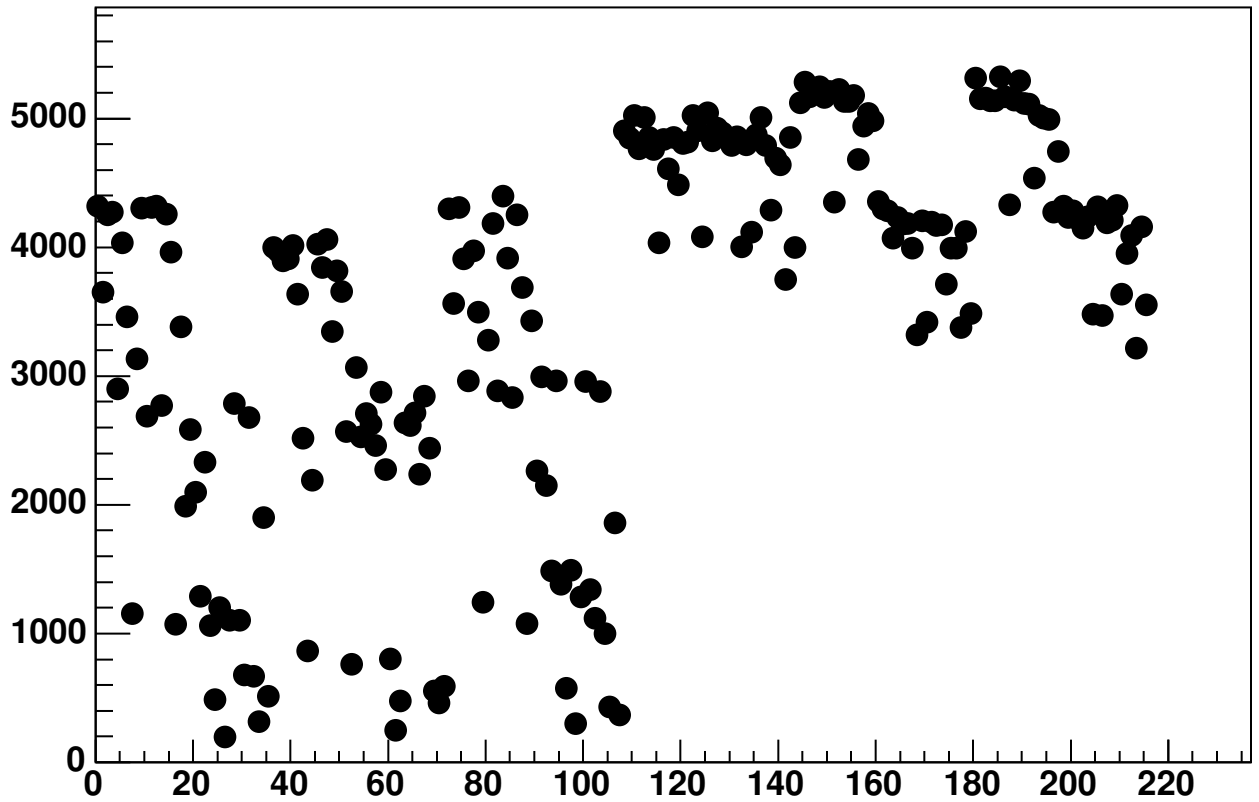
Enable 5, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



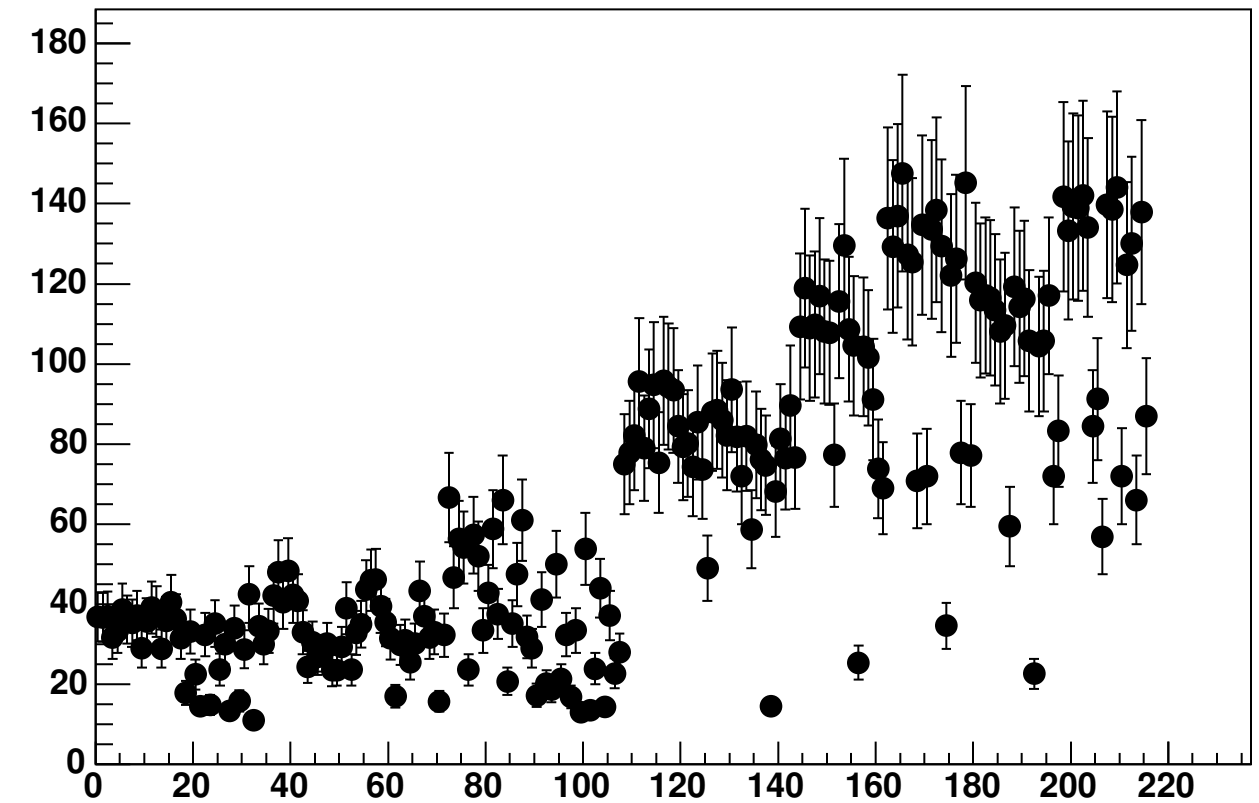
Enable 5, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



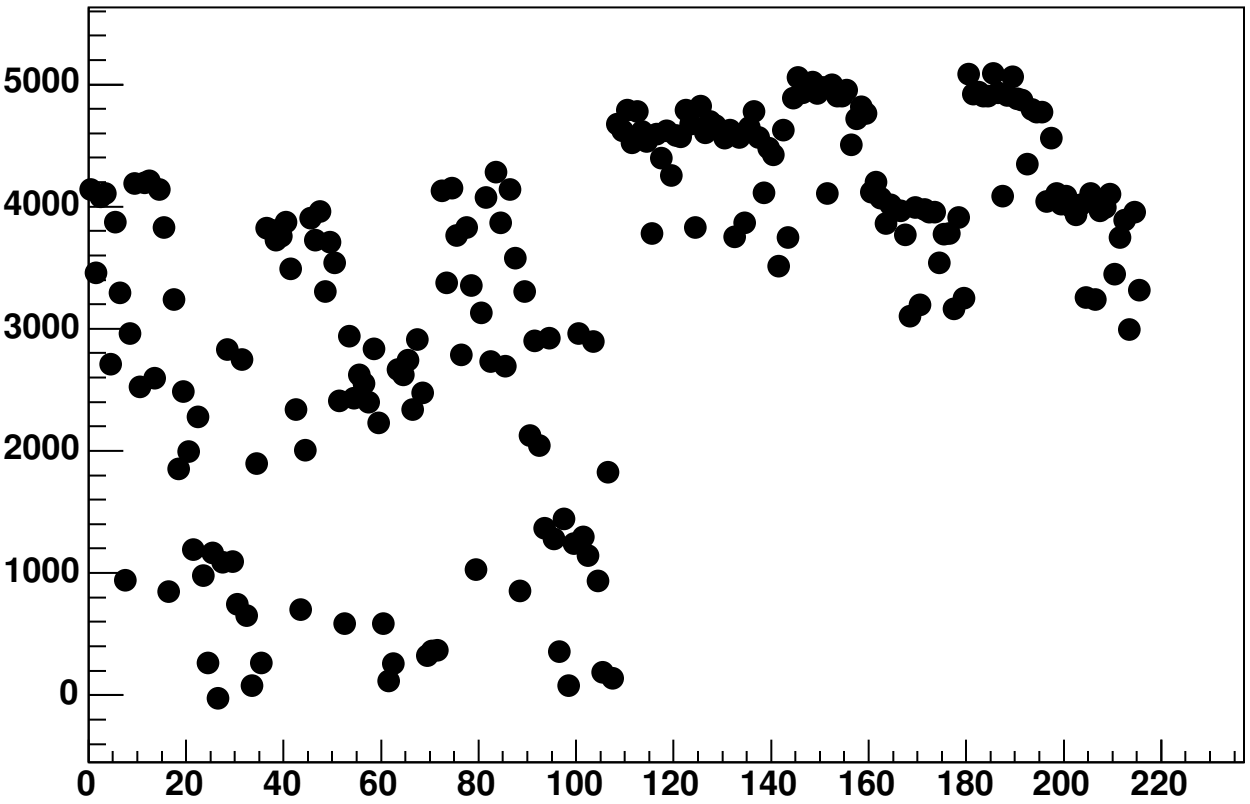
Enable 5, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



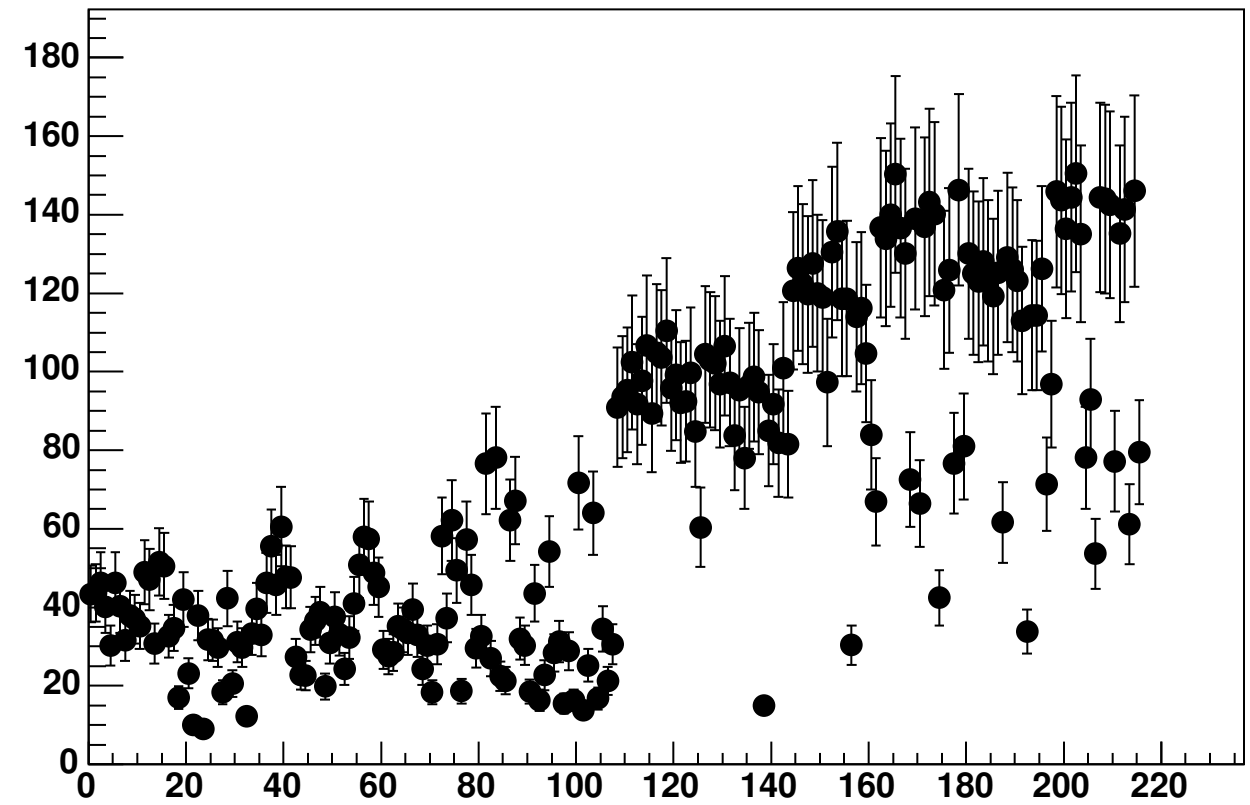
Enable 5, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



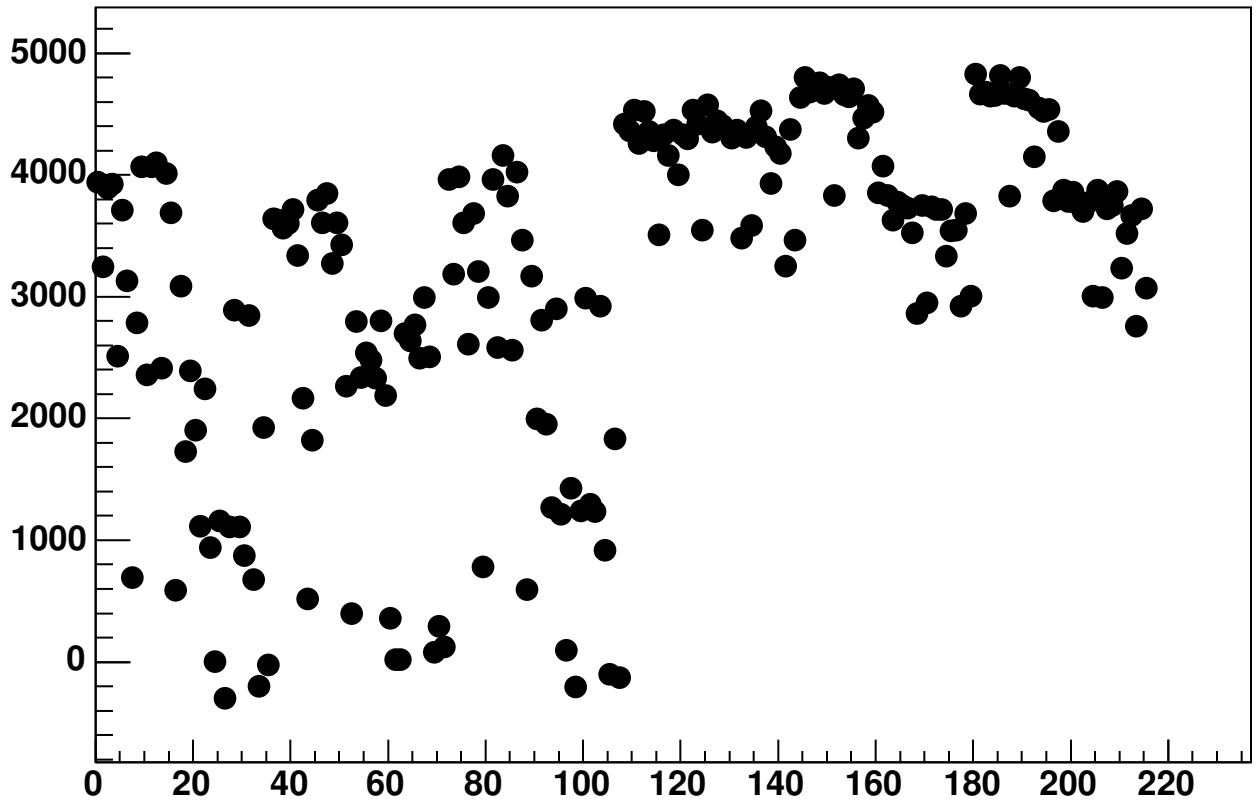
Enable 5, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



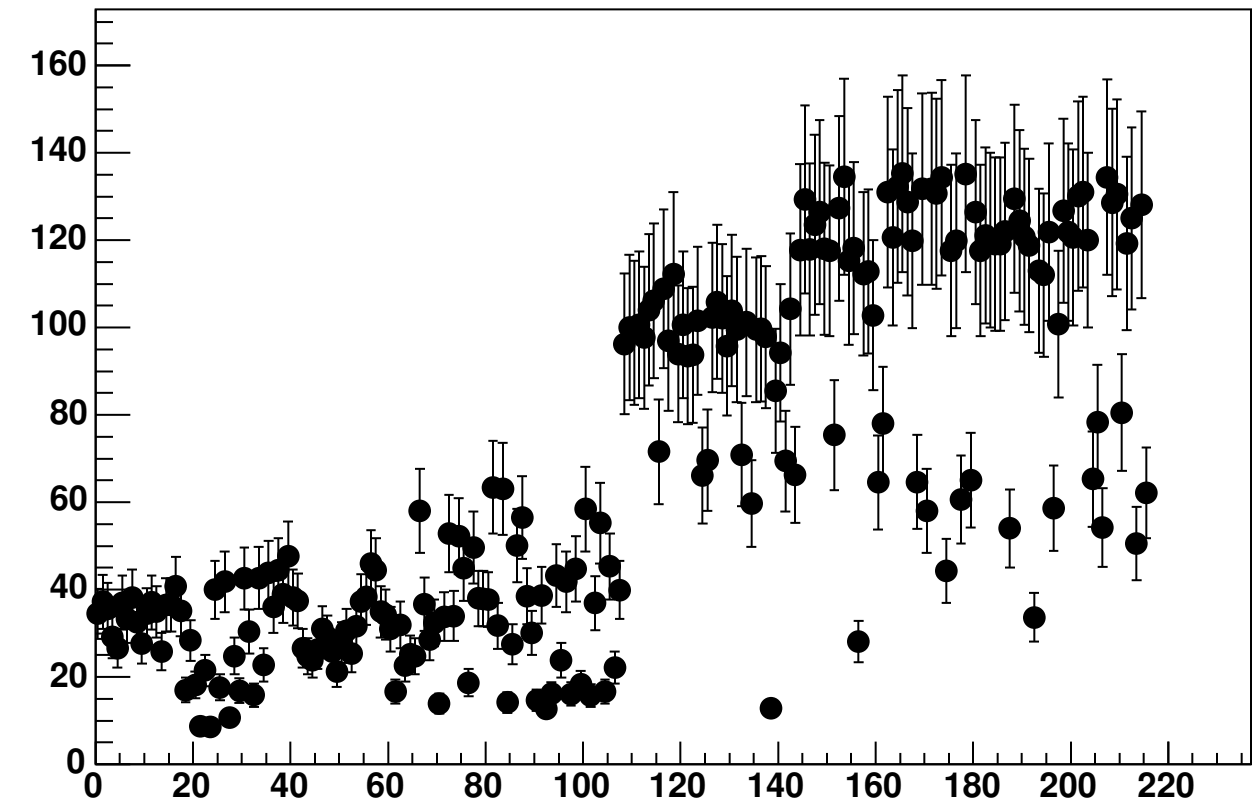
Enable 5, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



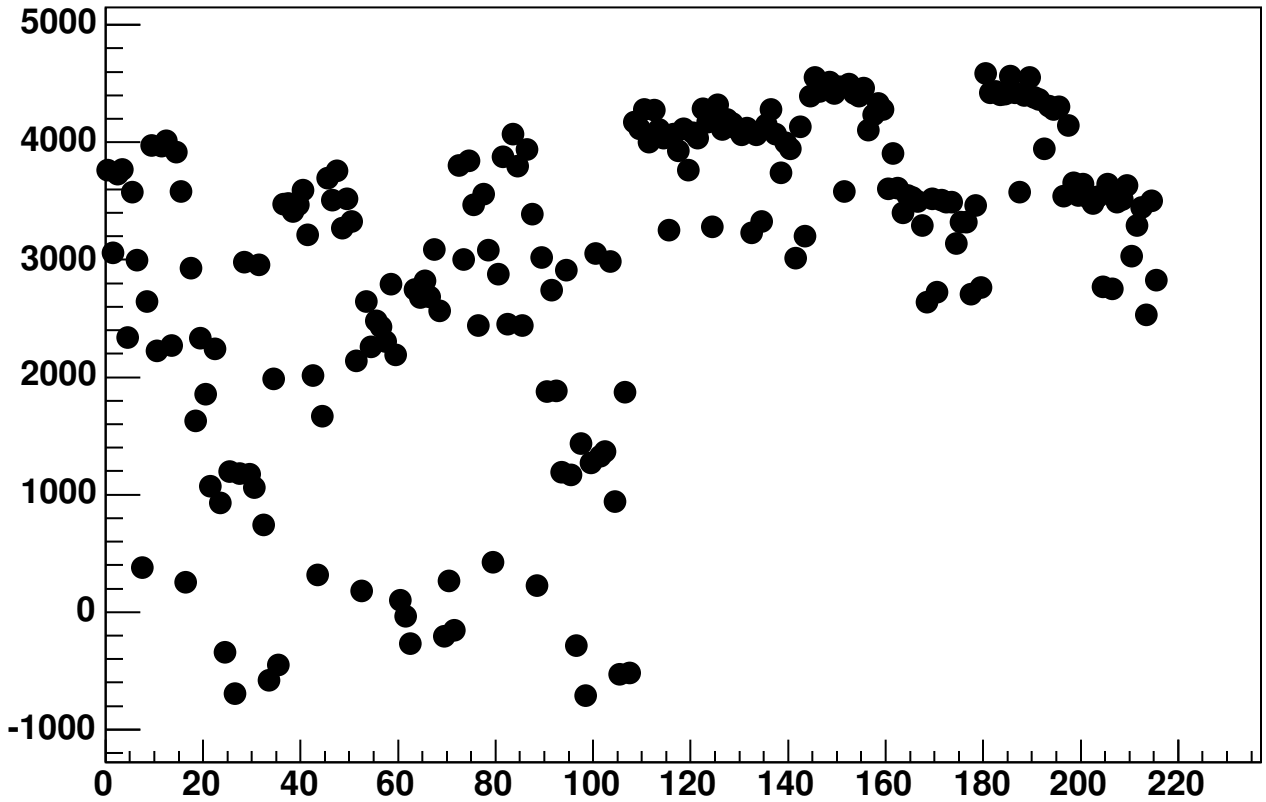
Enable 5, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



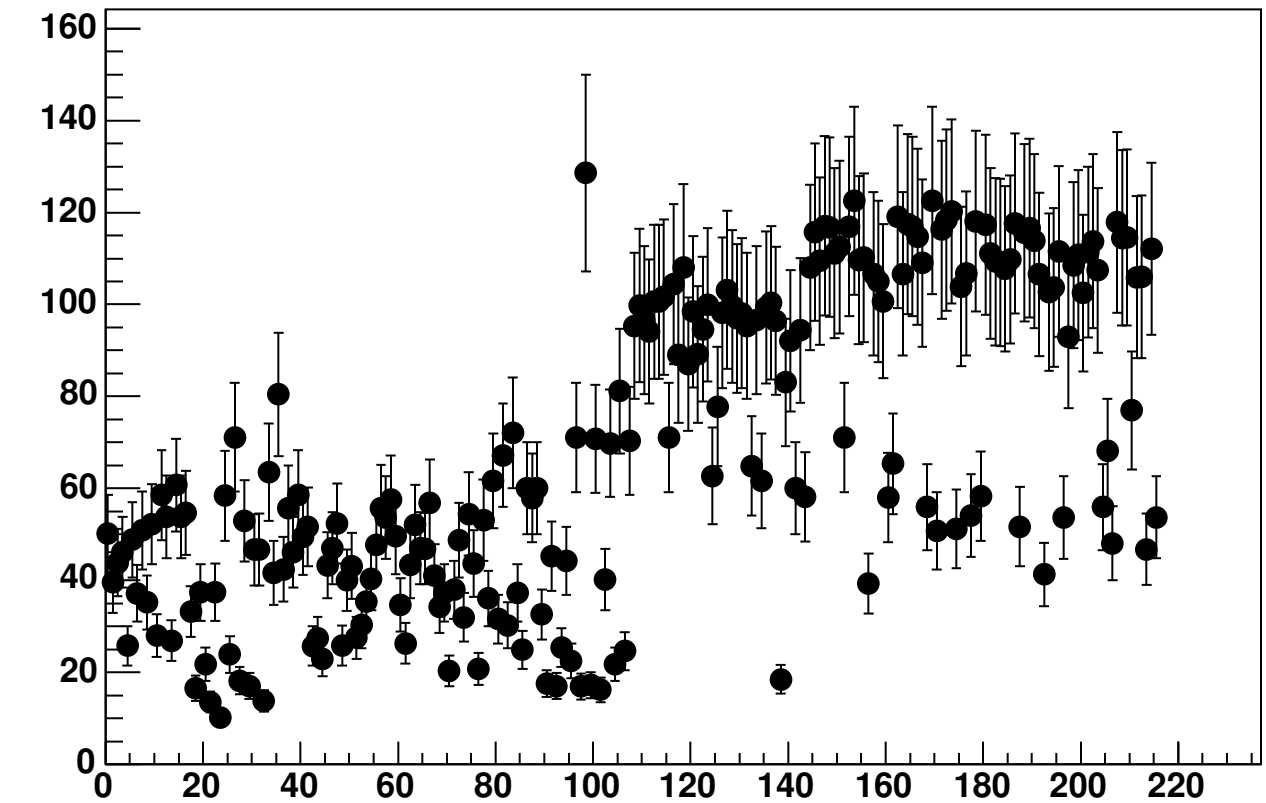
Enable 5, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



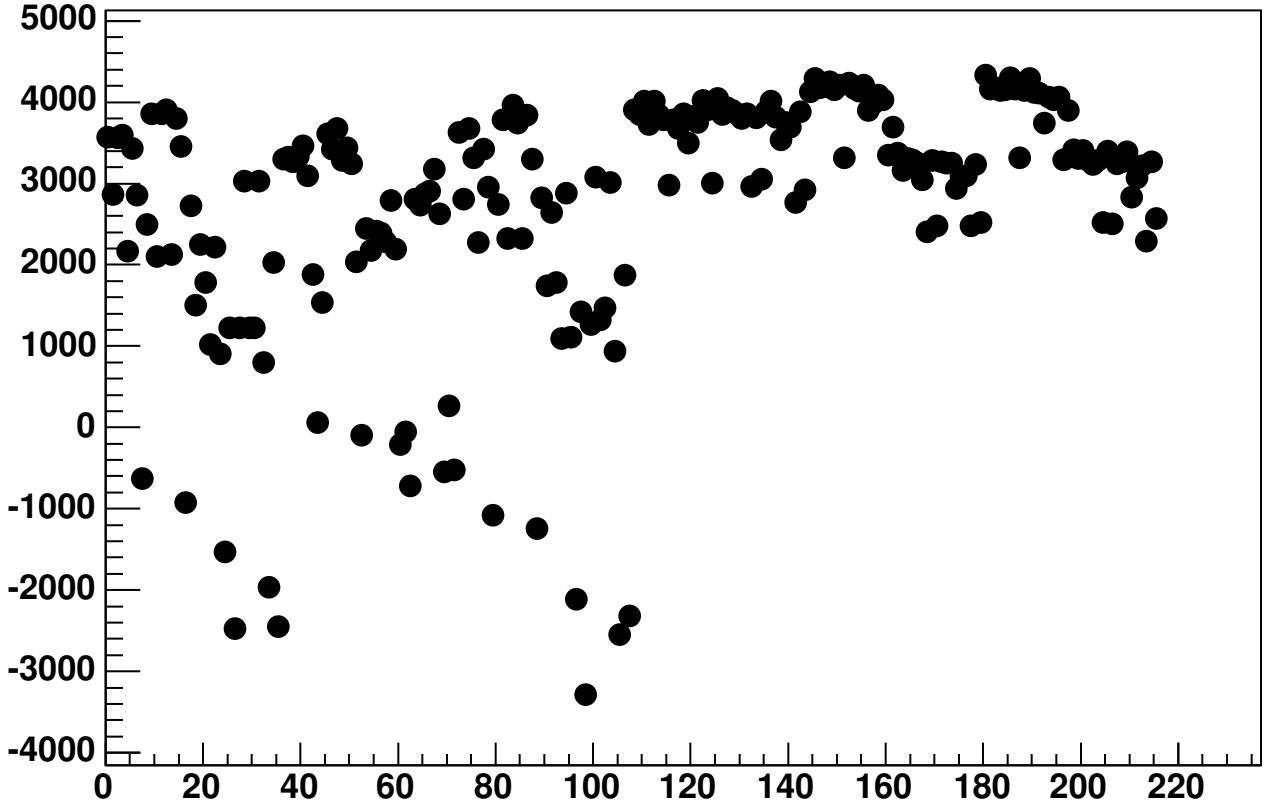
Enable 5, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



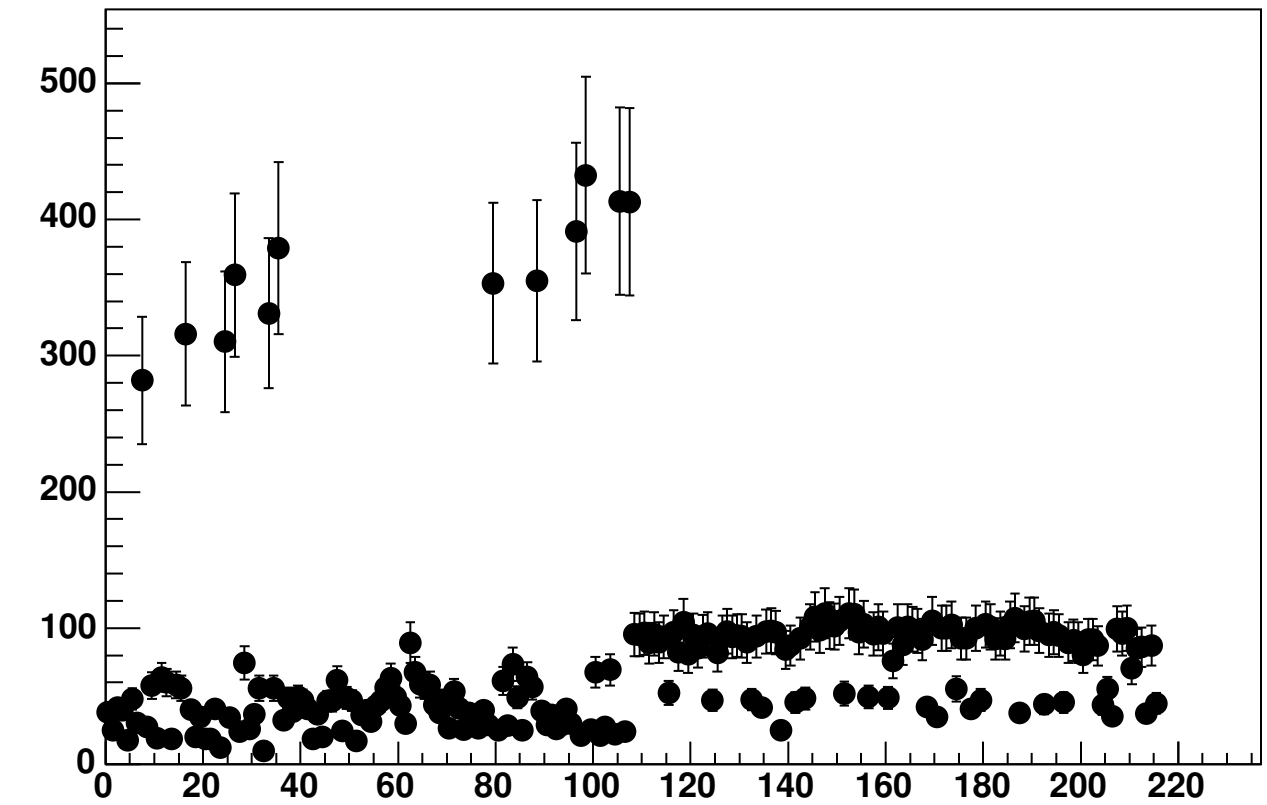
Enable 5, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



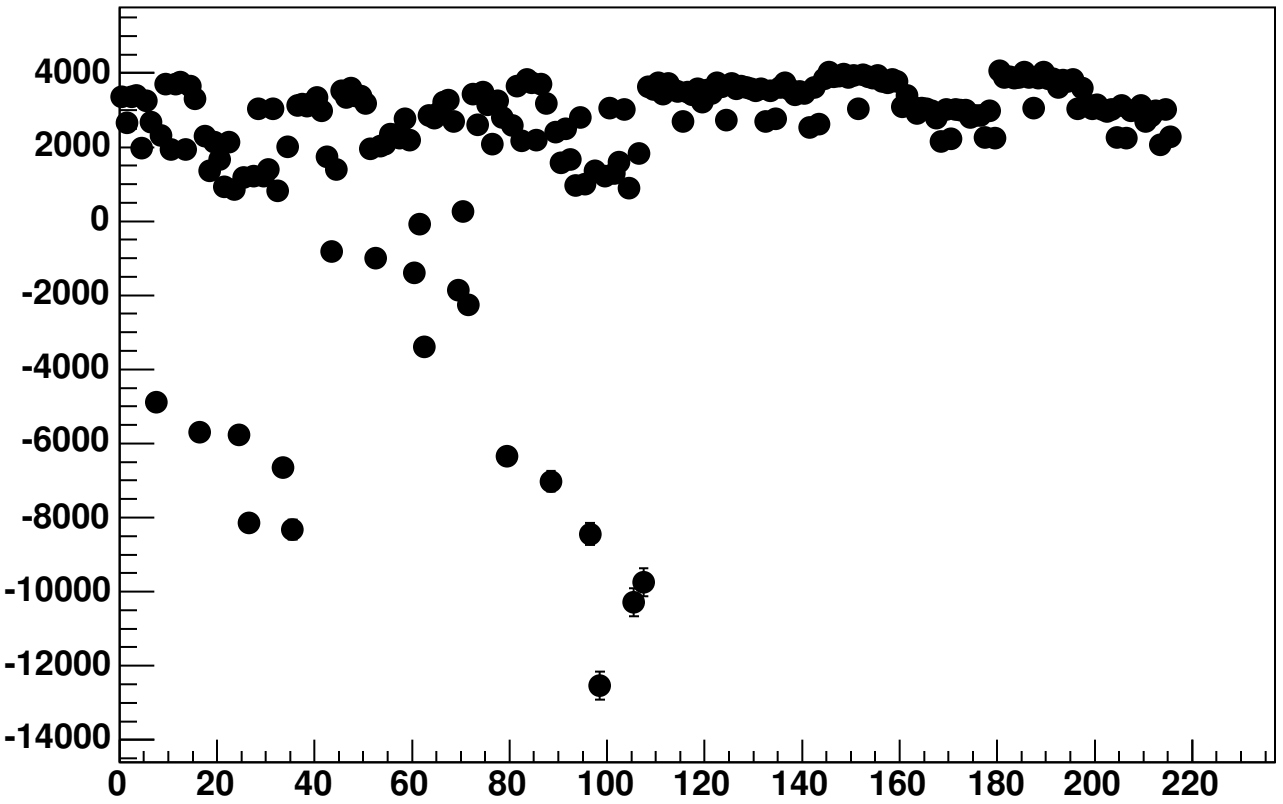
Enable 5, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



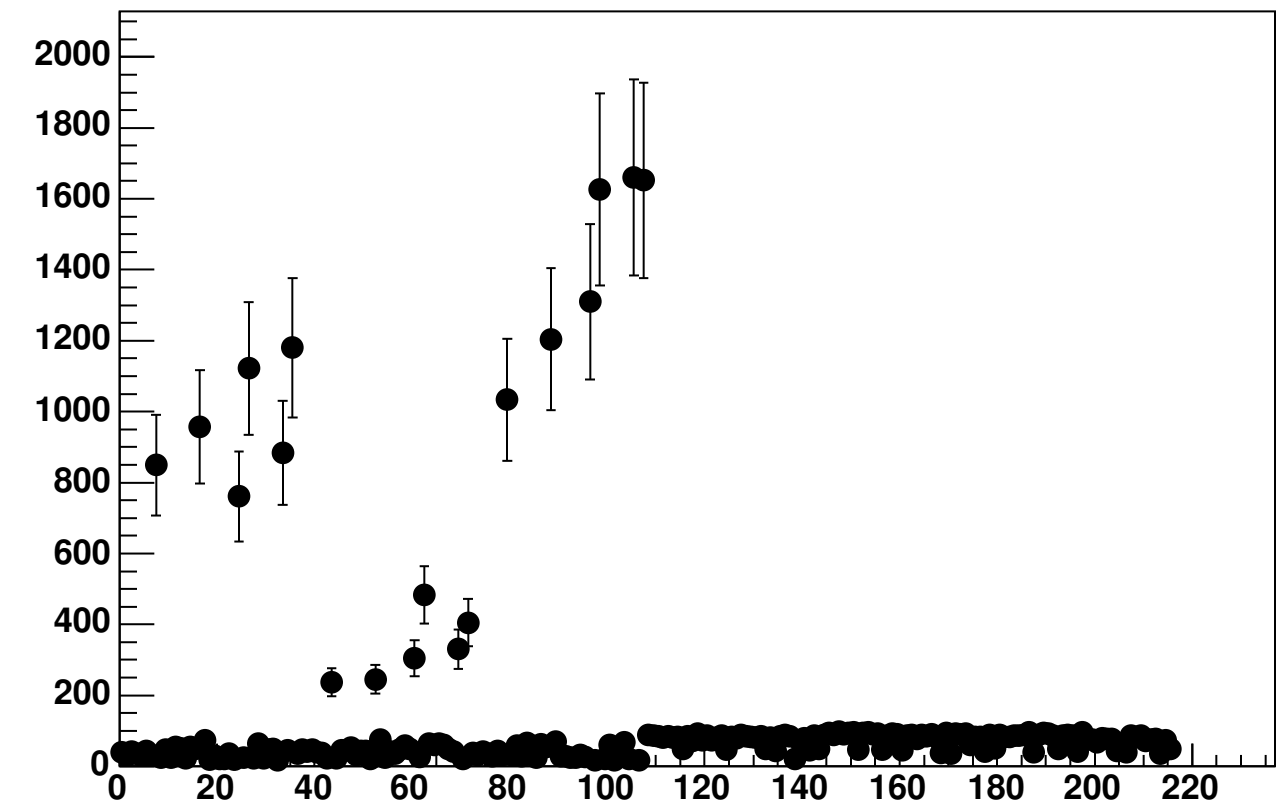
Enable 5, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



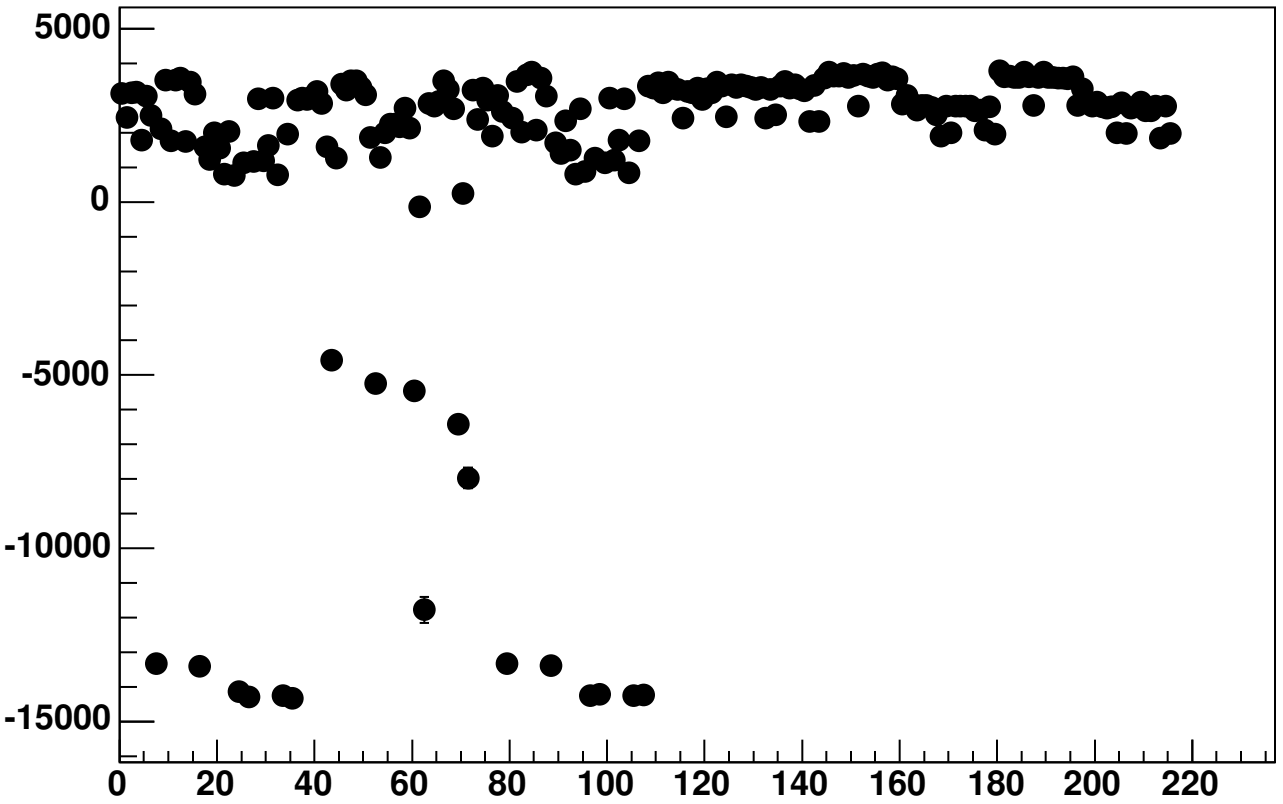
Enable 5, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



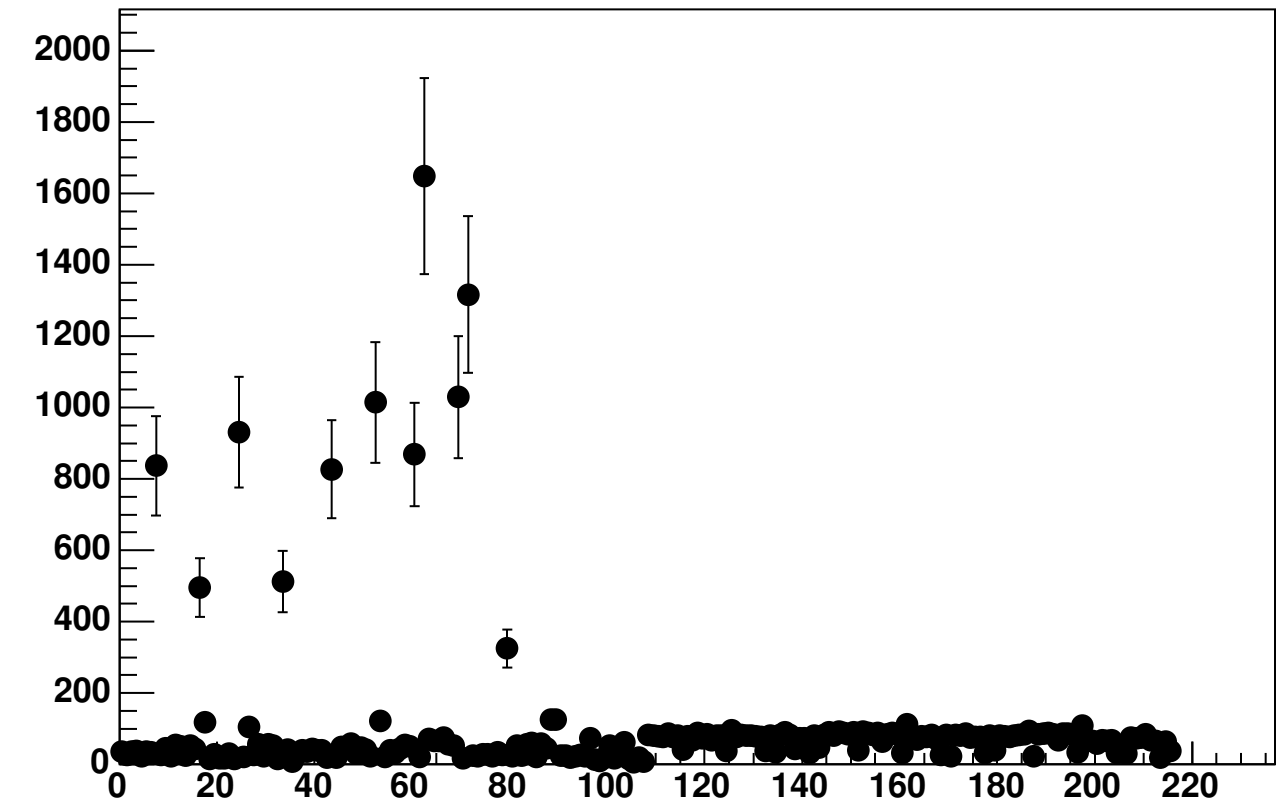
Enable 5, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



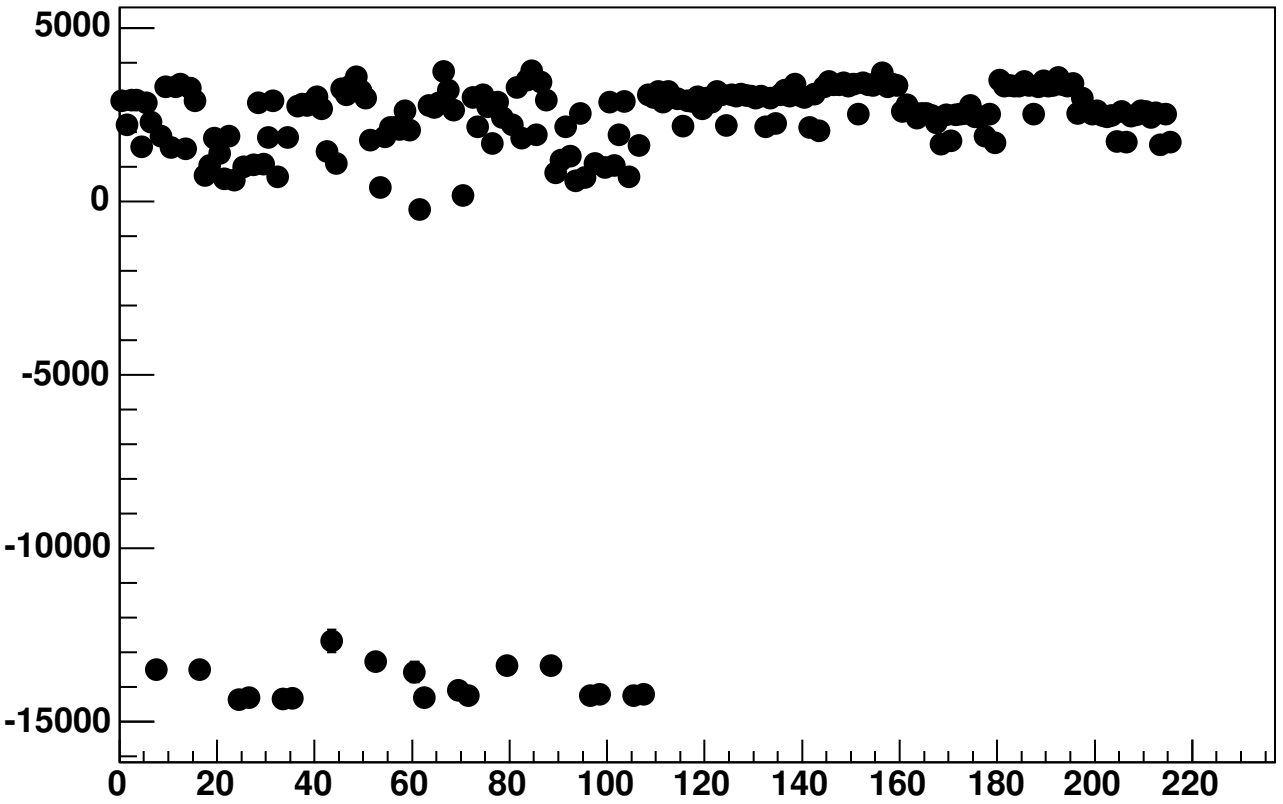
Enable 5, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



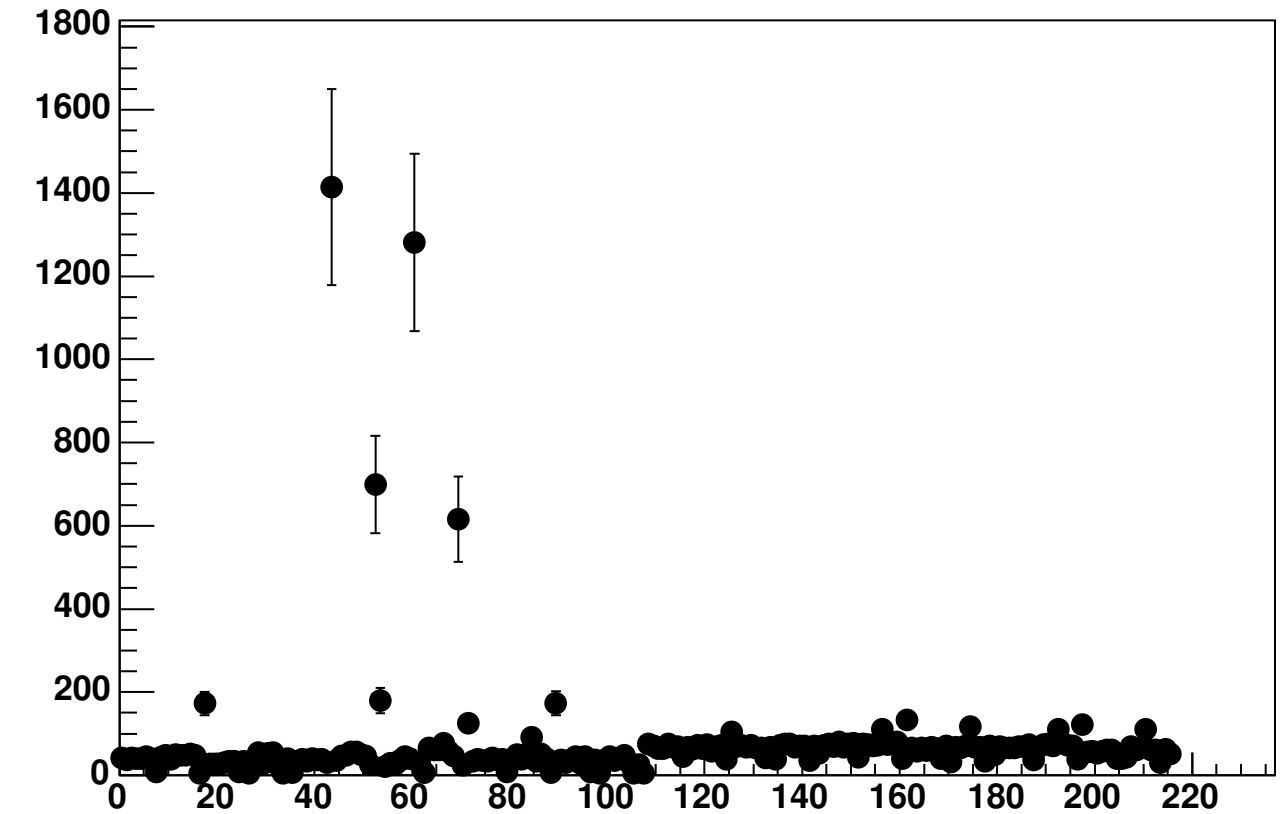
Enable 5, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



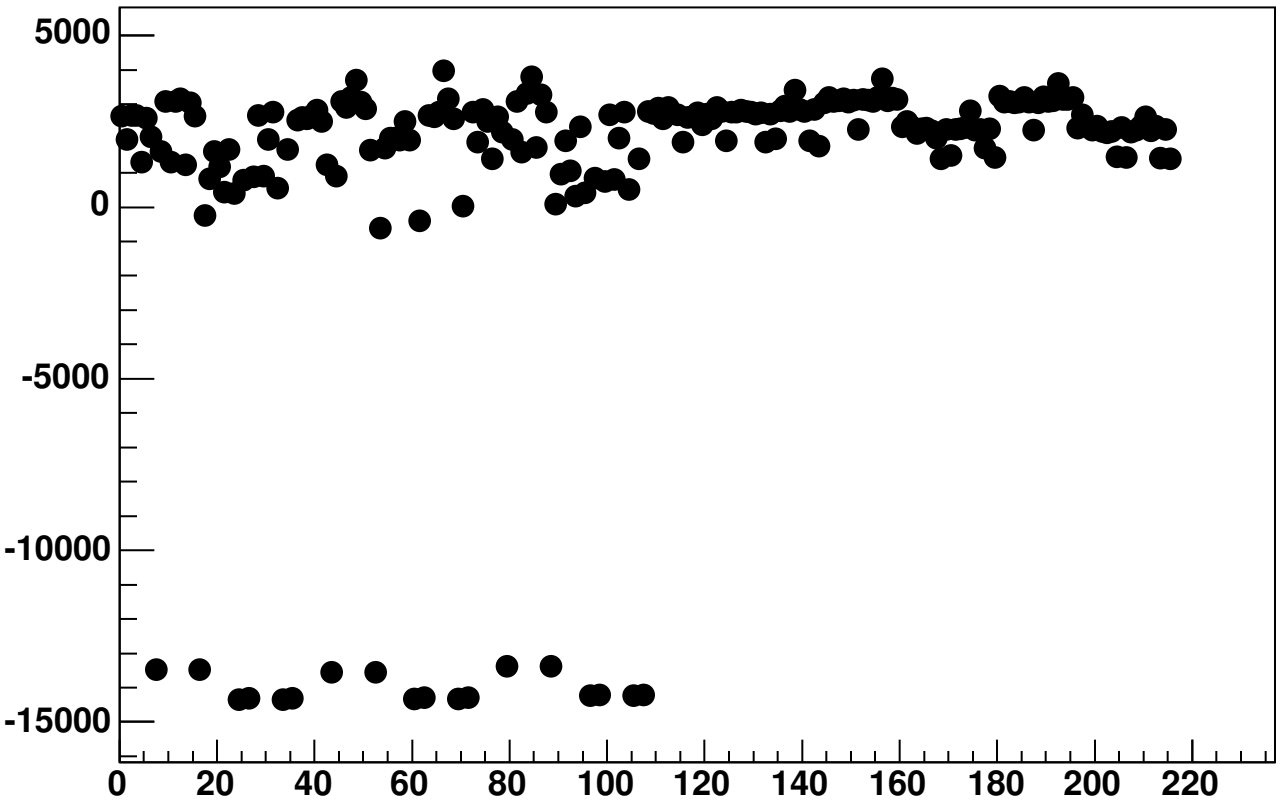
Enable 5, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



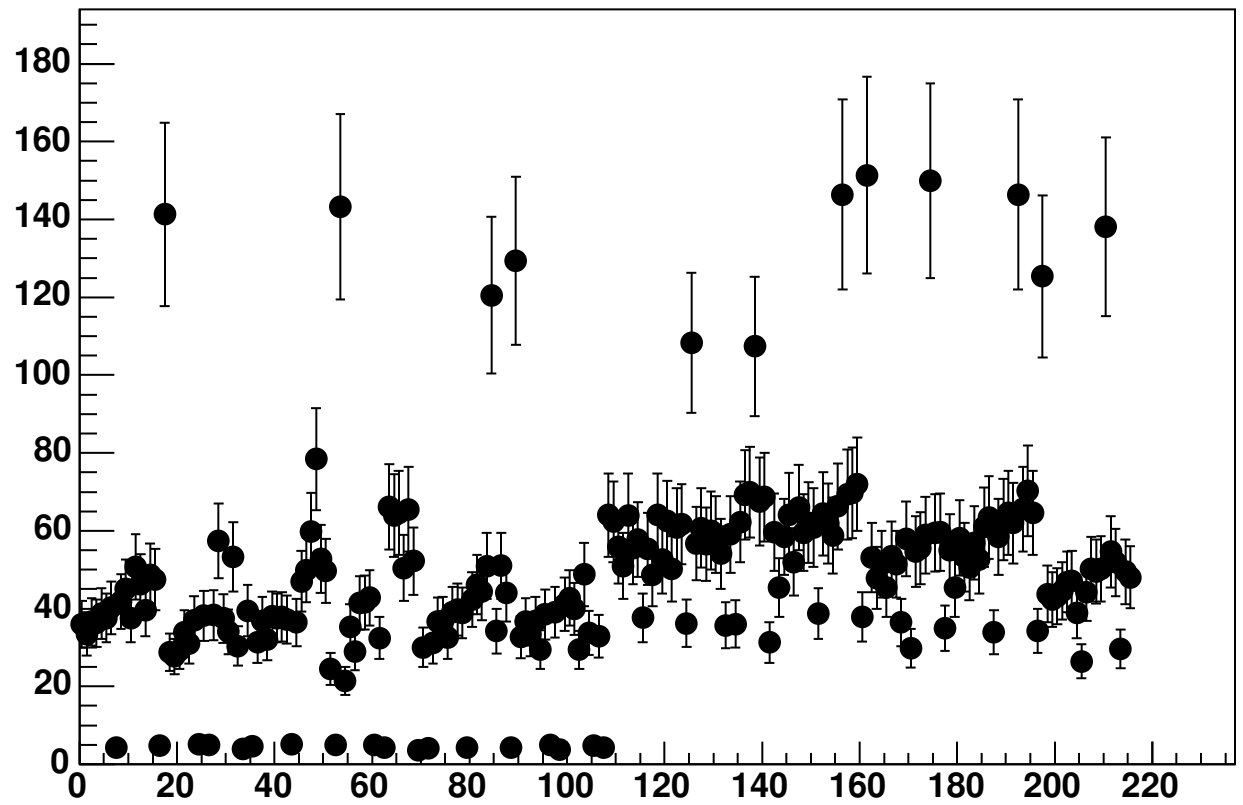
Enable 5, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



Enable 5, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan

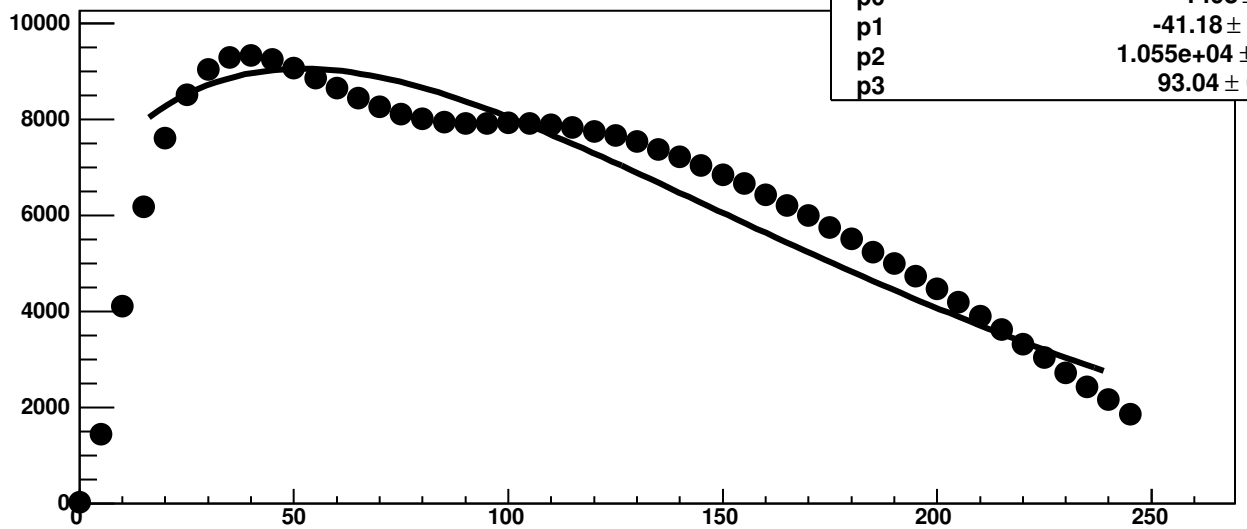


Enable 5, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan

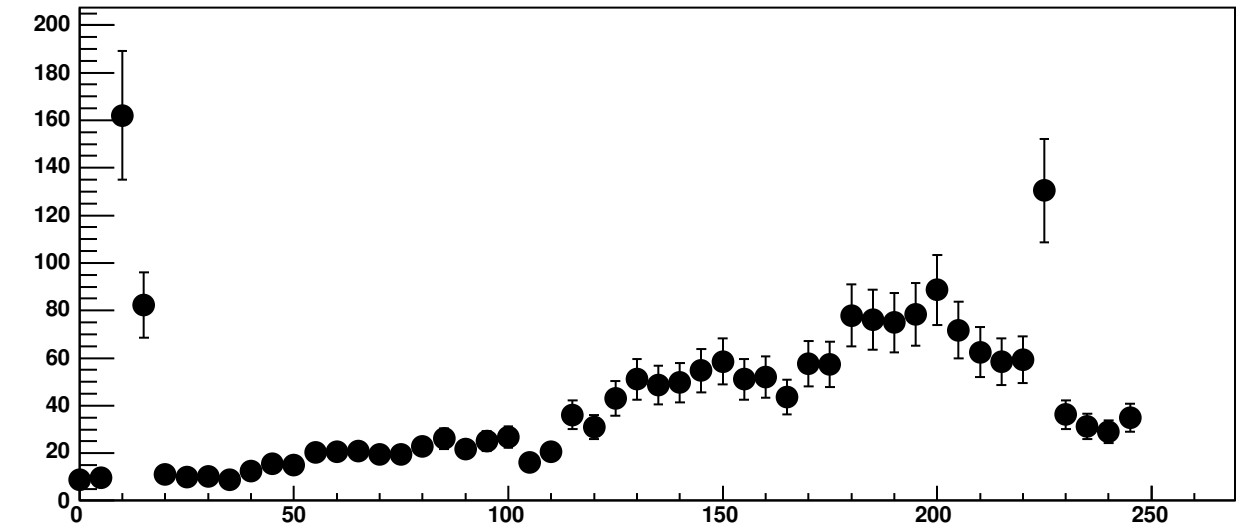


Chip 0, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold

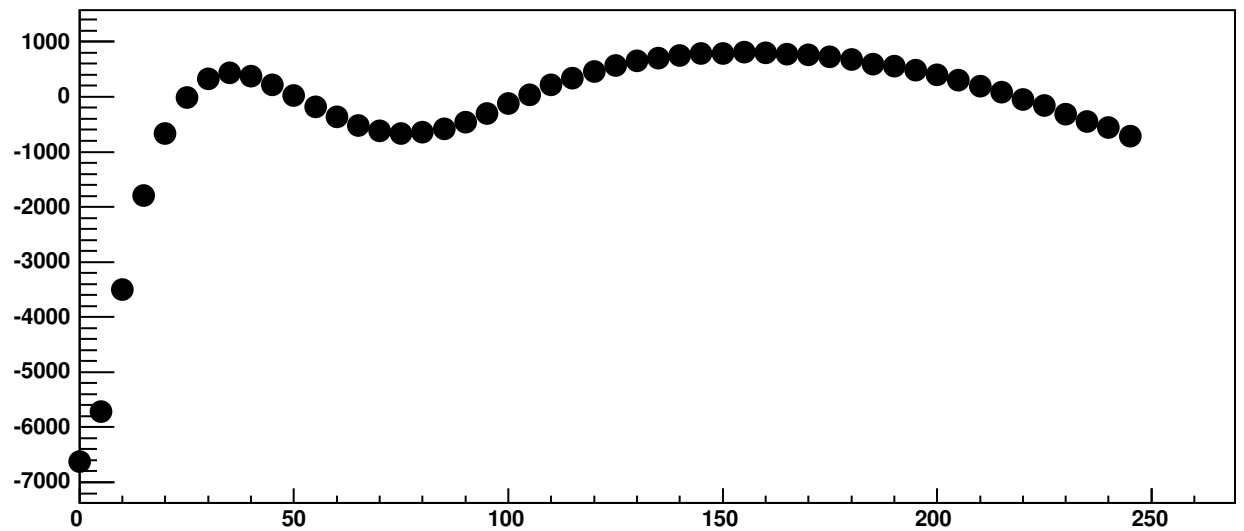
χ^2 / ndf	3.297e+05 / 42
p0	-1498 ± 30.21
p1	-41.18 ± 0.2457
p2	1.055e+04 ± 29.35
p3	93.04 ± 0.2486

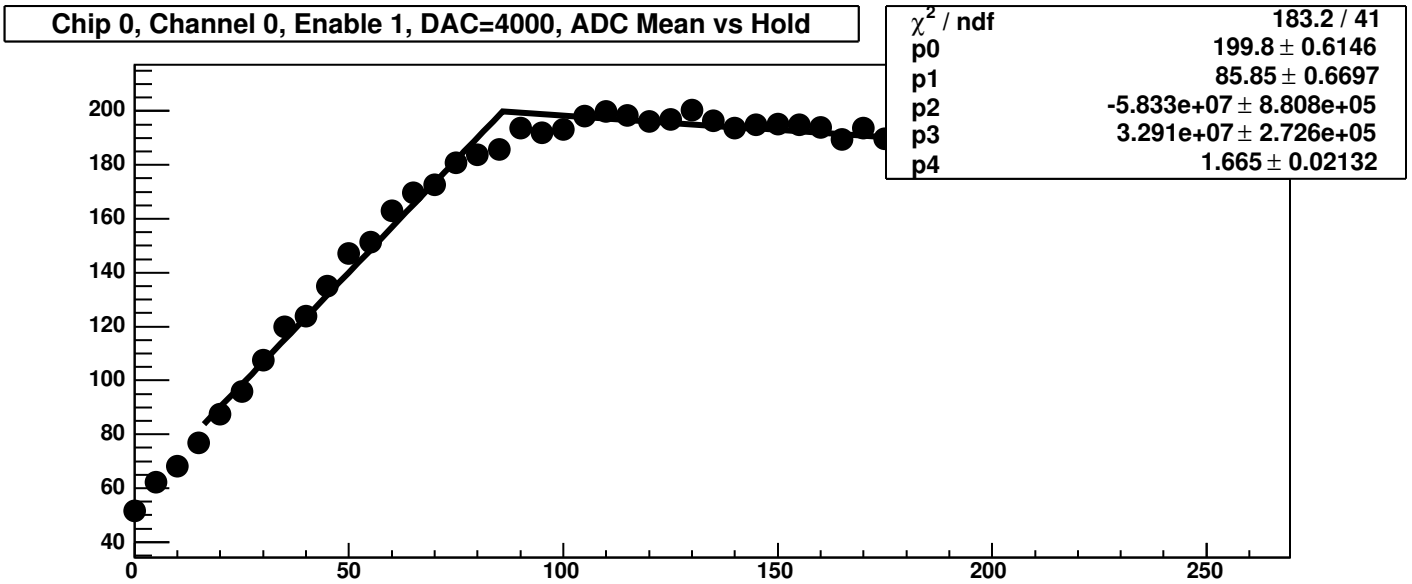


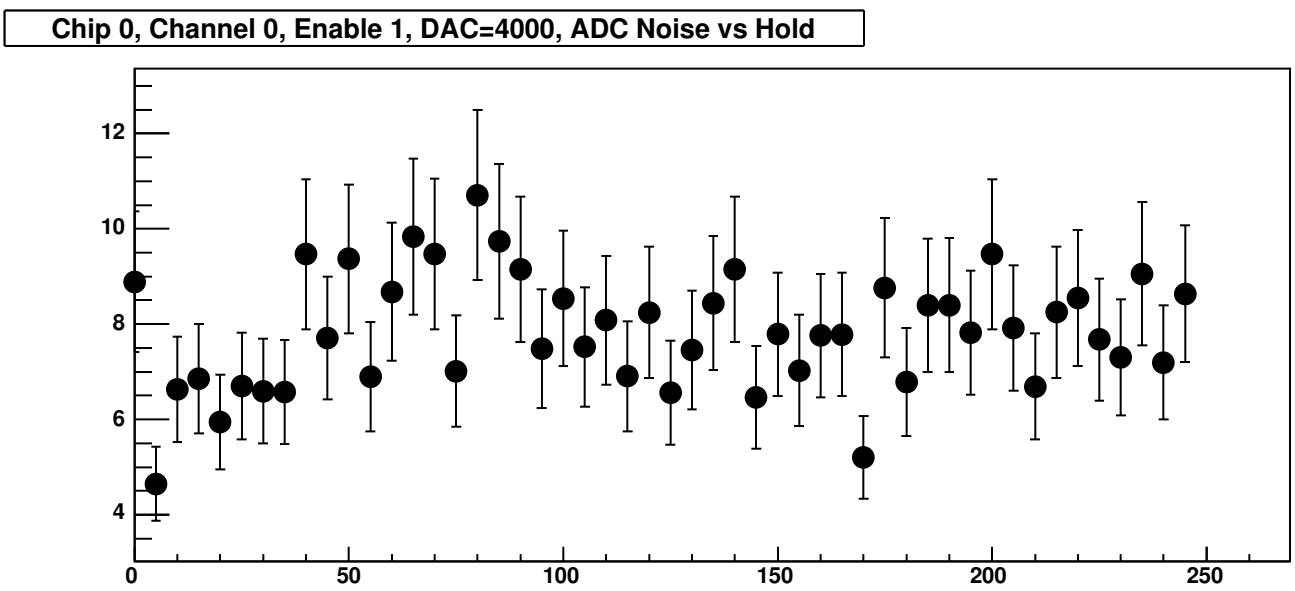
Chip 0, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold

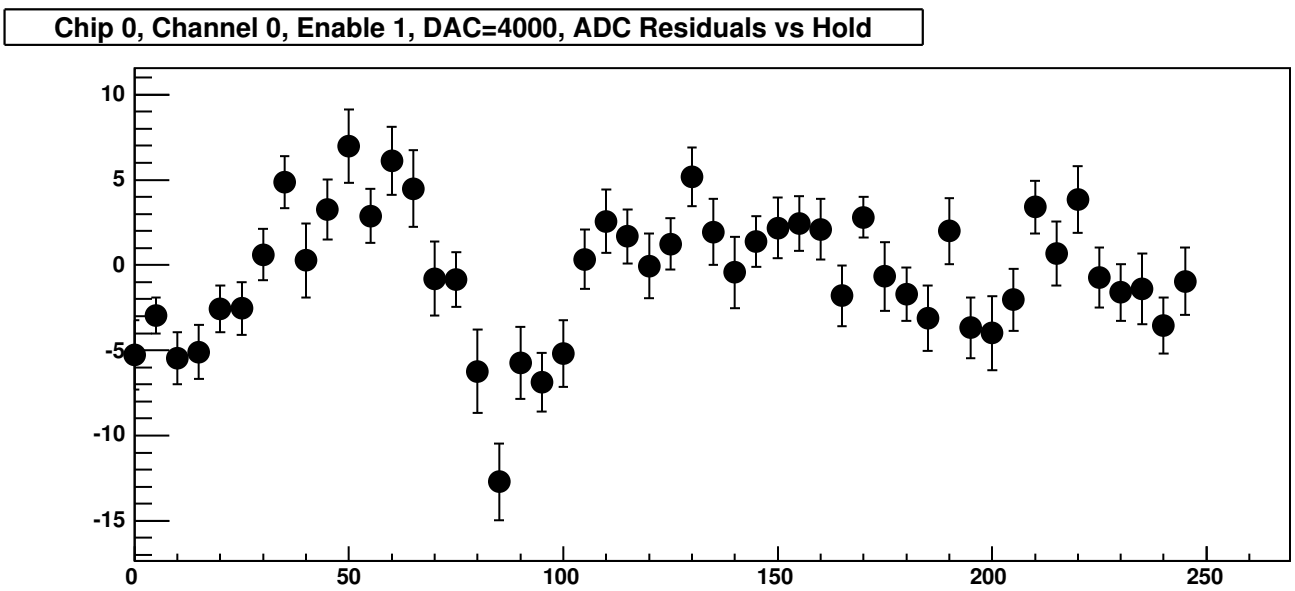


Chip 0, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold

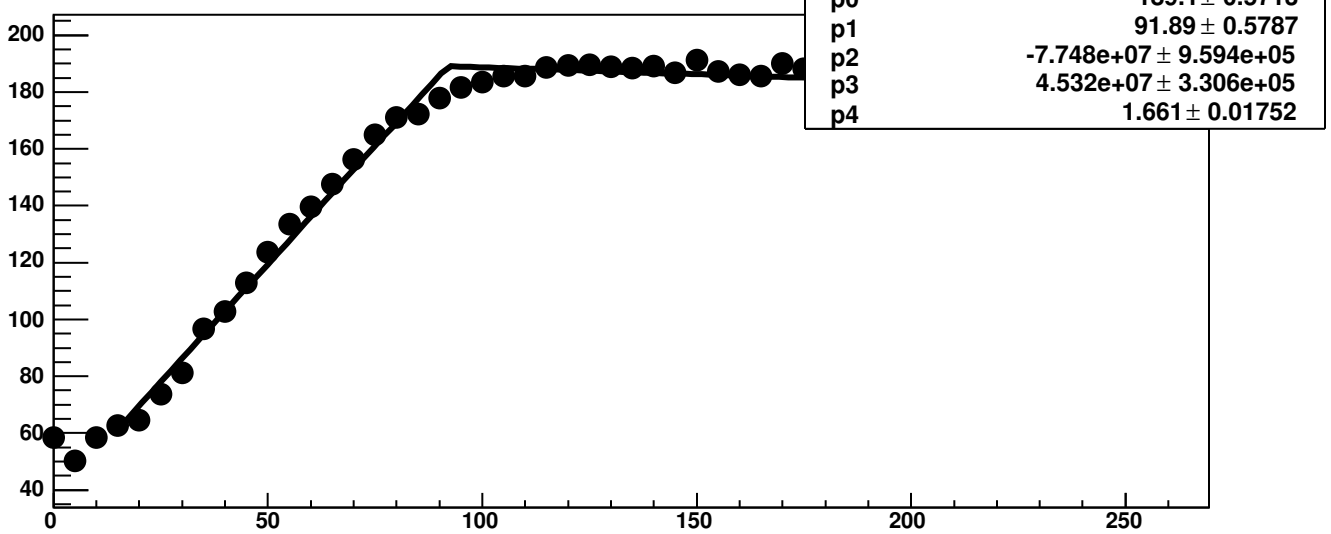



Chip 0, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 183.2 / 41
p0 199.8 ± 0.6146
p1 85.85 ± 0.6697
p2 -5.833e+07 ± 8.808e+05
p3 3.291e+07 ± 2.726e+05
p4 1.665 ± 0.02132

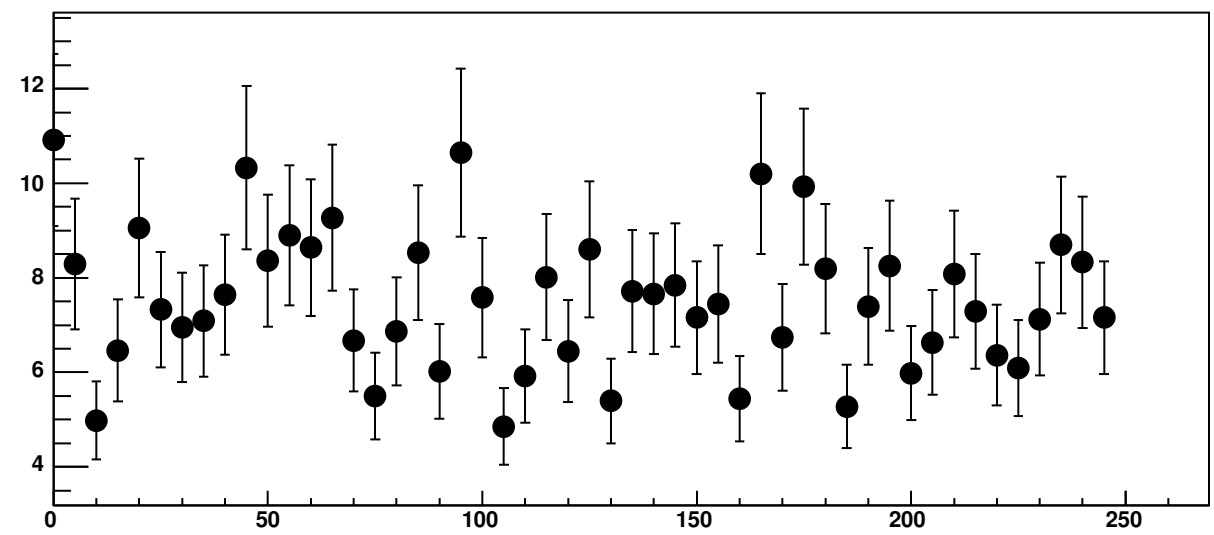

Chip 0, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold


Chip 0, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold

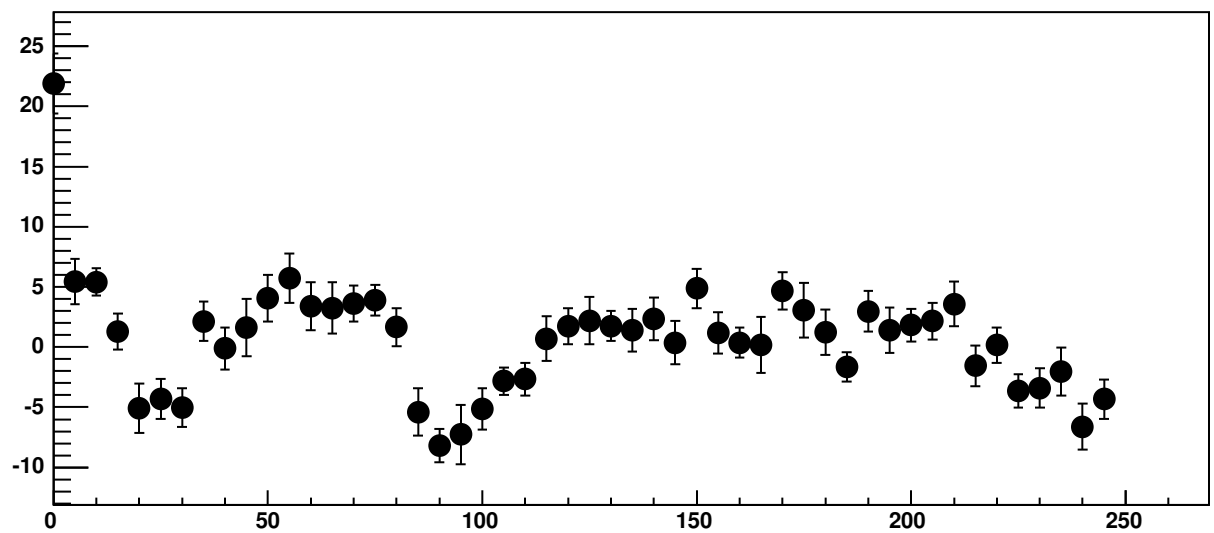
Chip 0, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold



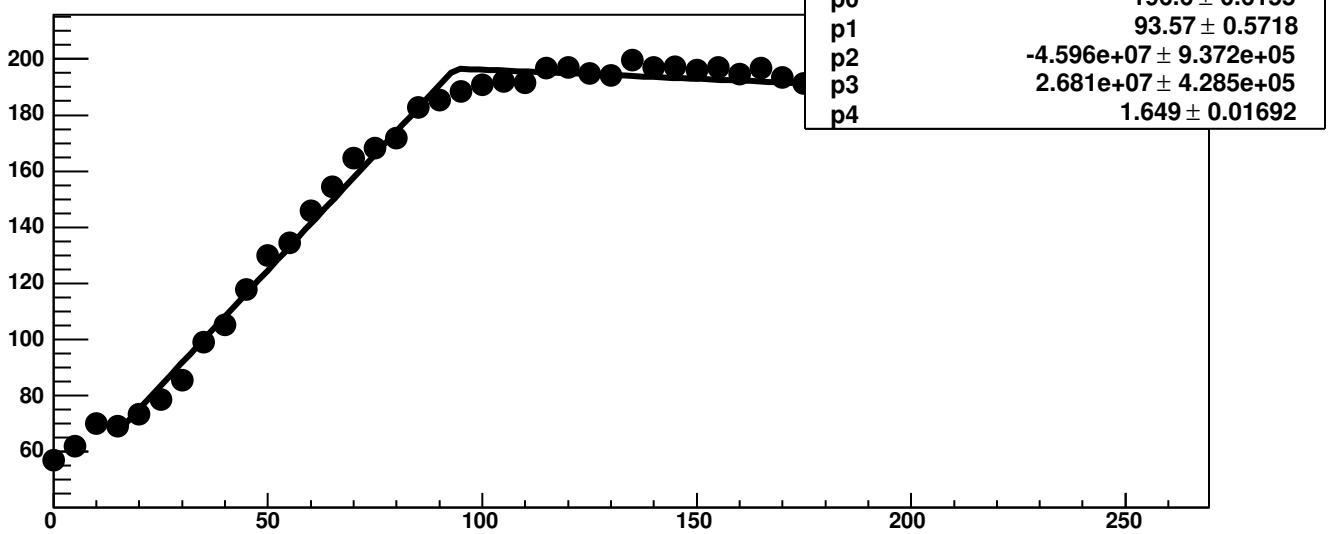
Chip 0, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



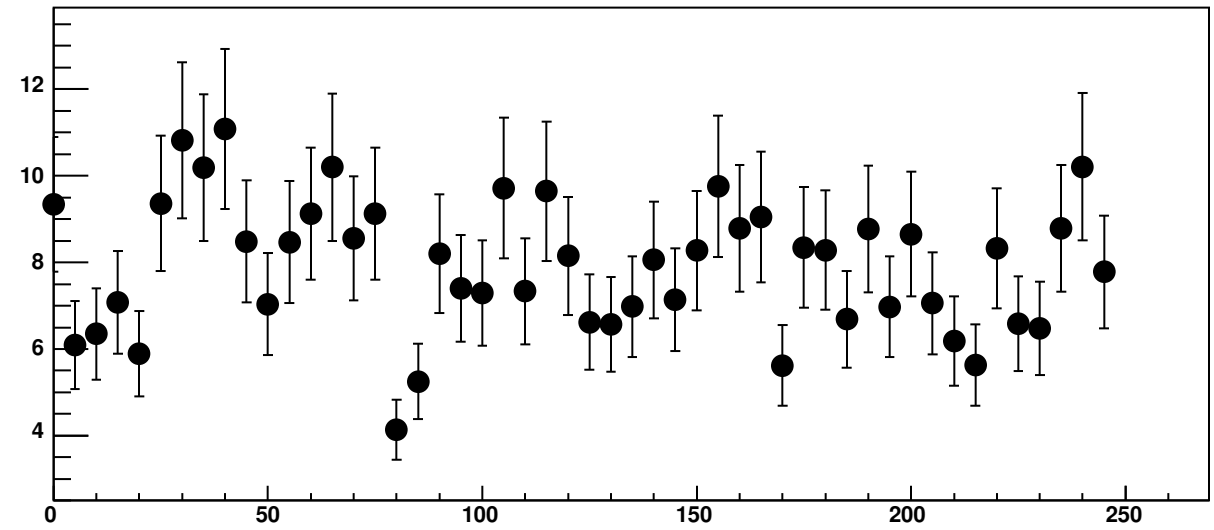
Chip 0, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold



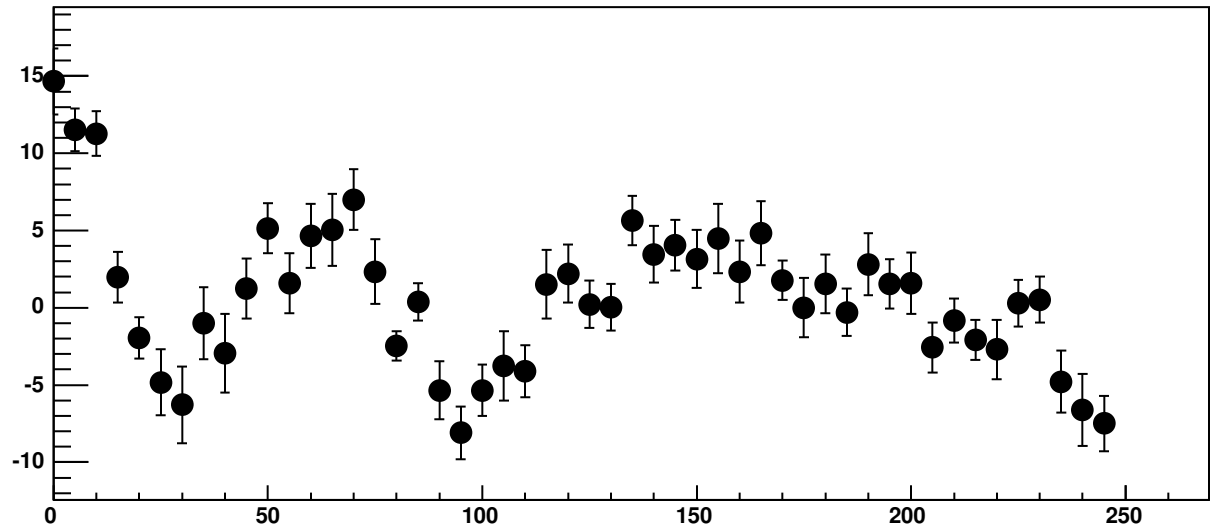
Chip 0, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold

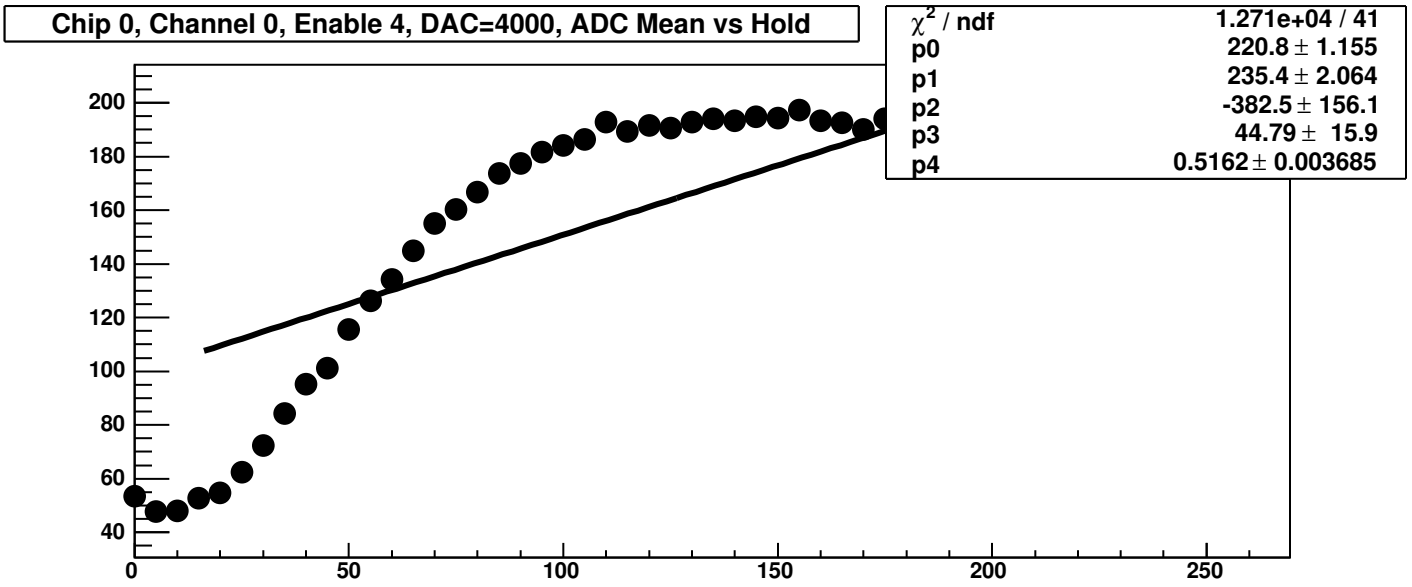


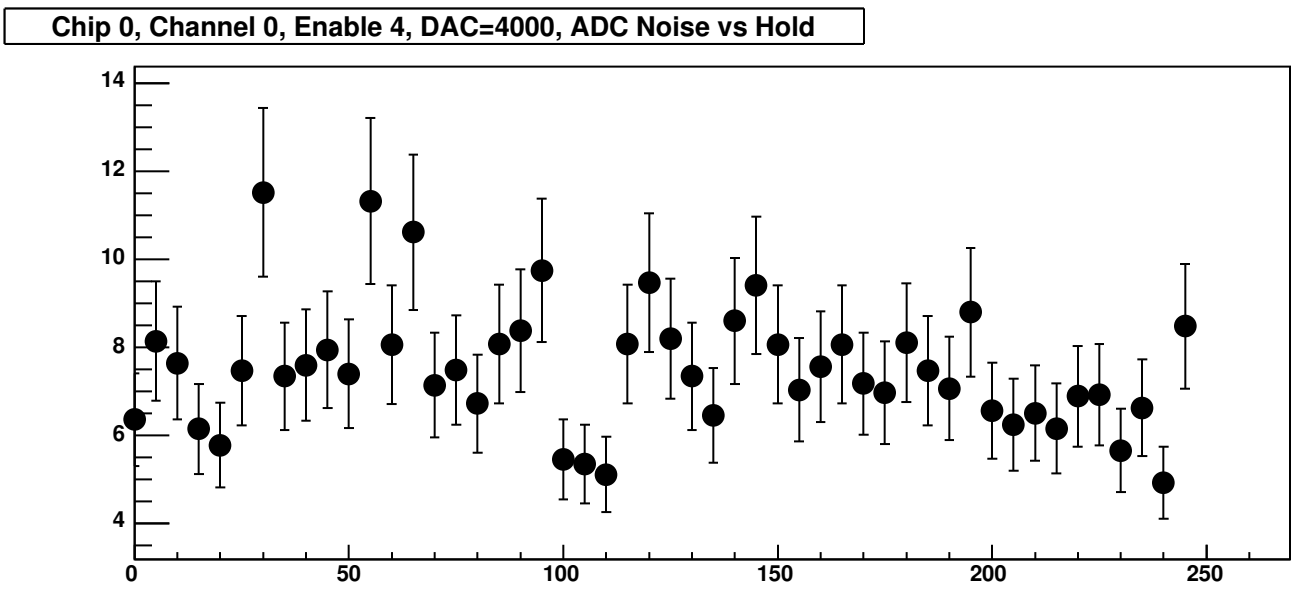
Chip 0, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold

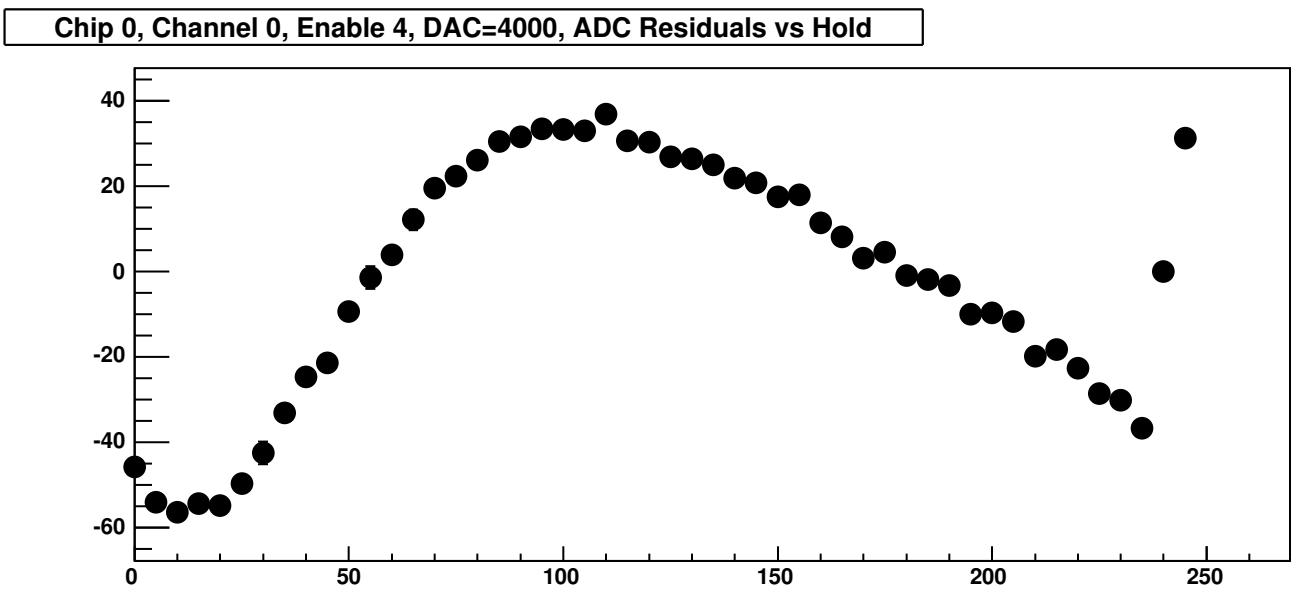


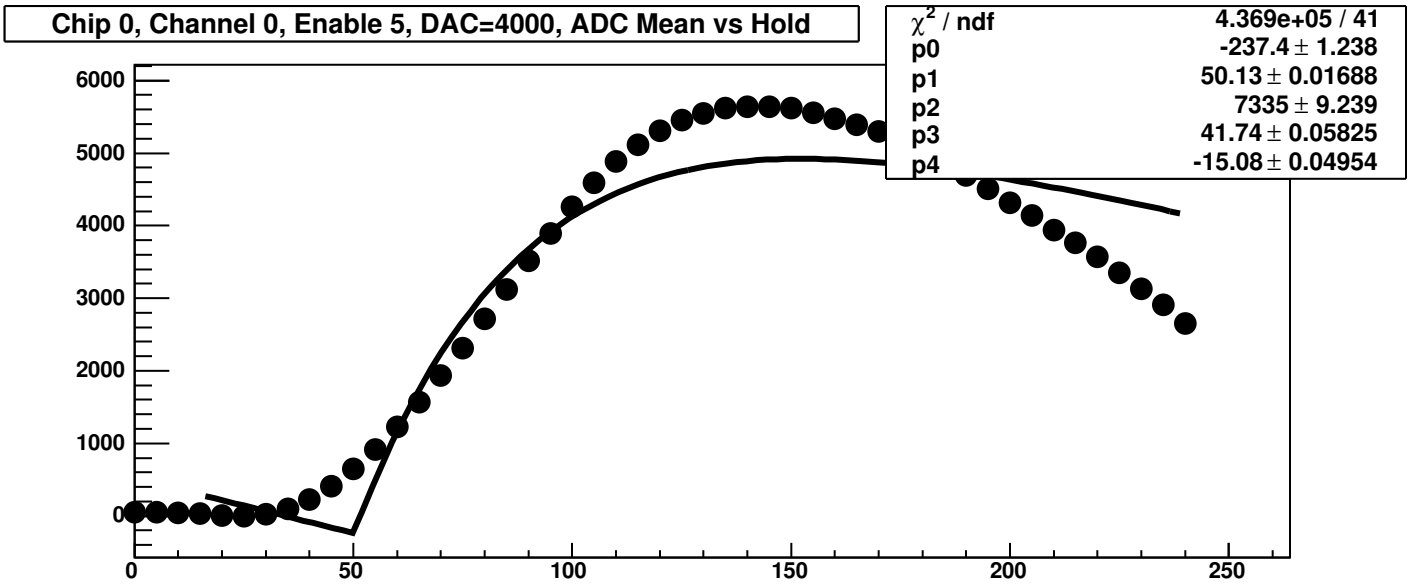
Chip 0, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold

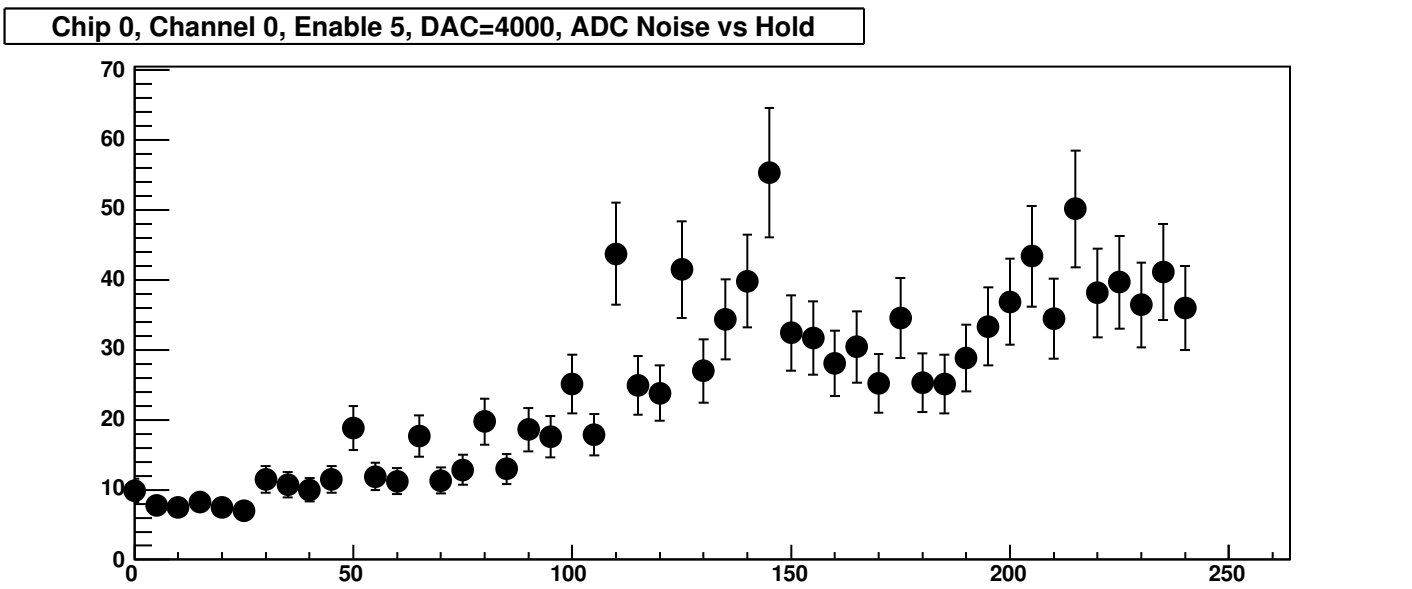


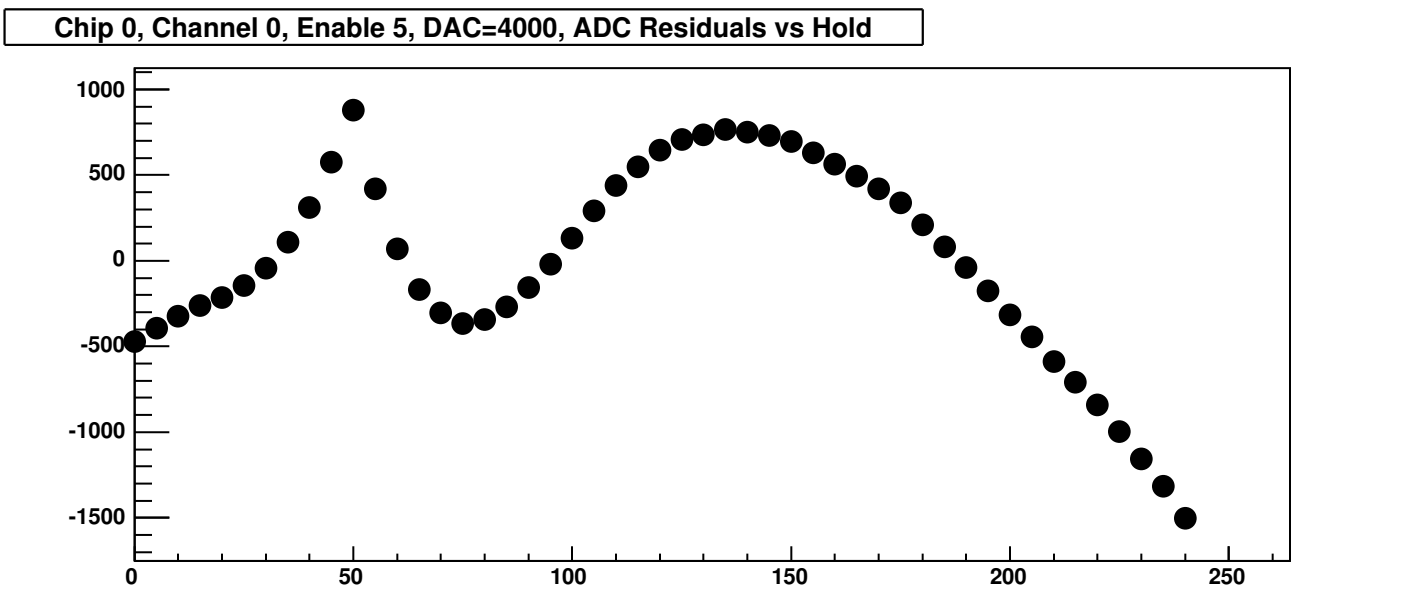

Chip 0, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.271e+04 / 41
p0 220.8 ± 1.155
p1 235.4 ± 2.064
p2 -382.5 ± 156.1
p3 44.79 ± 15.9
p4 0.5162 ± 0.003685
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250

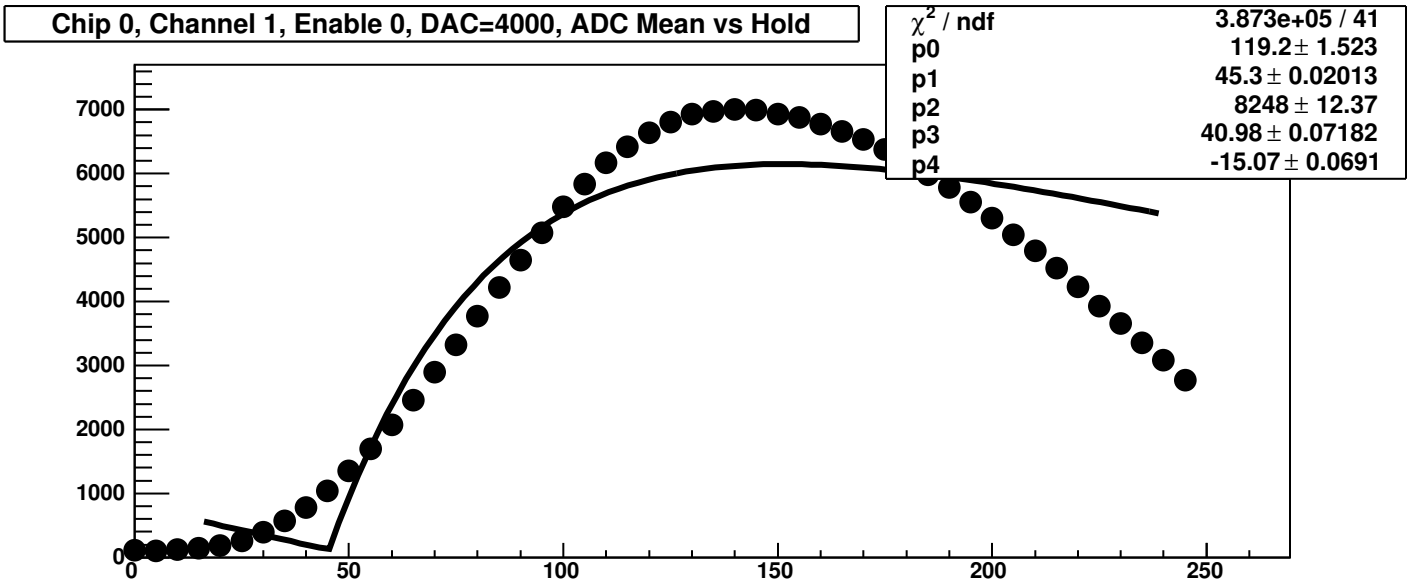

Chip 0, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

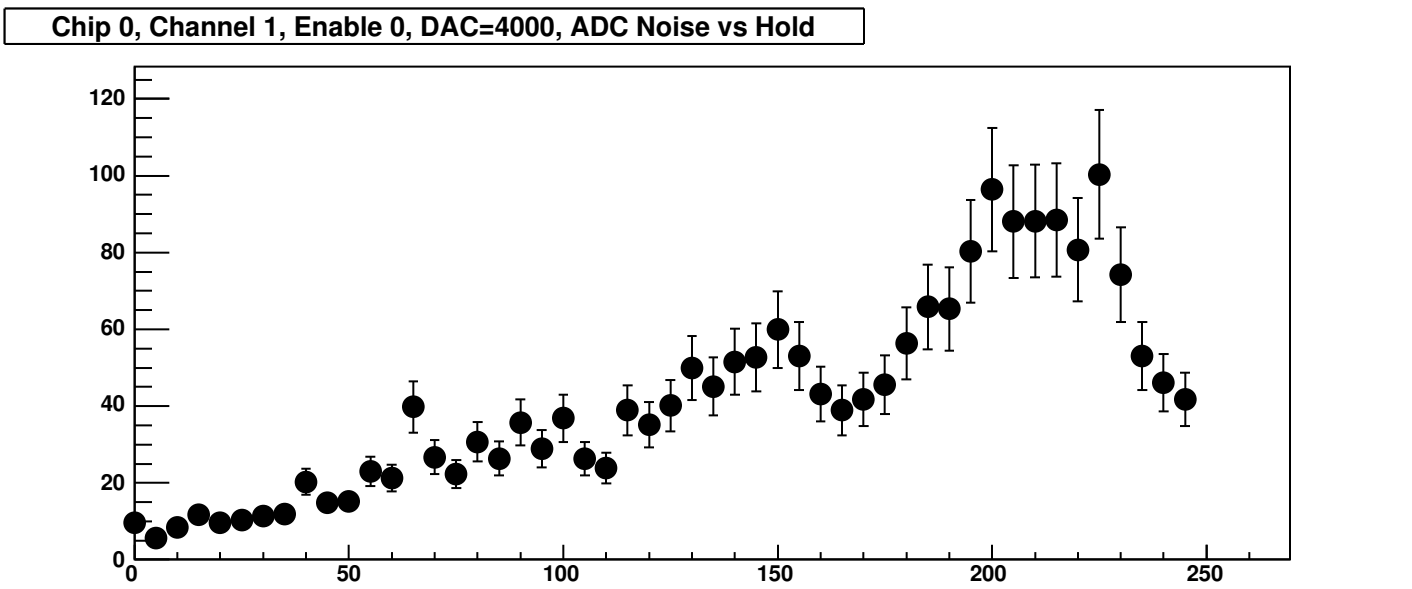

Chip 0, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
0
50
100
150
200
250

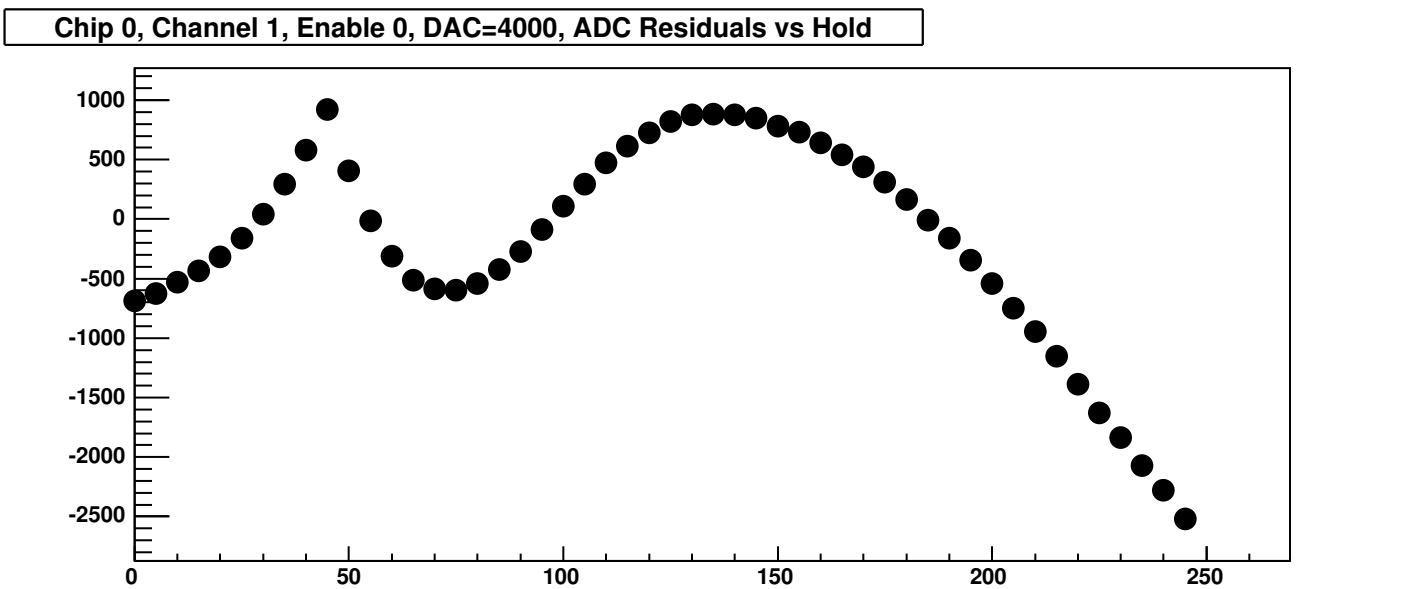

Chip 0, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.369e+05 / 41
p0 -237.4 ± 1.238
p1 50.13 ± 0.01688
p2 7335 ± 9.239
p3 41.74 ± 0.05825
p4 -15.08 ± 0.04954
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

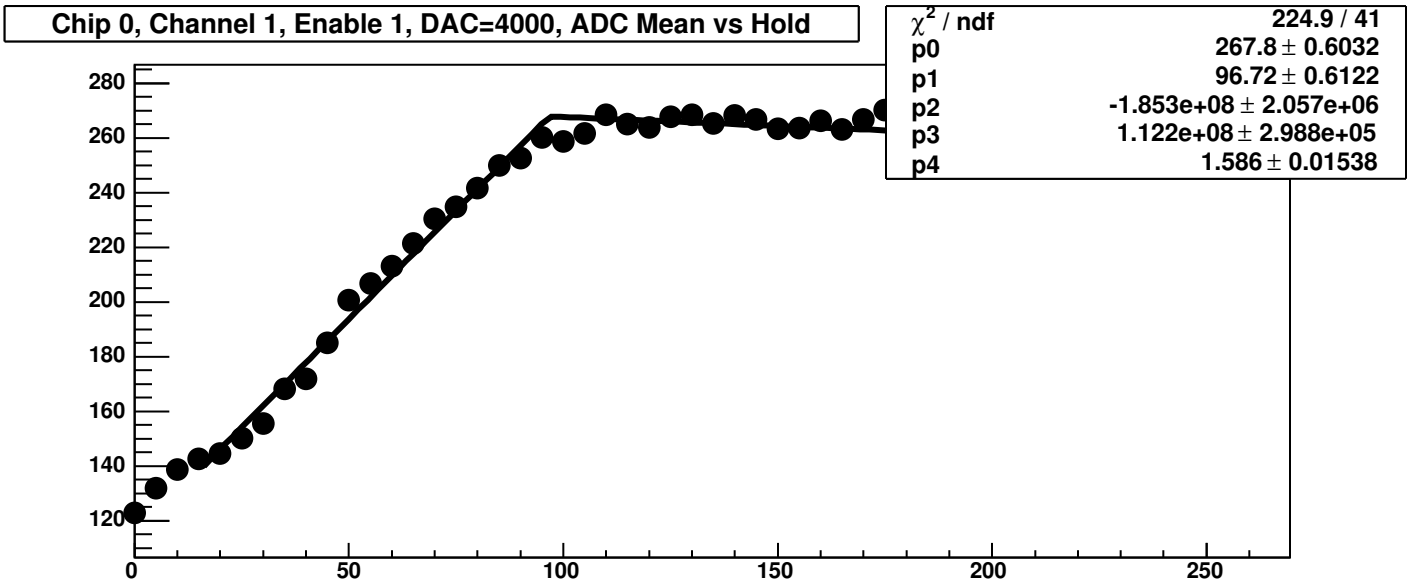

Chip 0, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold
70
60
50
40
30
20
10
0
0
50
100
150
200
250

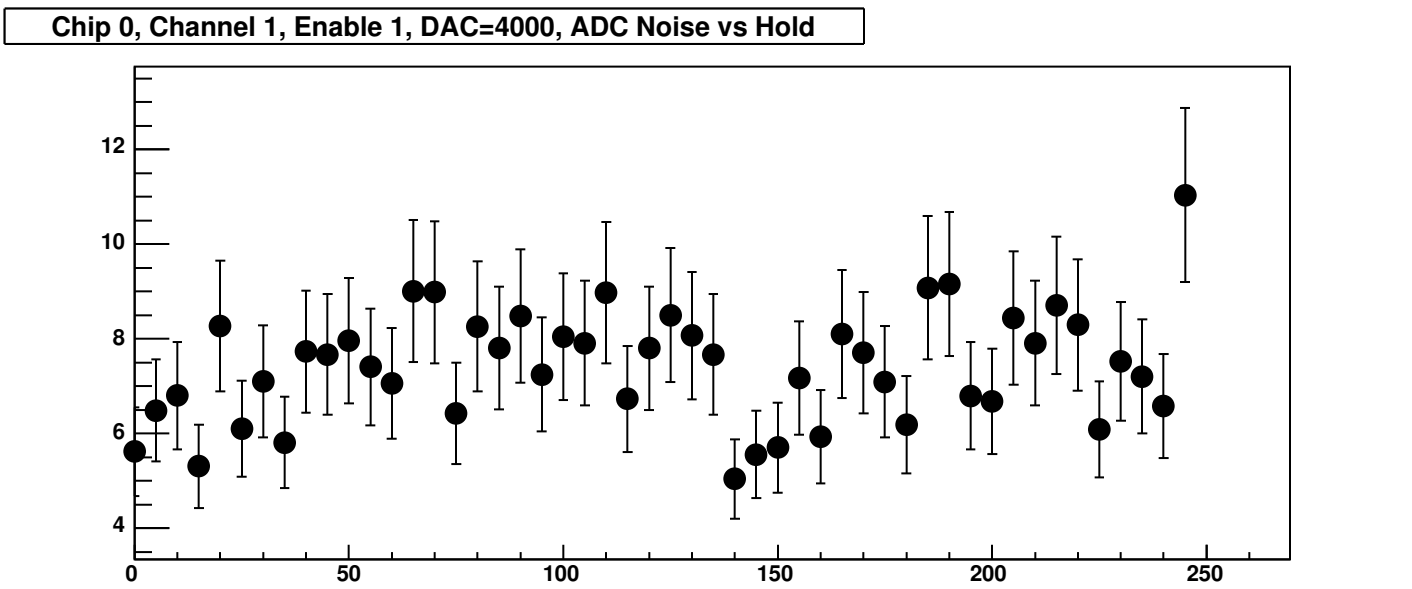

Chip 0, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

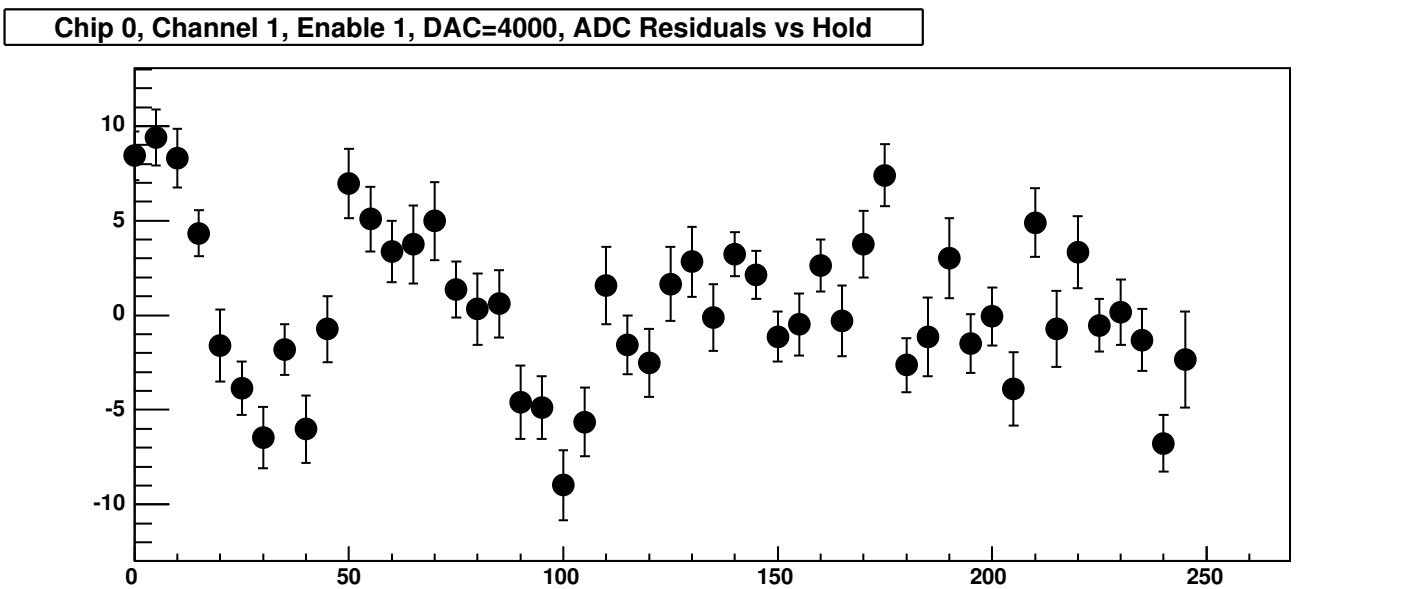

Chip 0, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.873e+05 / 41
p0 119.2 ± 1.523
p1 45.3 ± 0.02013
p2 8248 ± 12.37
p3 40.98 ± 0.07182
p4 -15.07 ± 0.0691
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 0, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250

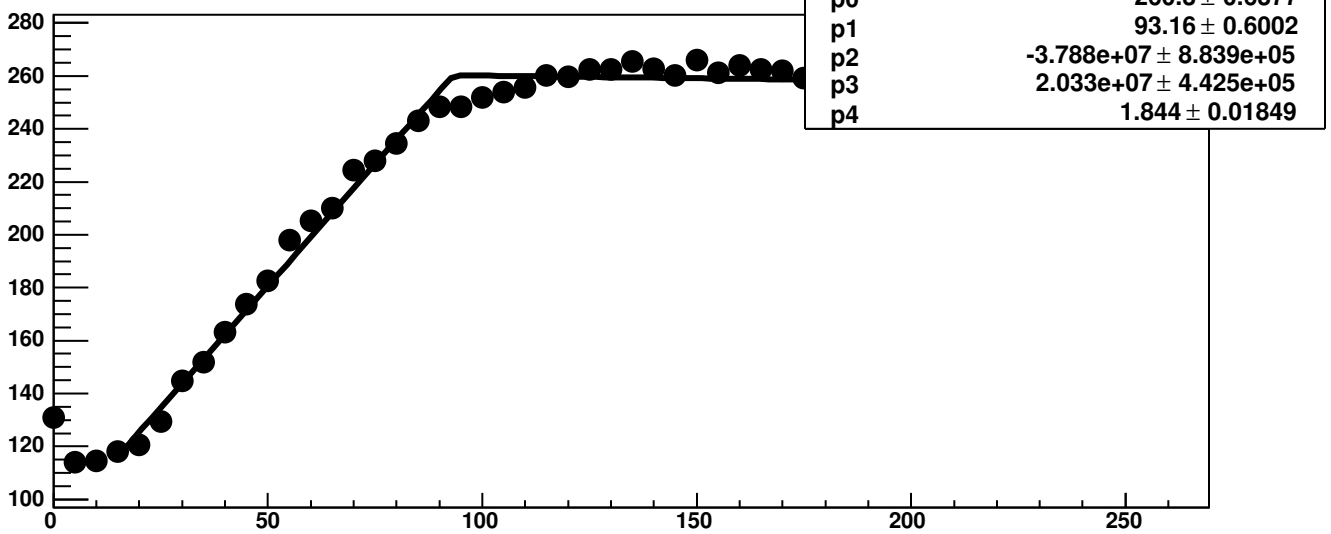

Chip 0, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
0
50
100
150
200
250


Chip 0, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 224.9 / 41
p0 267.8 ± 0.6032
p1 96.72 ± 0.6122
p2 -1.853e+08 ± 2.057e+06
p3 1.122e+08 ± 2.988e+05
p4 1.586 ± 0.01538
280
260
240
220
200
180
160
140
120
0
50
100
150
200
250

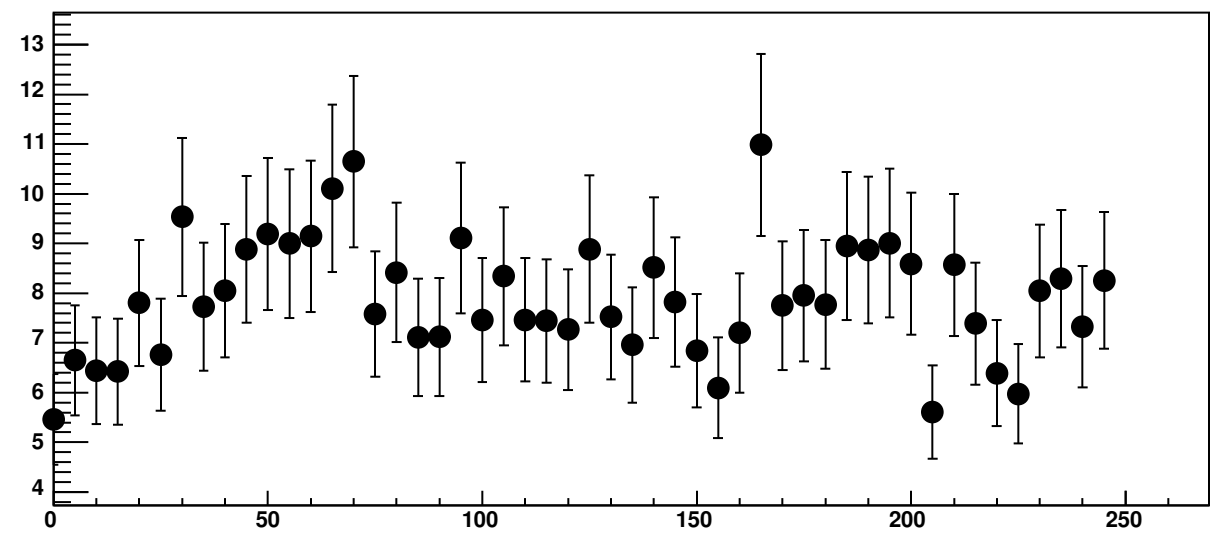

Chip 0, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 0, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250

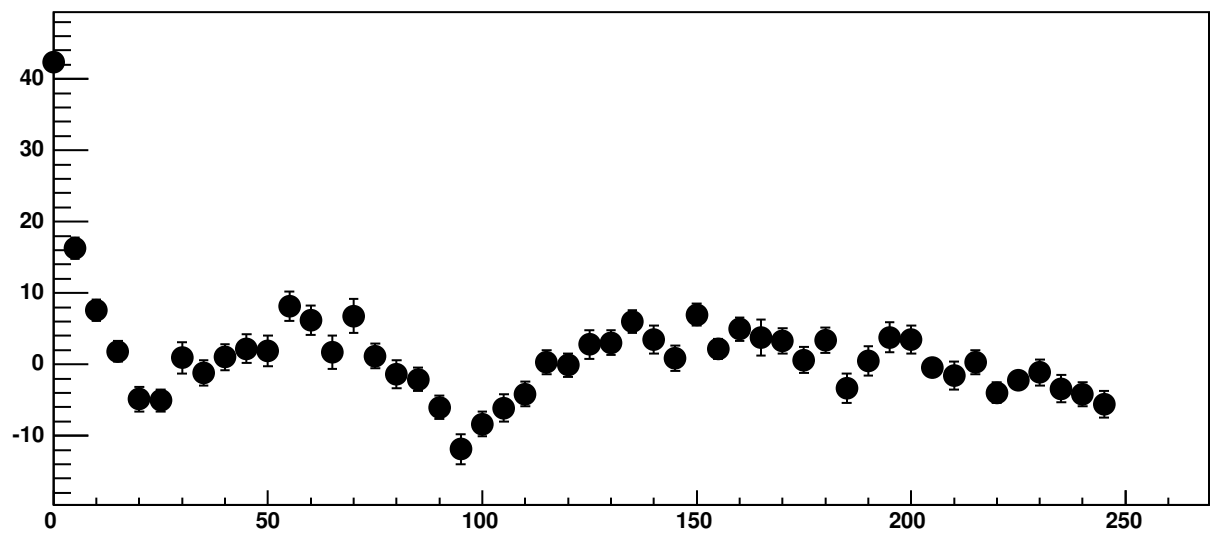
Chip 0, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold



Chip 0, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold

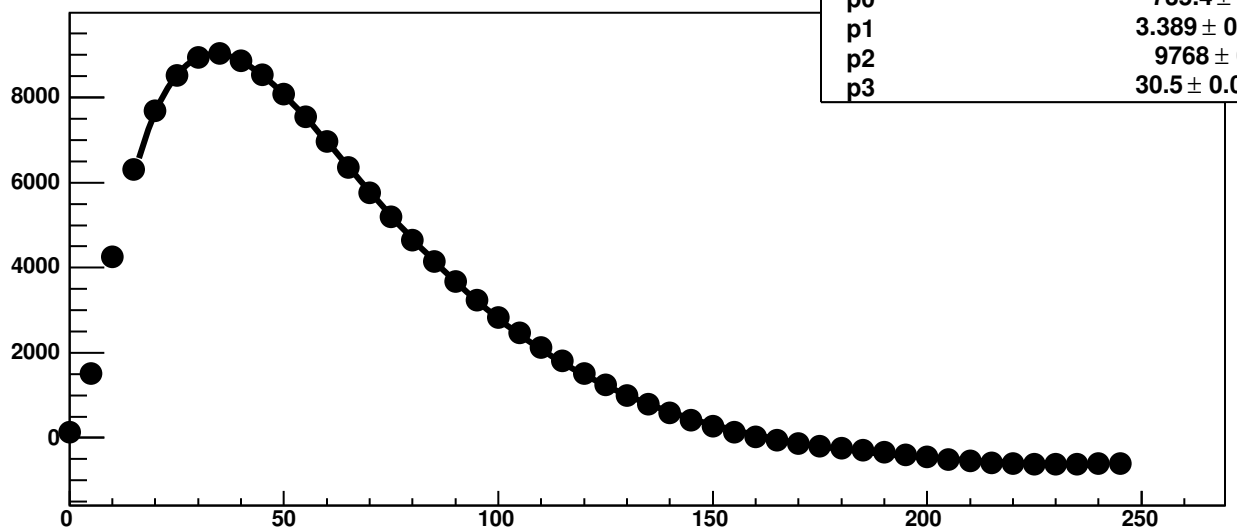


Chip 0, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

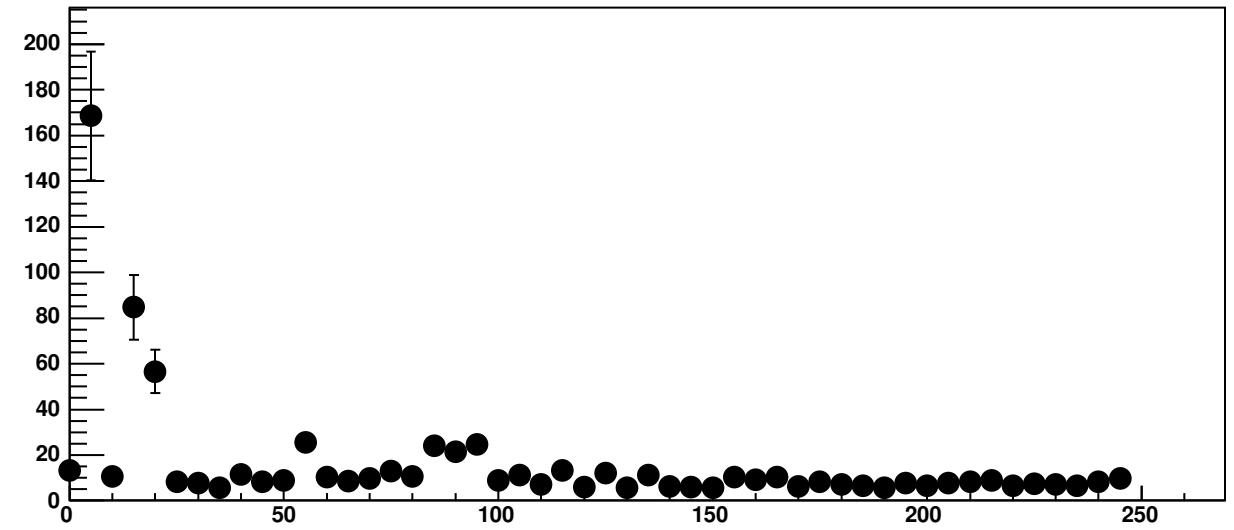


Chip 0, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold

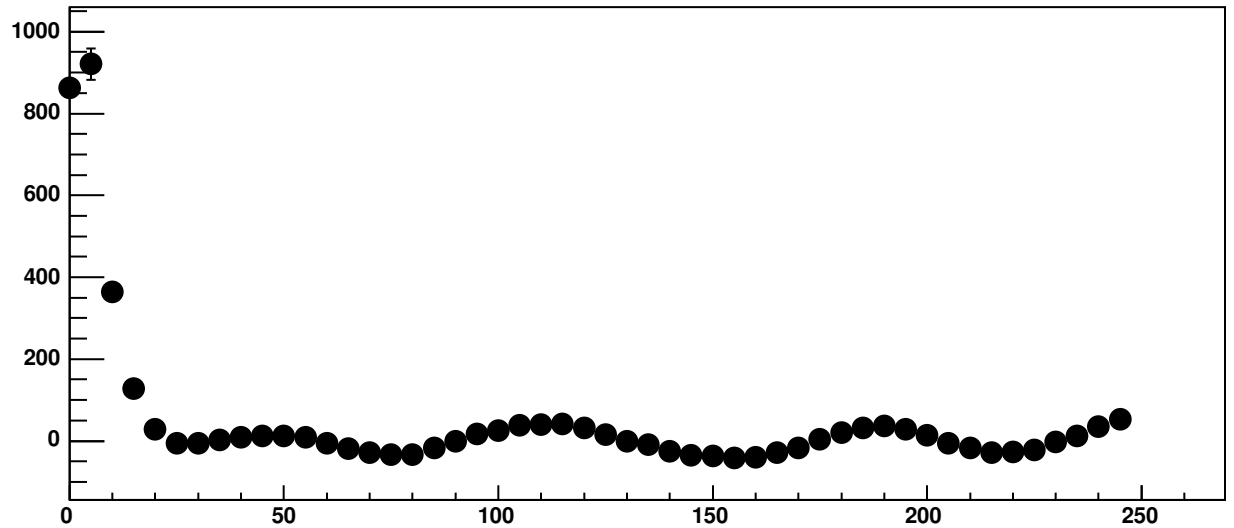
χ^2 / ndf	8068 / 42
p0	-735.4 ± 0.5662
p1	3.389 ± 0.01396
p2	9768 ± 0.8645
p3	30.5 ± 0.006875

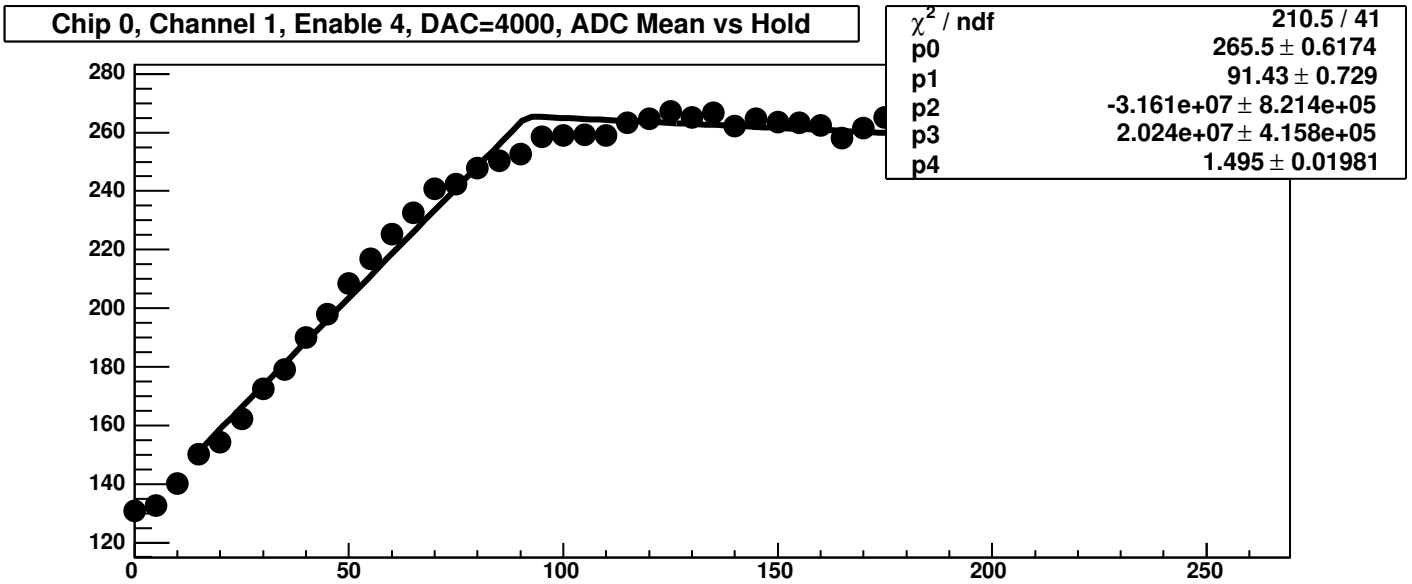


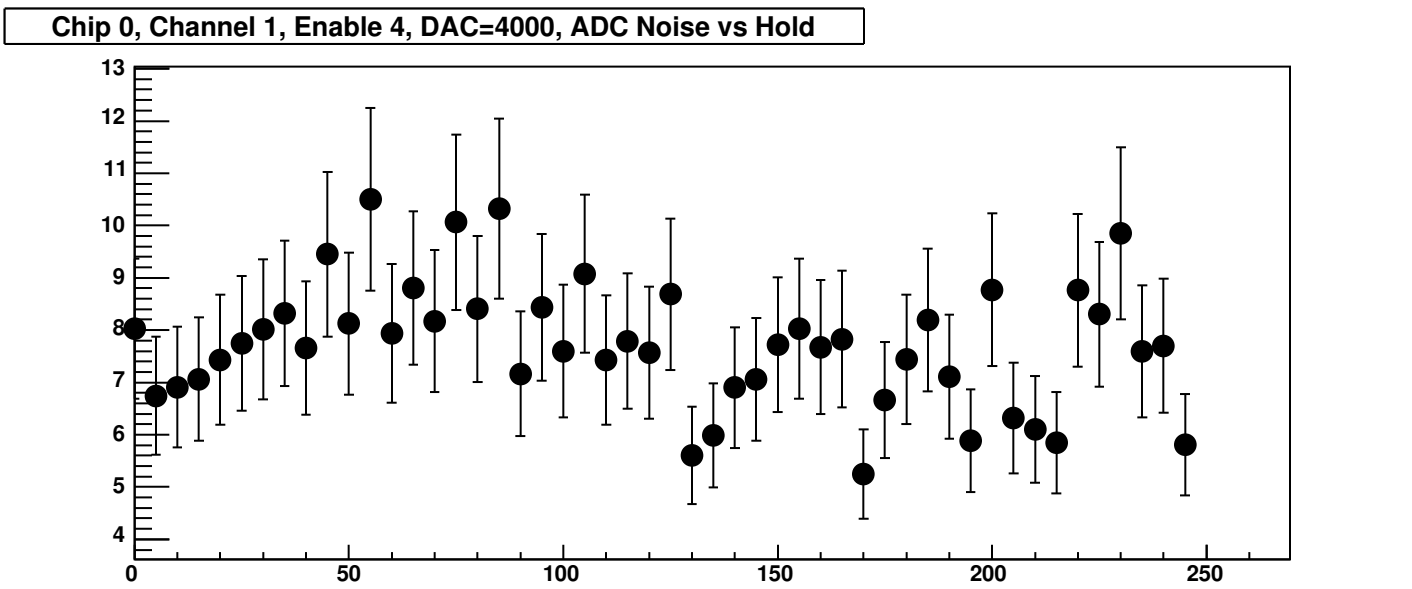
Chip 0, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold

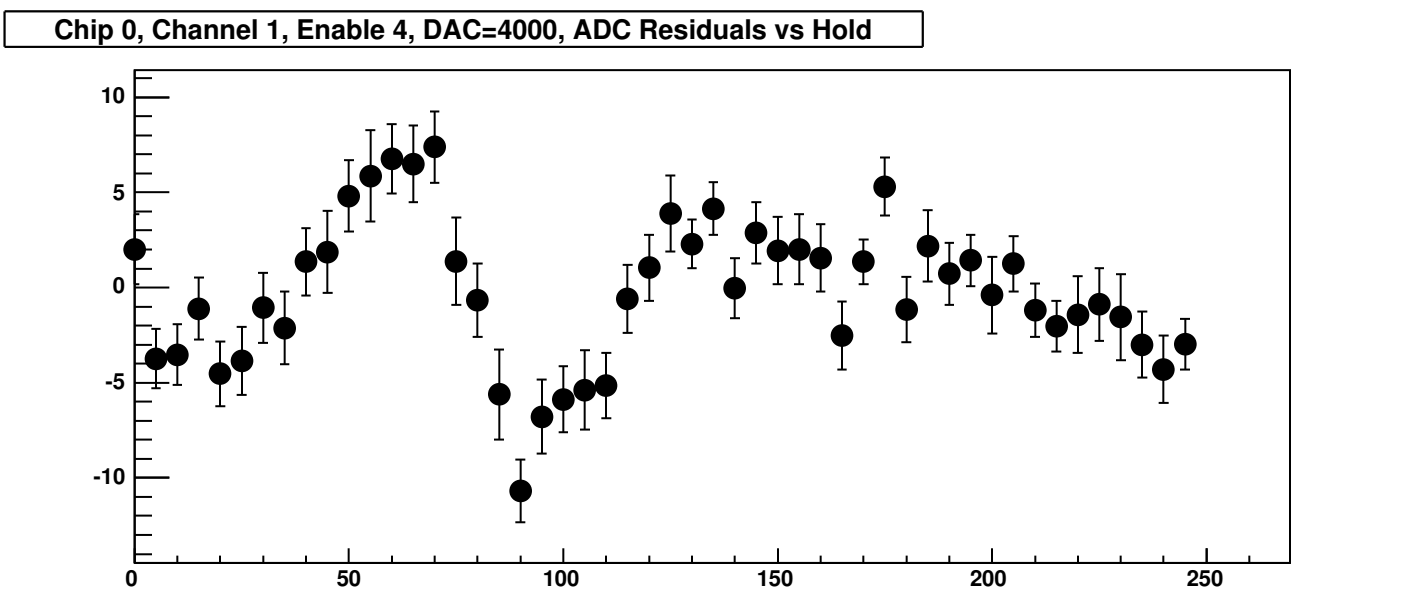


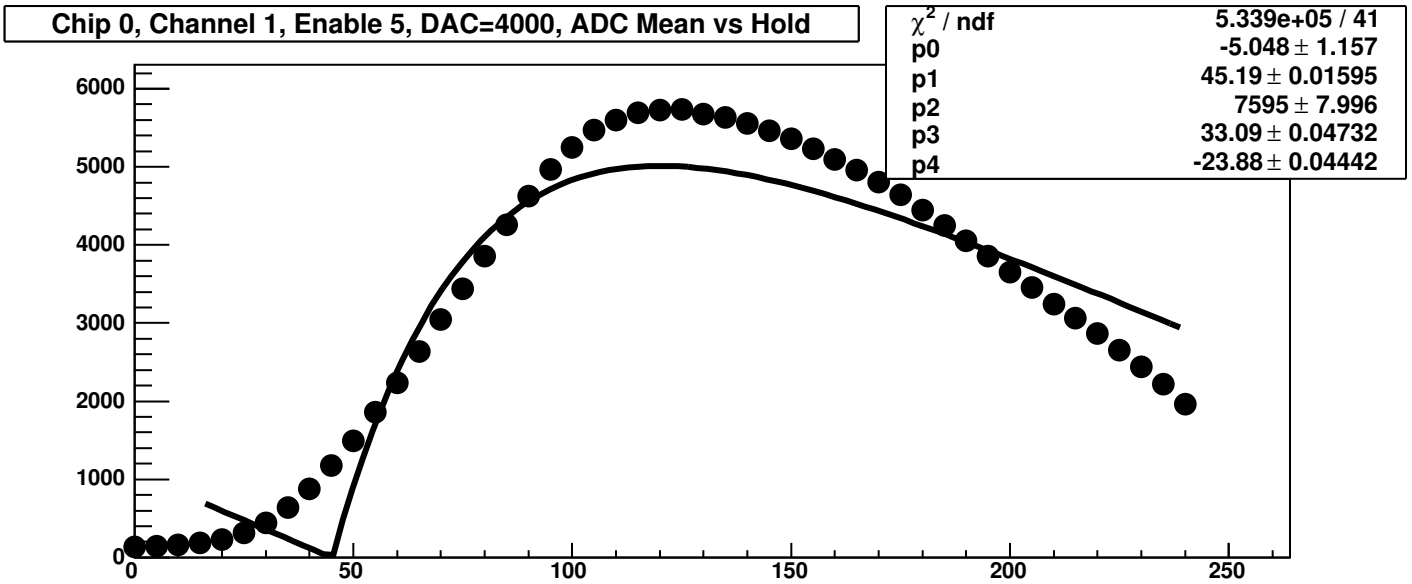
Chip 0, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold

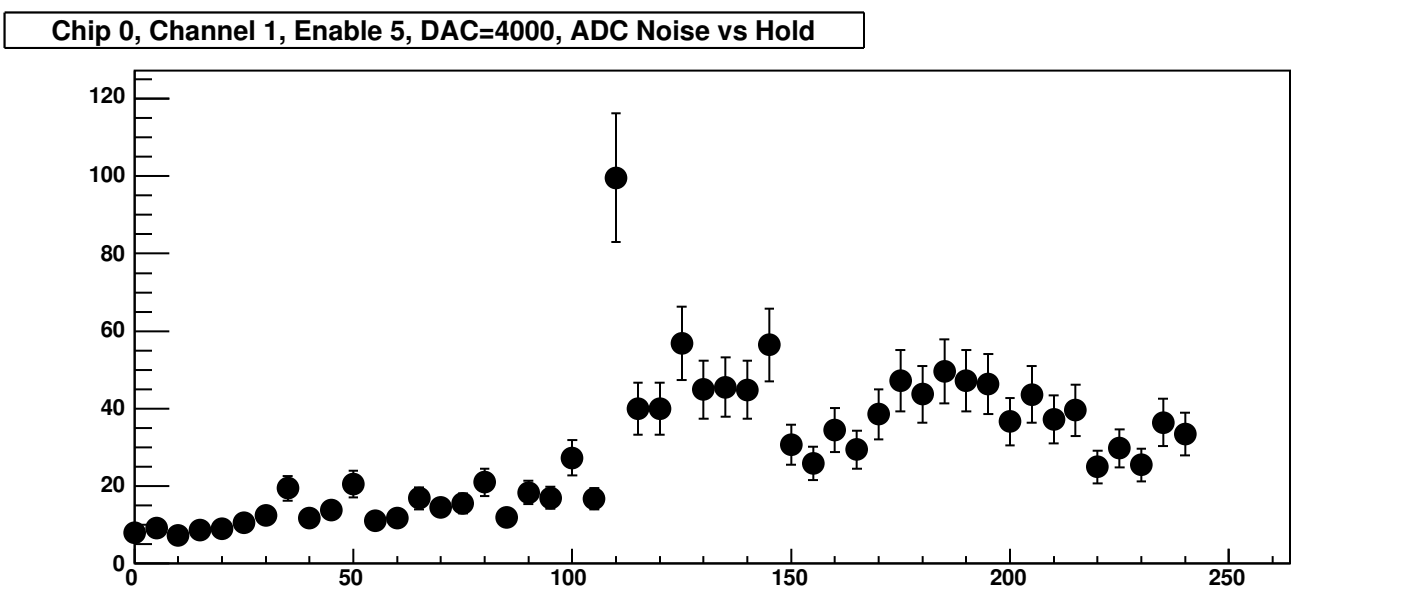


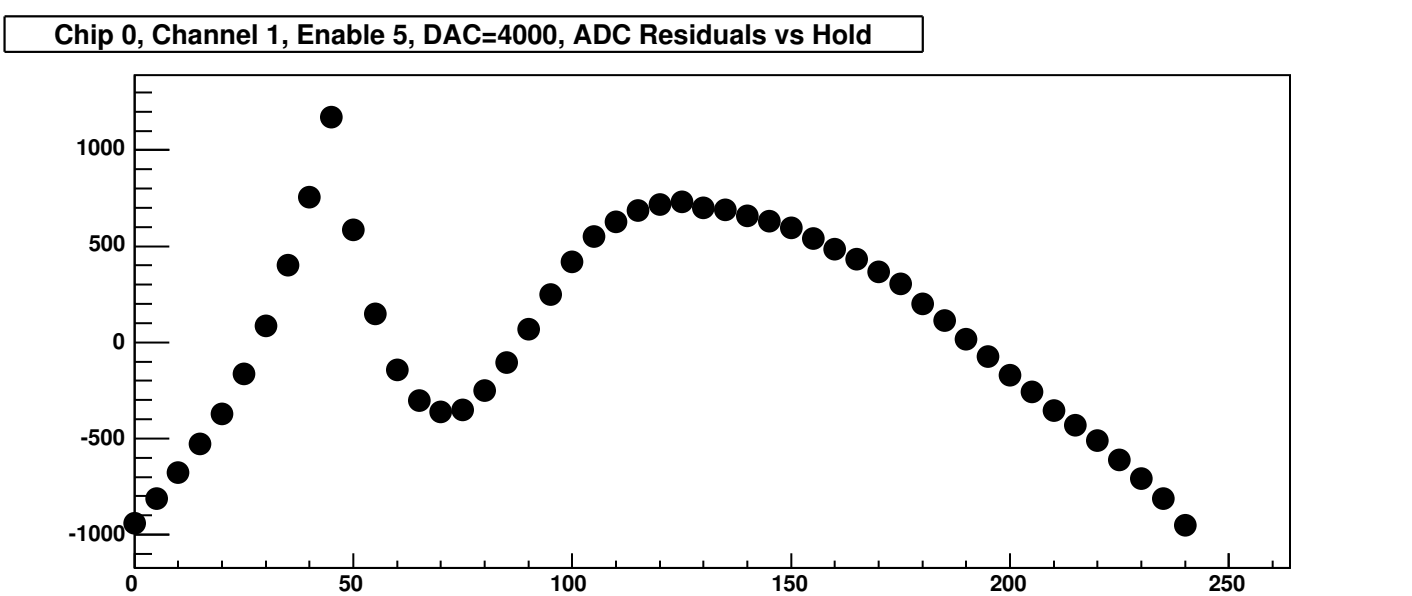

Chip 0, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 210.5 / 41
p0 265.5 ± 0.6174
p1 91.43 ± 0.729
p2 -3.161e+07 ± 8.214e+05
p3 2.024e+07 ± 4.158e+05
p4 1.495 ± 0.01981
280
260
240
220
200
180
160
140
120
0
50
100
150
200
250


Chip 0, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

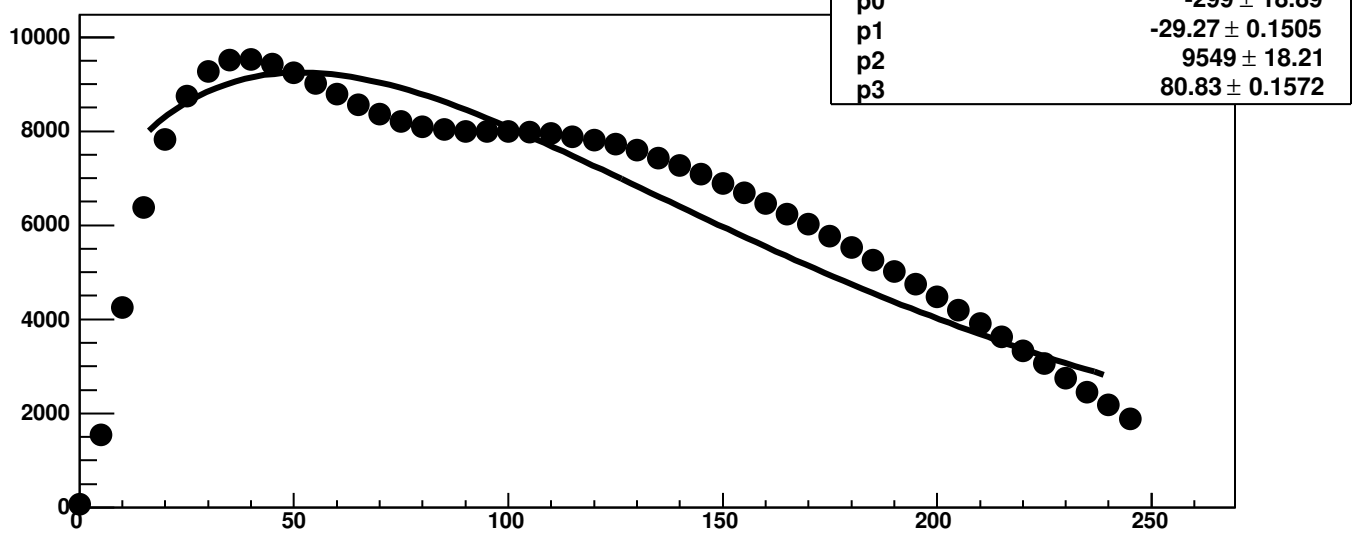

Chip 0, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250


Chip 0, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.339e+05 / 41
p0 -5.048 ± 1.157
p1 45.19 ± 0.01595
p2 7595 ± 7.996
p3 33.09 ± 0.04732
p4 -23.88 ± 0.04442
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

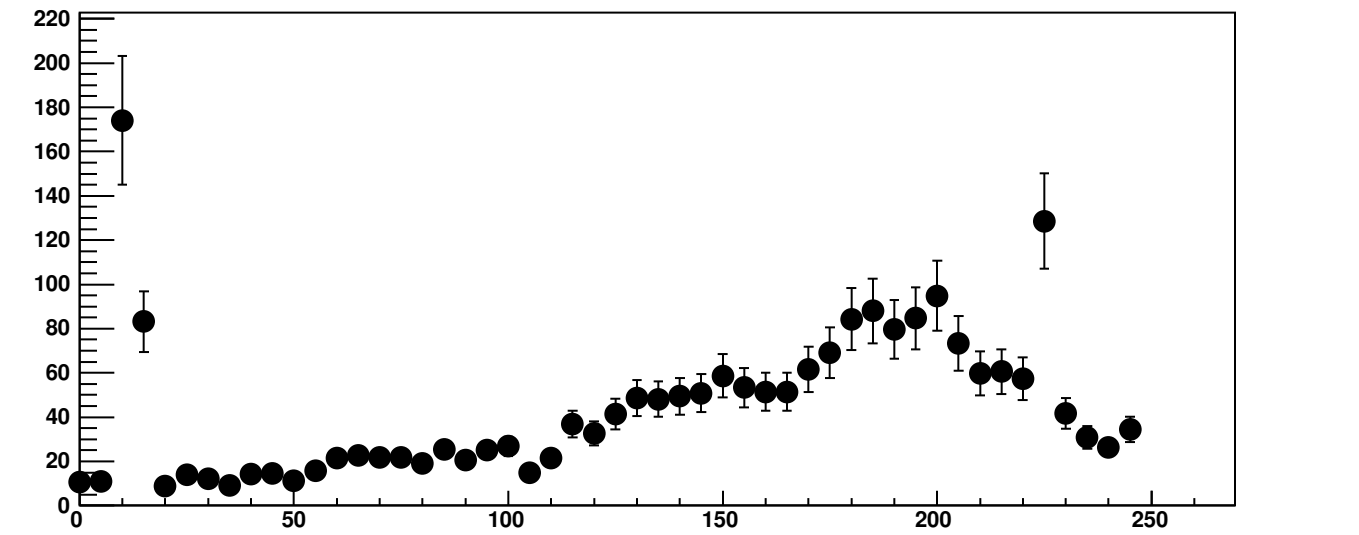

Chip 0, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
0
50
100
150
200
250

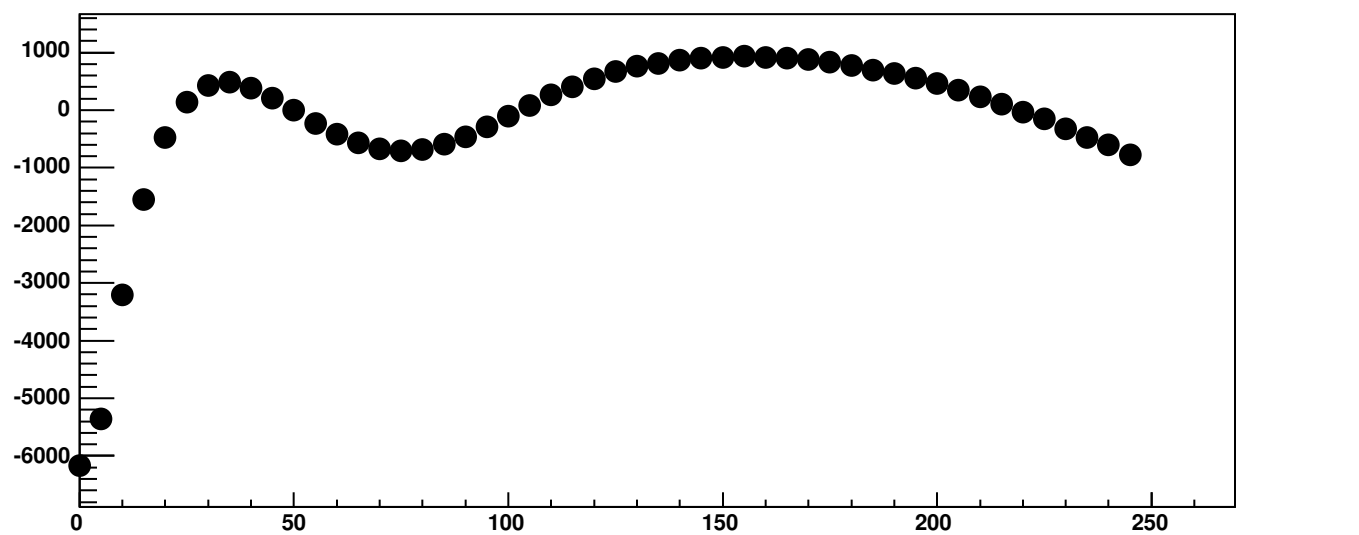
Chip 0, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold



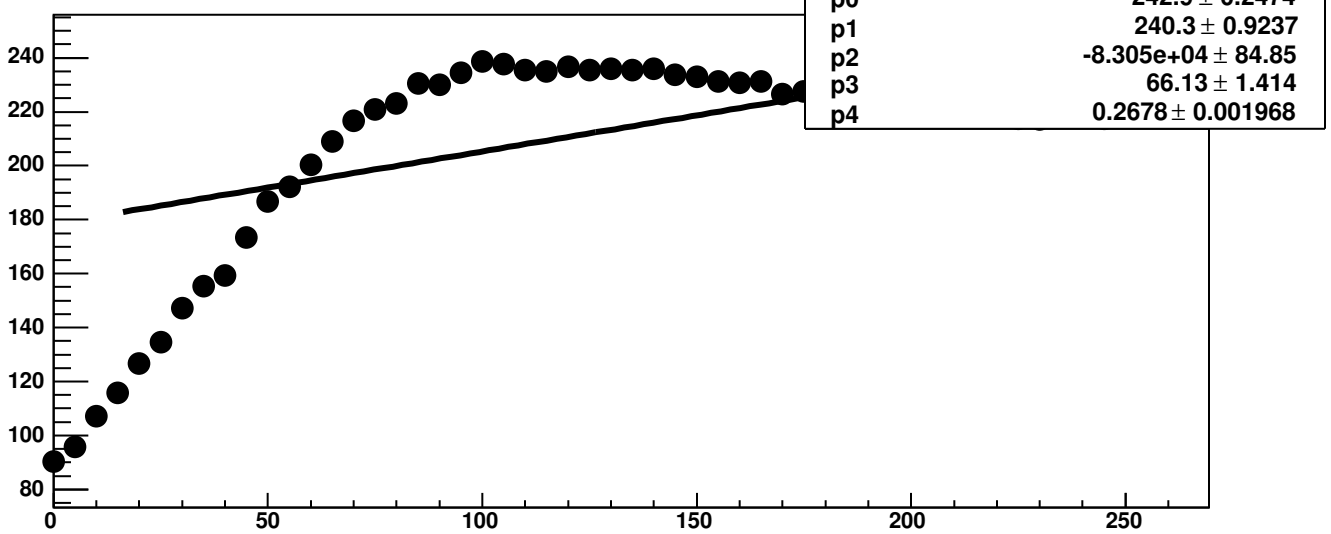
Chip 0, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold



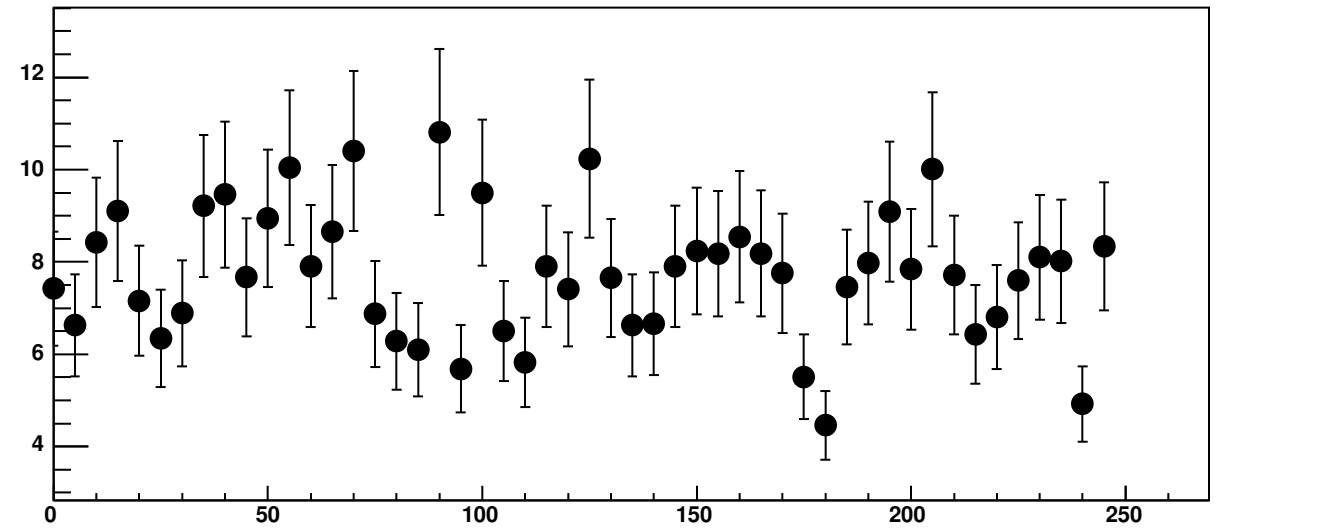
Chip 0, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold



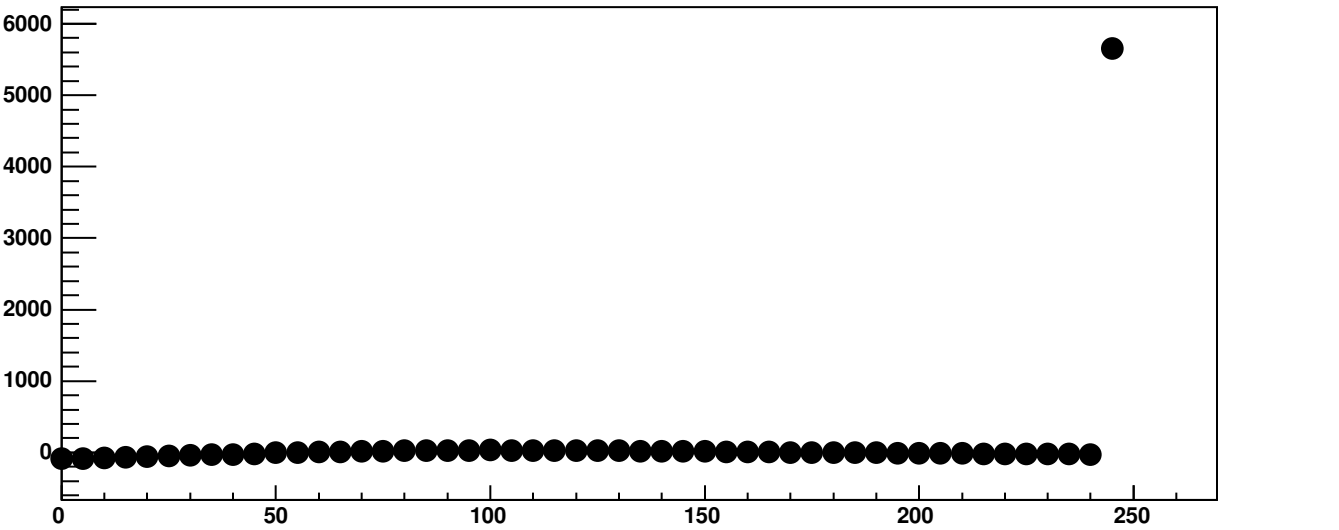
Chip 0, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold



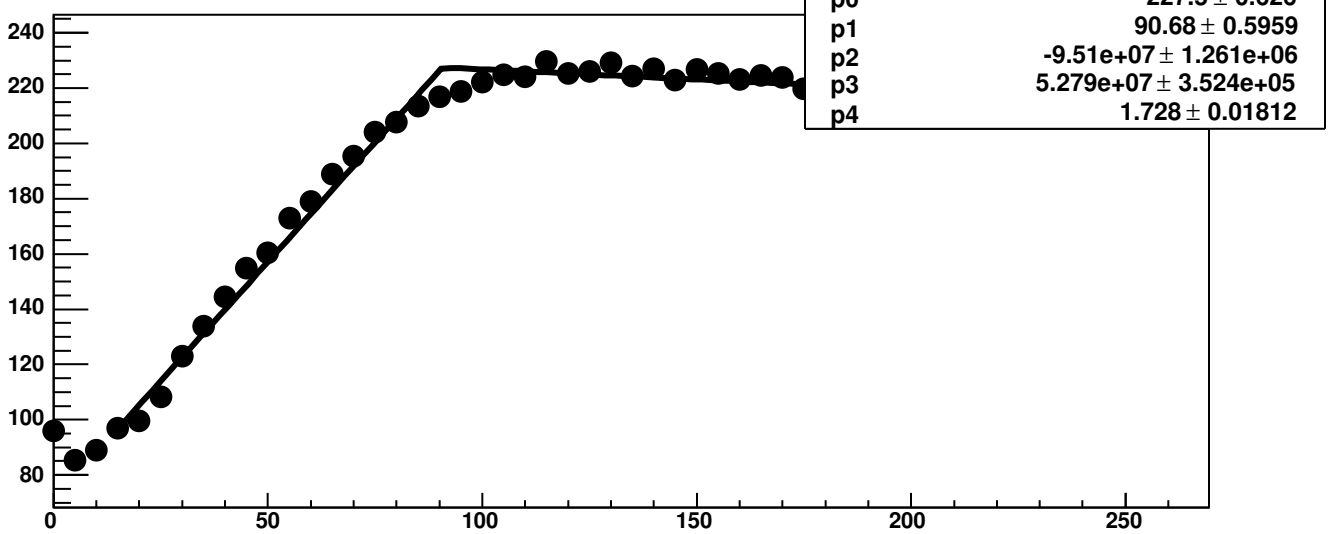
Chip 0, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



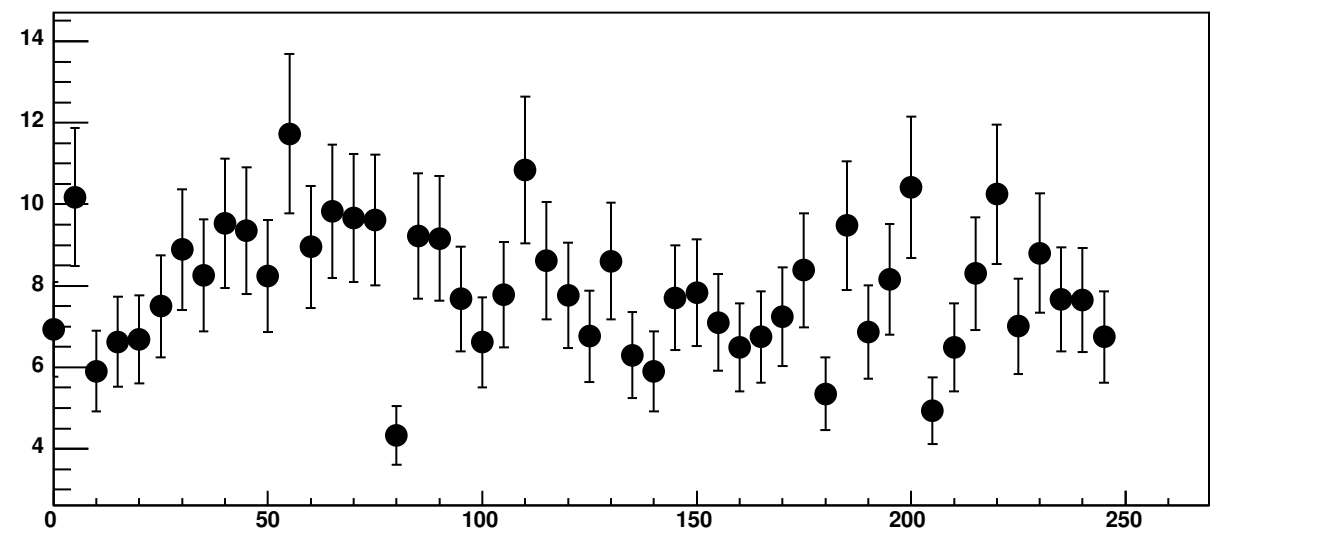
Chip 0, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold



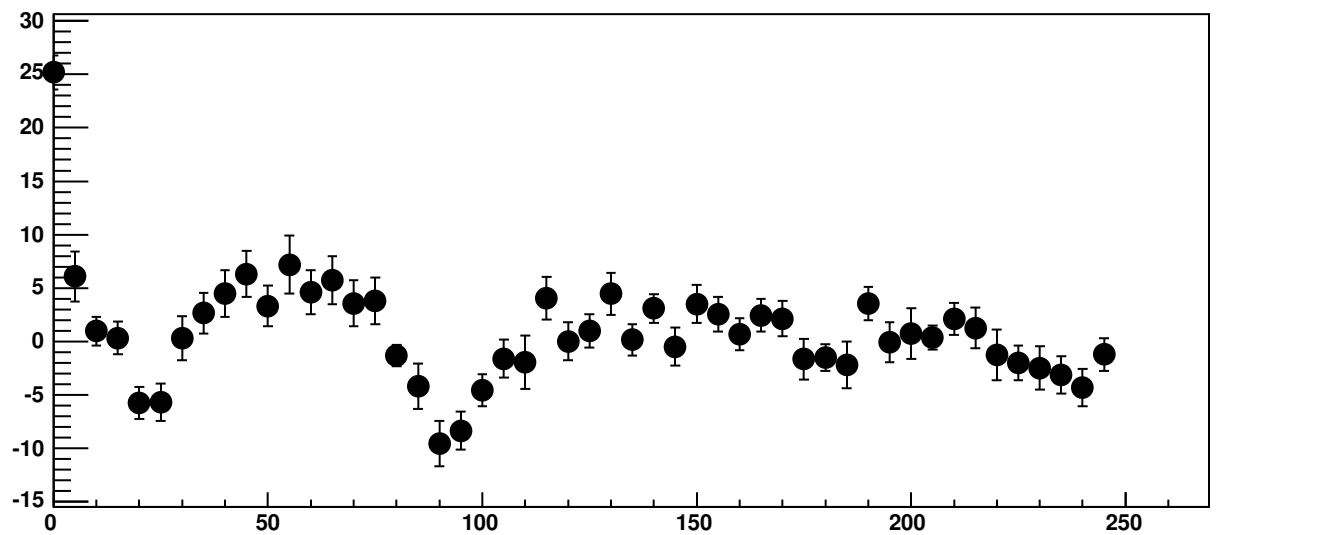
Chip 0, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold



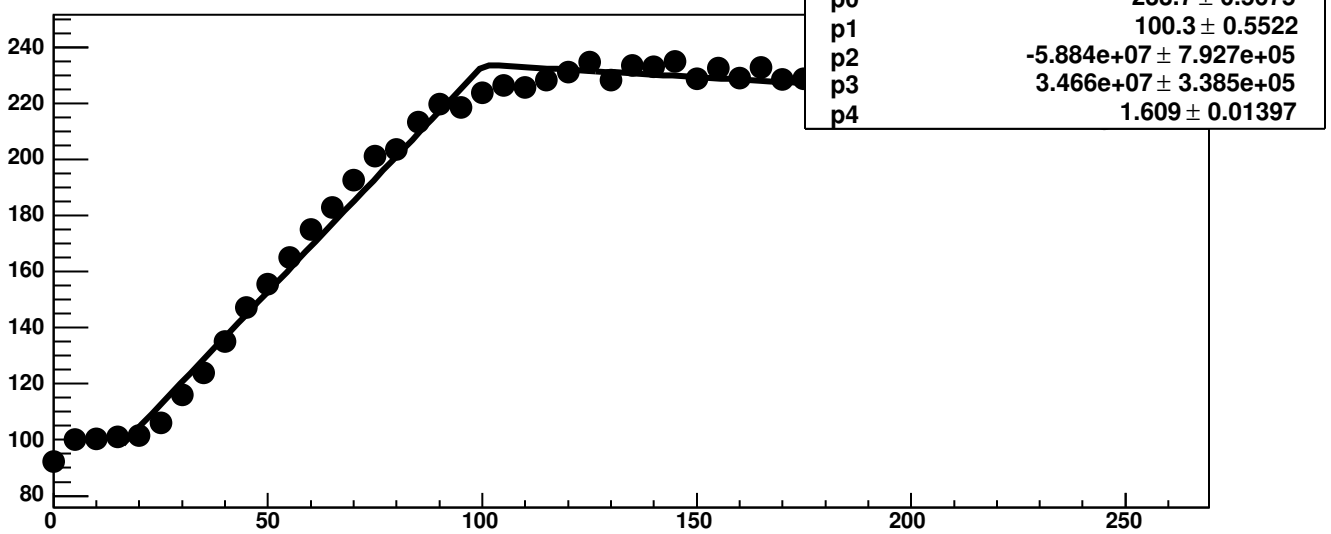
Chip 0, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



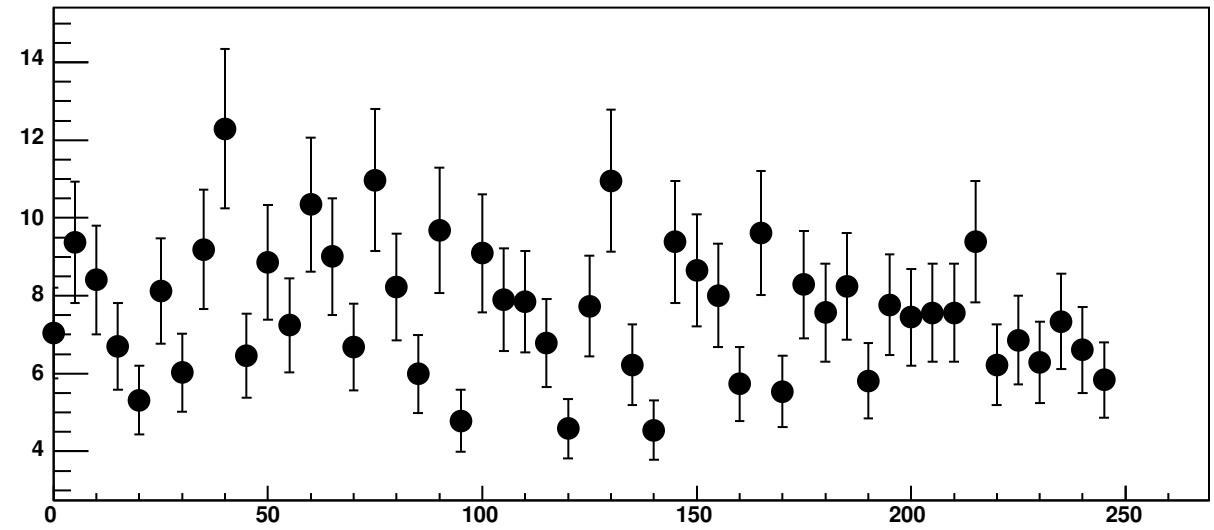
Chip 0, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold



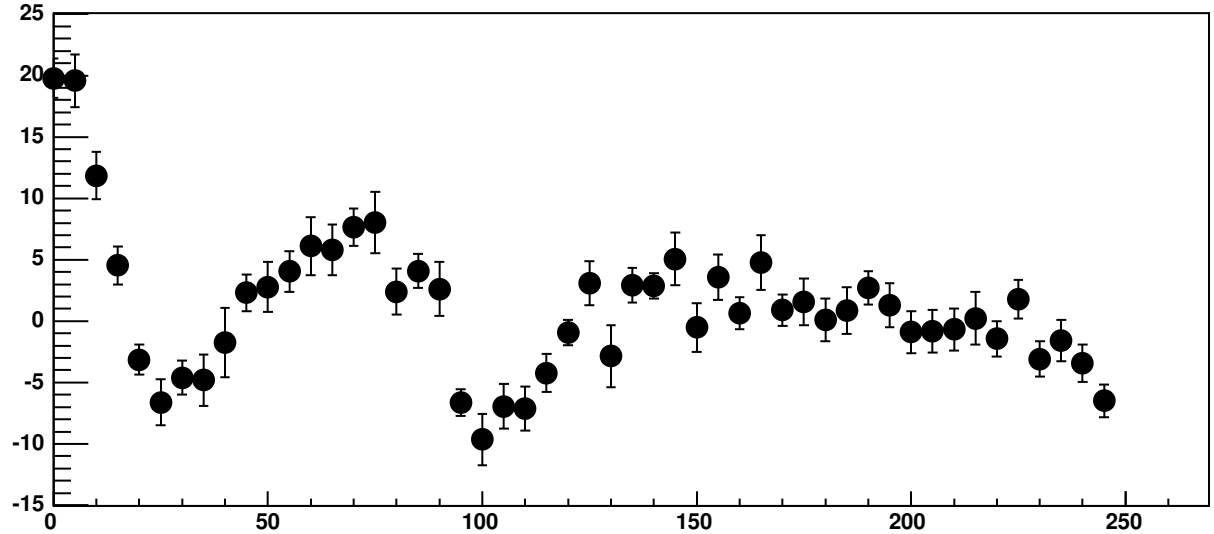
Chip 0, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold



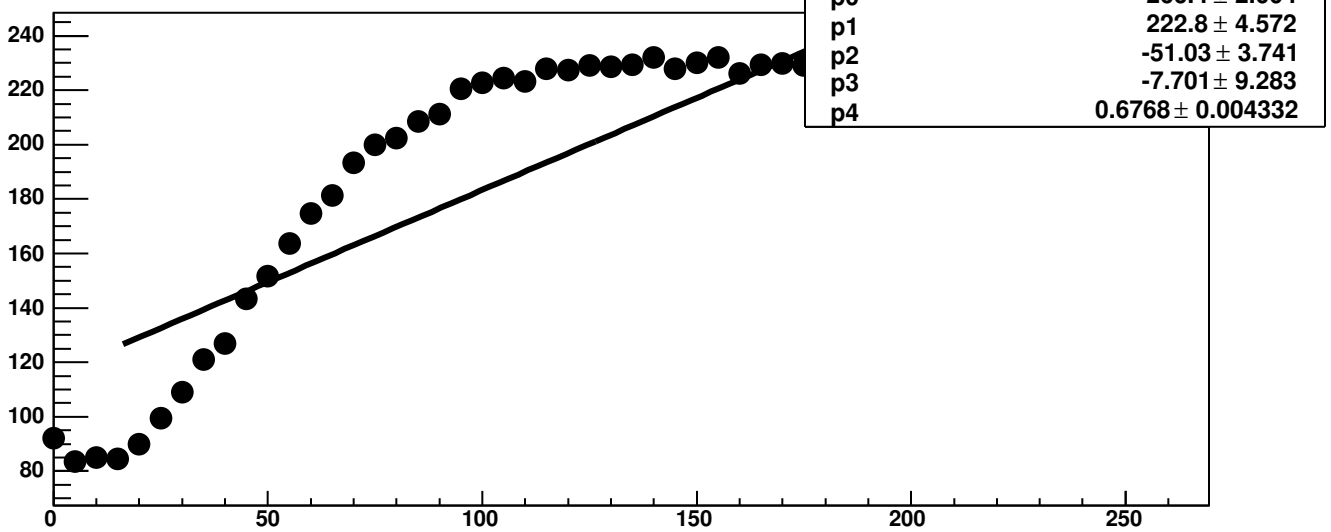
Chip 0, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold



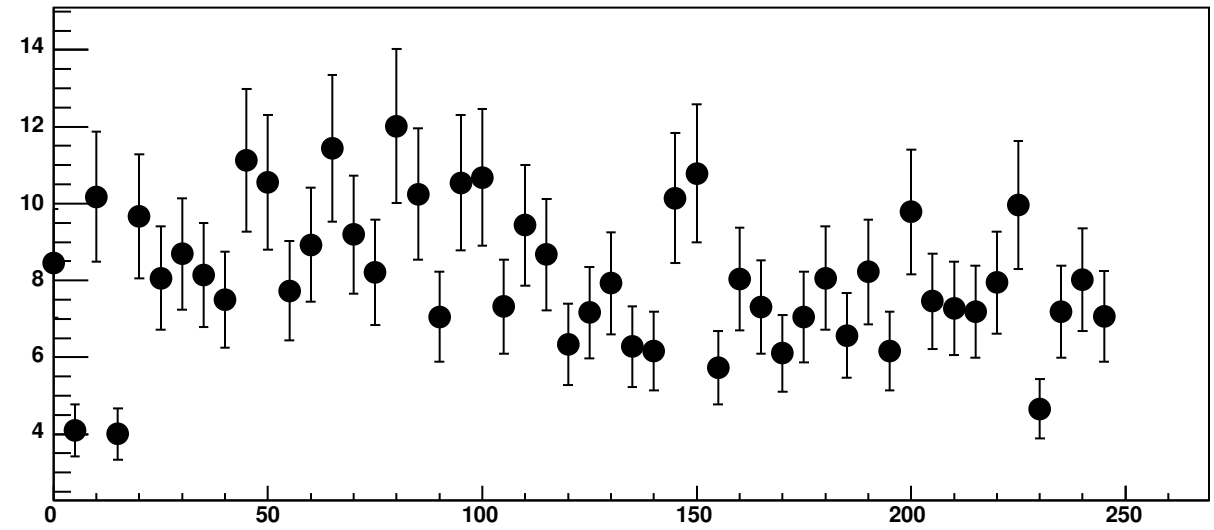
Chip 0, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold



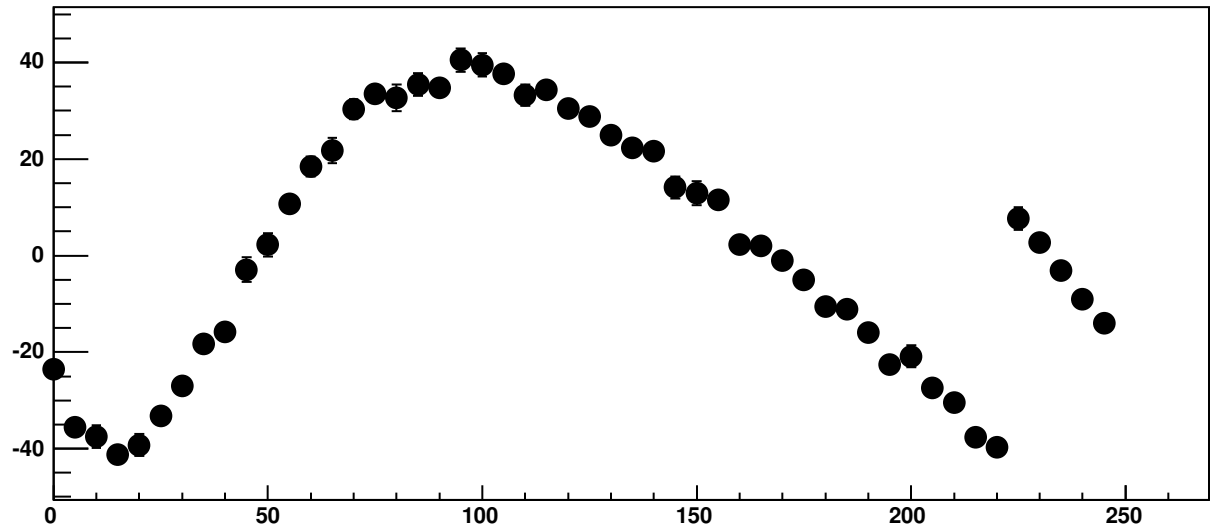
Chip 0, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold

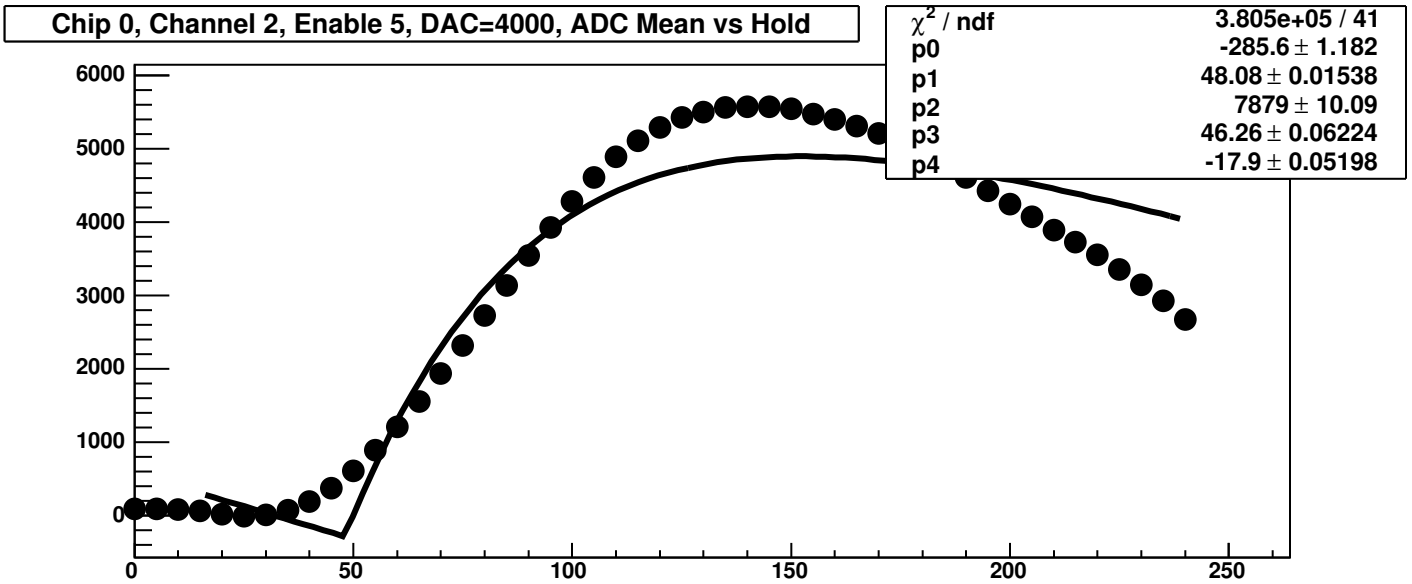


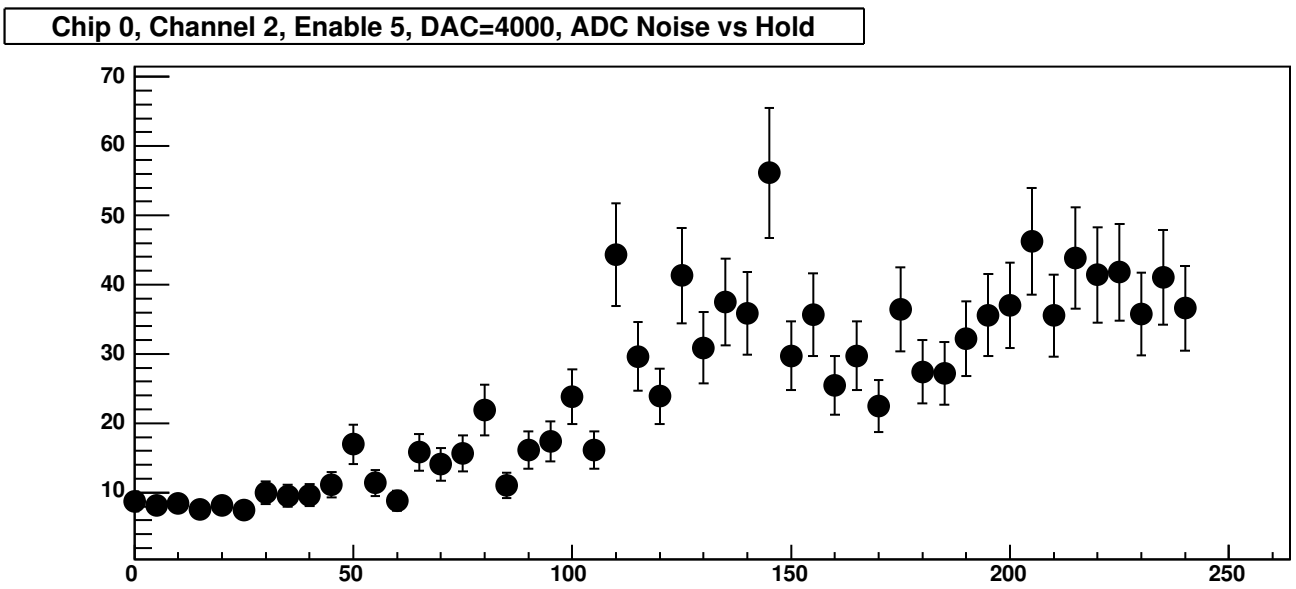
Chip 0, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold

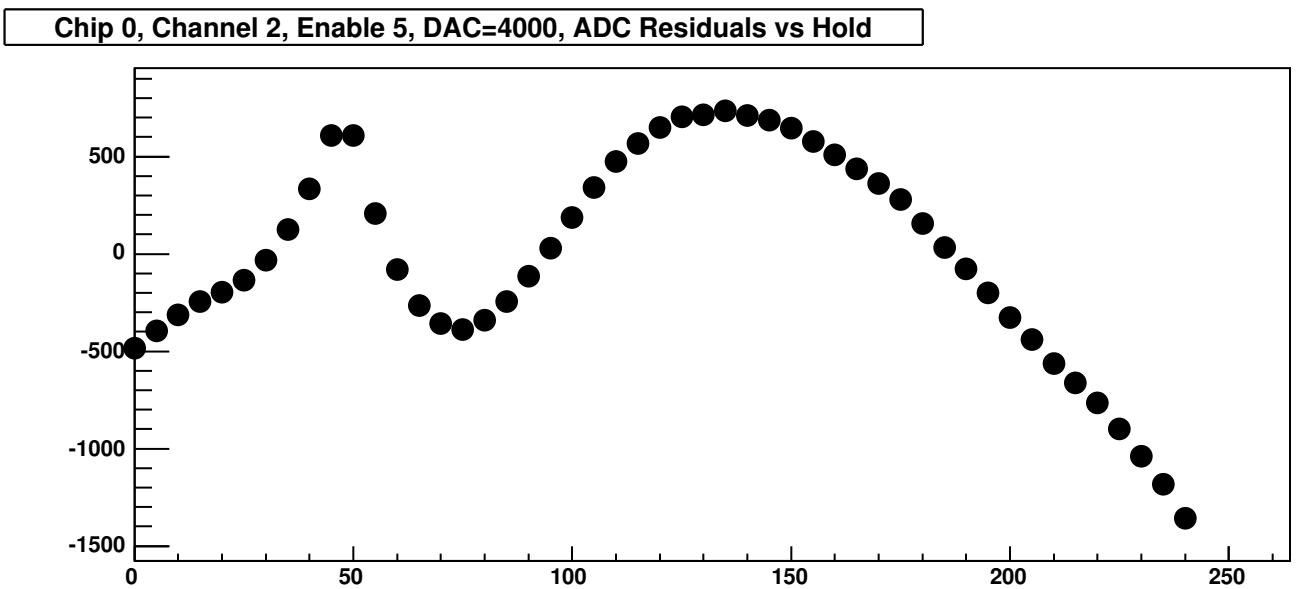


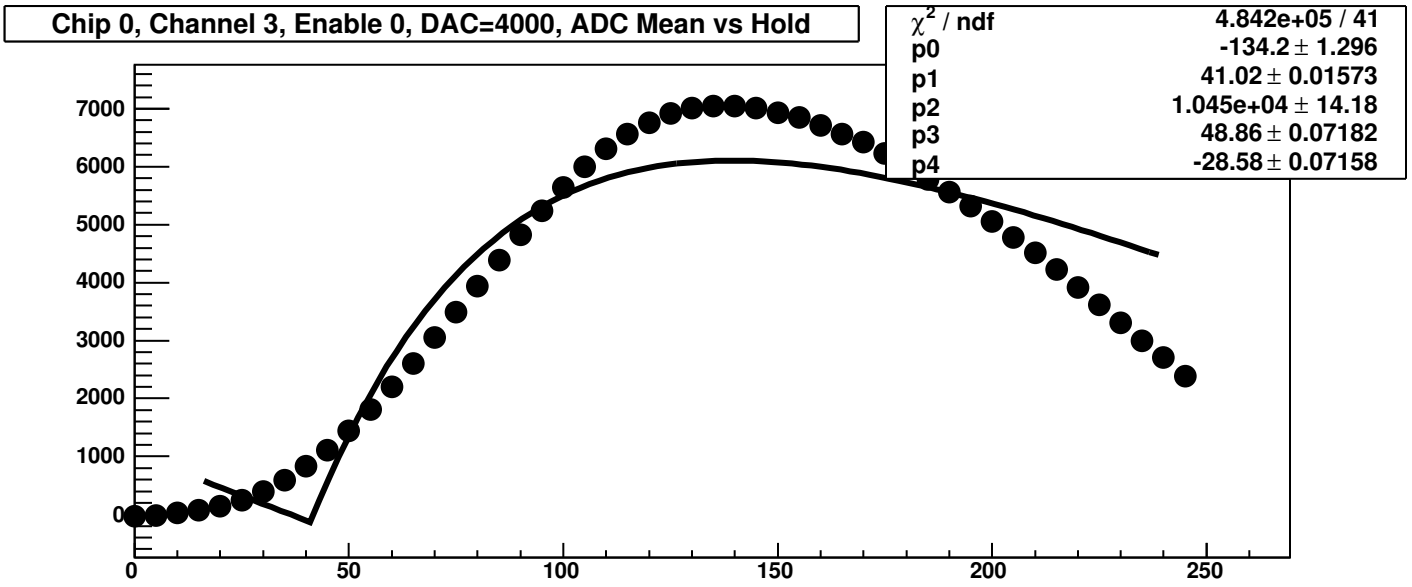
Chip 0, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

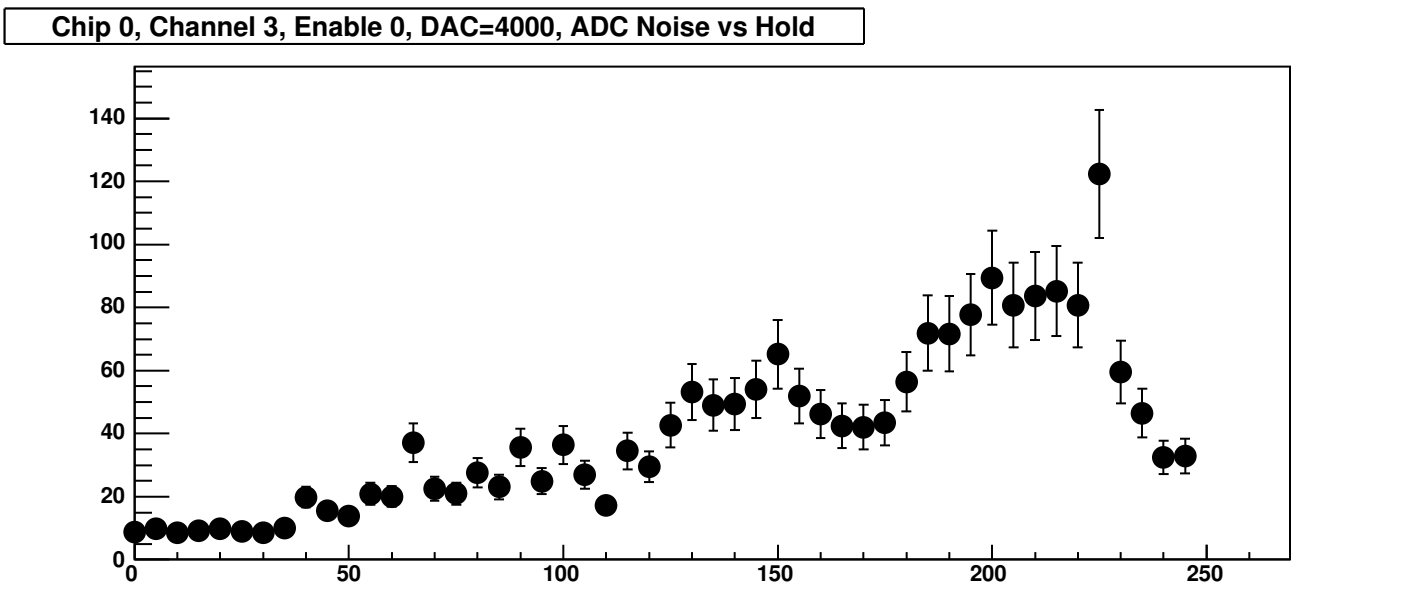


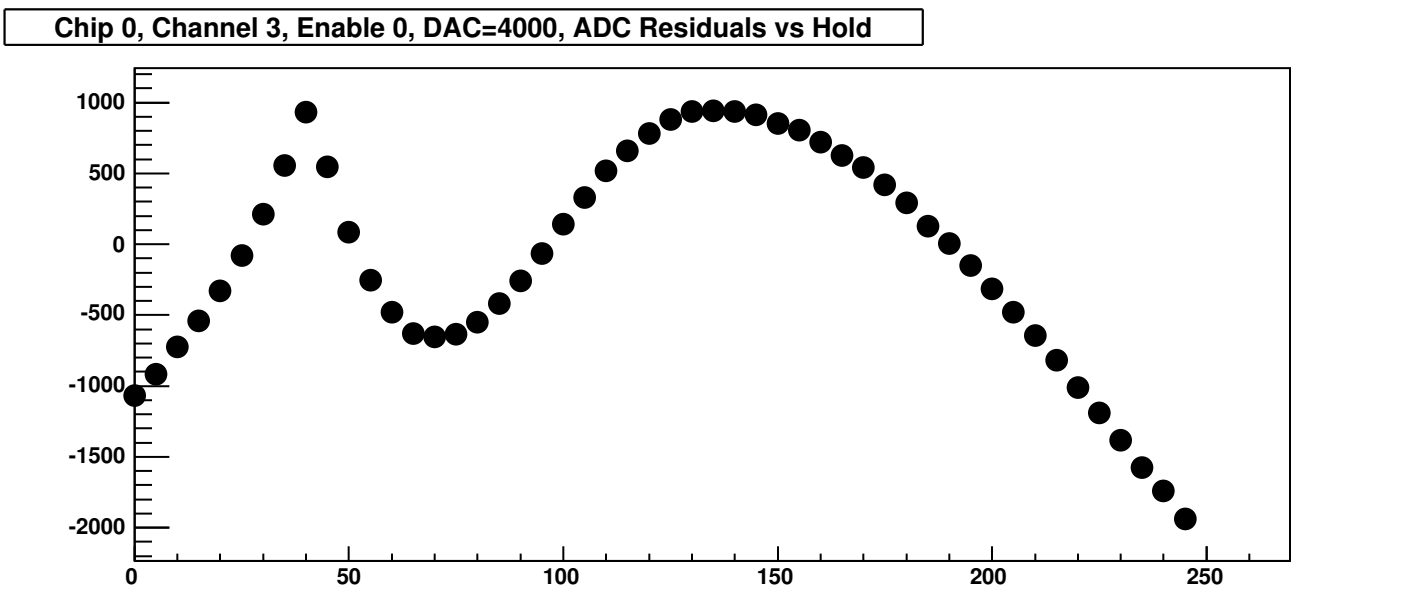

Chip 0, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.805e+05 / 41
p0 -285.6 ± 1.182
p1 48.08 ± 0.01538
p2 7879 ± 10.09
p3 46.26 ± 0.06224
p4 -17.9 ± 0.05198
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

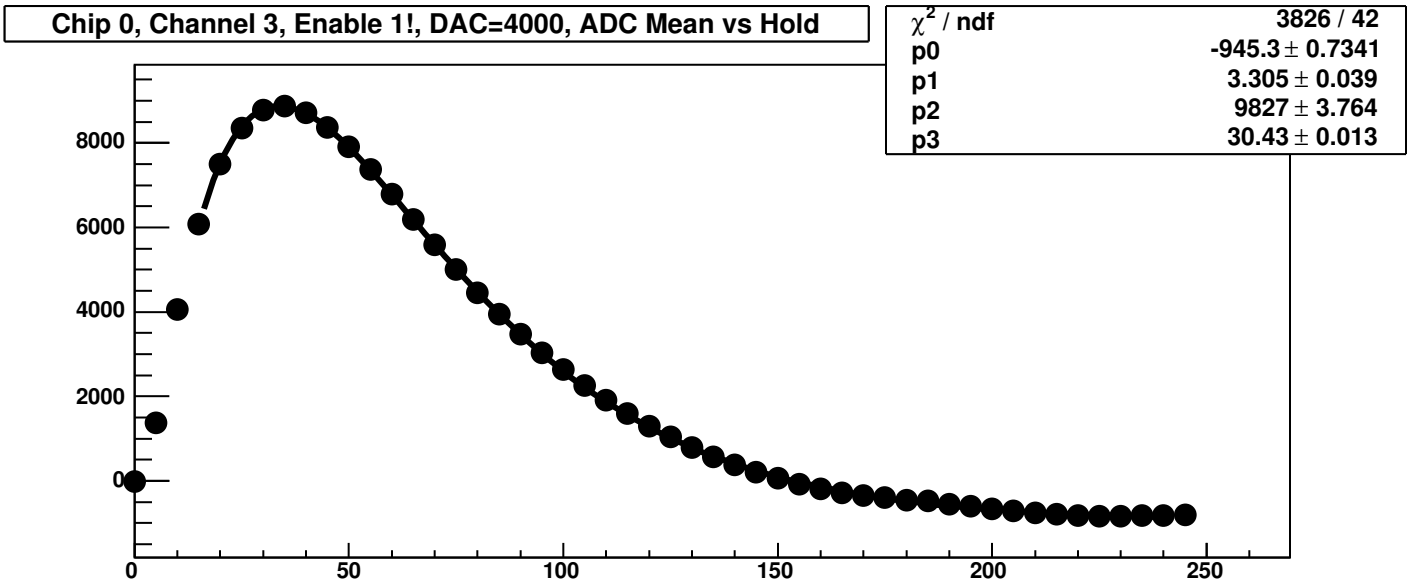

Chip 0, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold
70
60
50
40
30
20
10
0
50
100
150
200
250

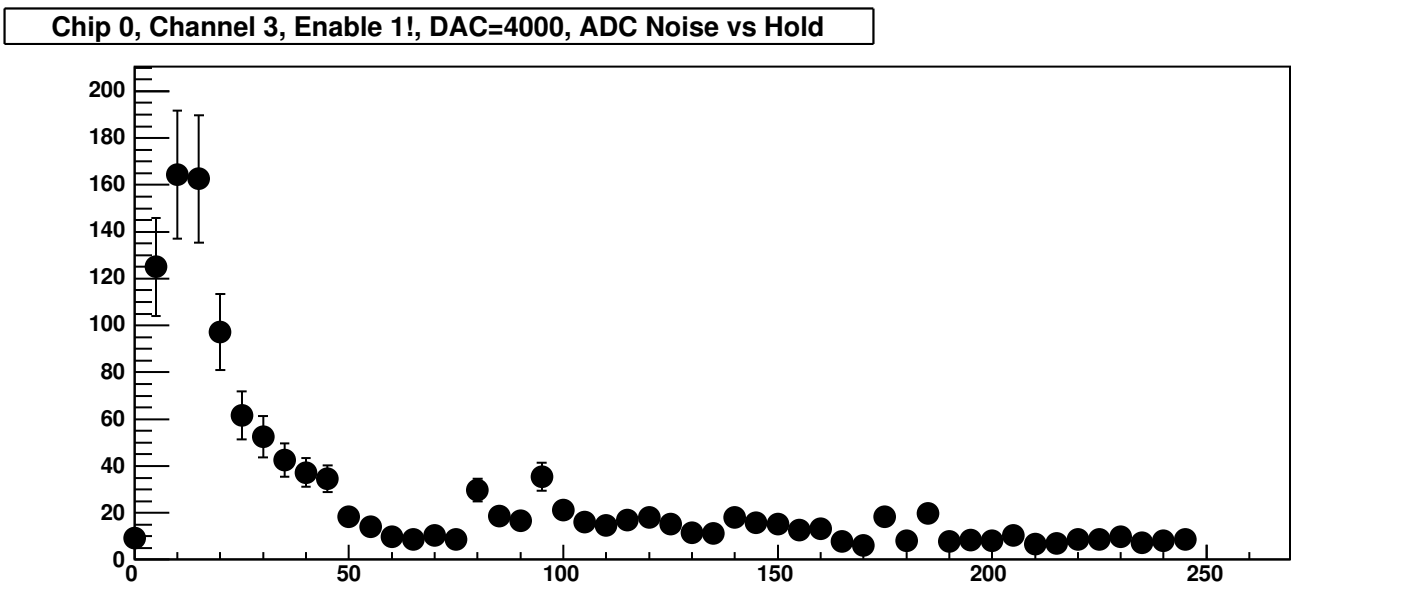

Chip 0, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold
500
0
-500
-1000
-1500
0
50
100
150
200
250

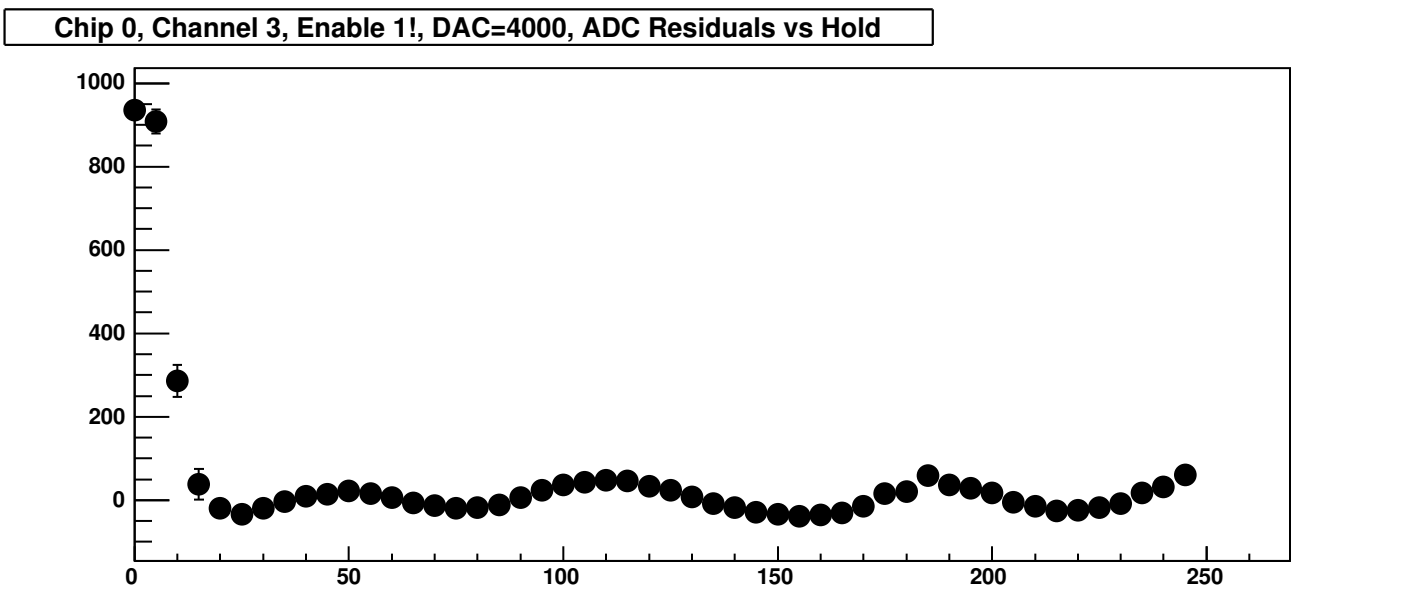

Chip 0, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.842e+05 / 41
p0 -134.2 ± 1.296
p1 41.02 ± 0.01573
p2 1.045e+04 ± 14.18
p3 48.86 ± 0.07182
p4 -28.58 ± 0.07158
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 0, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250

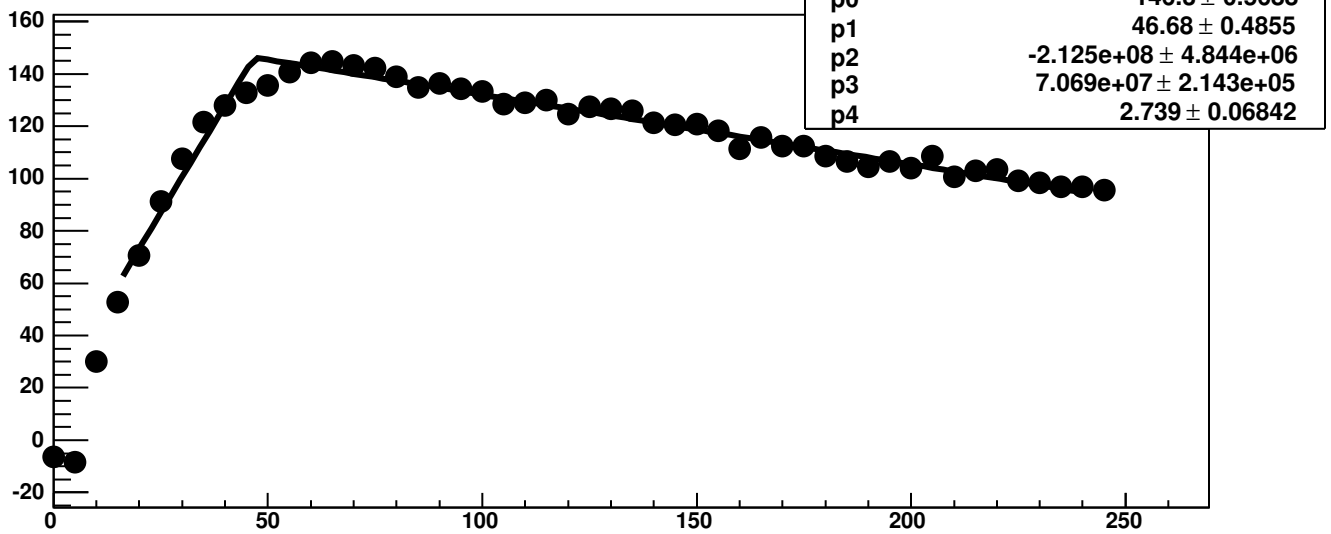

Chip 0, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250


Chip 0, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 3826 / 42
p0 -945.3 ± 0.7341
p1 3.305 ± 0.039
p2 9827 ± 3.764
p3 30.43 ± 0.013
8000
6000
4000
2000
0
0
50
100
150
200
250

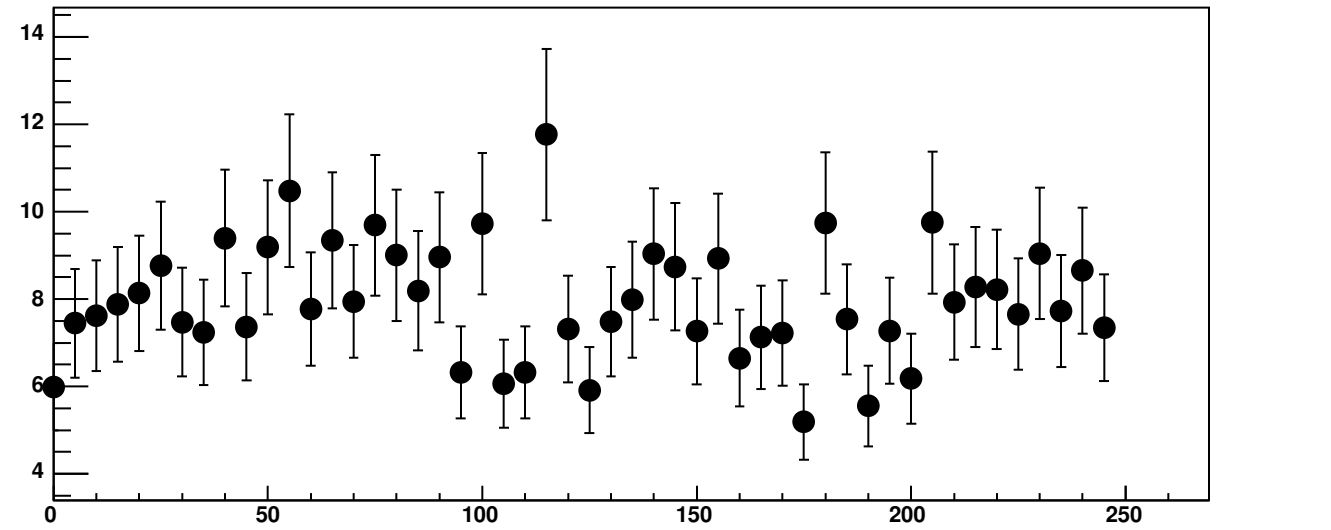

Chip 0, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold
1000
800
600
400
200
0
0
50
100
150
200
250

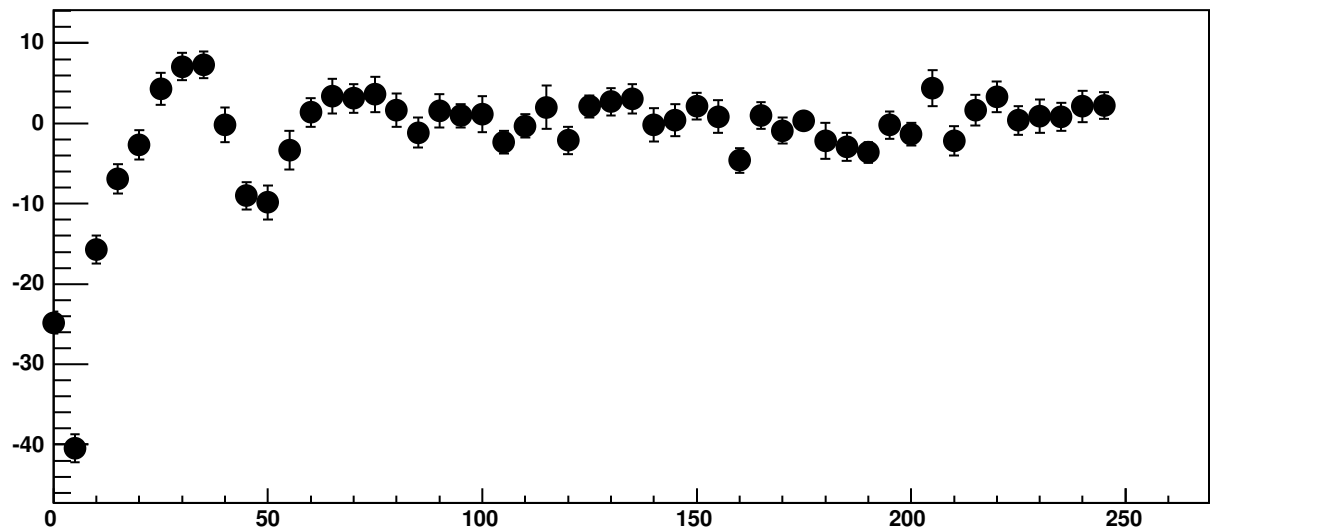
Chip 0, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

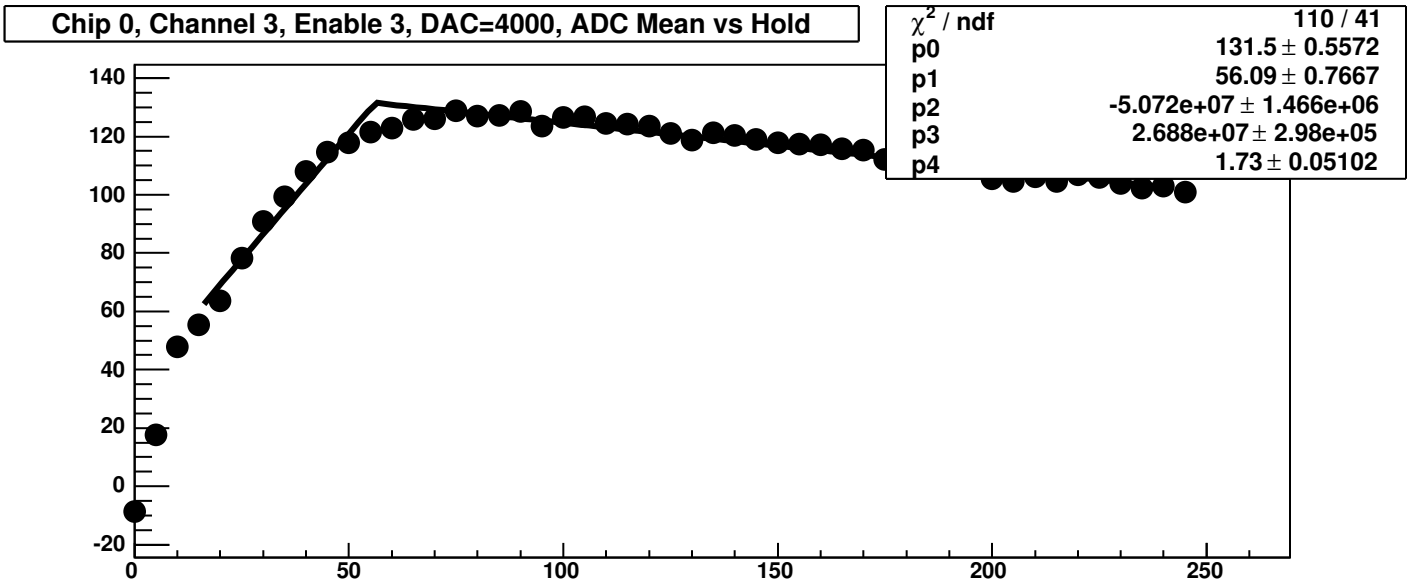


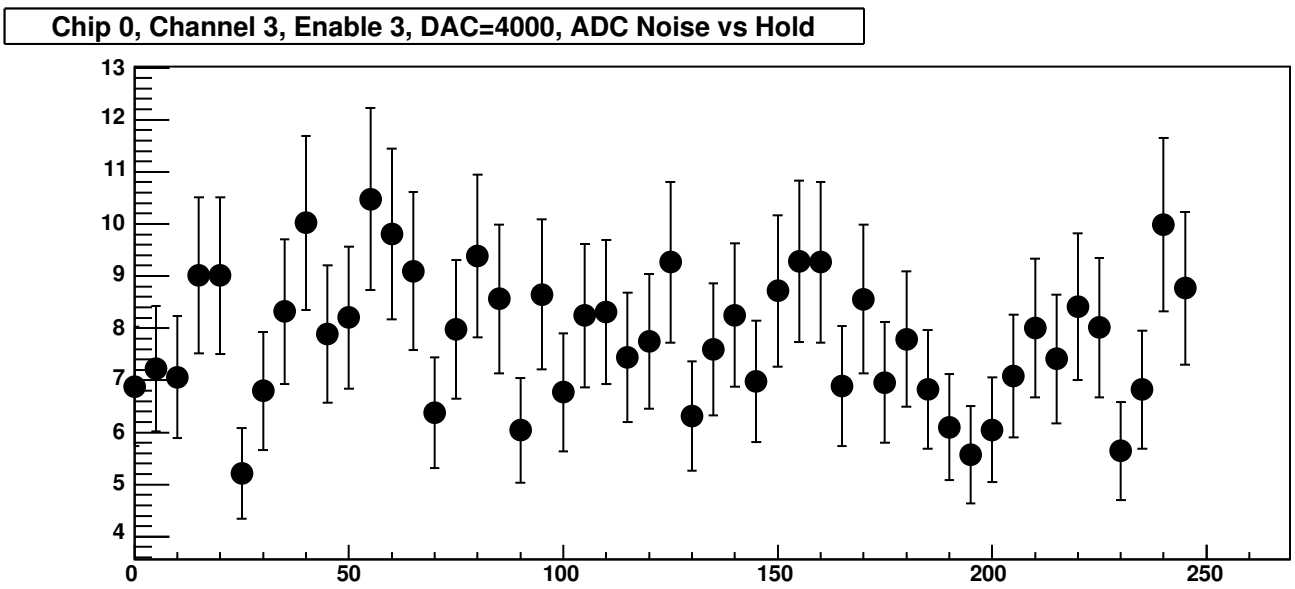
Chip 0, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold

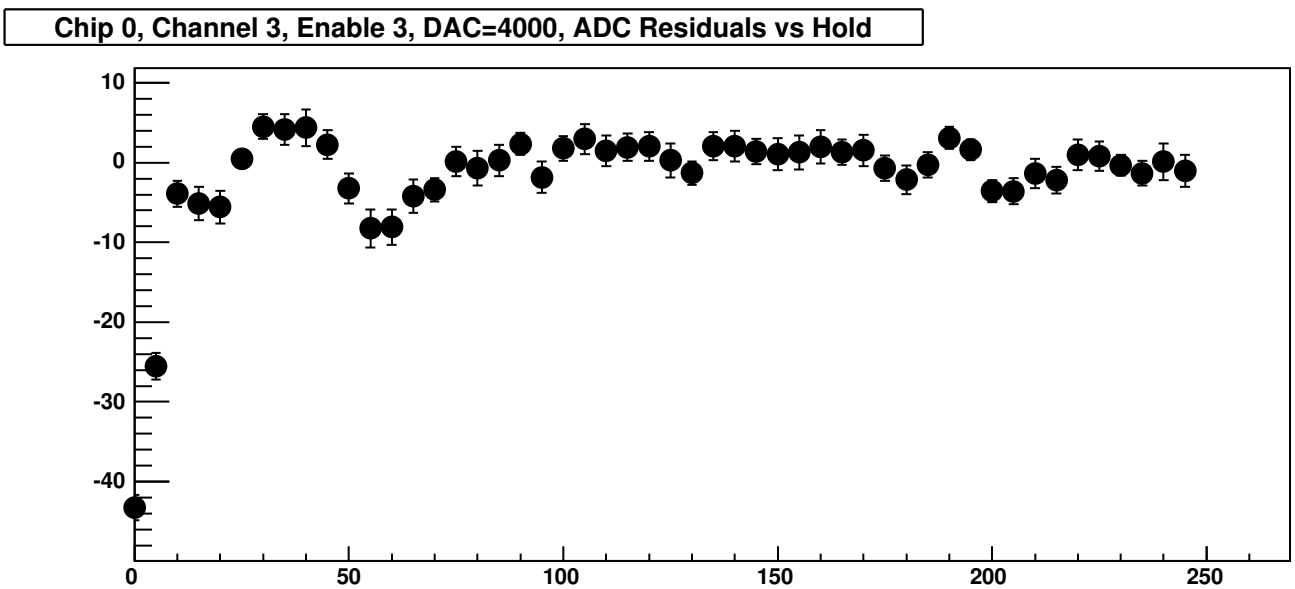


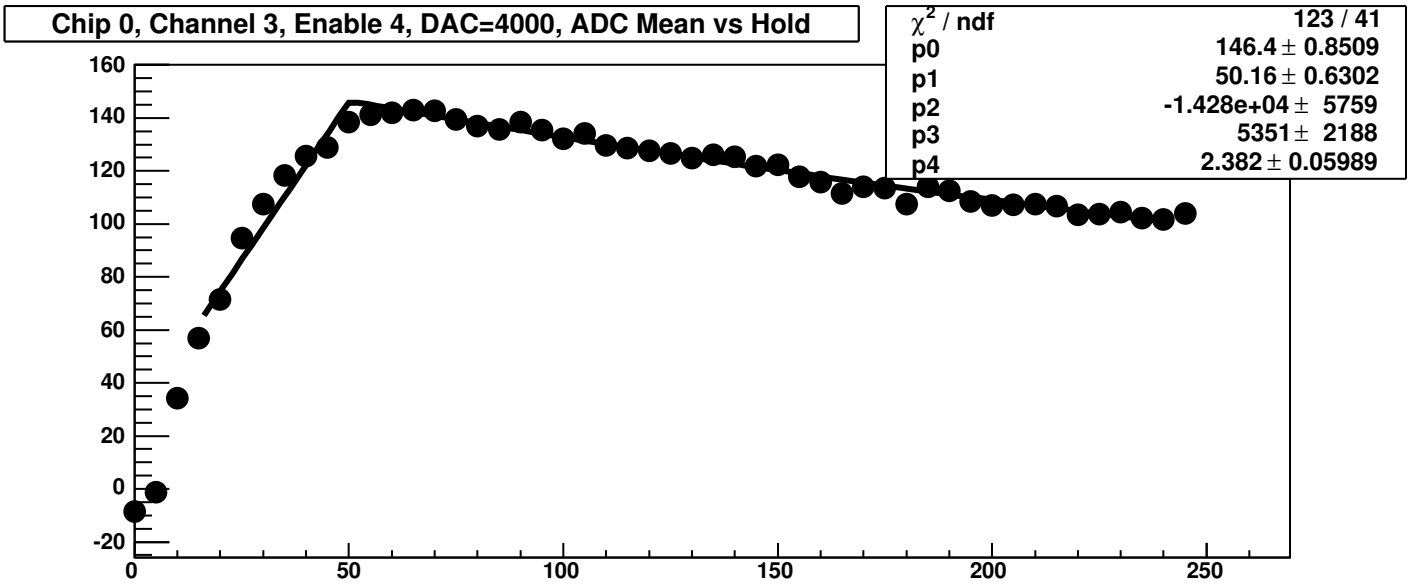
Chip 0, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

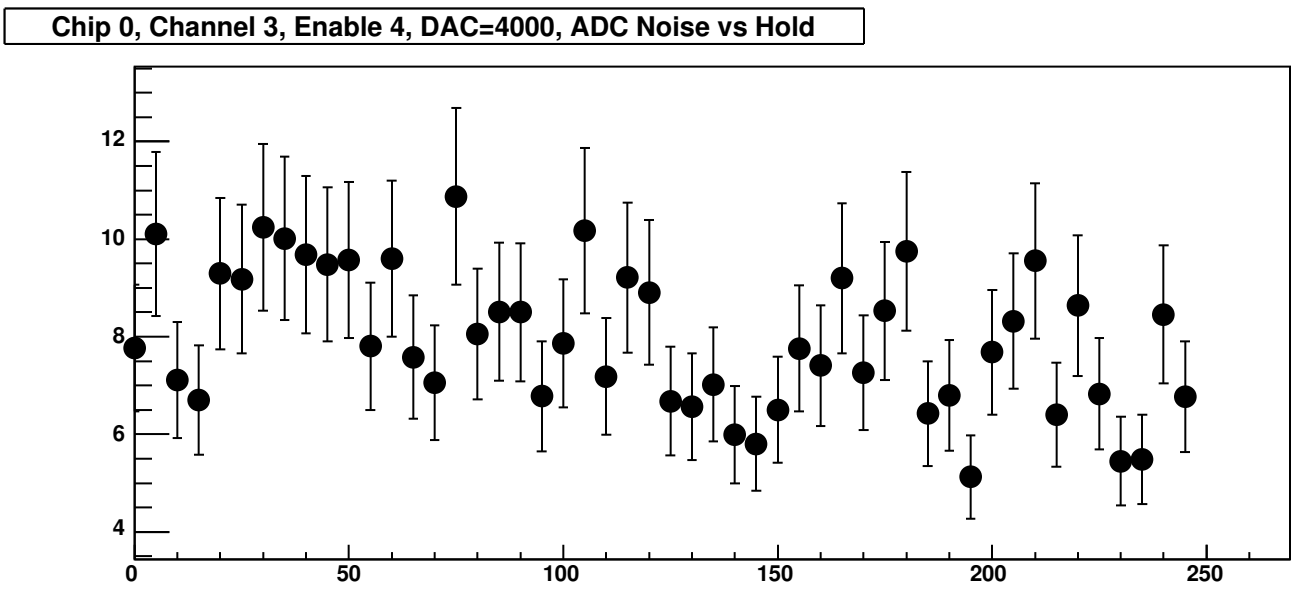


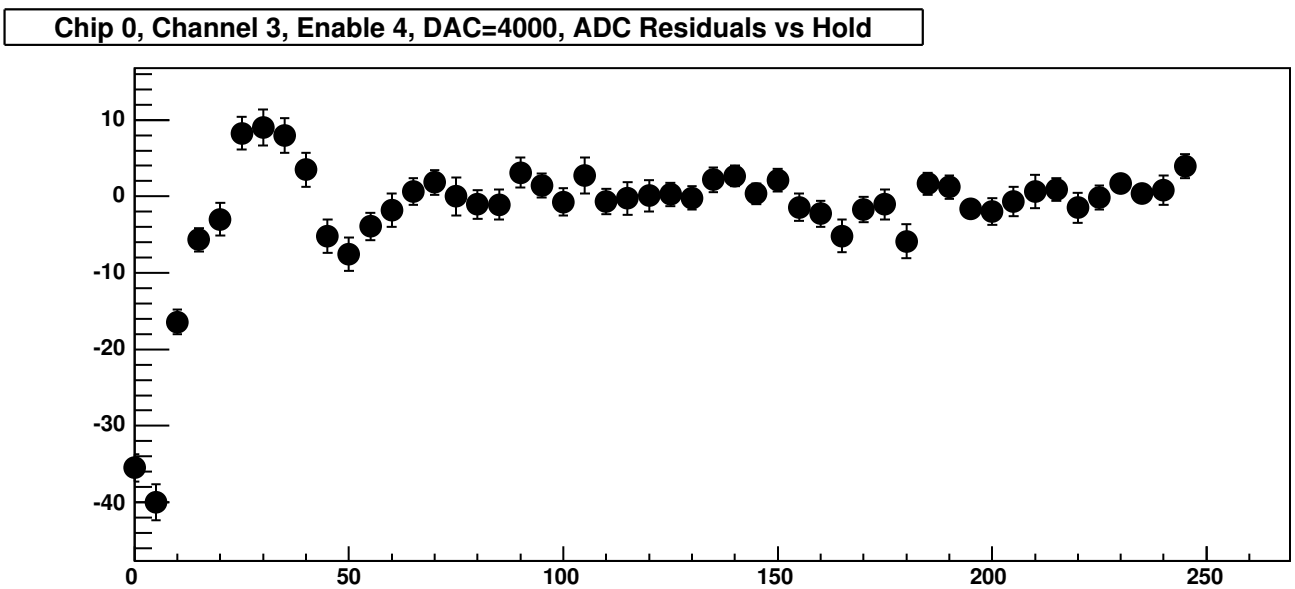

Chip 0, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 110 / 41
p0 131.5 ± 0.5572
p1 56.09 ± 0.7667
p2 -5.072e+07 ± 1.466e+06
p3 2.688e+07 ± 2.98e+05
p4 1.73 ± 0.05102

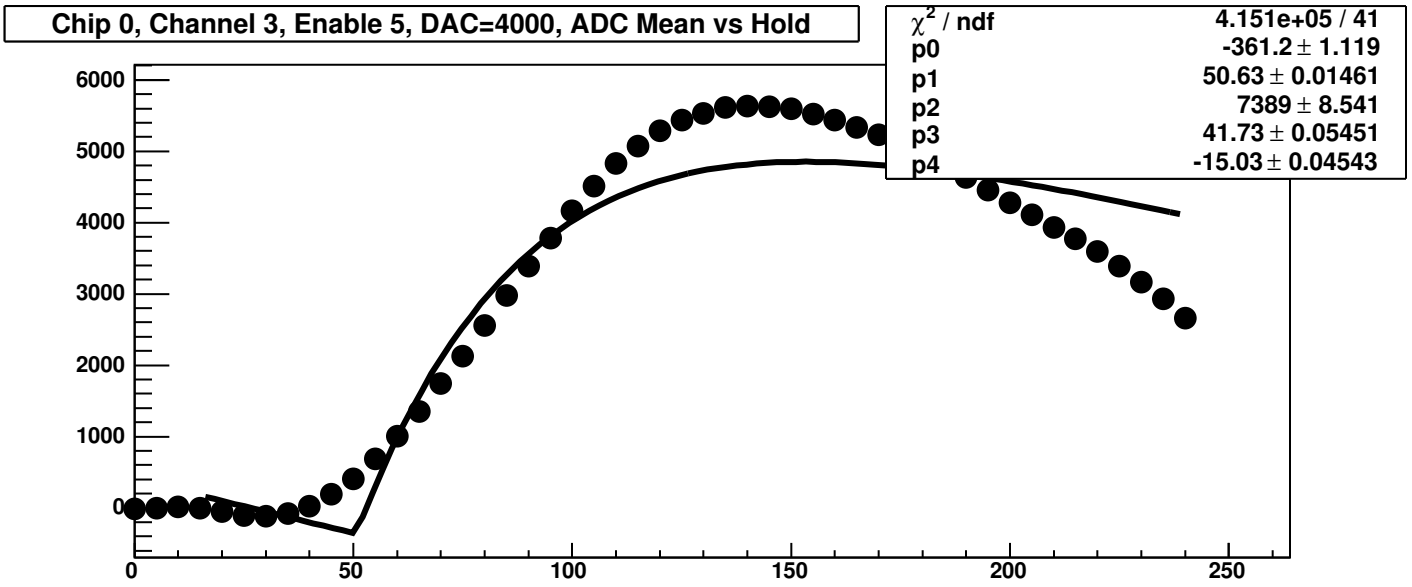

Chip 0, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold

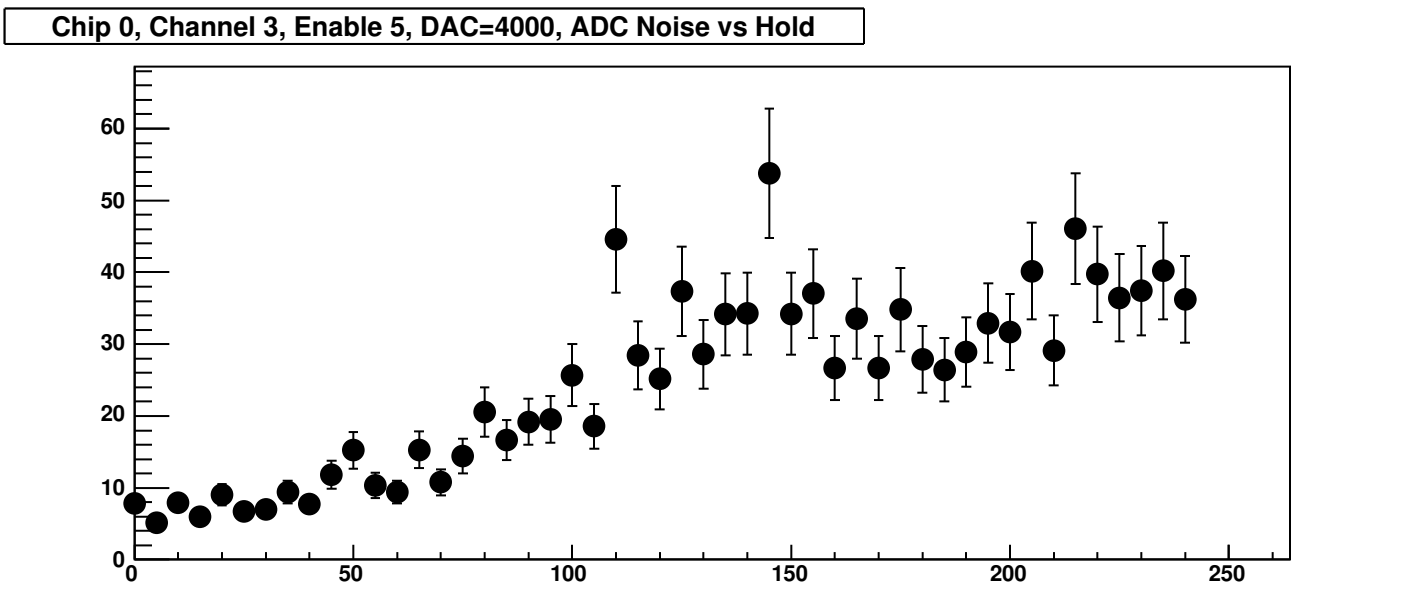

Chip 0, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

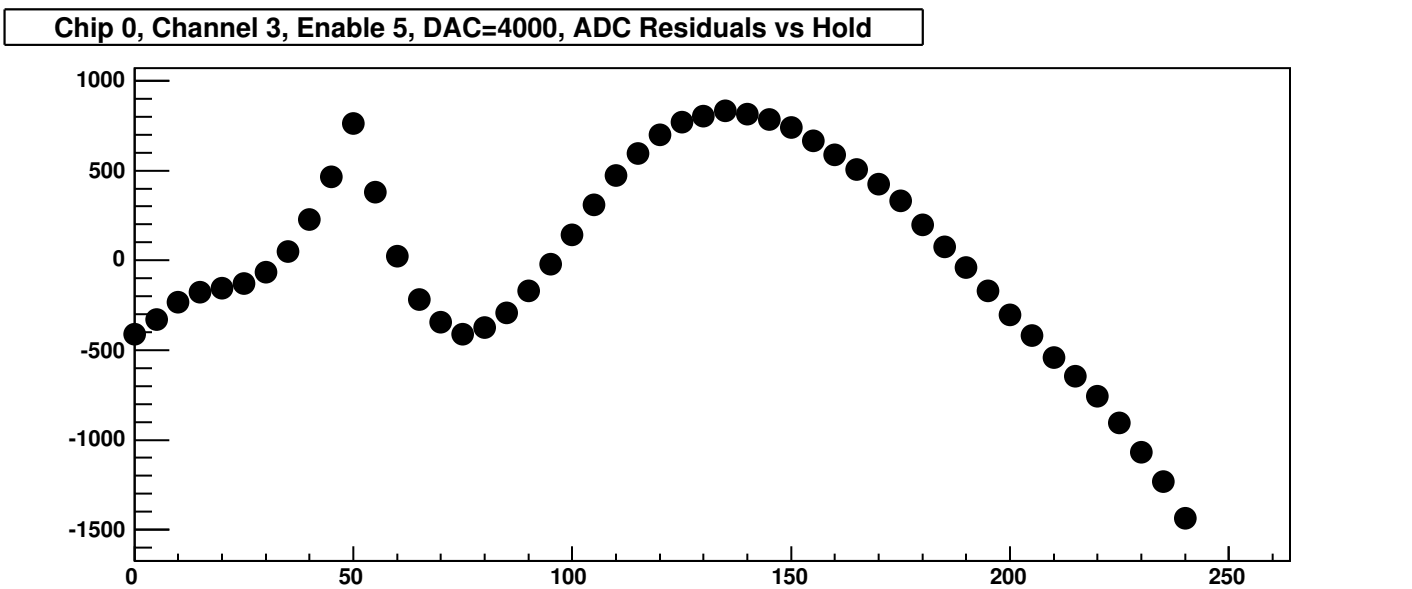

Chip 0, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 123 / 41
p0 146.4 ± 0.8509
p1 50.16 ± 0.6302
p2 -1.428e+04 ± 5759
p3 5351 ± 2188
p4 2.382 ± 0.05989

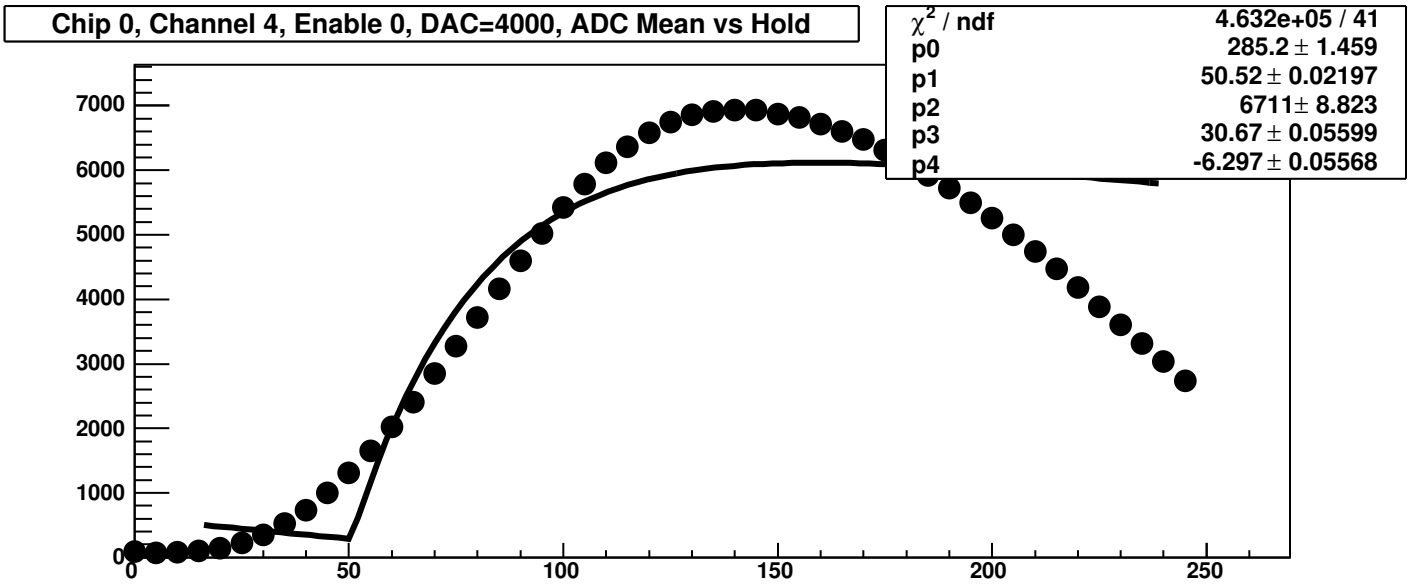

Chip 0, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold

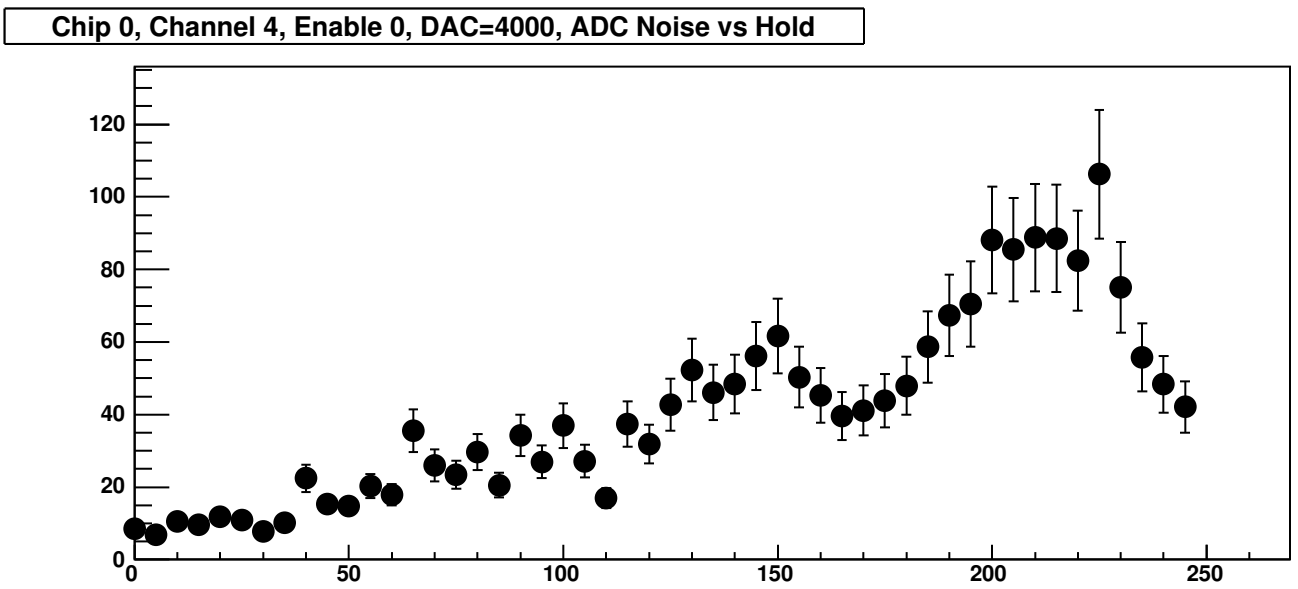

Chip 0, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold

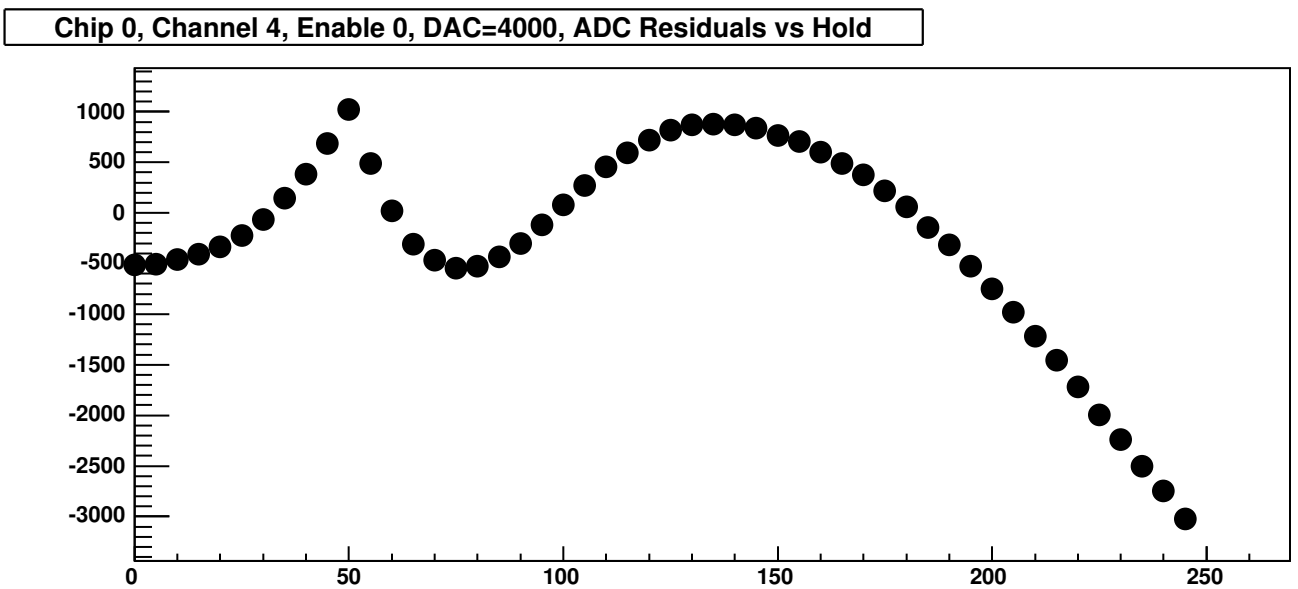

Chip 0, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.151e+05 / 41
p0 -361.2 ± 1.119
p1 50.63 ± 0.01461
p2 7389 ± 8.541
p3 41.73 ± 0.05451
p4 -15.03 ± 0.04543
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 0, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold
60
50
40
30
20
10
0
0
50
100
150
200
250

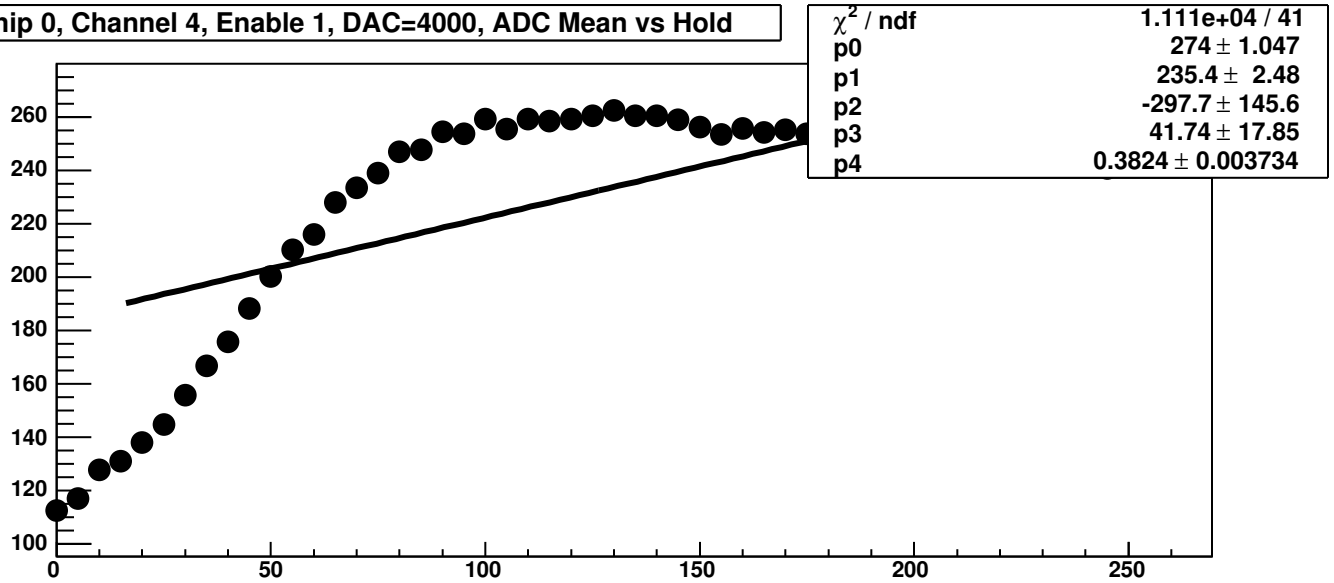

Chip 0, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250


Chip 0, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.632e+05 / 41
p0 285.2 ± 1.459
p1 50.52 ± 0.02197
p2 6711 ± 8.823
p3 30.67 ± 0.05599
p4 -6.297 ± 0.05568
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

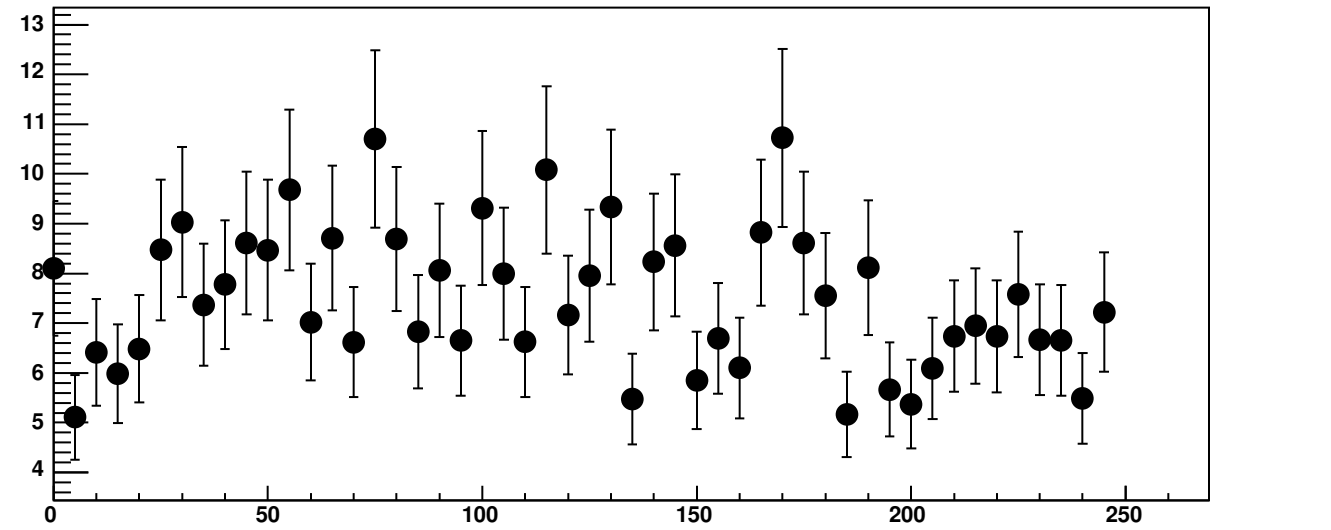

Chip 0, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250

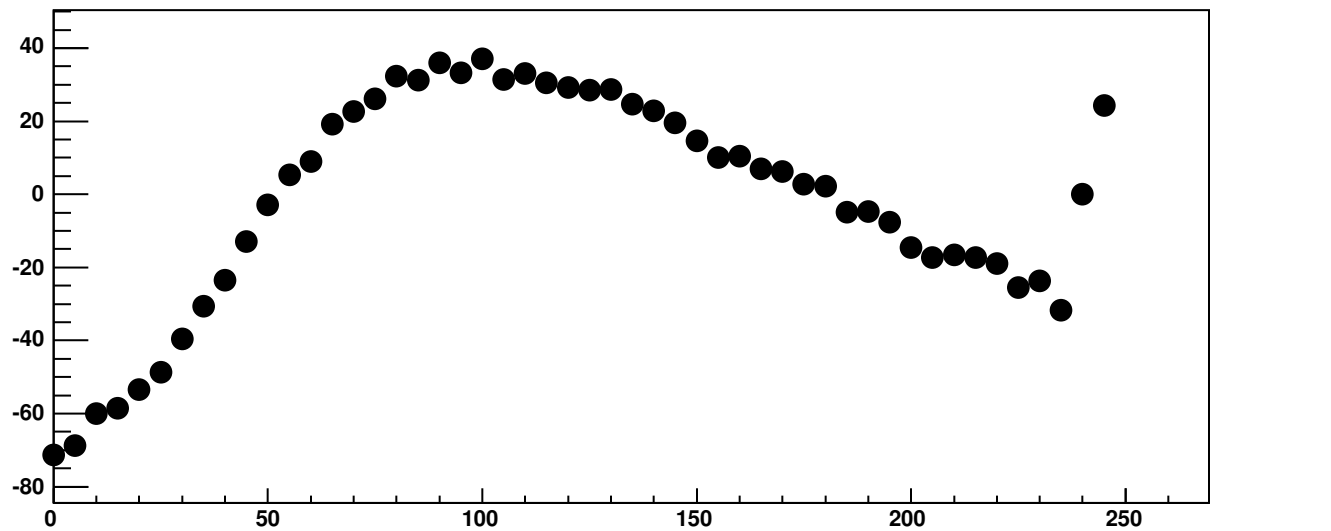
Chip 0, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold



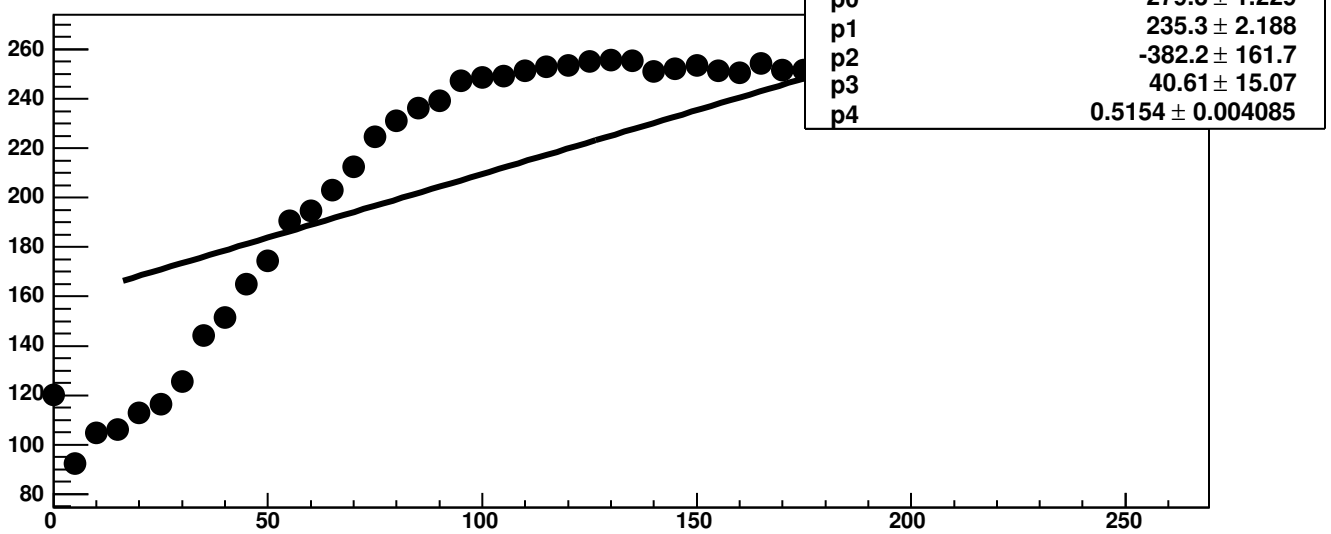
Chip 0, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold



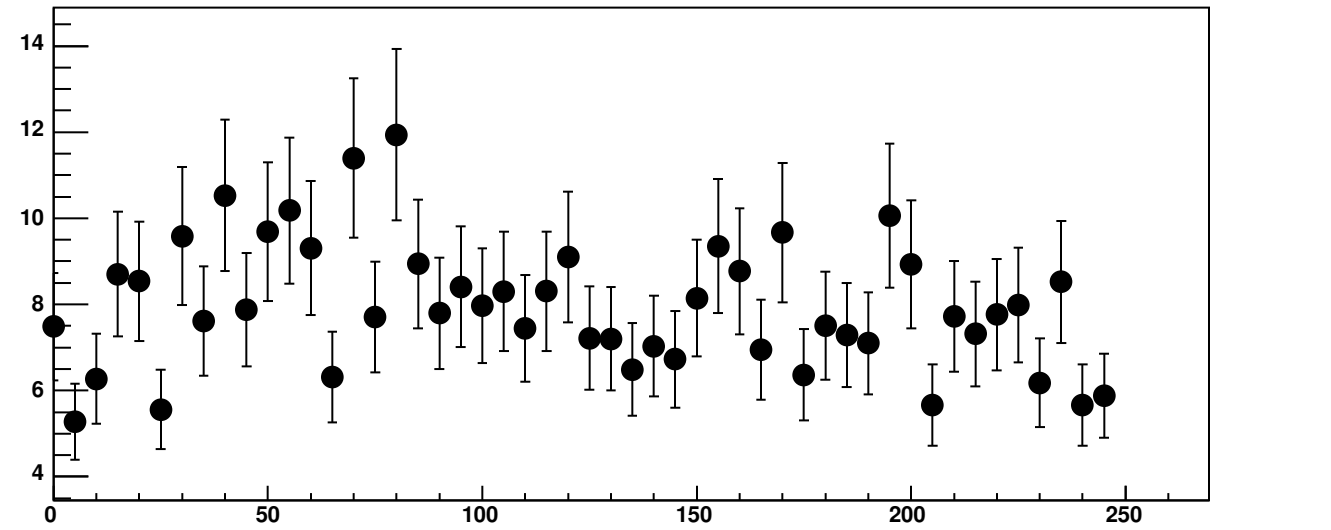
Chip 0, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold



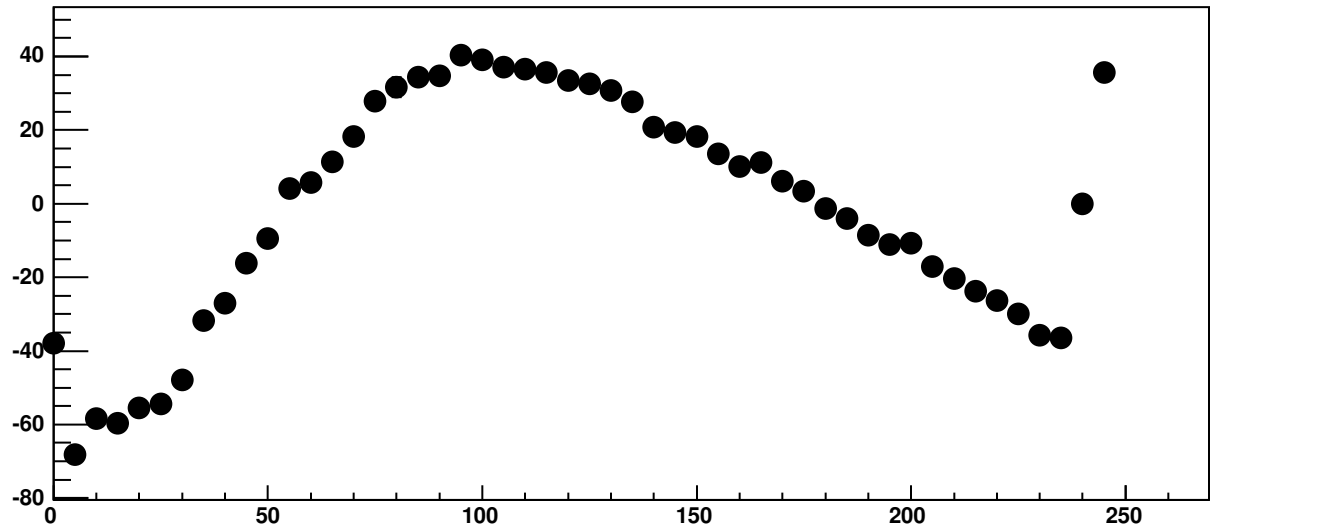
Chip 0, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

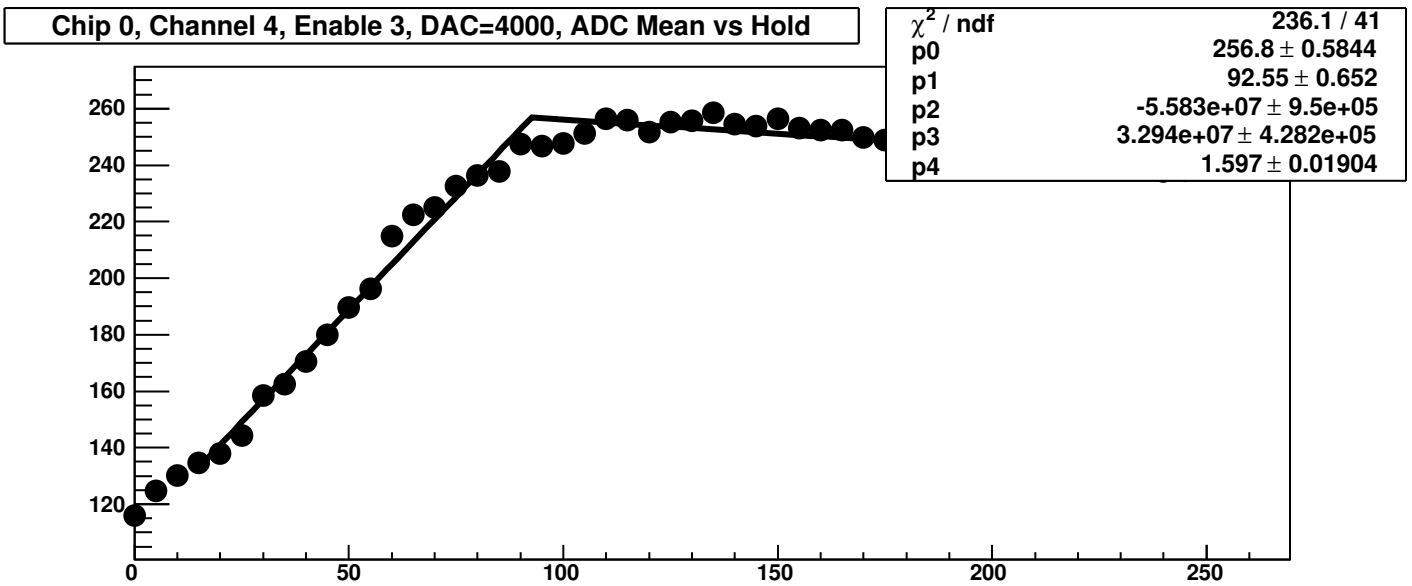


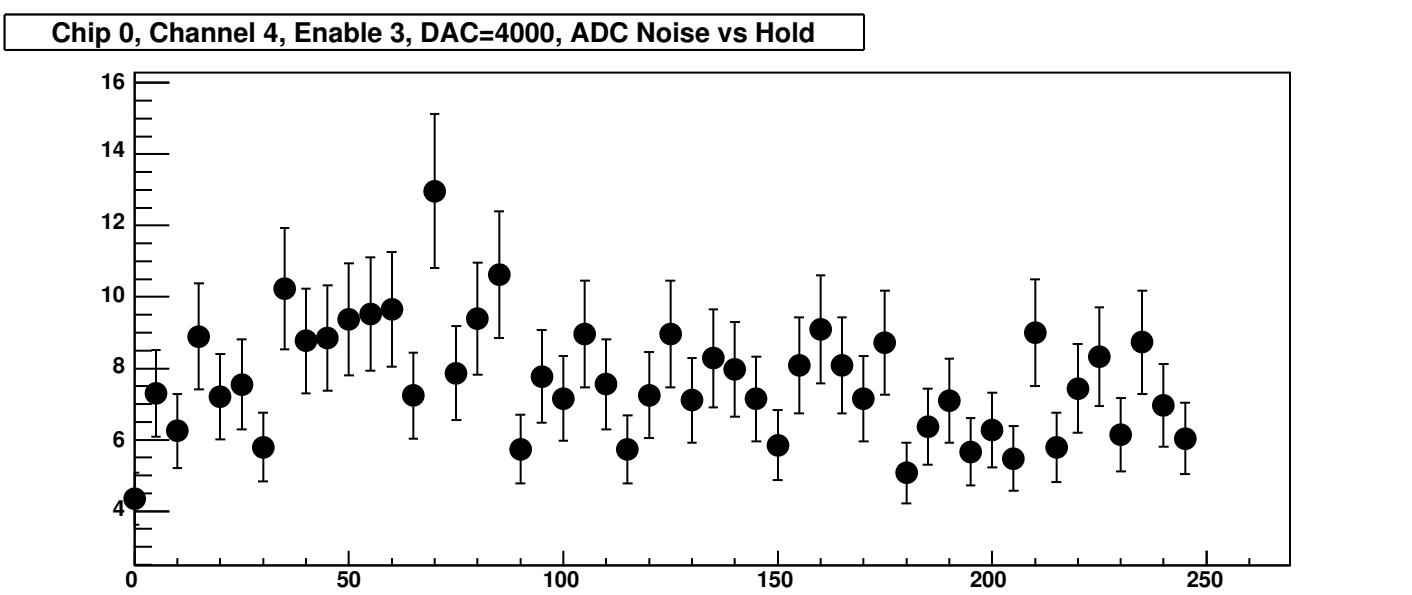
Chip 0, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold

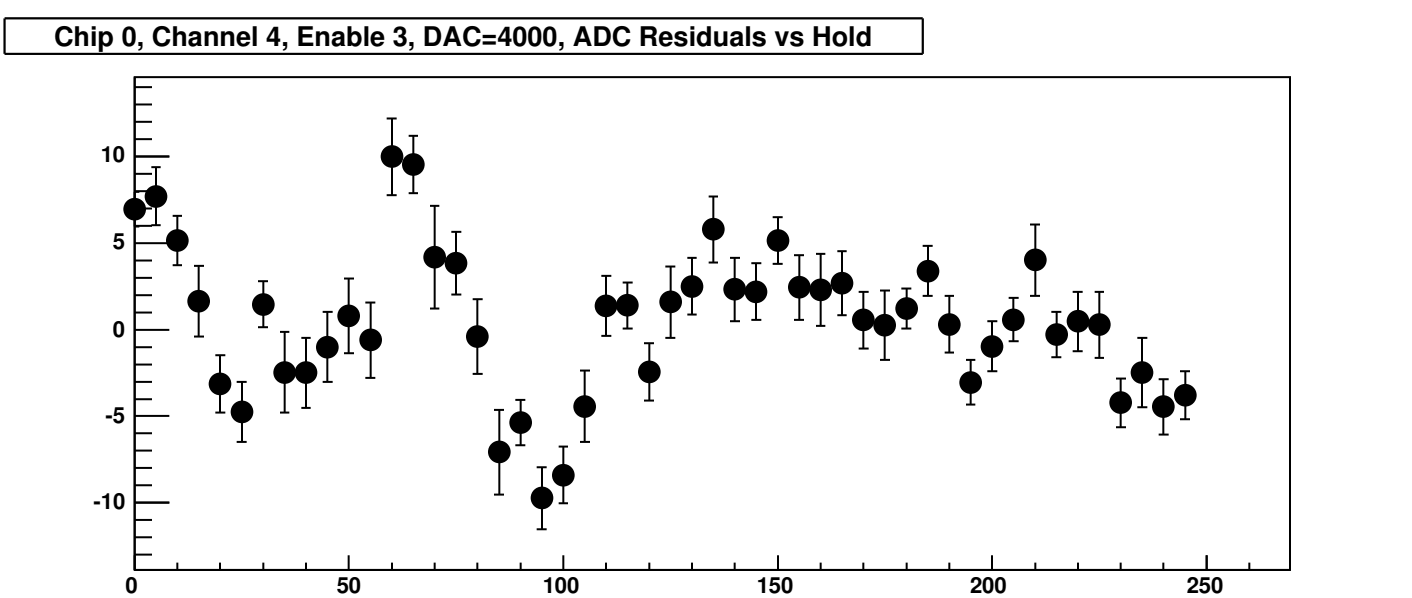


Chip 0, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold

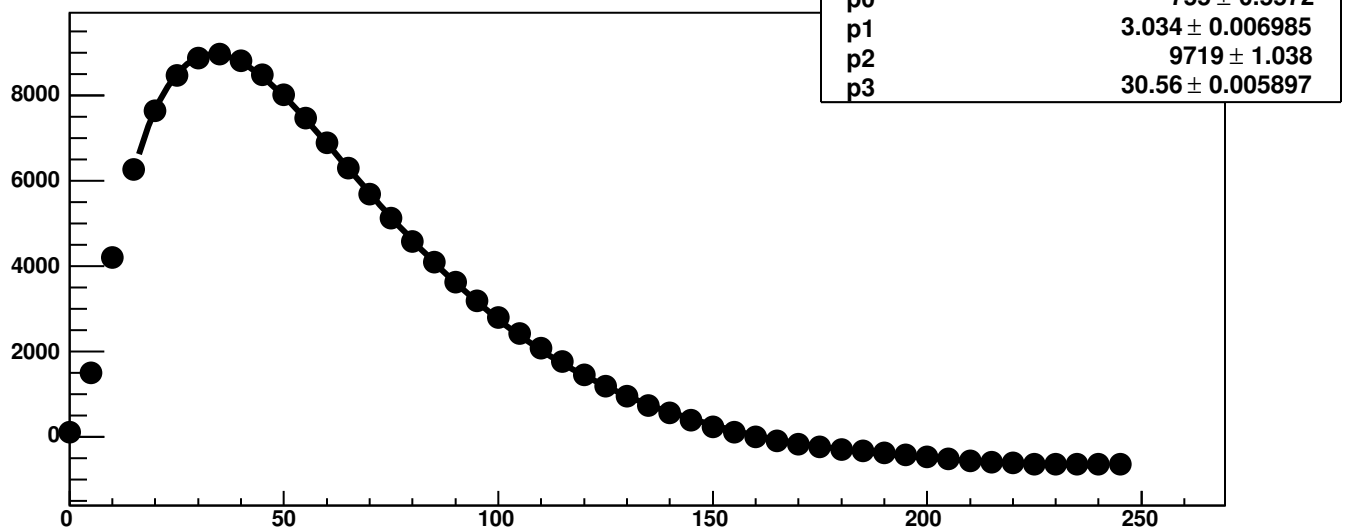



Chip 0, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 236.1 / 41
p0 256.8 ± 0.5844
p1 92.55 ± 0.652
p2 -5.583e+07 ± 9.5e+05
p3 3.294e+07 ± 4.282e+05
p4 1.597 ± 0.01904
260
240
220
200
180
160
140
120
0
50
100
150
200
250

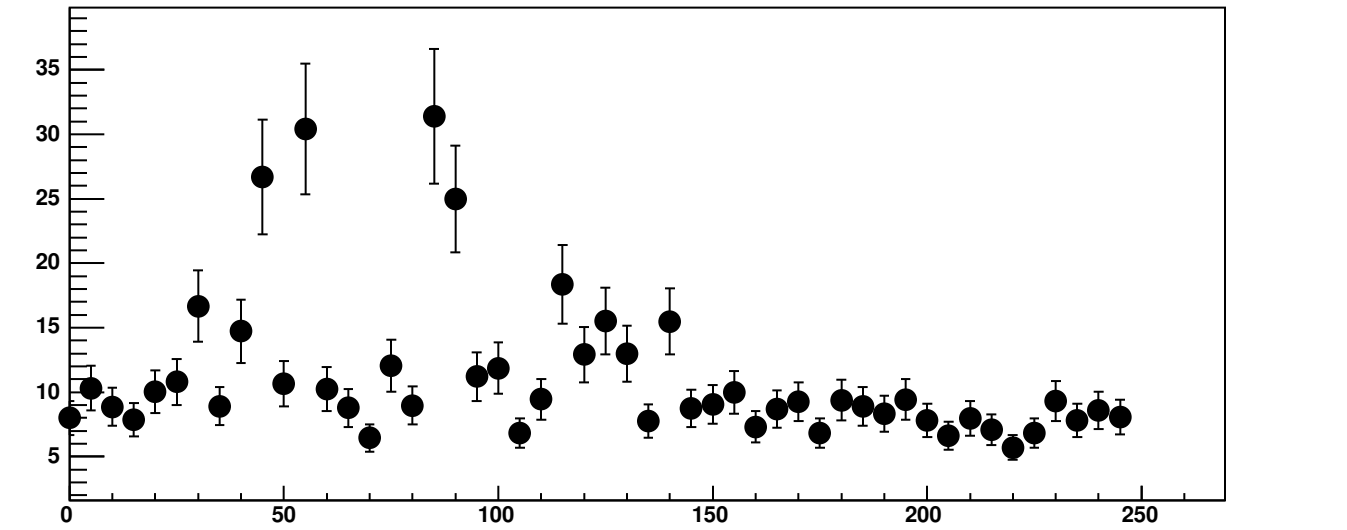

Chip 0, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold
16
14
12
10
8
6
4
0
50
100
150
200
250


Chip 0, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250

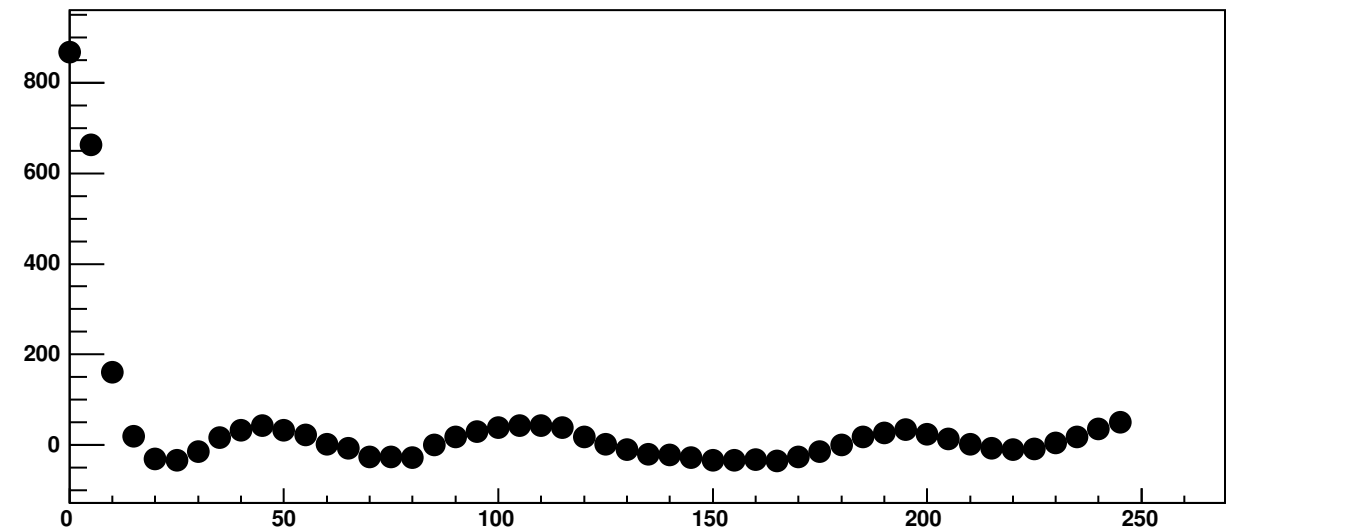
Chip 0, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold

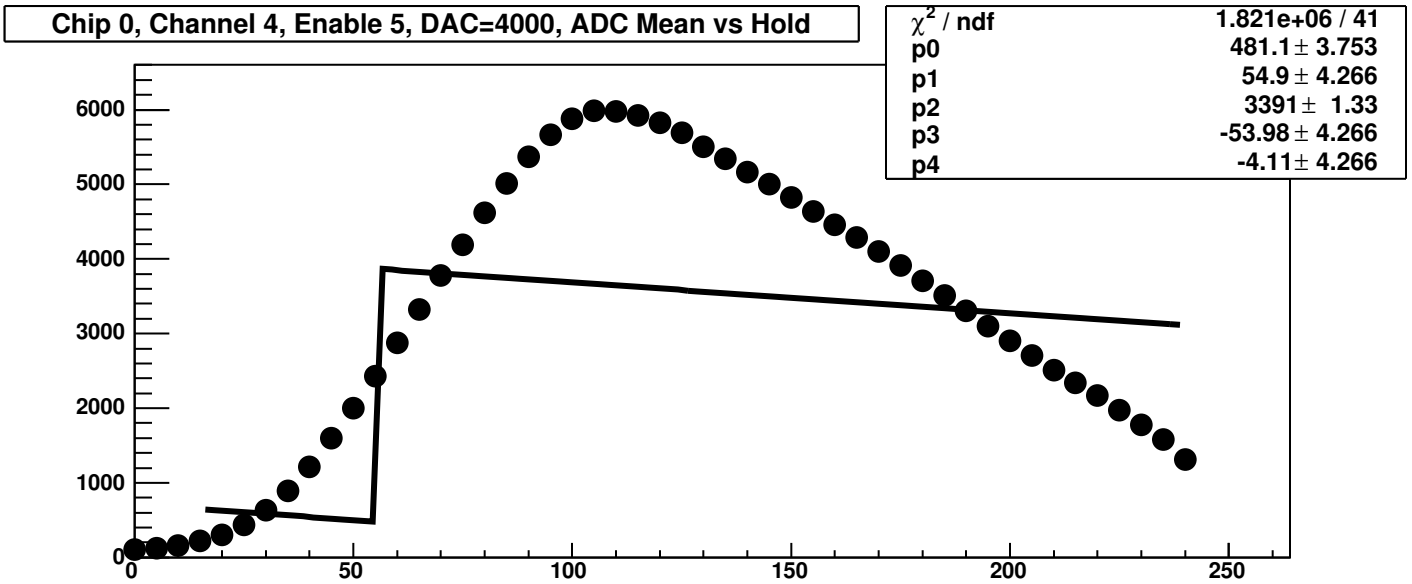


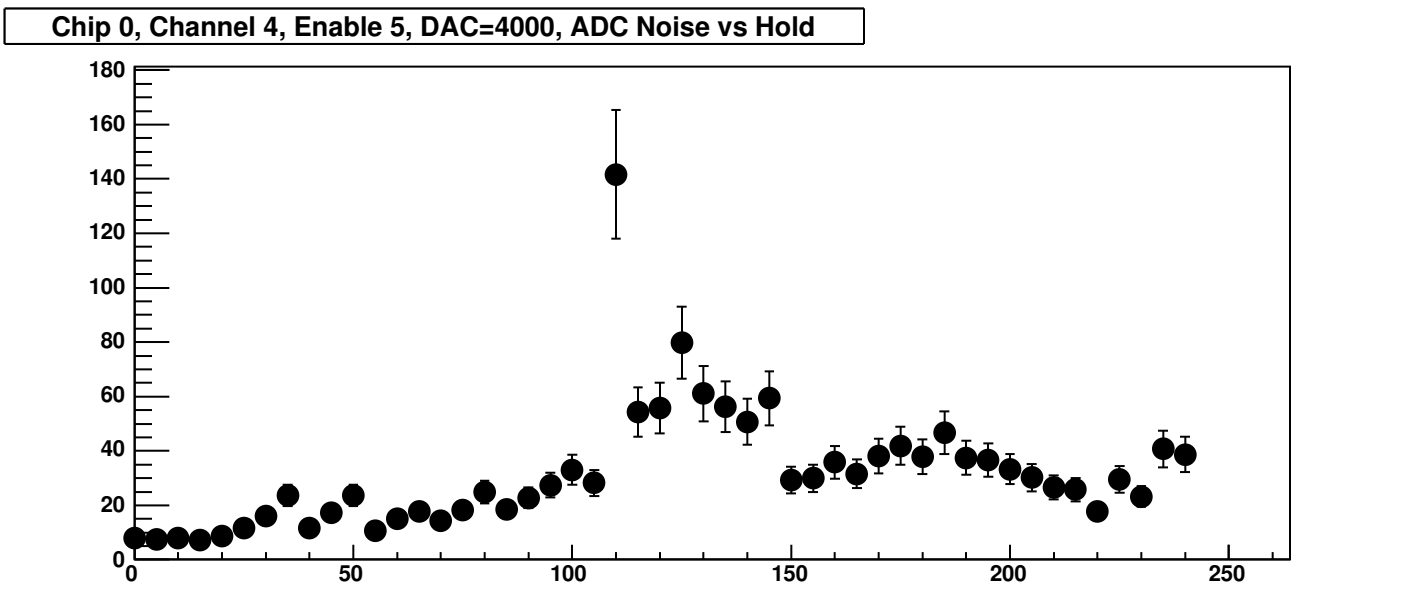
Chip 0, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold

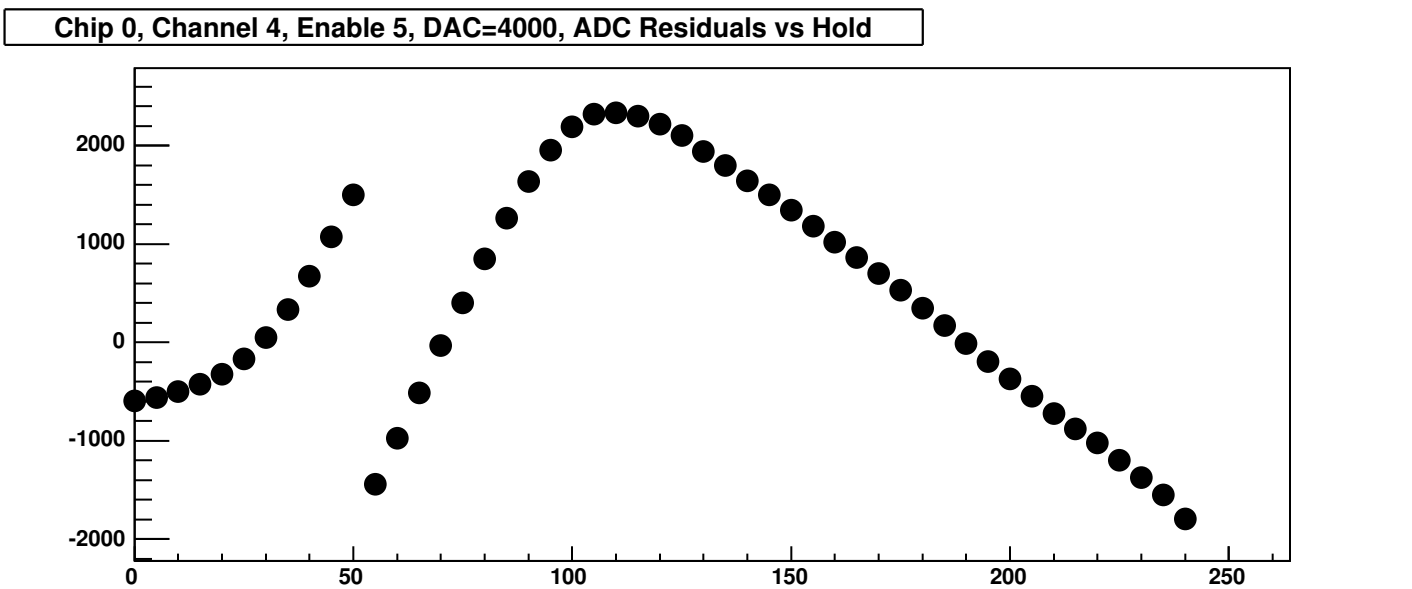


Chip 0, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold

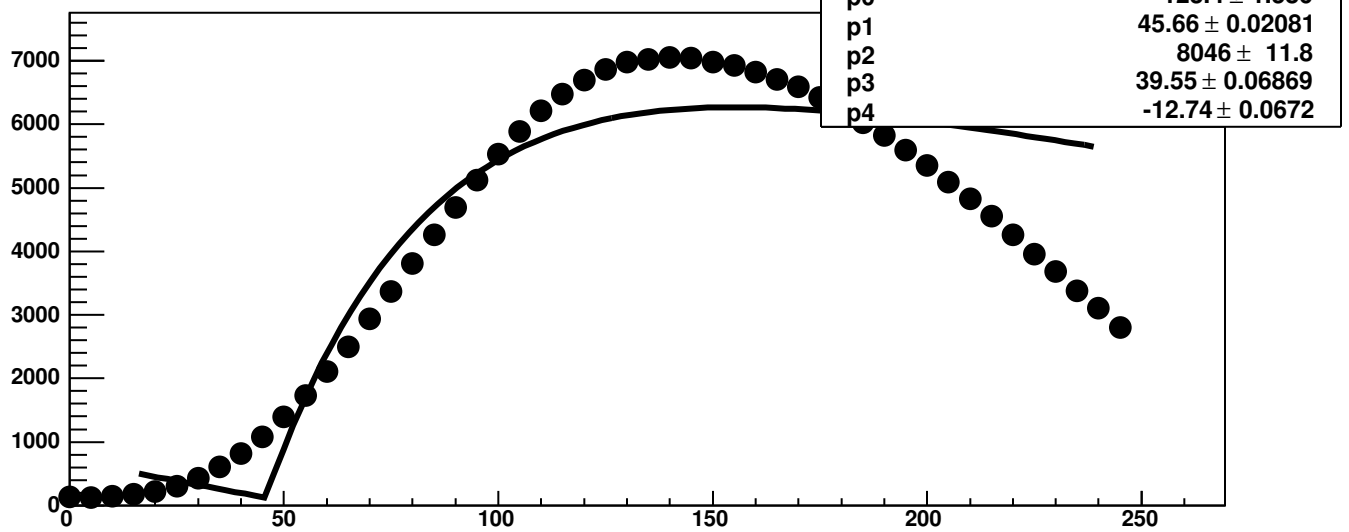



Chip 0, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.821e+06 / 41
p0 481.1 ± 3.753
p1 54.9 ± 4.266
p2 3391 ± 1.33
p3 -53.98 ± 4.266
p4 -4.11 ± 4.266
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

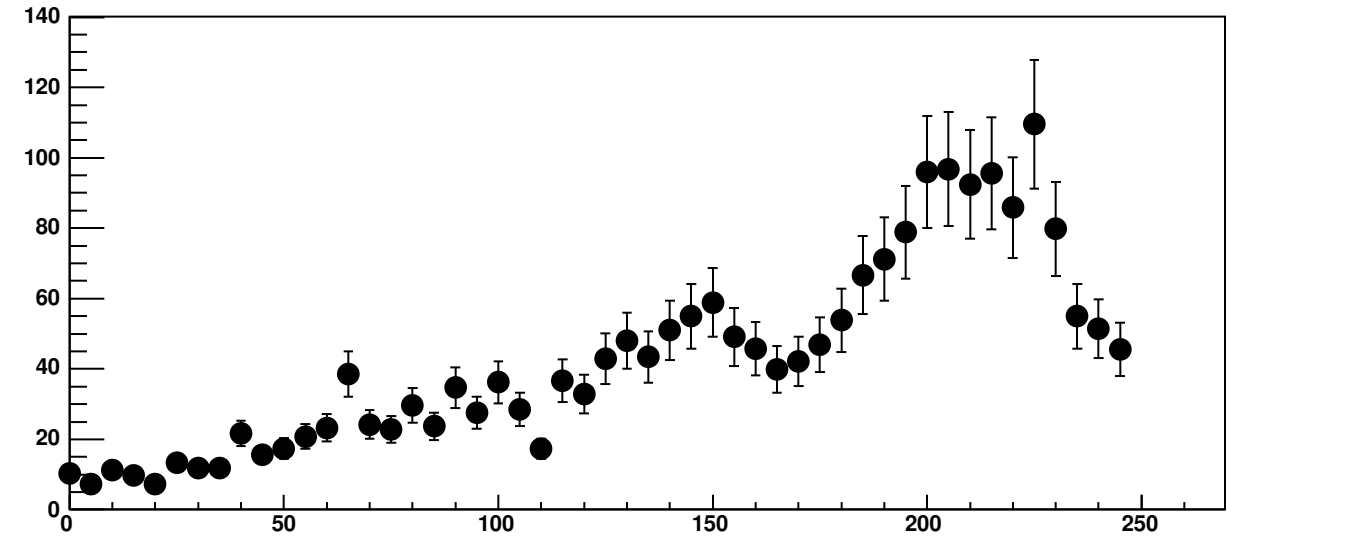

Chip 0, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
0
50
100
150
200
250

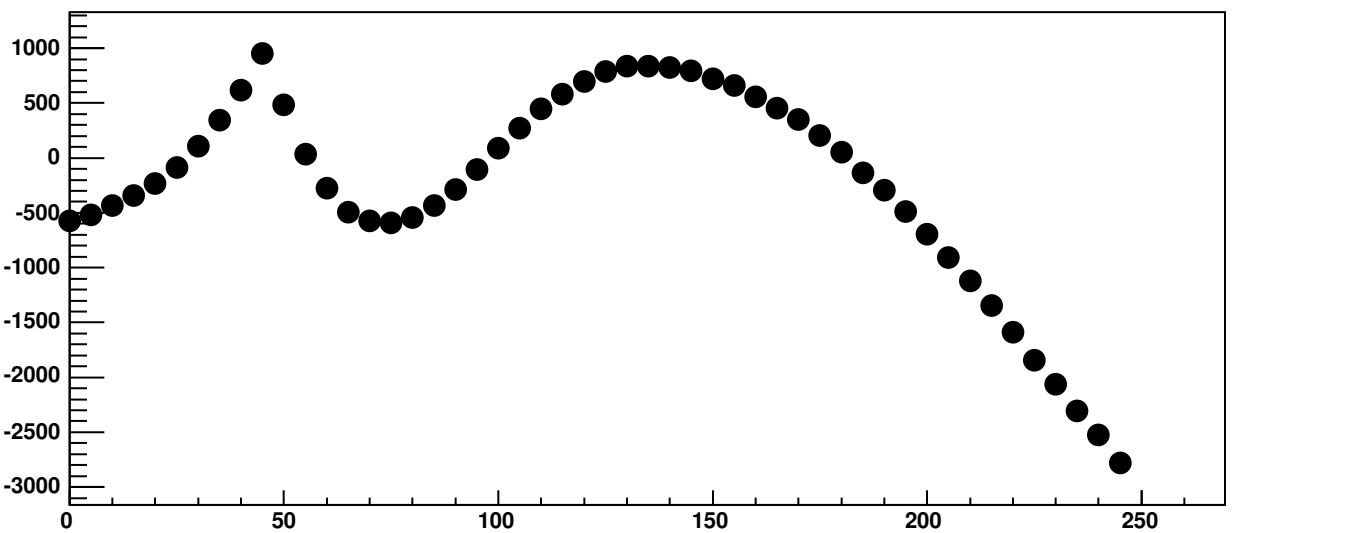
Chip 0, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold

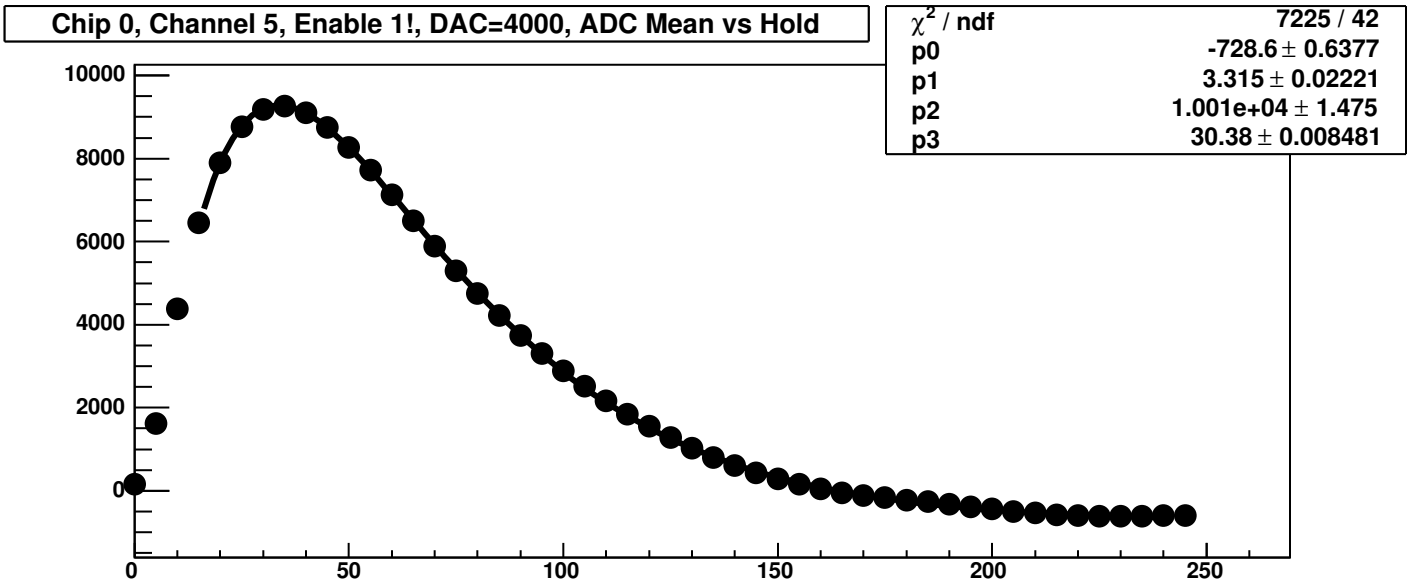


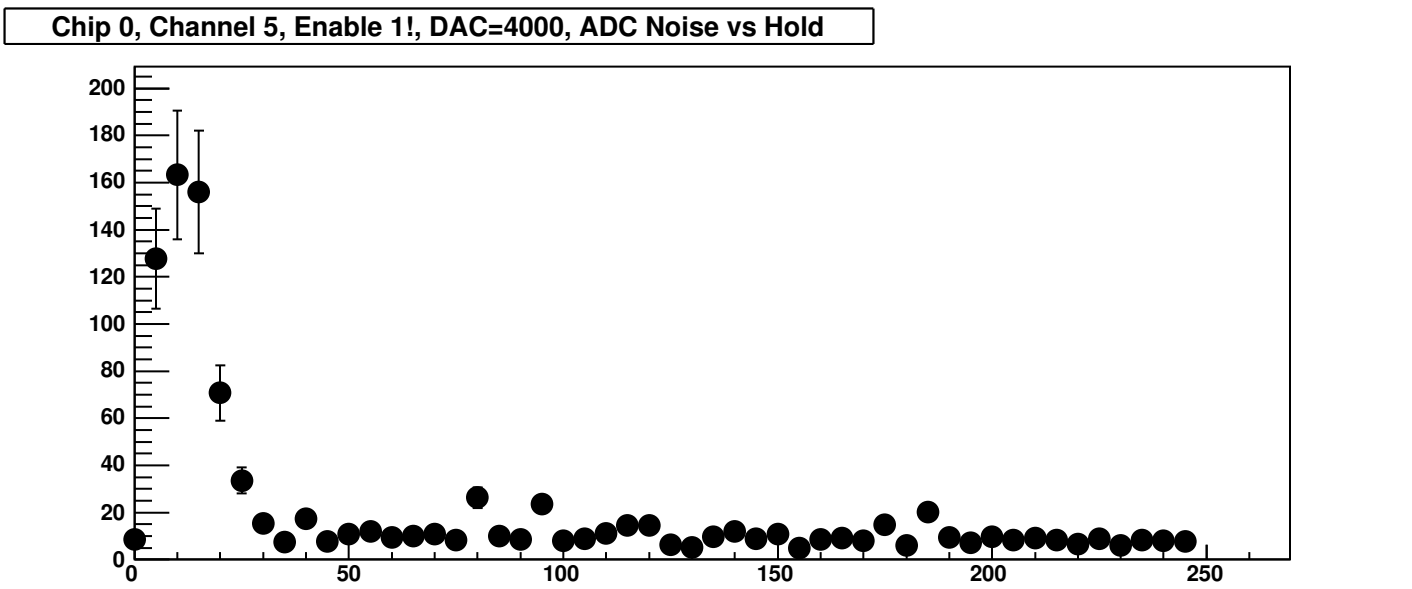
Chip 0, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold

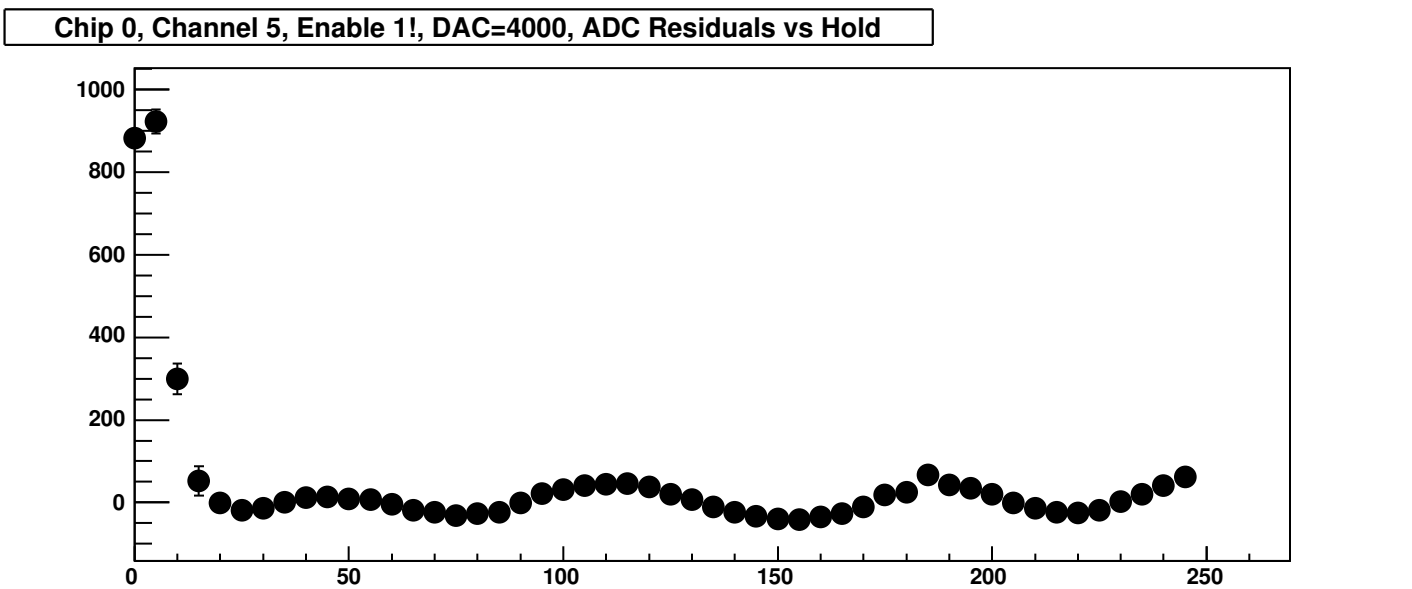


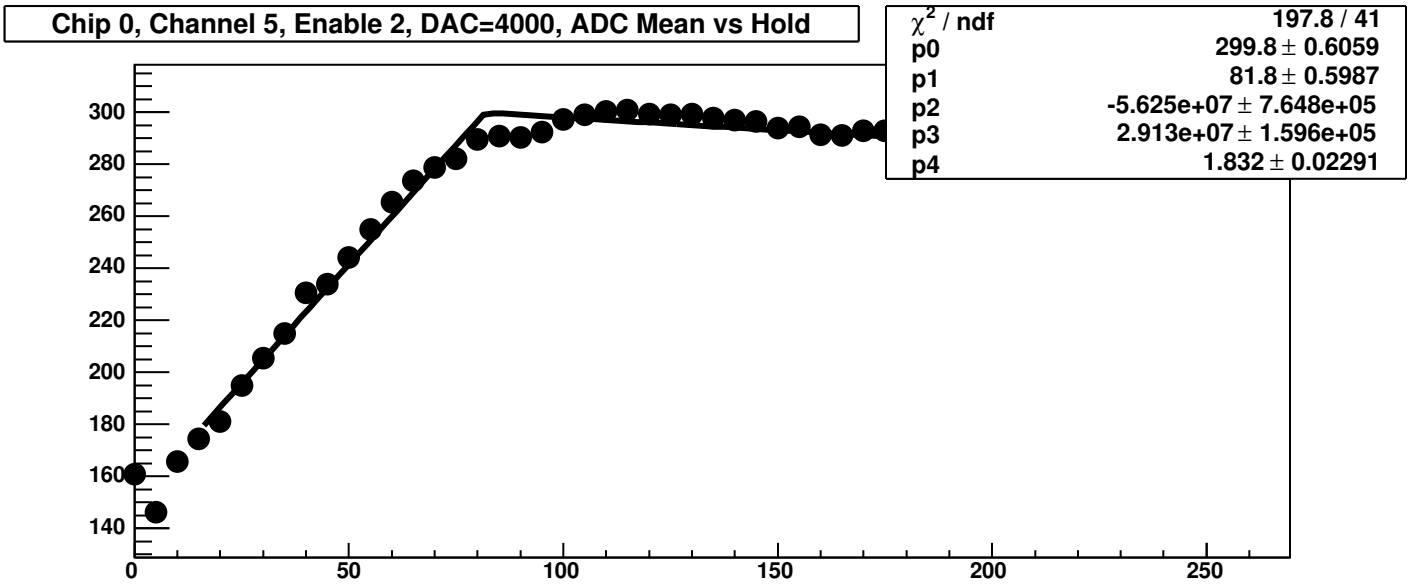
Chip 0, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold

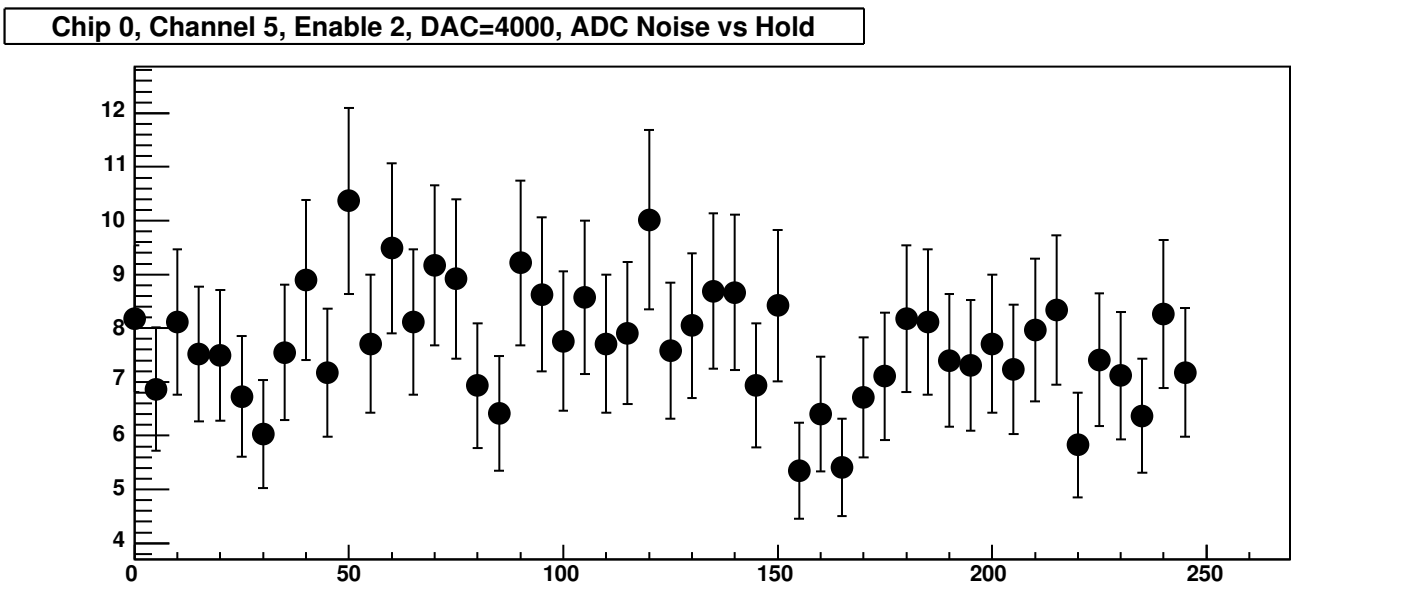


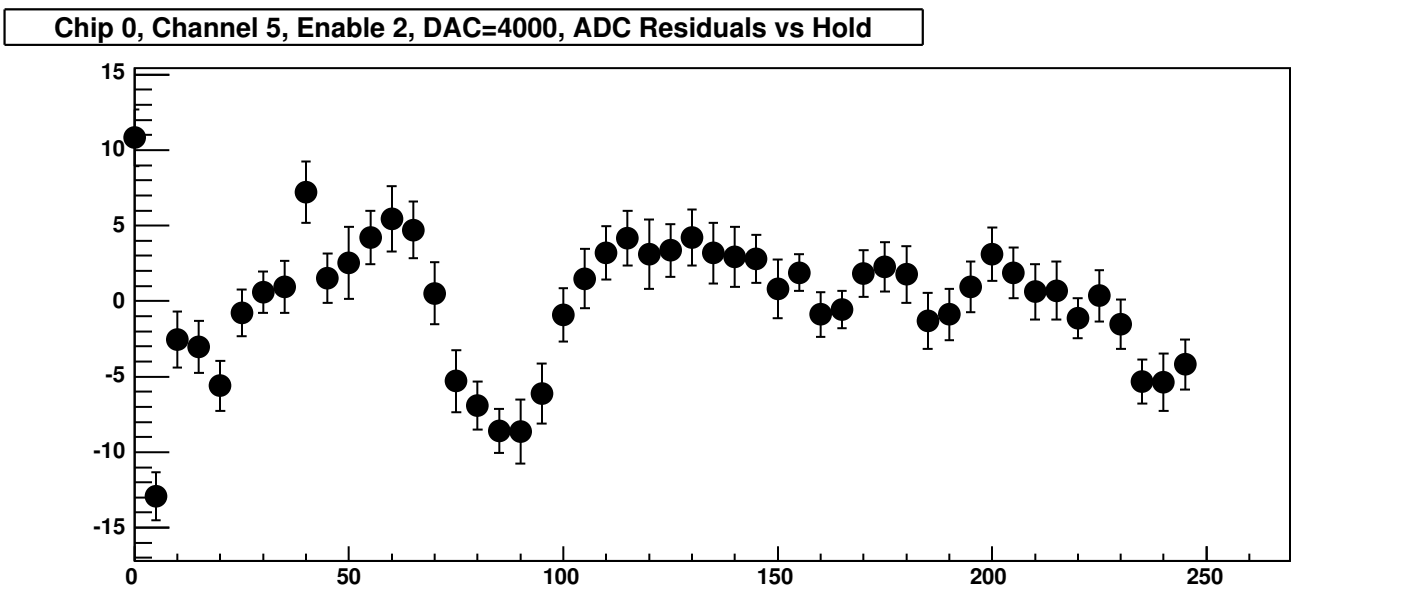

Chip 0, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 7225 / 42
p0 -728.6 ± 0.6377
p1 3.315 ± 0.02221
p2 1.001e+04 ± 1.475
p3 30.38 ± 0.008481
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 0, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

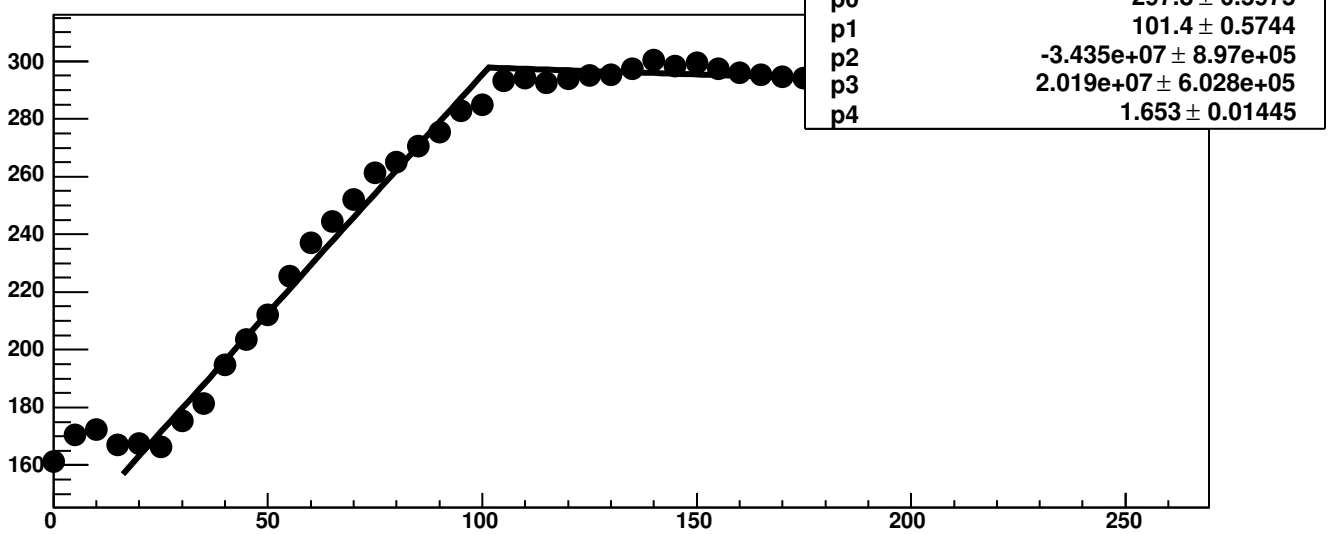

Chip 0, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 0, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 197.8 / 41
p0 299.8 ± 0.6059
p1 81.8 ± 0.5987
p2 -5.625e+07 ± 7.648e+05
p3 2.913e+07 ± 1.596e+05
p4 1.832 ± 0.02291
300
280
260
240
220
200
180
160
140
0
50
100
150
200
250

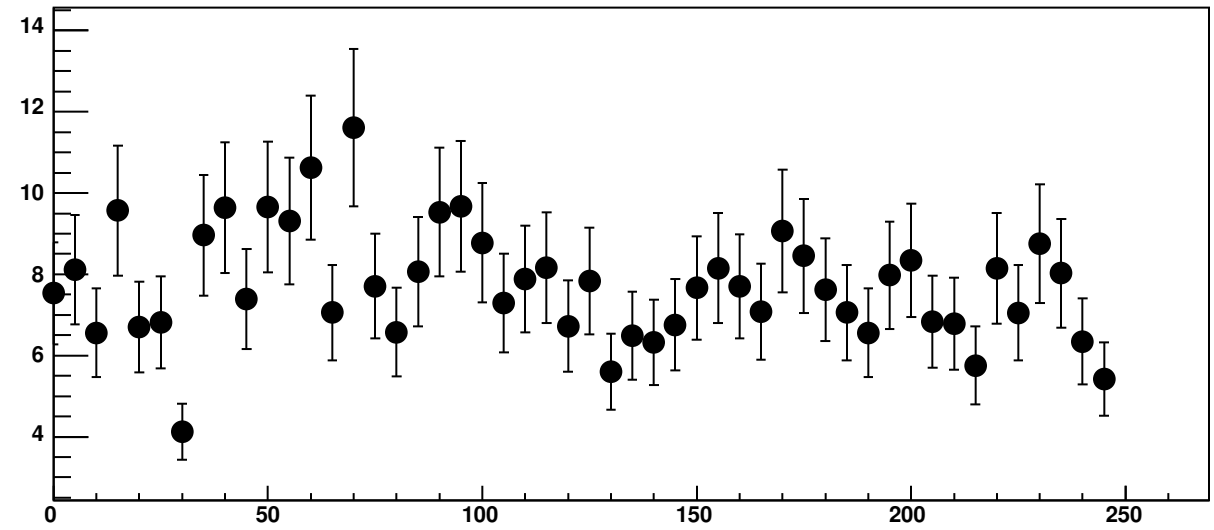

Chip 0, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 0, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

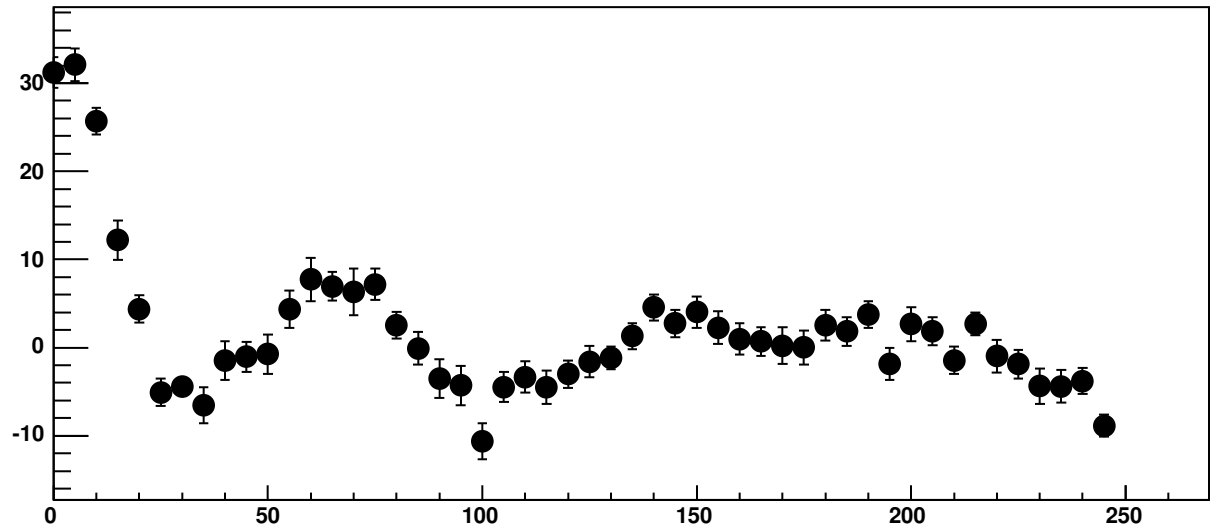
Chip 0, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold



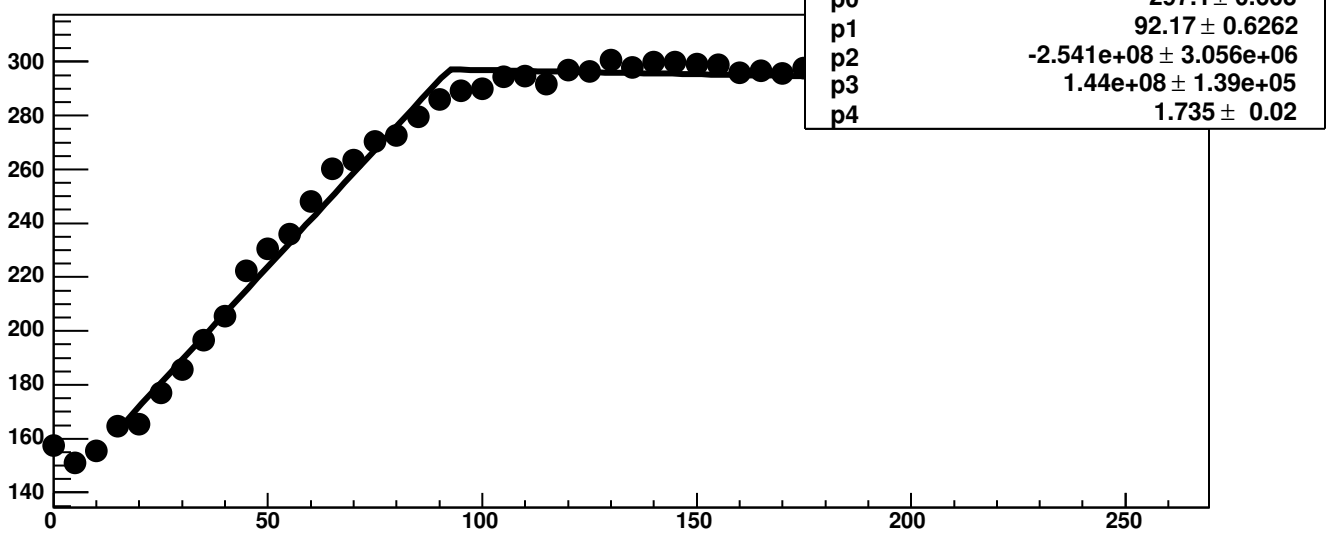
Chip 0, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



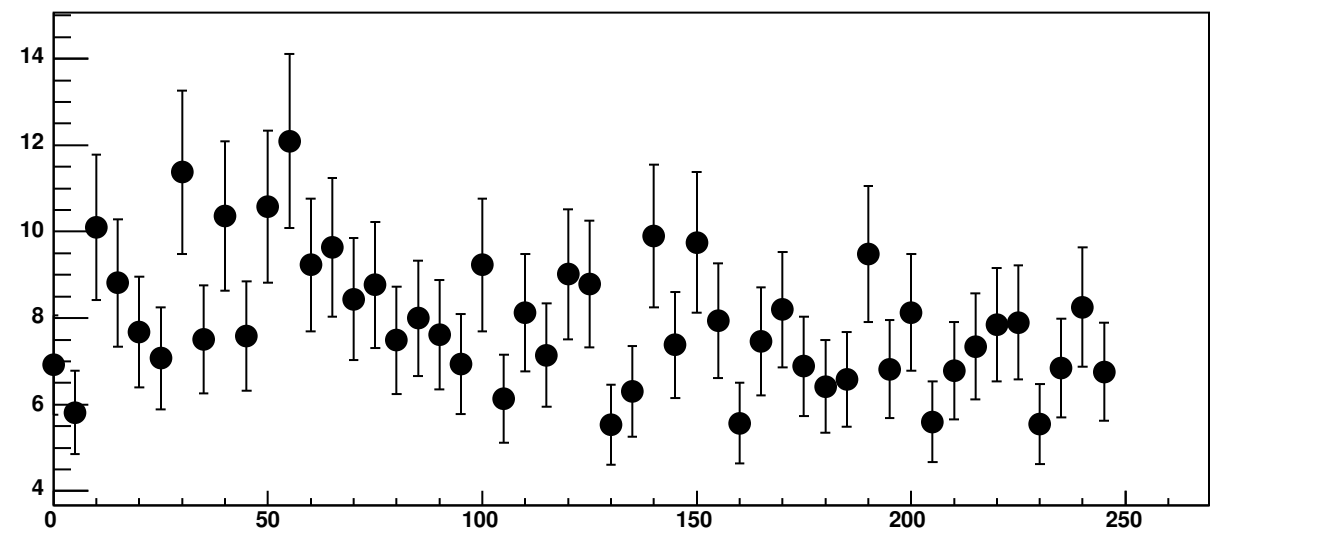
Chip 0, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold



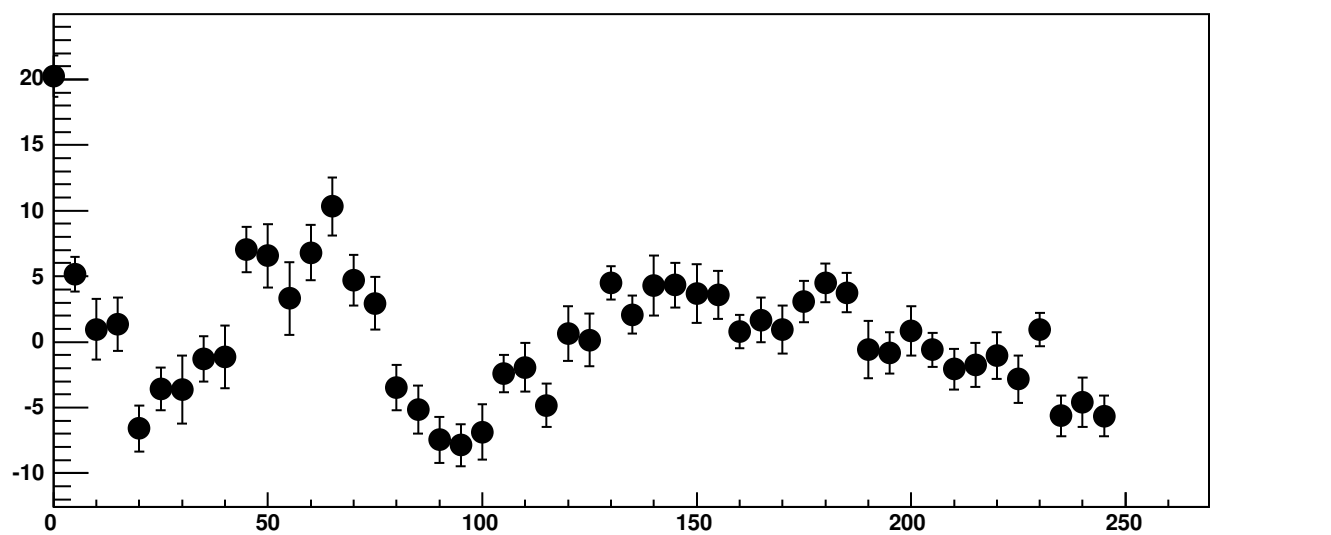
Chip 0, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold

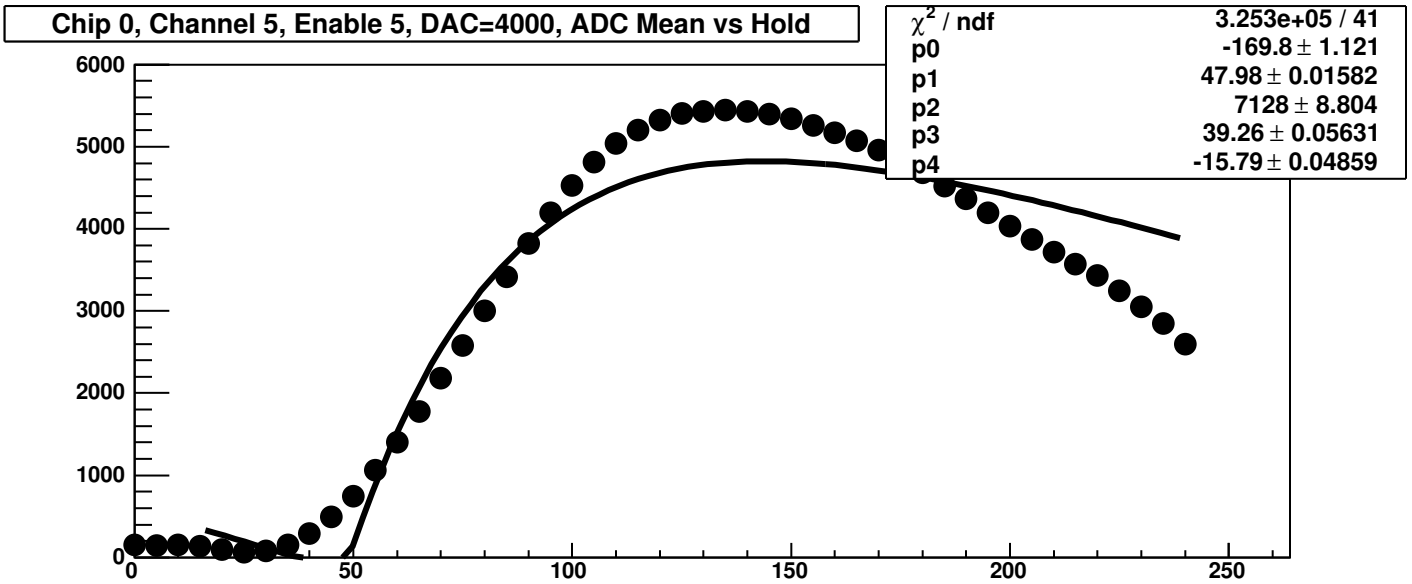


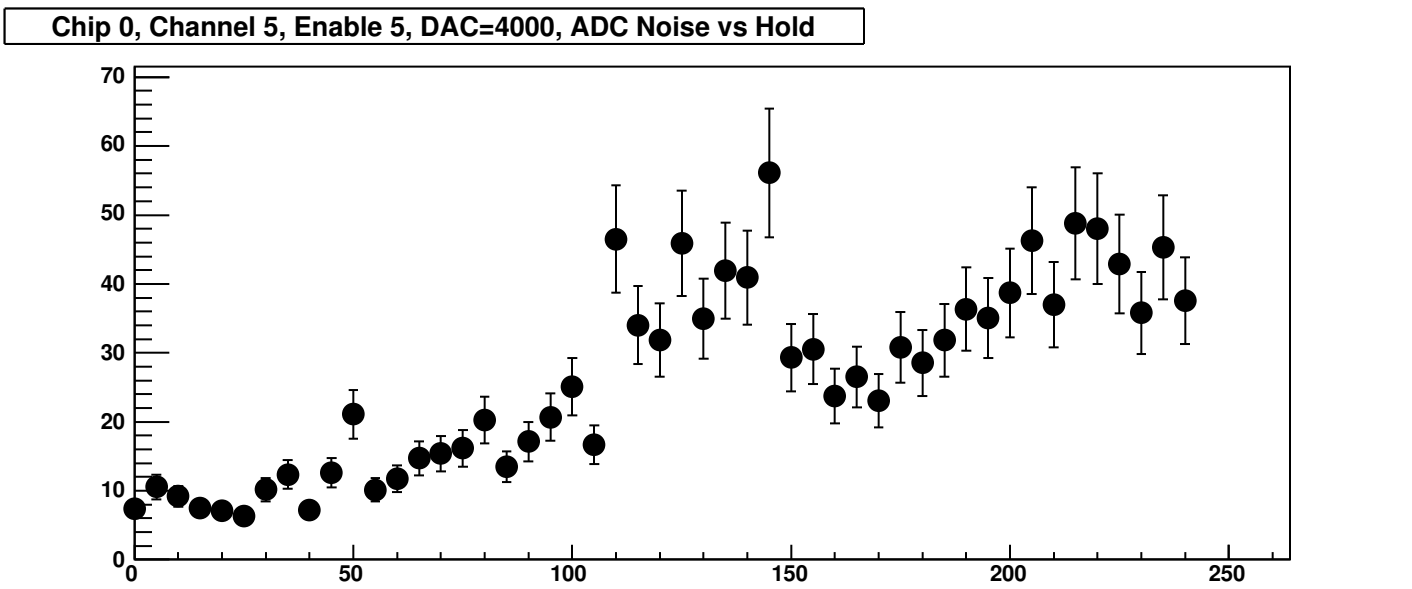
Chip 0, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold

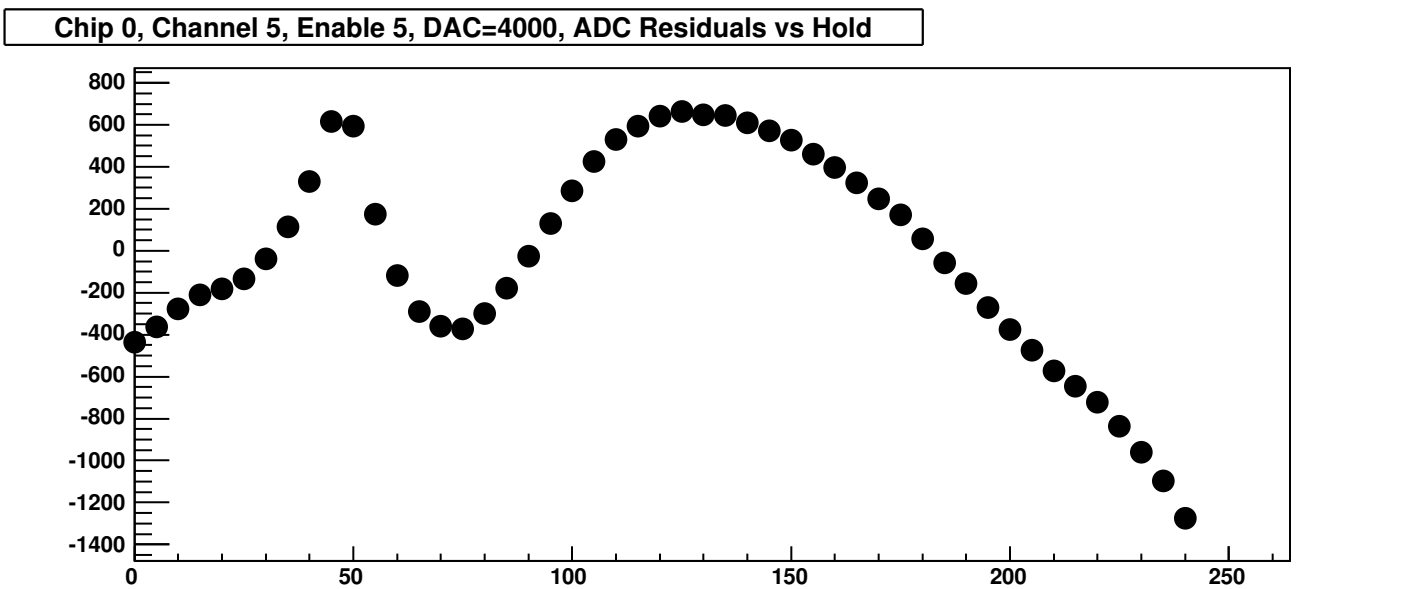


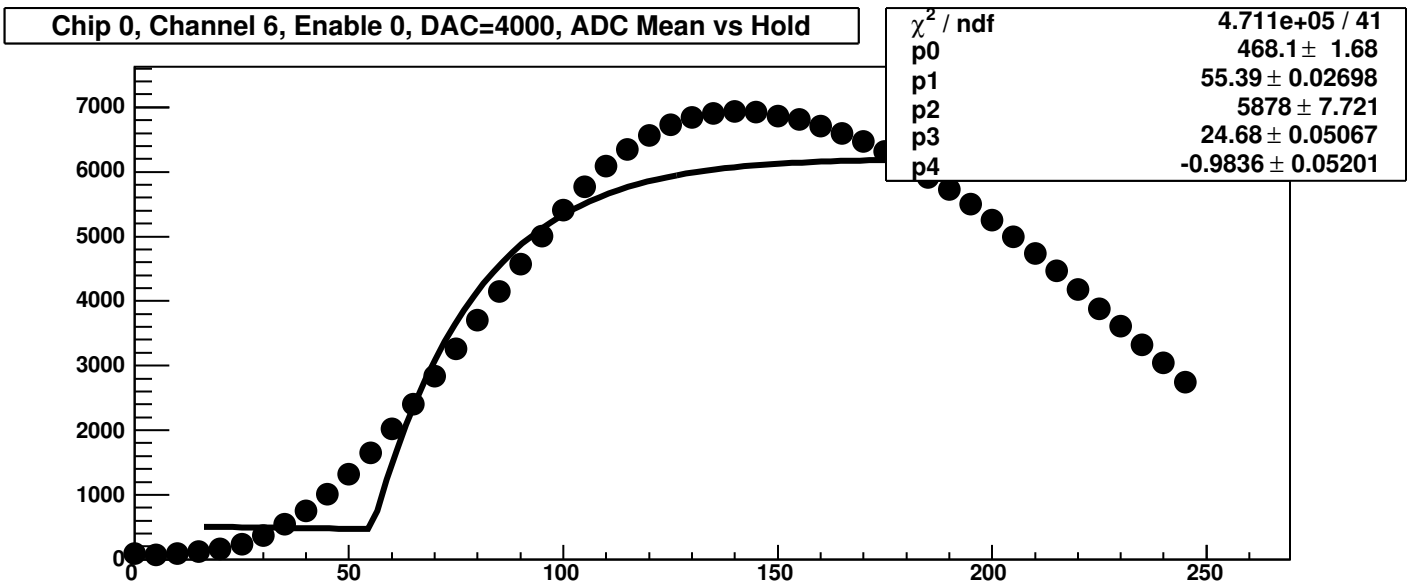
Chip 0, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold

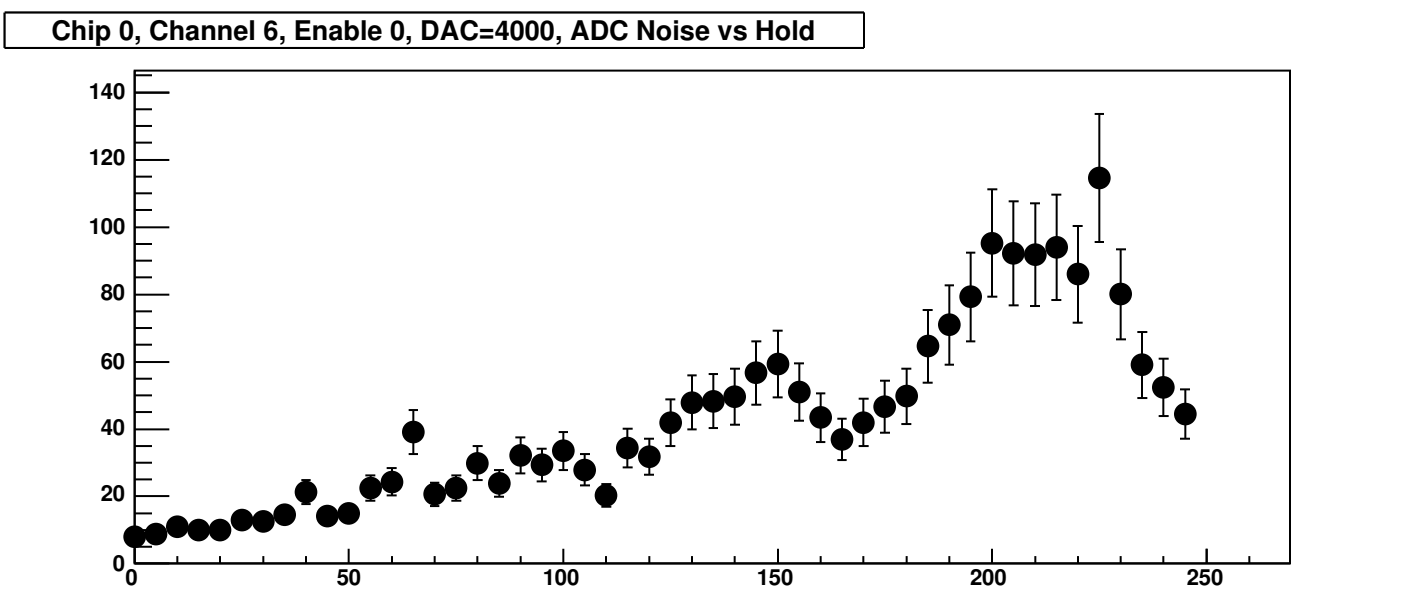


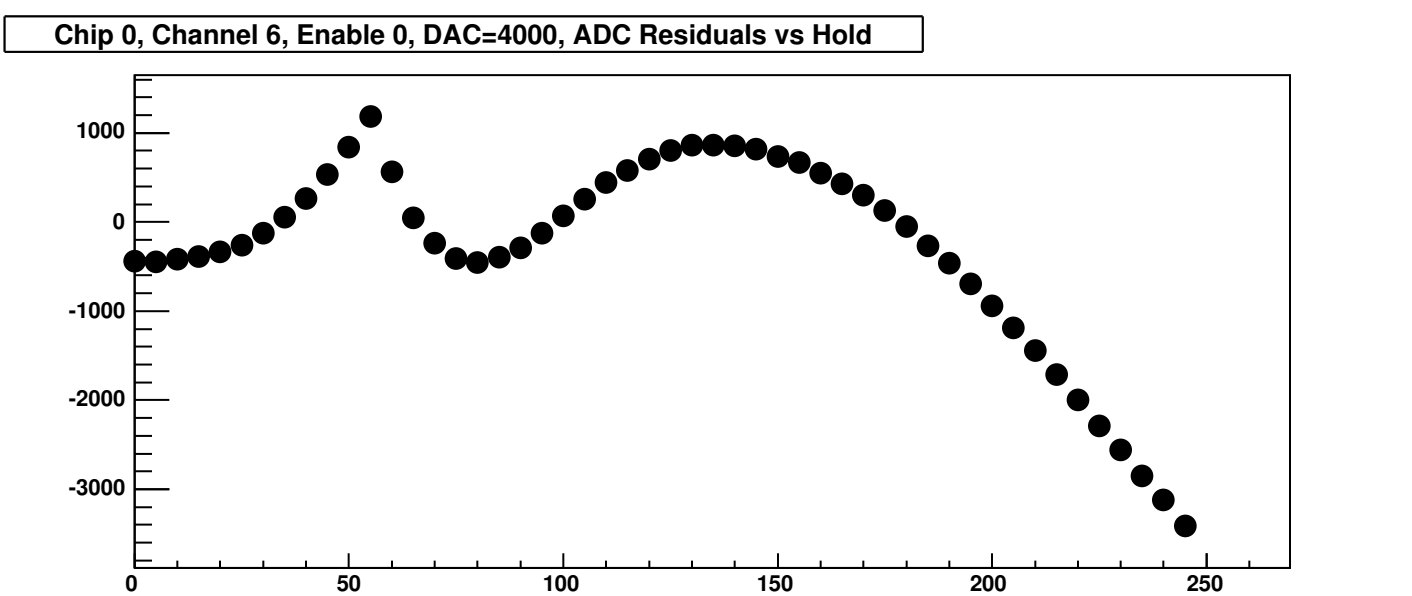

Chip 0, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.253e+05 / 41
p0 -169.8 ± 1.121
p1 47.98 ± 0.01582
p2 7128 ± 8.804
p3 39.26 ± 0.05631
p4 -15.79 ± 0.04859
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 0, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold
70
60
50
40
30
20
10
0
0
50
100
150
200
250

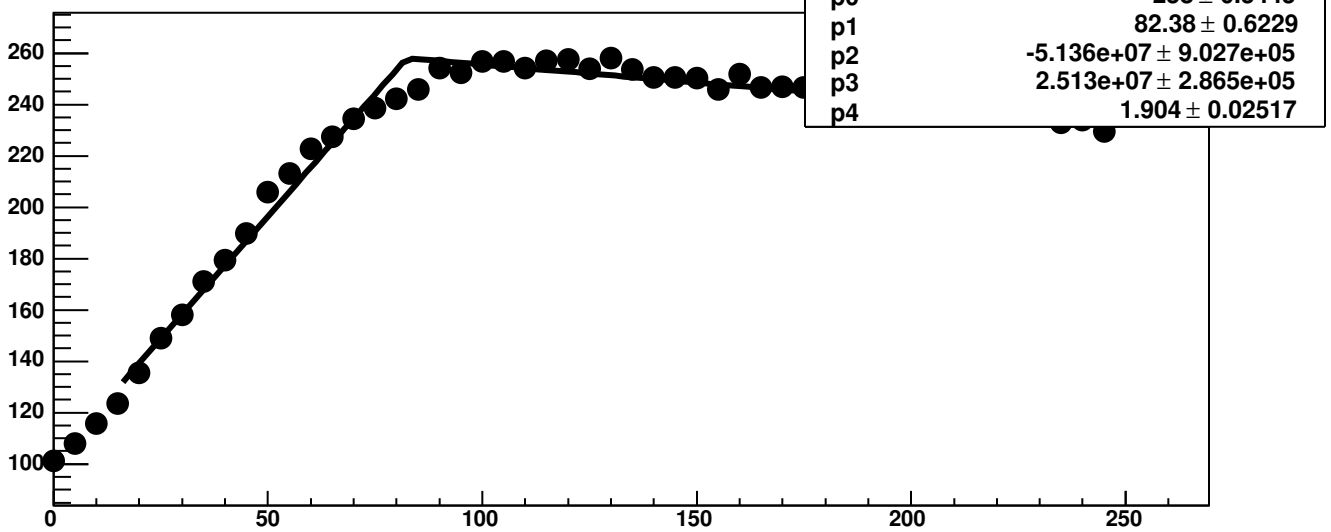

Chip 0, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold
800
600
400
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250


Chip 0, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.711e+05 / 41
p0 468.1 ± 1.68
p1 55.39 ± 0.02698
p2 5878 ± 7.721
p3 24.68 ± 0.05067
p4 -0.9836 ± 0.05201
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

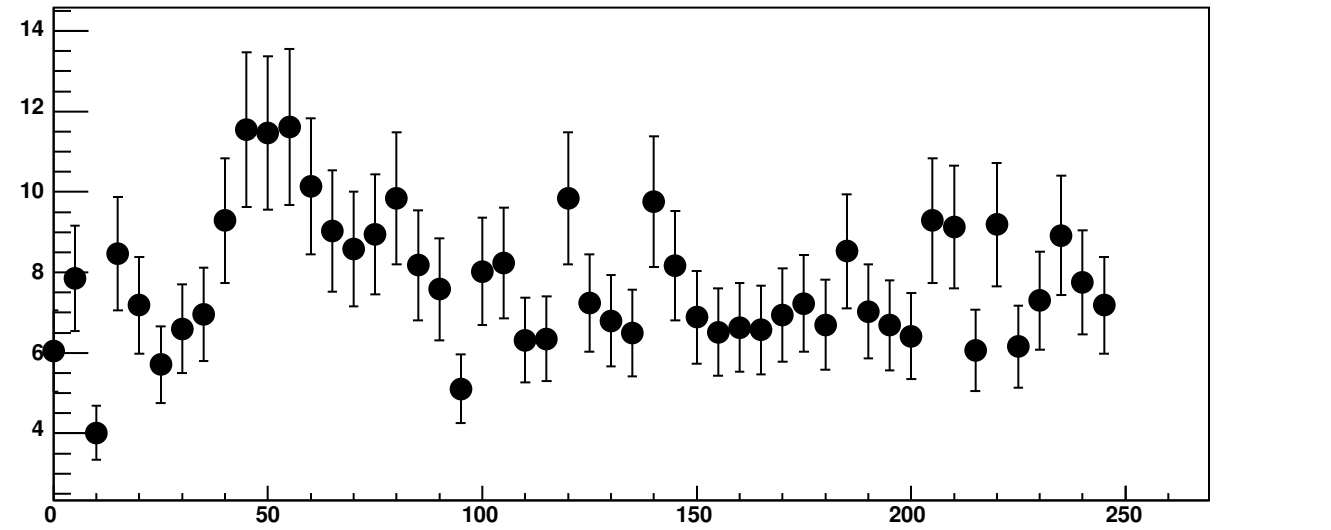

Chip 0, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

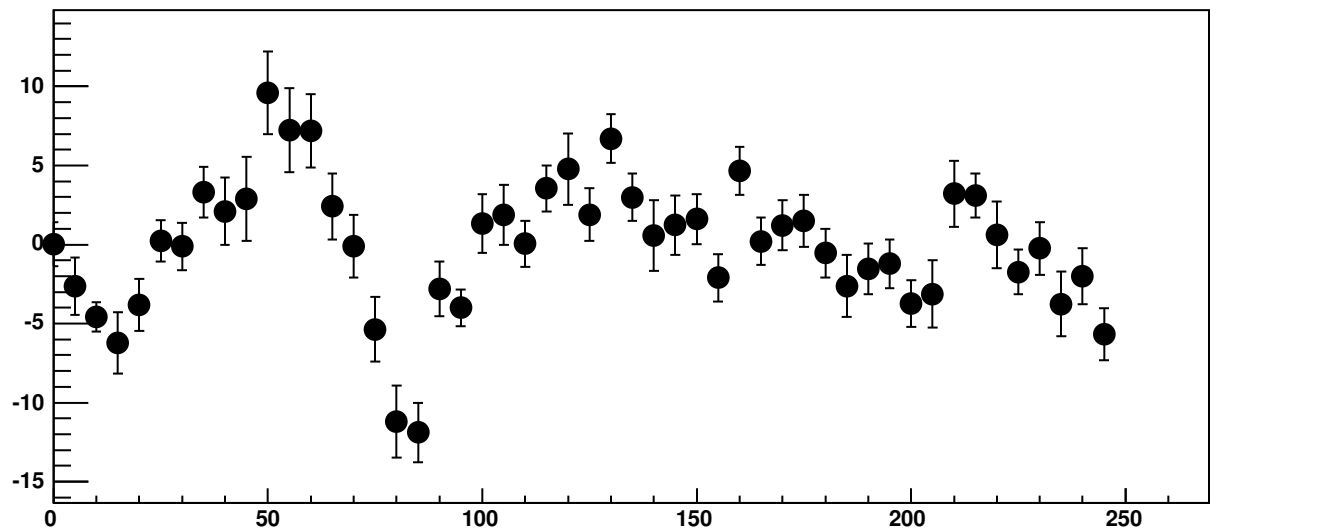
Chip 0, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold



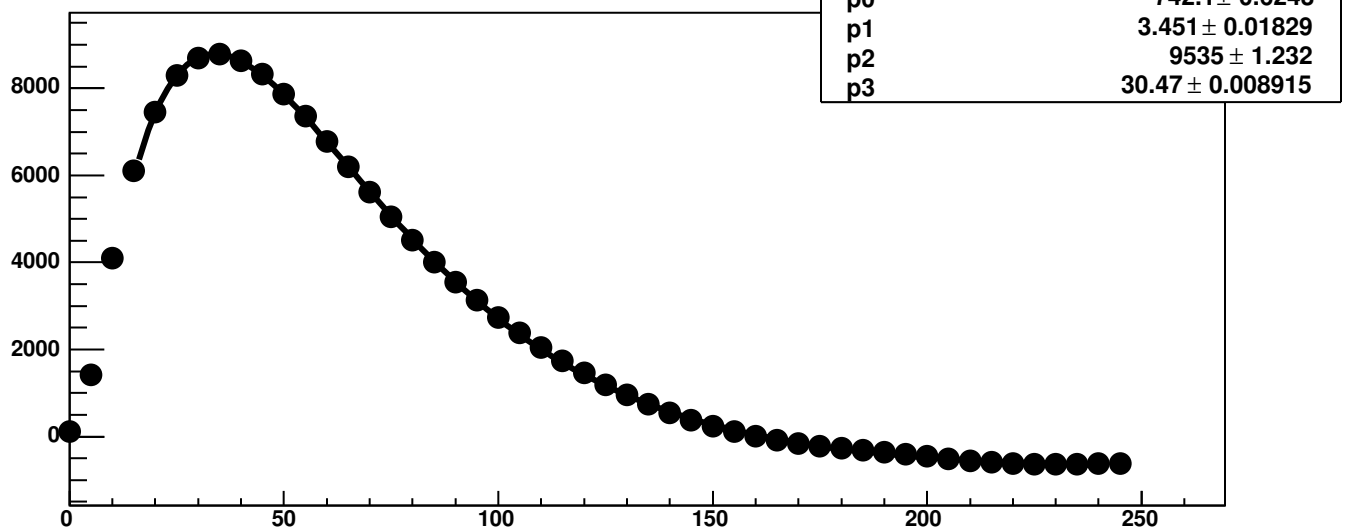
Chip 0, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



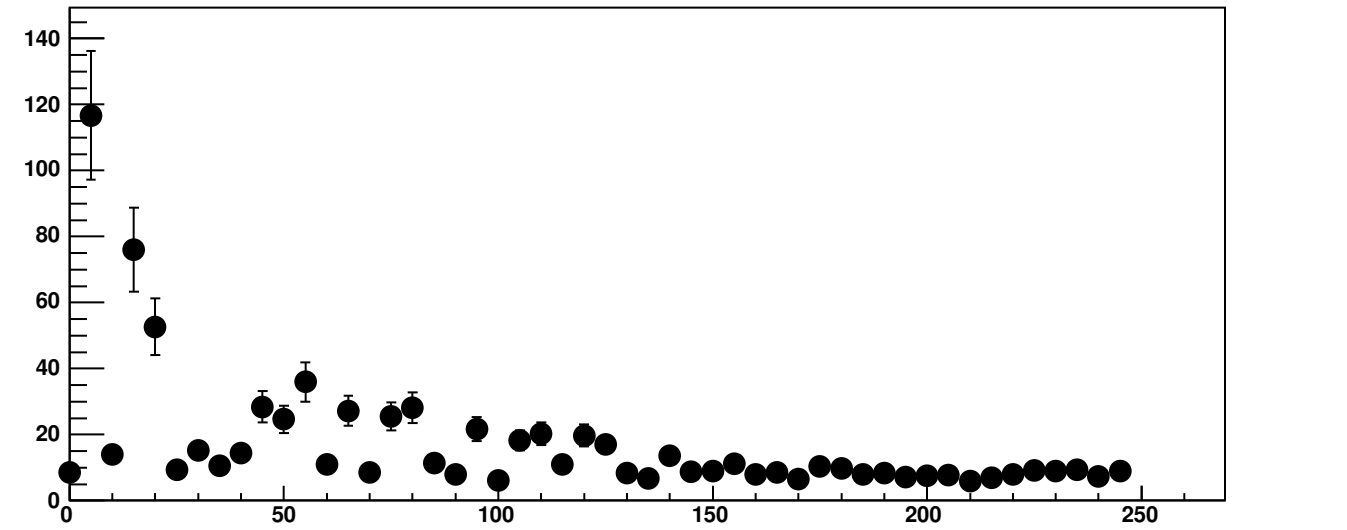
Chip 0, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold



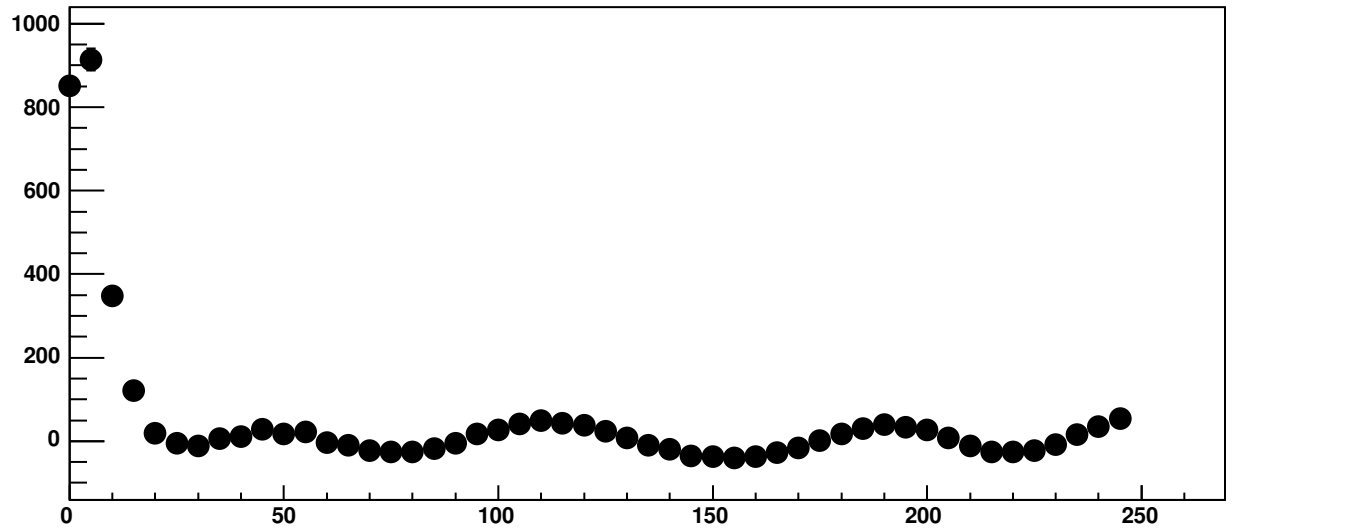
Chip 0, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold



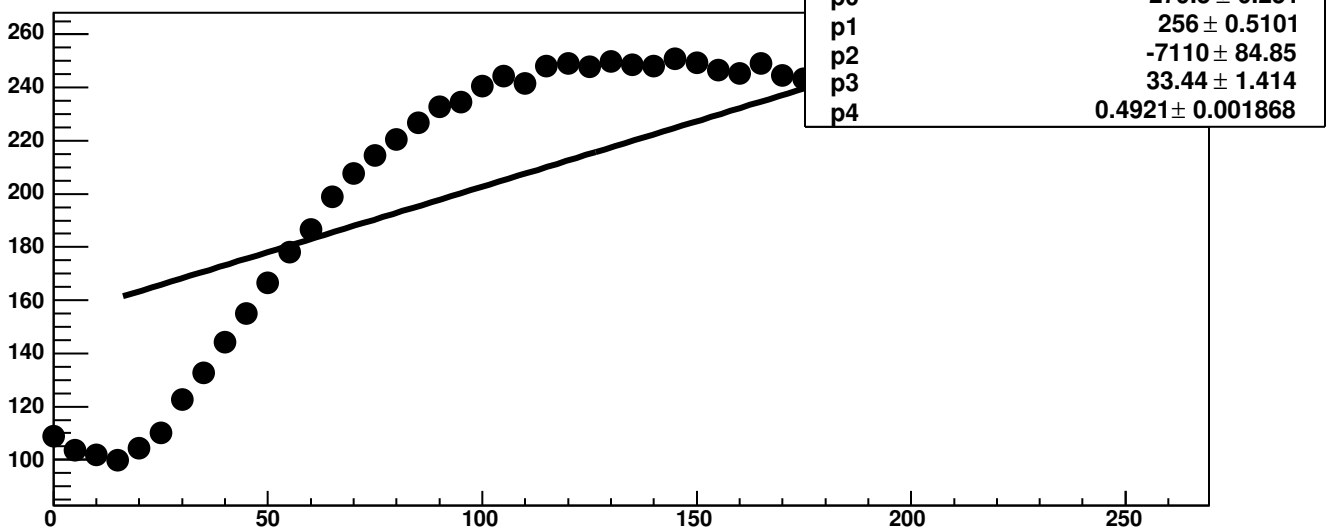
Chip 0, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold



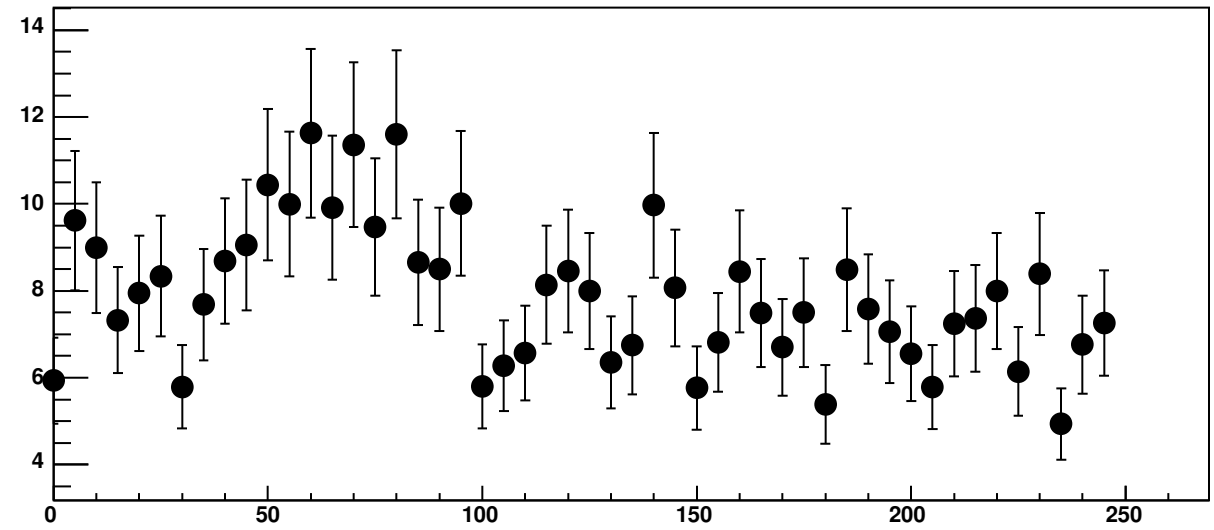
Chip 0, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold



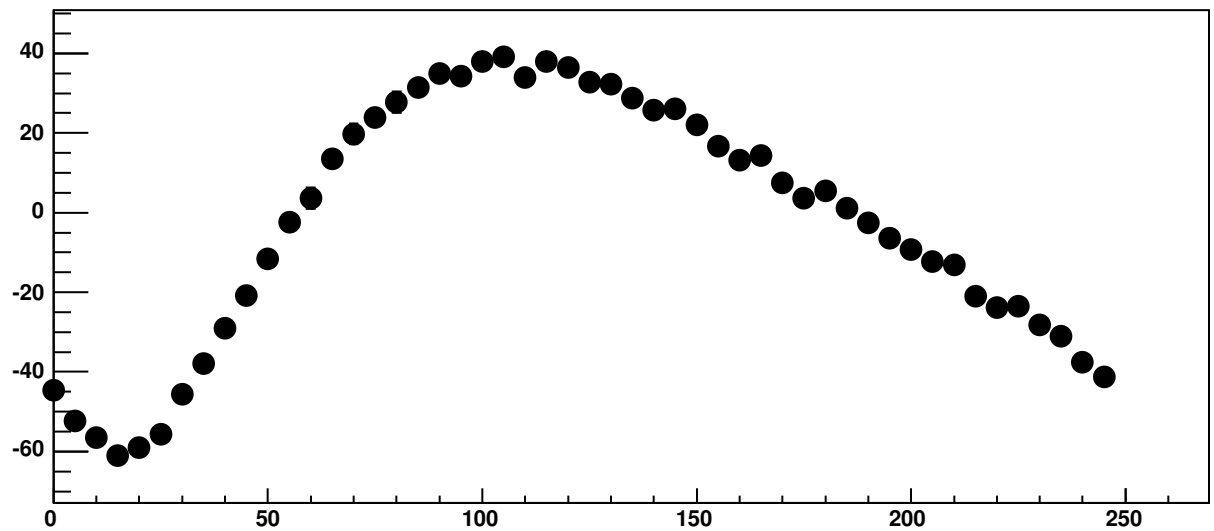
Chip 0, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

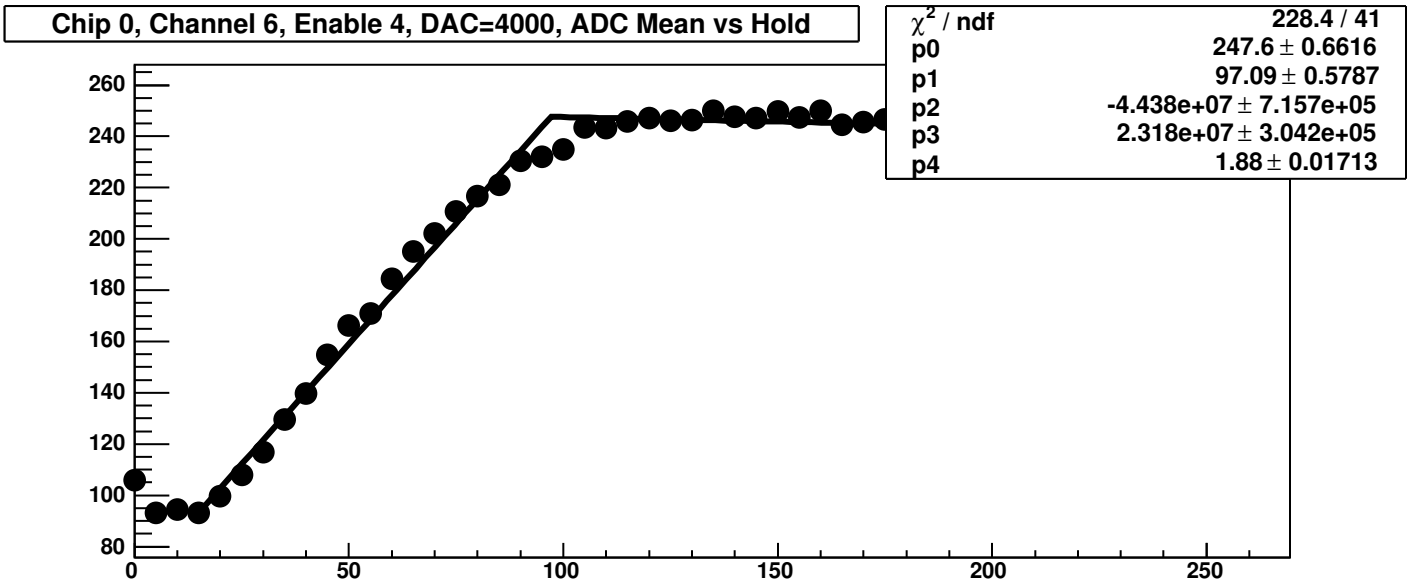


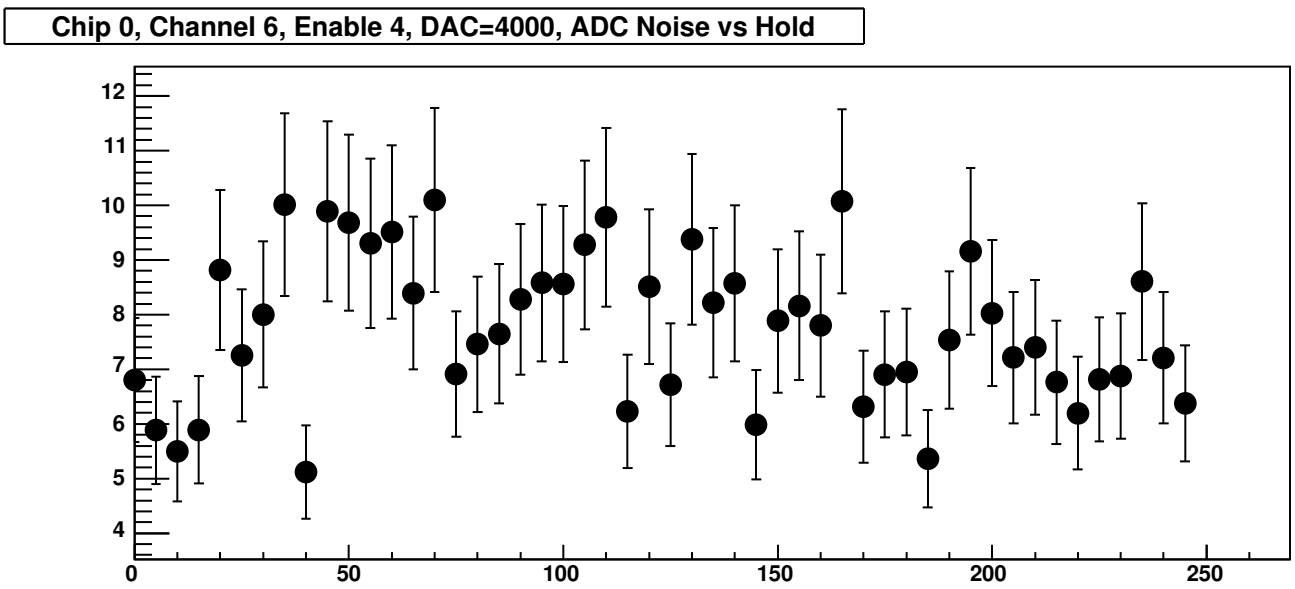
Chip 0, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold

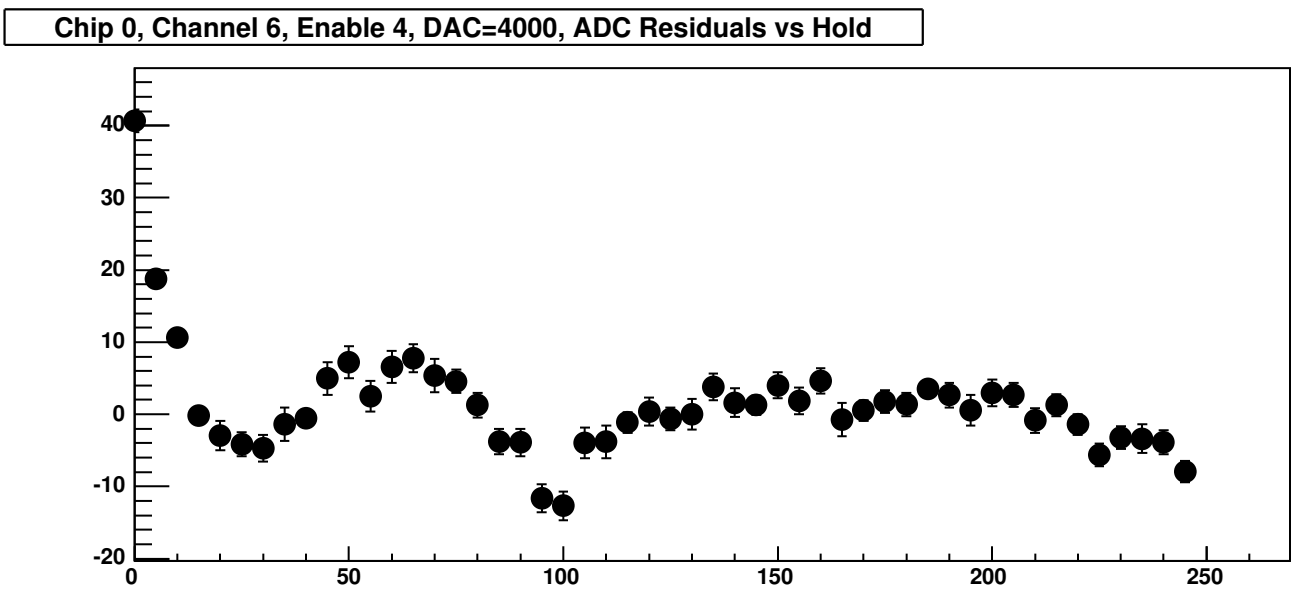


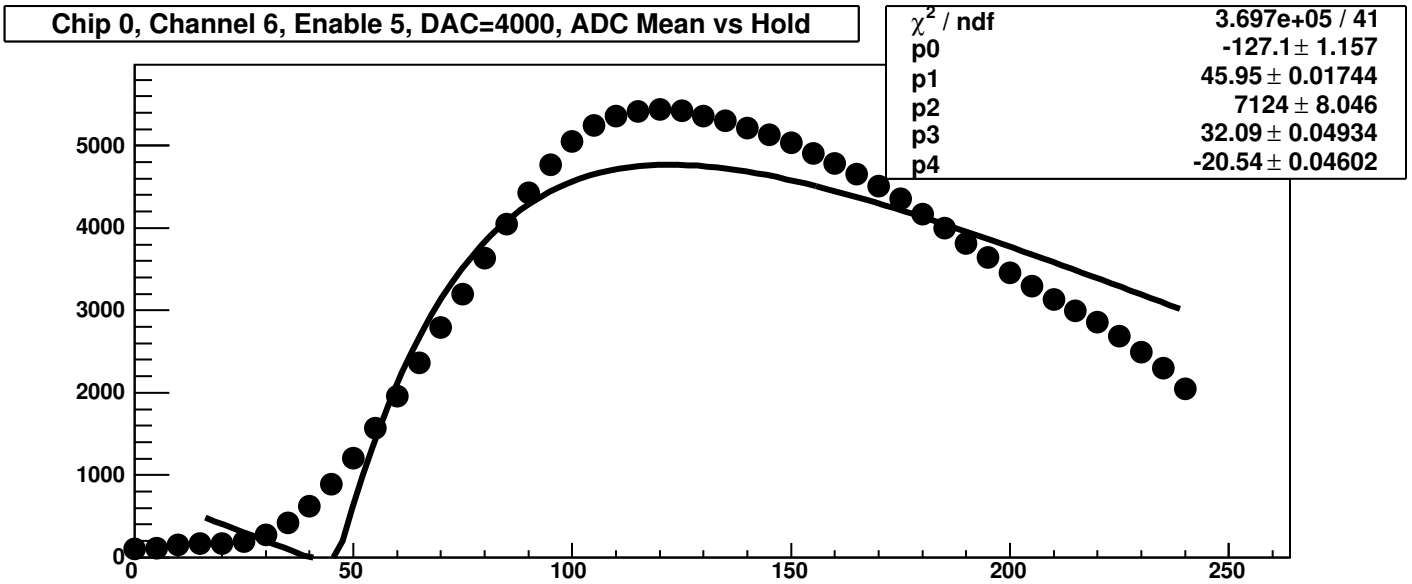
Chip 0, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

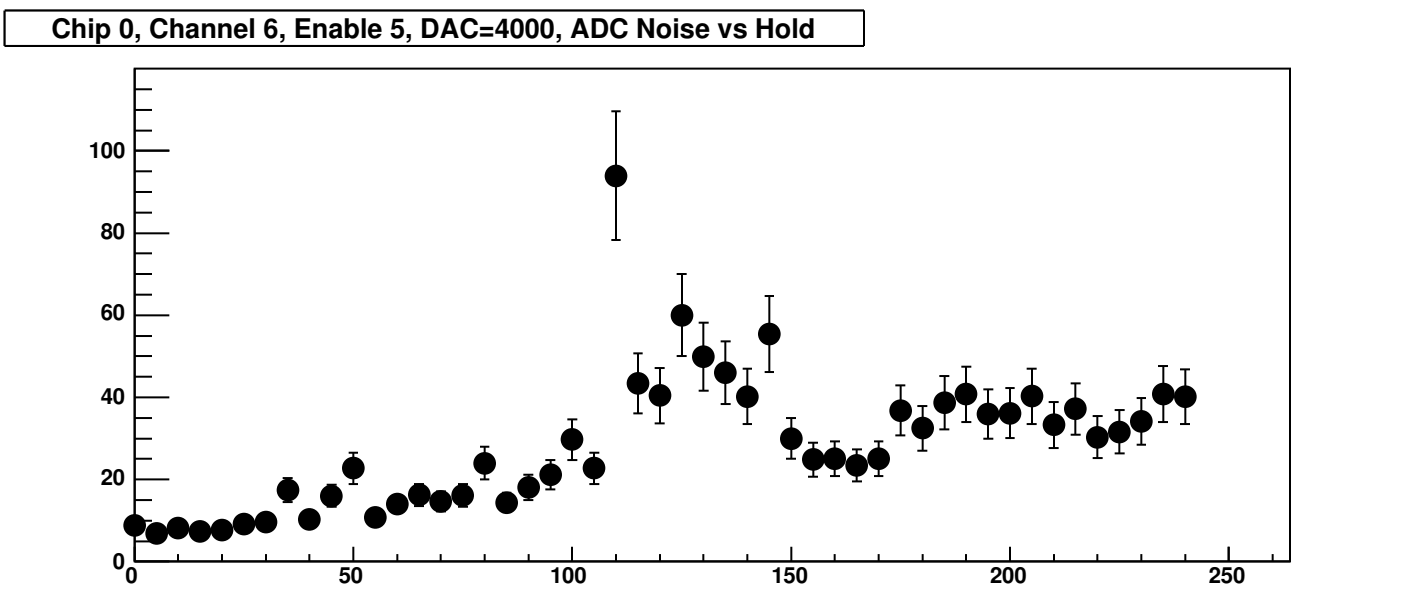


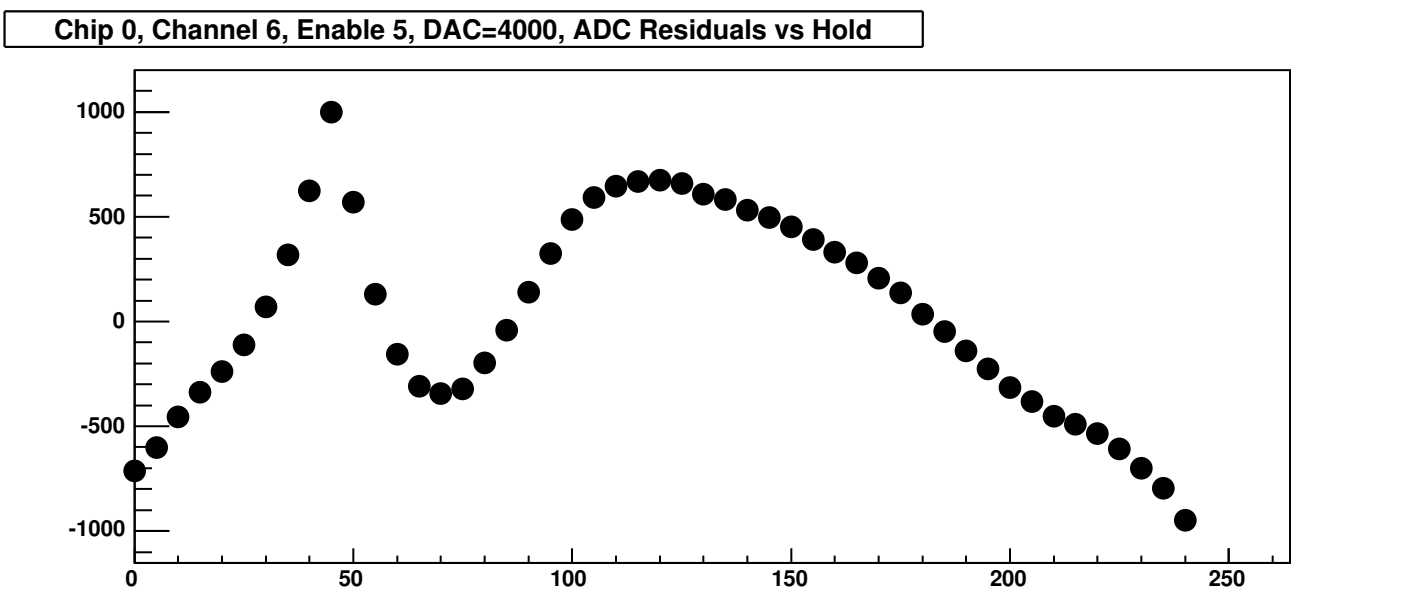

Chip 0, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 228.4 / 41
p0 247.6 ± 0.6616
p1 97.09 ± 0.5787
p2 -4.438e+07 ± 7.157e+05
p3 2.318e+07 ± 3.042e+05
p4 1.88 ± 0.01713


Chip 0, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold

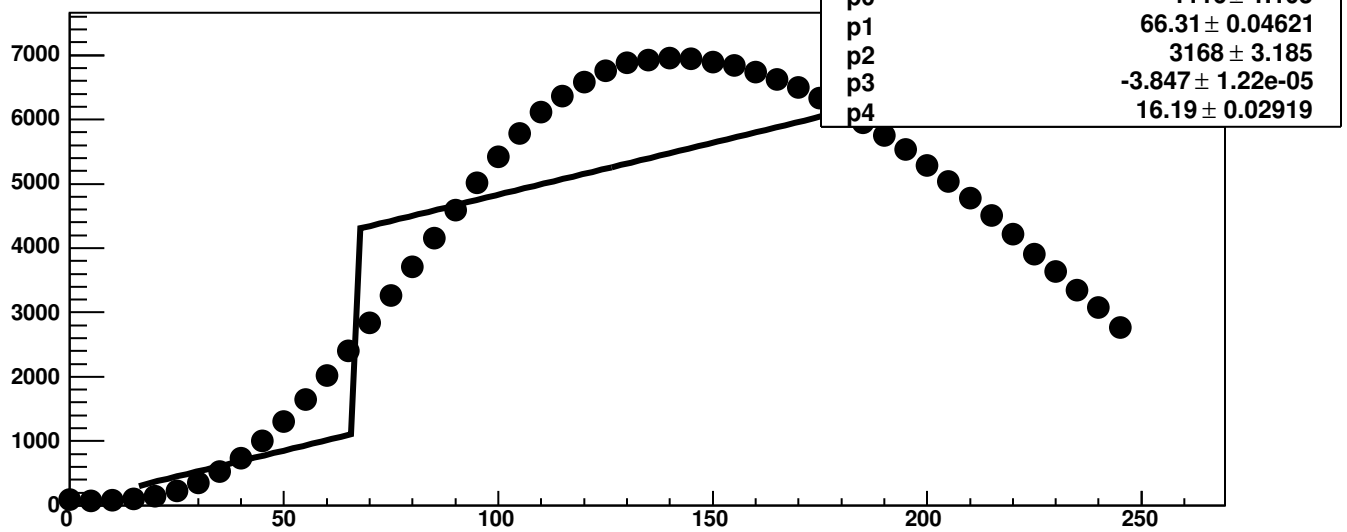

Chip 0, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold


Chip 0, Channel 6, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.697e+05 / 41
p0 -127.1 ± 1.157
p1 45.95 ± 0.01744
p2 7124 ± 8.046
p3 32.09 ± 0.04934
p4 -20.54 ± 0.04602
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

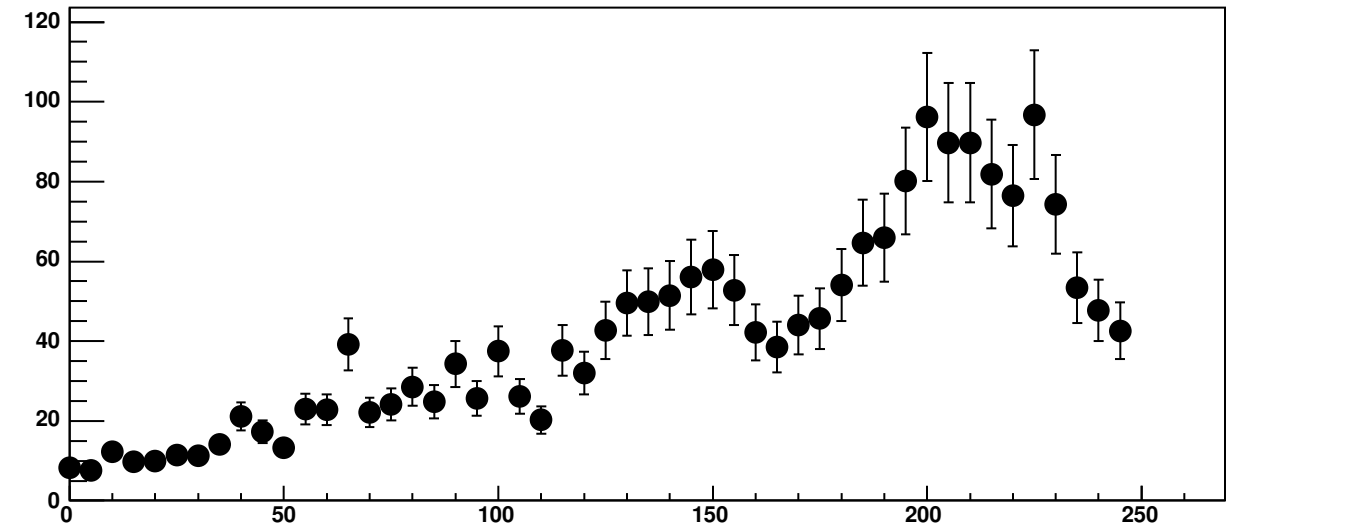

Chip 0, Channel 6, Enable 5, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 6, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
0
50
100
150
200
250

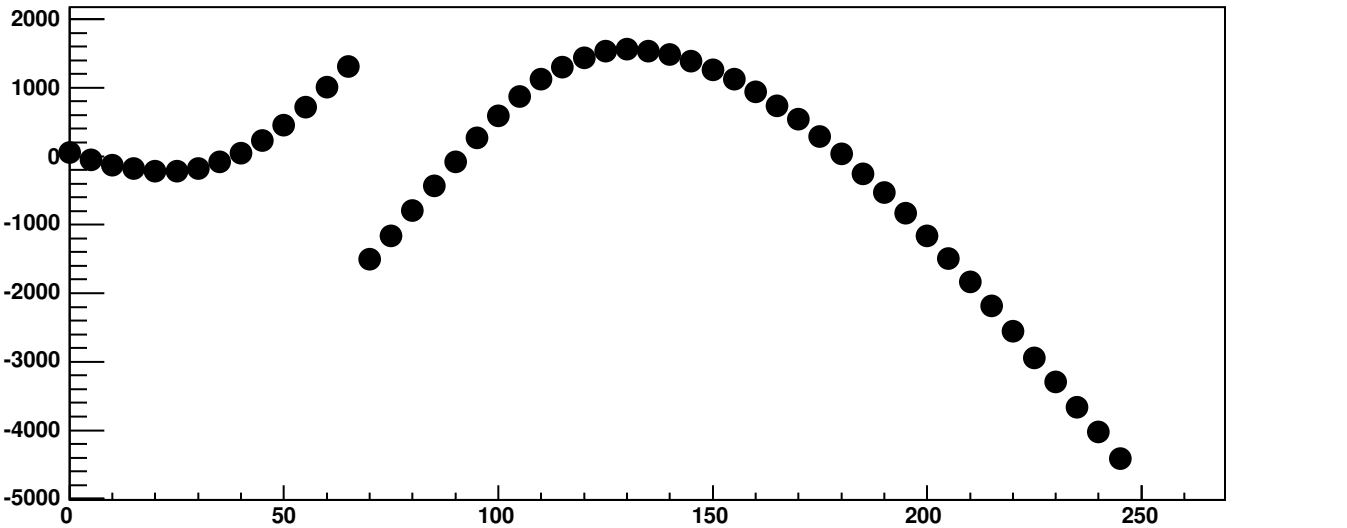
Chip 0, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

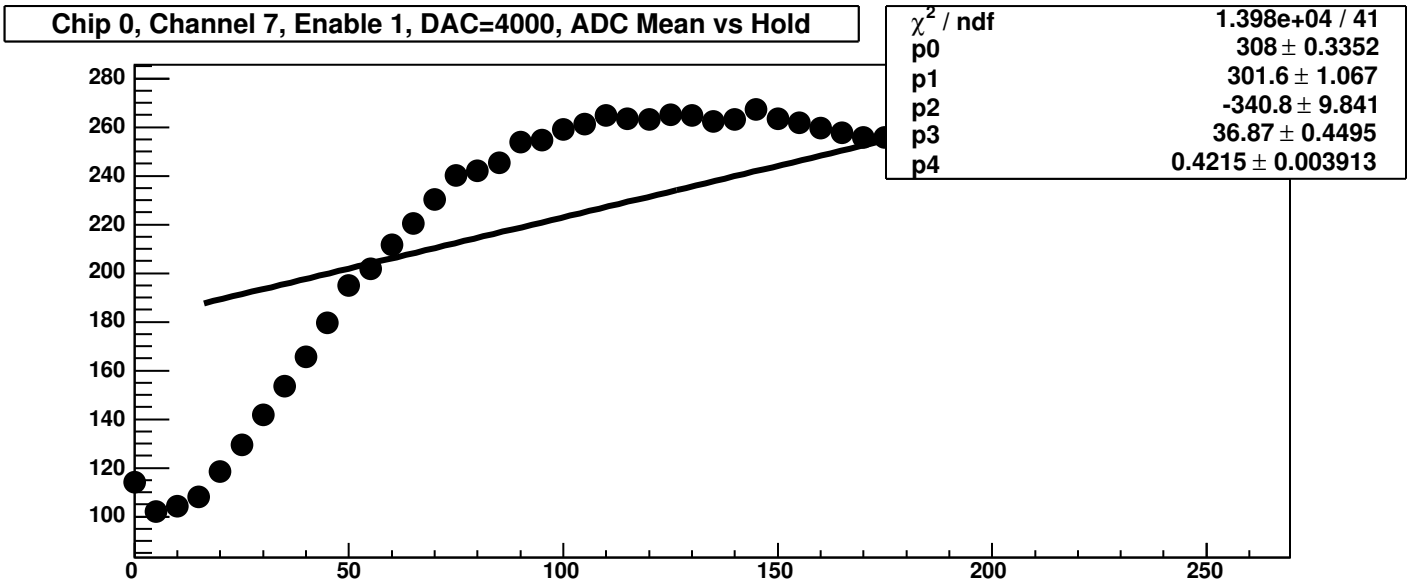


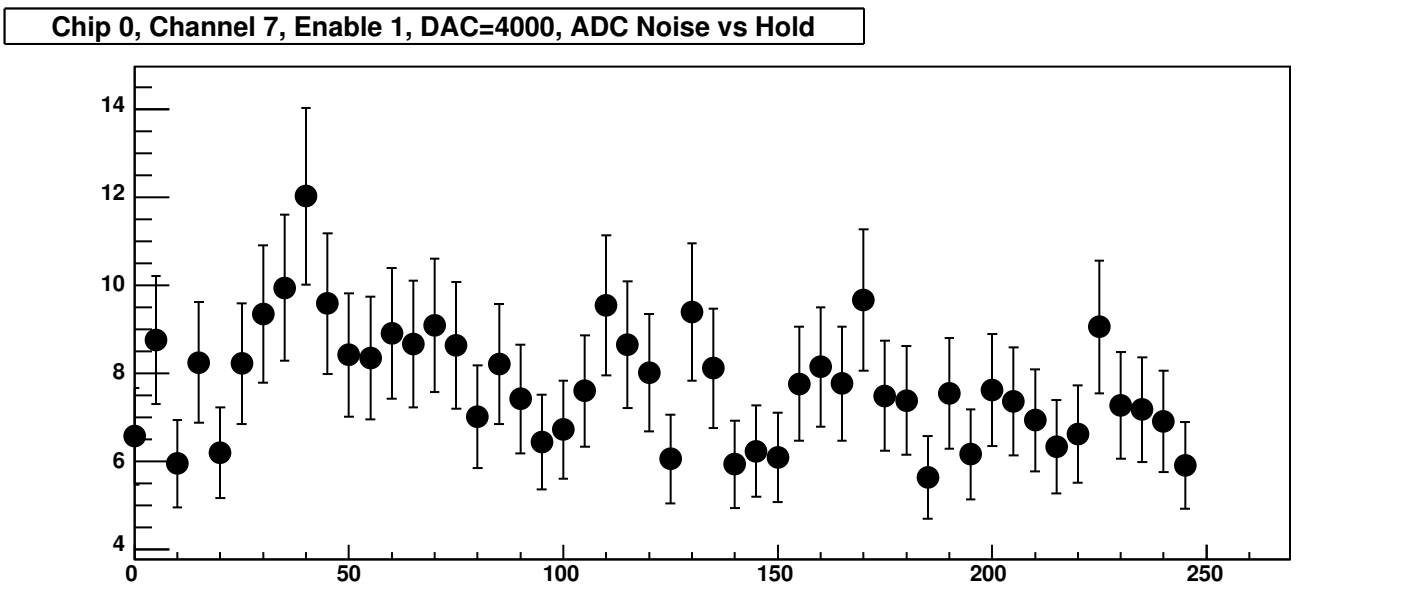
Chip 0, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold

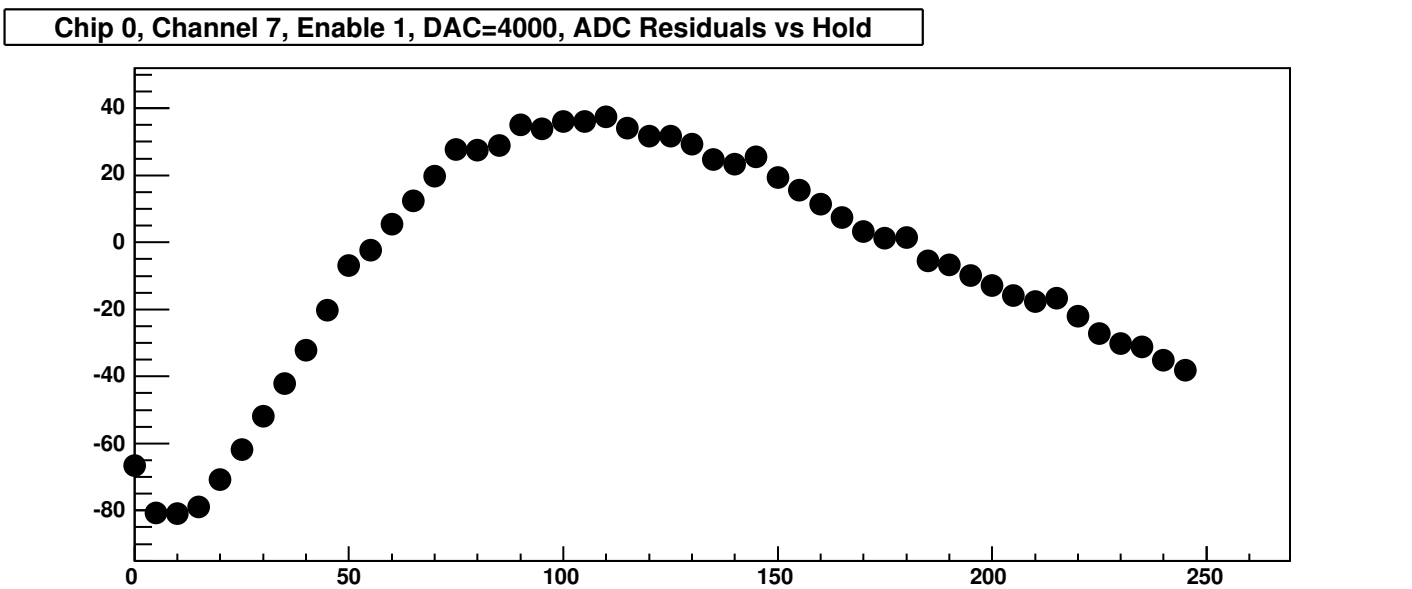


Chip 0, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

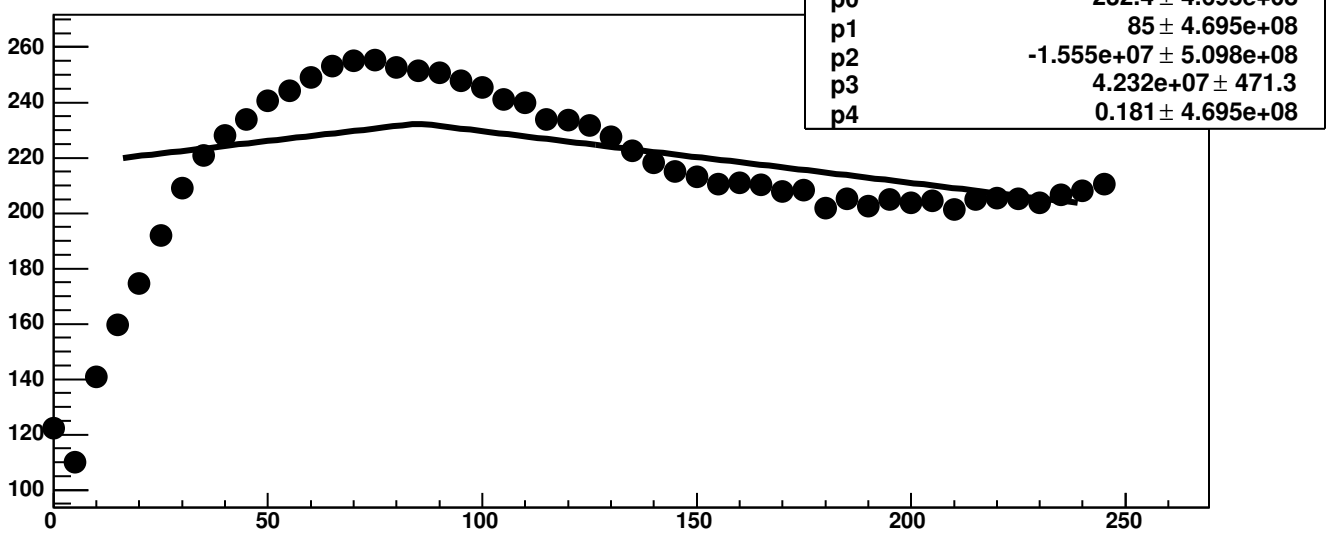



Chip 0, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.398e+04 / 41
p0 308 ± 0.3352
p1 301.6 ± 1.067
p2 -340.8 ± 9.841
p3 36.87 ± 0.4495
p4 0.4215 ± 0.003913
280
260
240
220
200
180
160
140
120
100
0
50
100
150
200
250

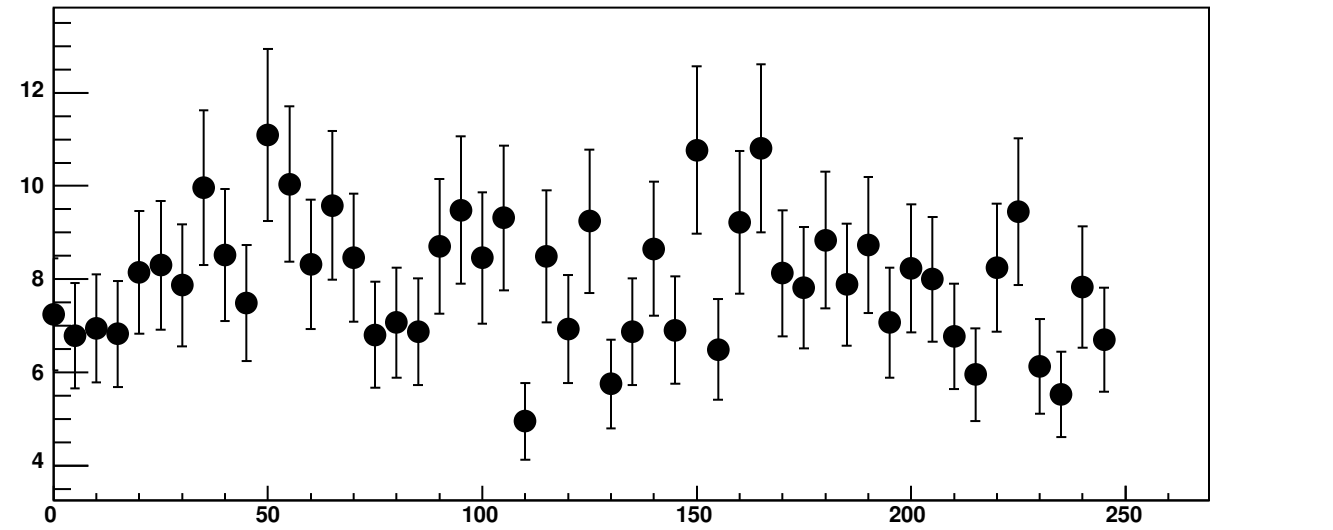

Chip 0, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 0, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
0
50
100
150
200
250

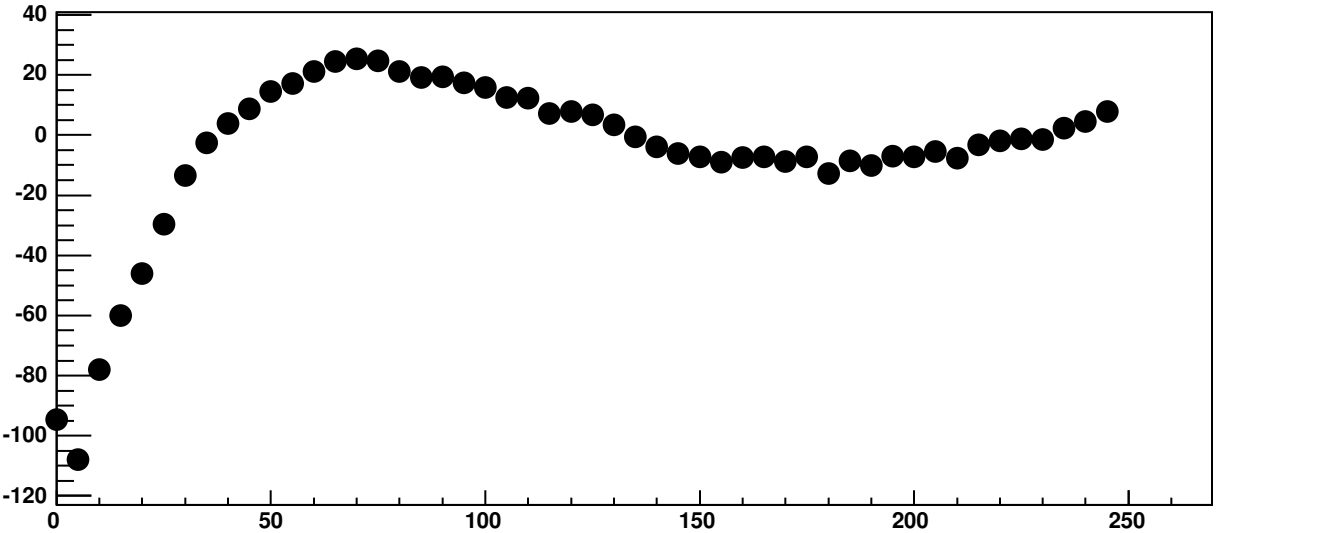
Chip 0, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold



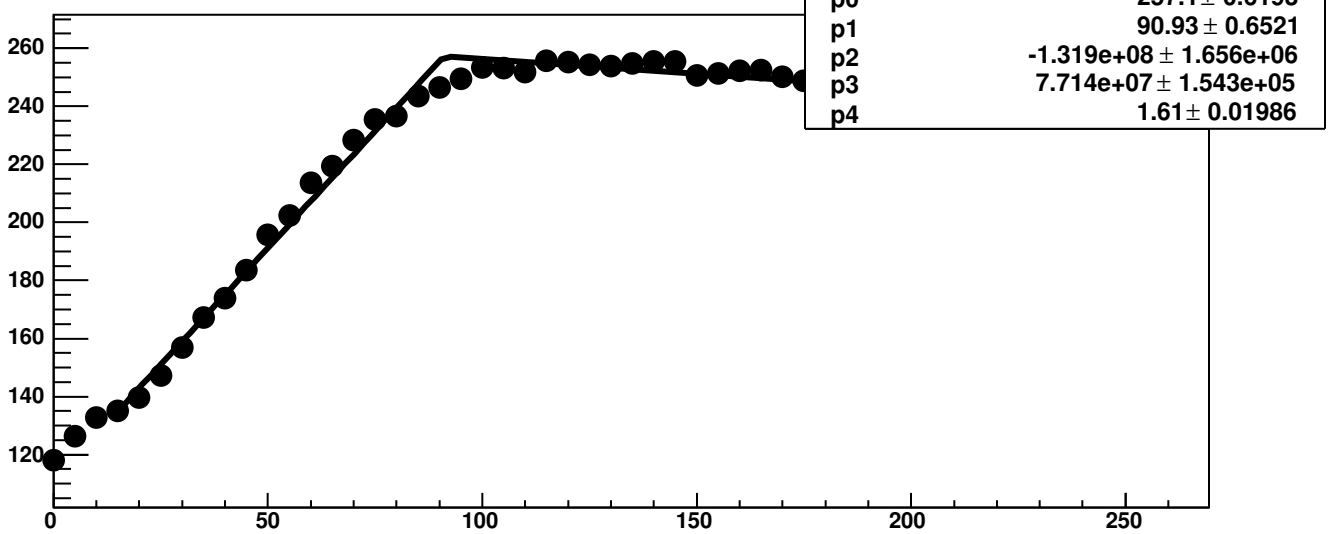
Chip 0, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold



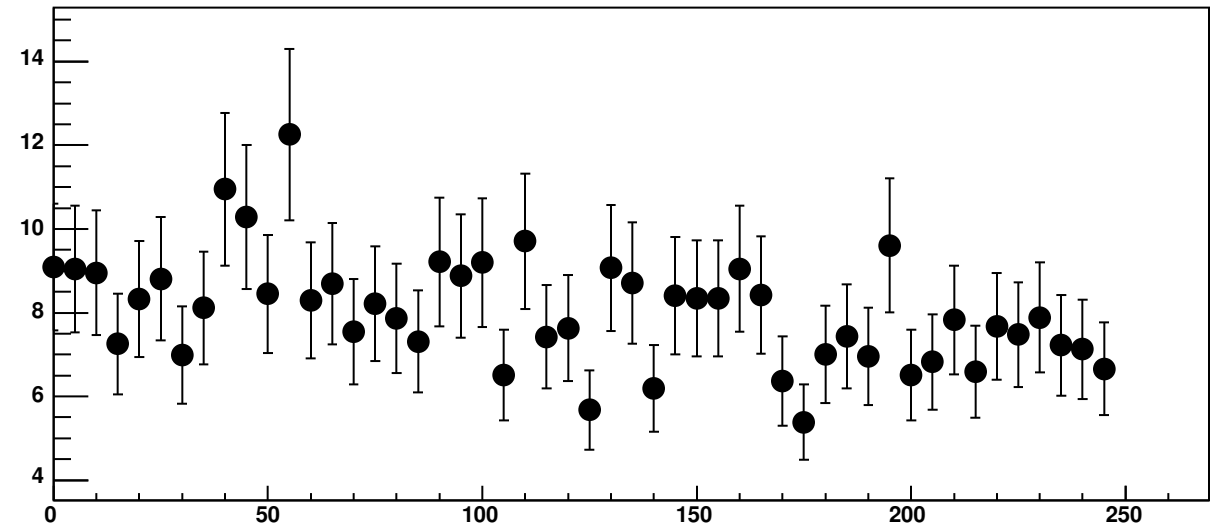
Chip 0, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold



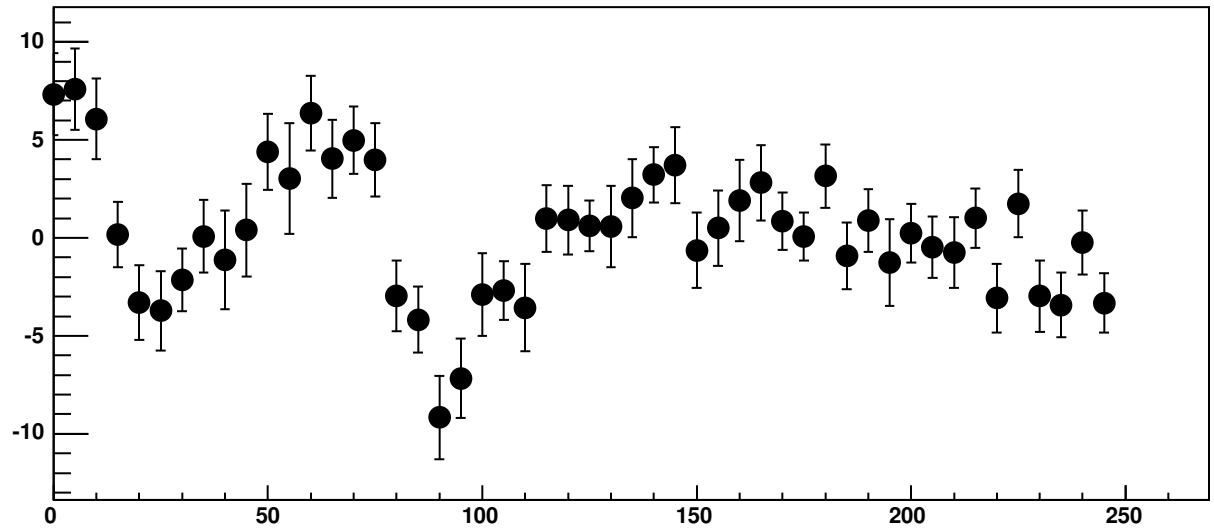
Chip 0, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

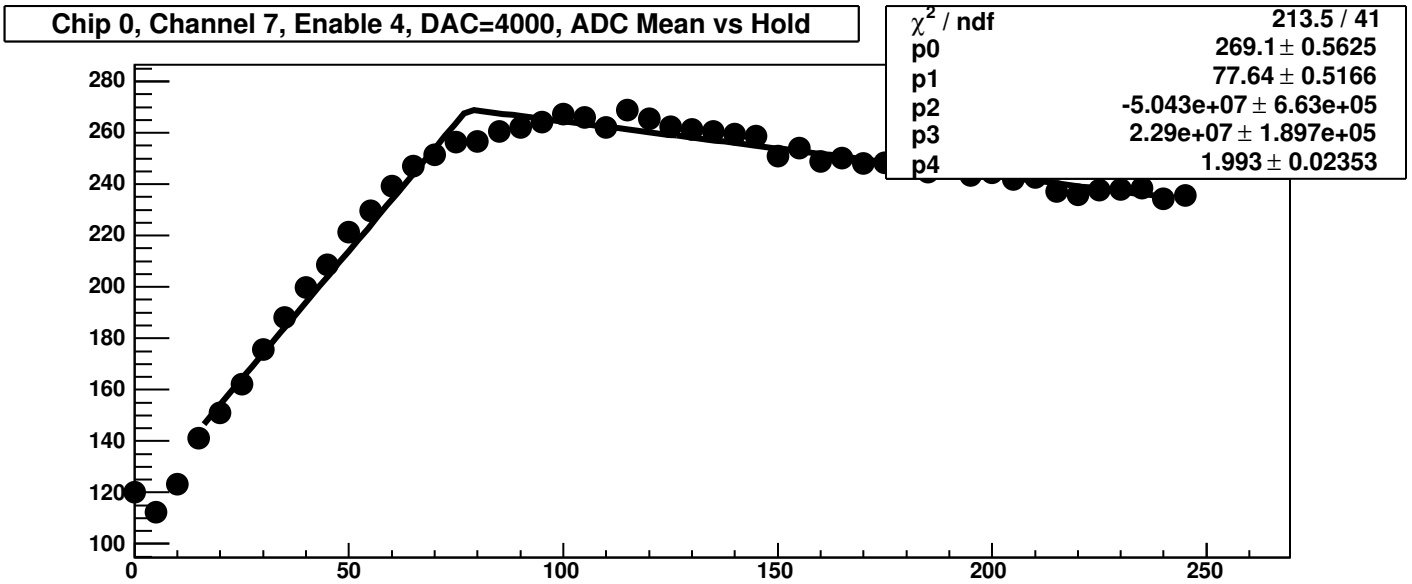


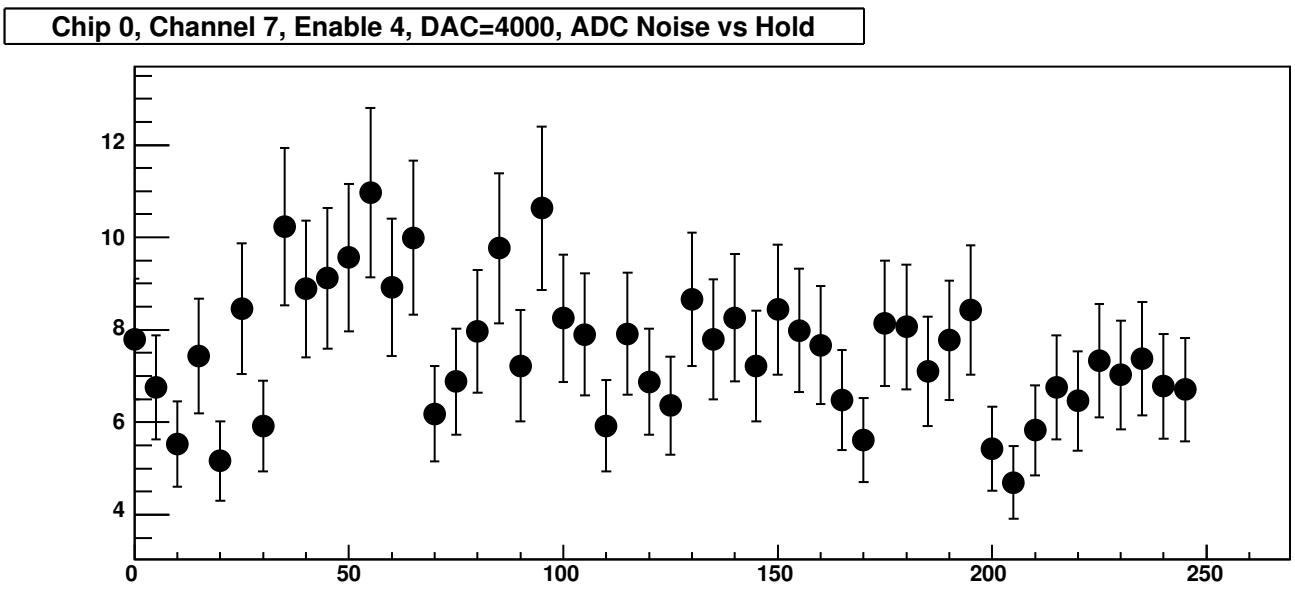
Chip 0, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold

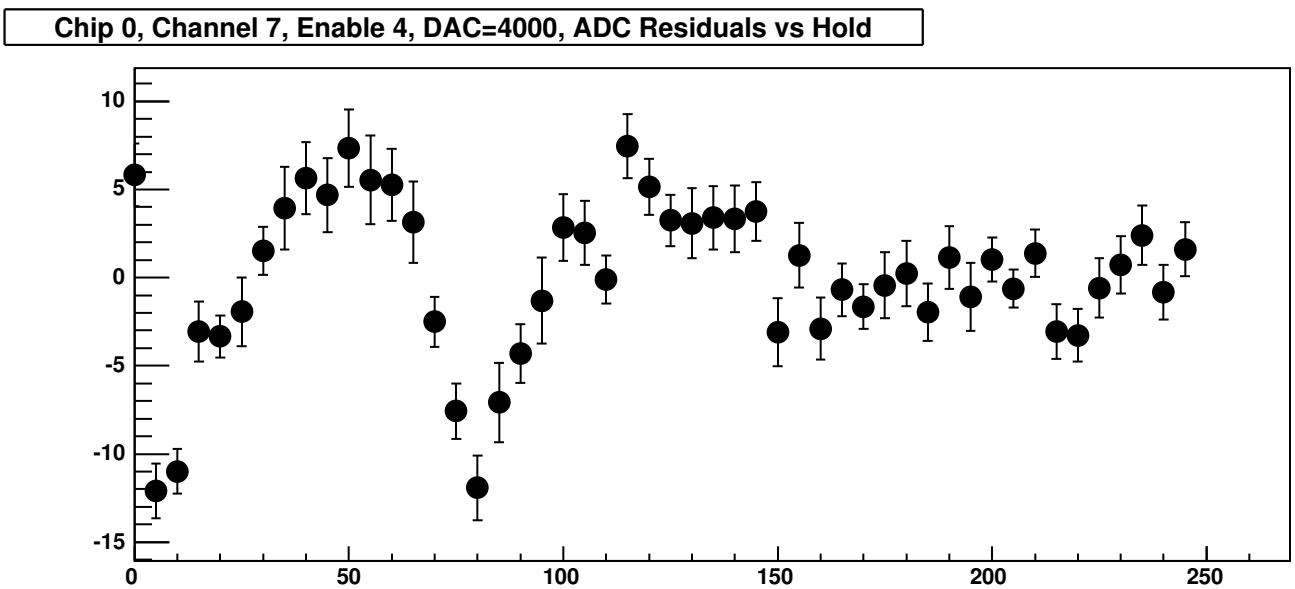


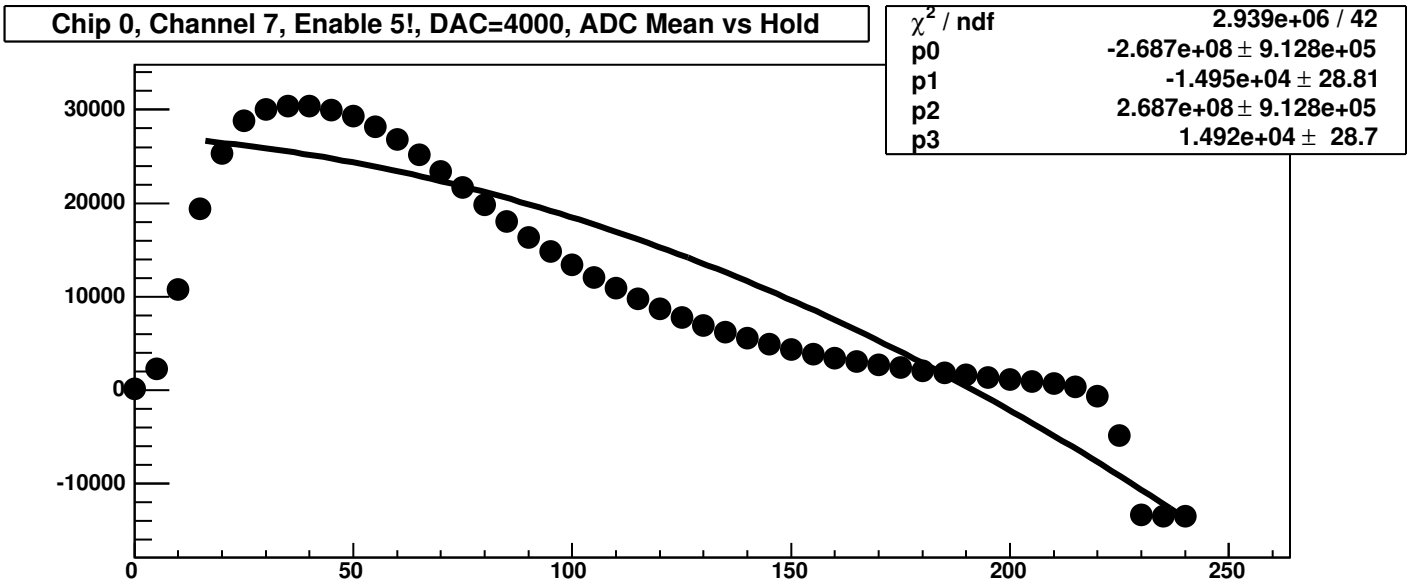
Chip 0, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

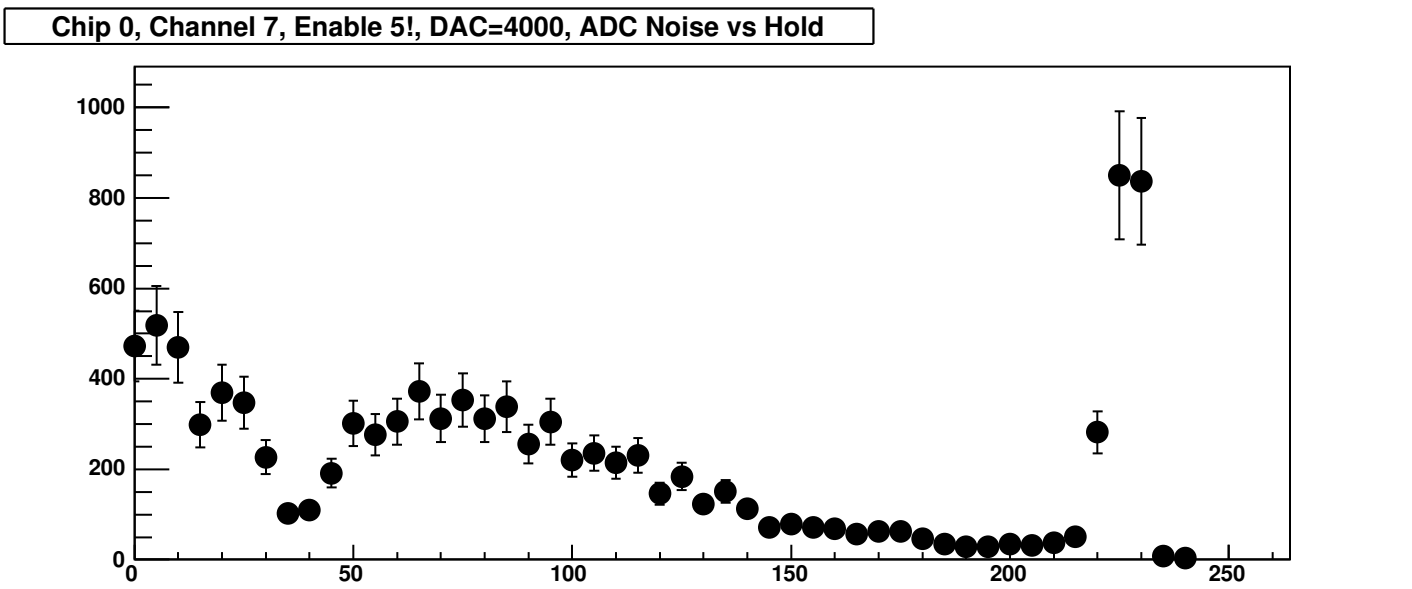


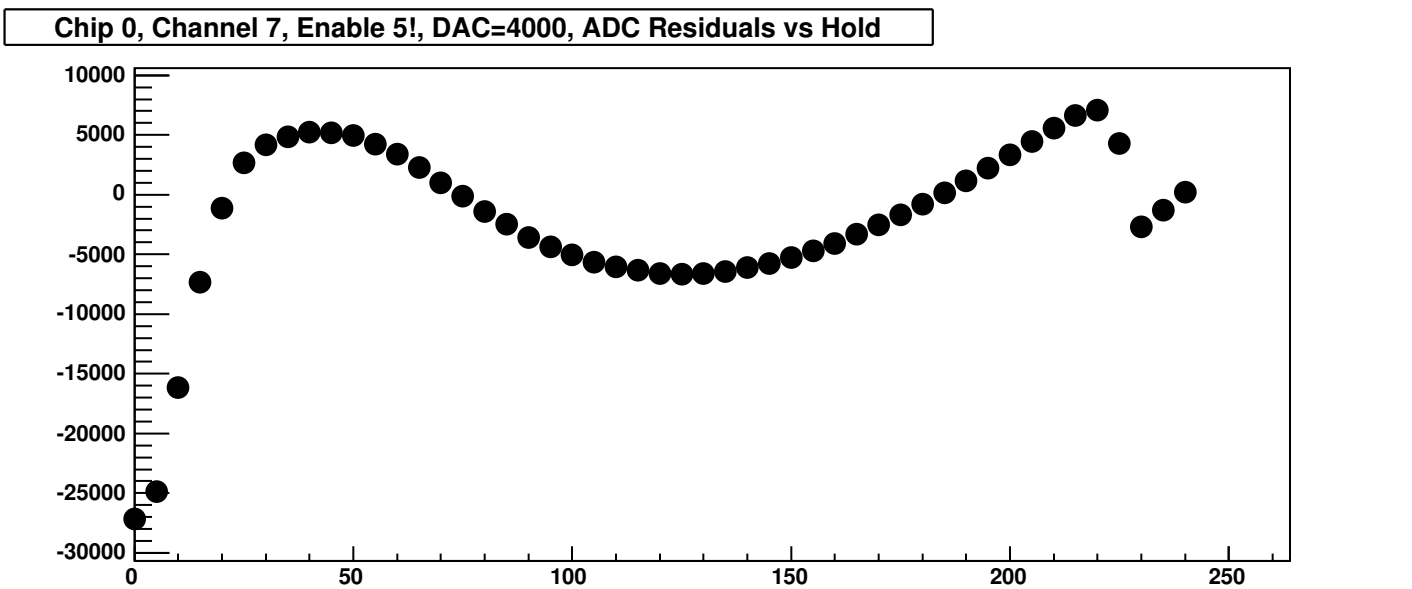

Chip 0, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 213.5 / 41
p0 269.1 ± 0.5625
p1 77.64 ± 0.5166
p2 -5.043e+07 ± 6.63e+05
p3 2.29e+07 ± 1.897e+05
p4 1.993 ± 0.02353


Chip 0, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold

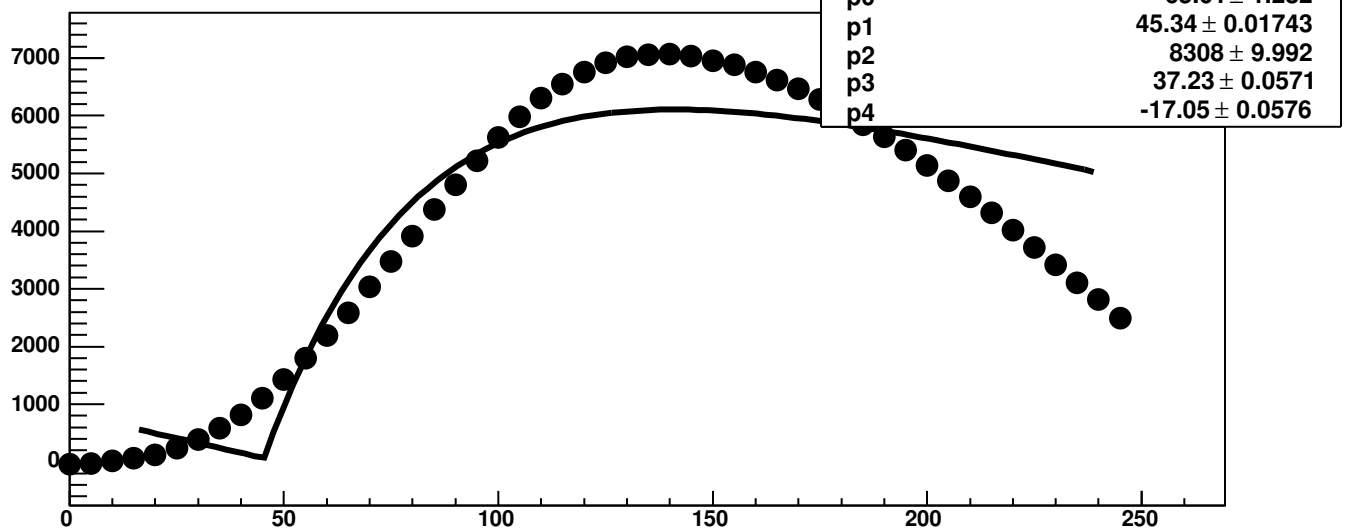

Chip 0, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold


Chip 0, Channel 7, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.939e+06 / 42
p0 -2.687e+08 ± 9.128e+05
p1 -1.495e+04 ± 28.81
p2 2.687e+08 ± 9.128e+05
p3 1.492e+04 ± 28.7
30000
20000
10000
0
-10000
0
50
100
150
200
250

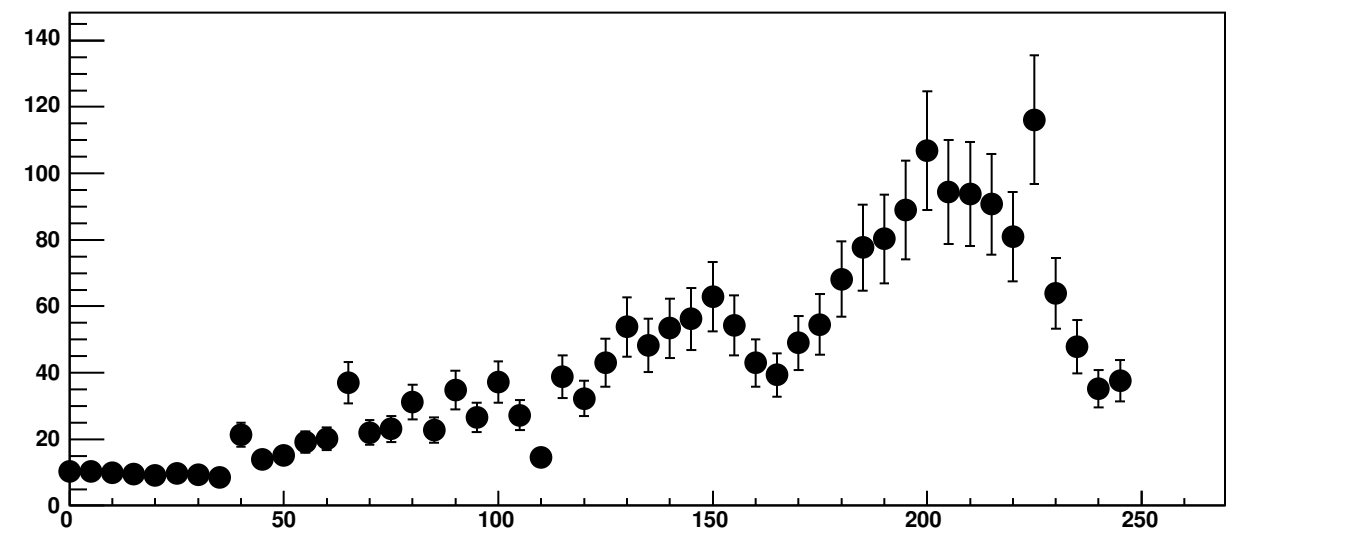

Chip 0, Channel 7, Enable 5!, DAC=4000, ADC Noise vs Hold
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 0, Channel 7, Enable 5!, DAC=4000, ADC Residuals vs Hold
10000
5000
0
-5000
-10000
-15000
-20000
-25000
-30000
0
50
100
150
200
250

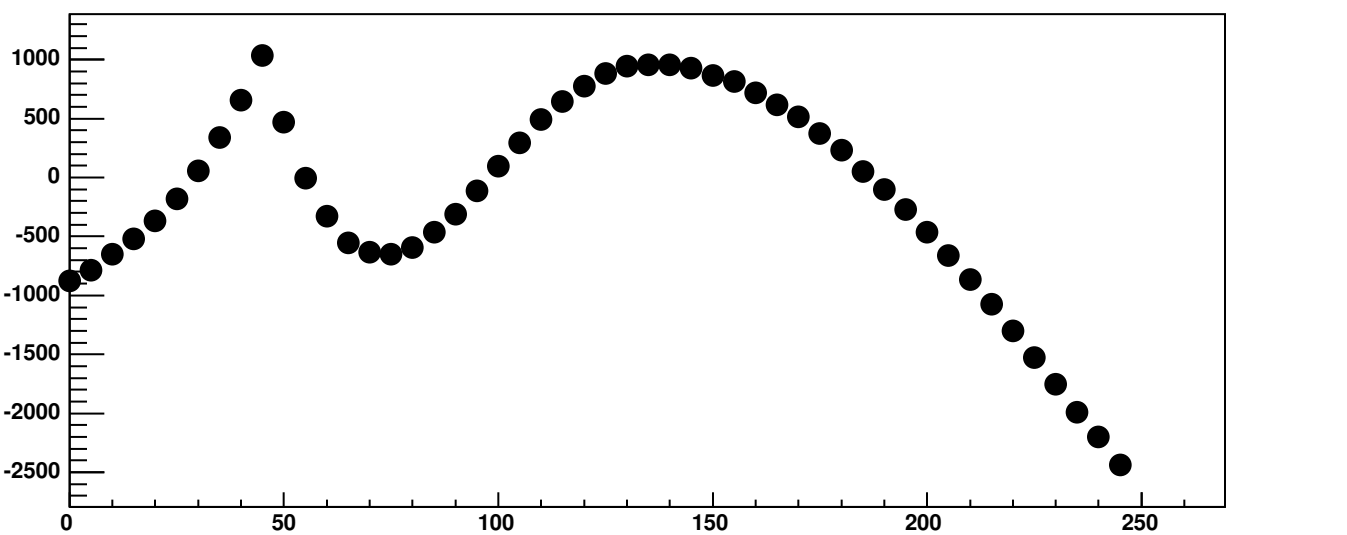
Chip 0, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold



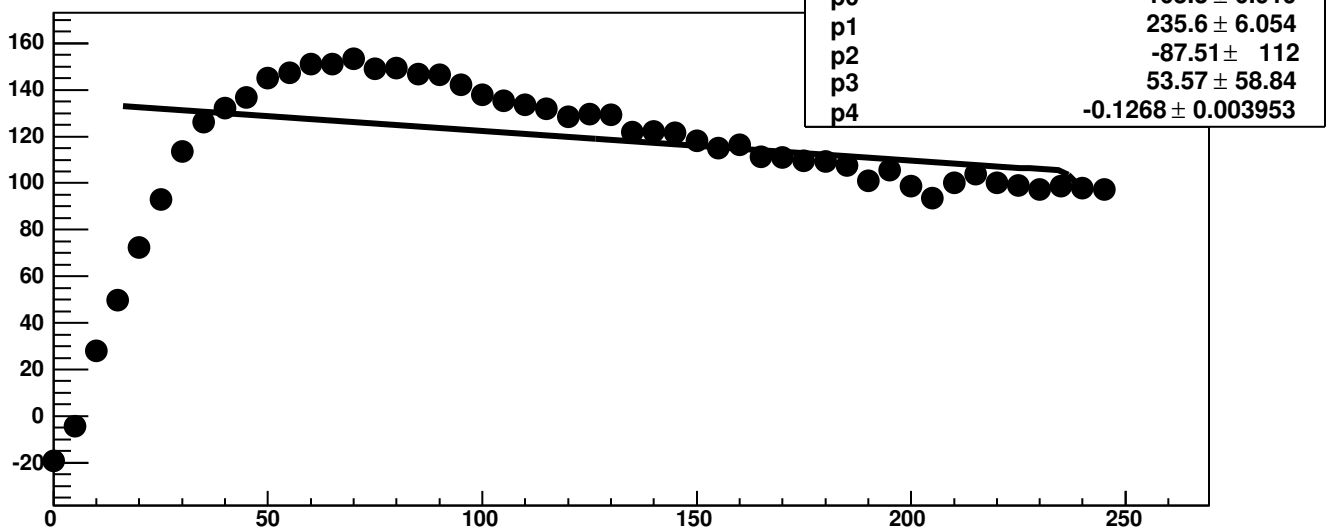
Chip 0, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



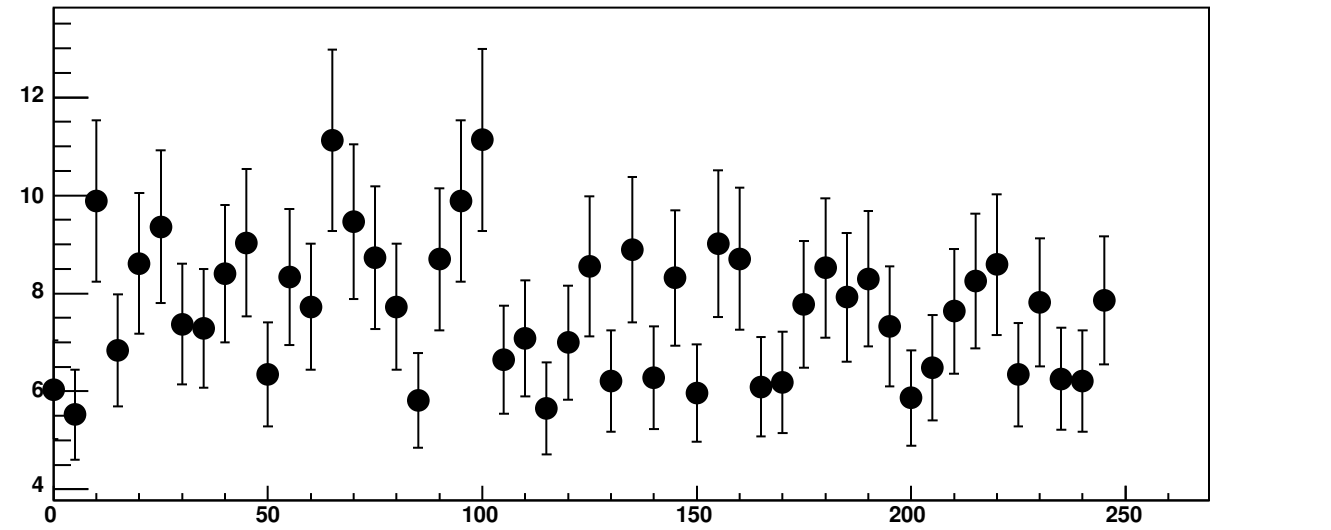
Chip 0, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold



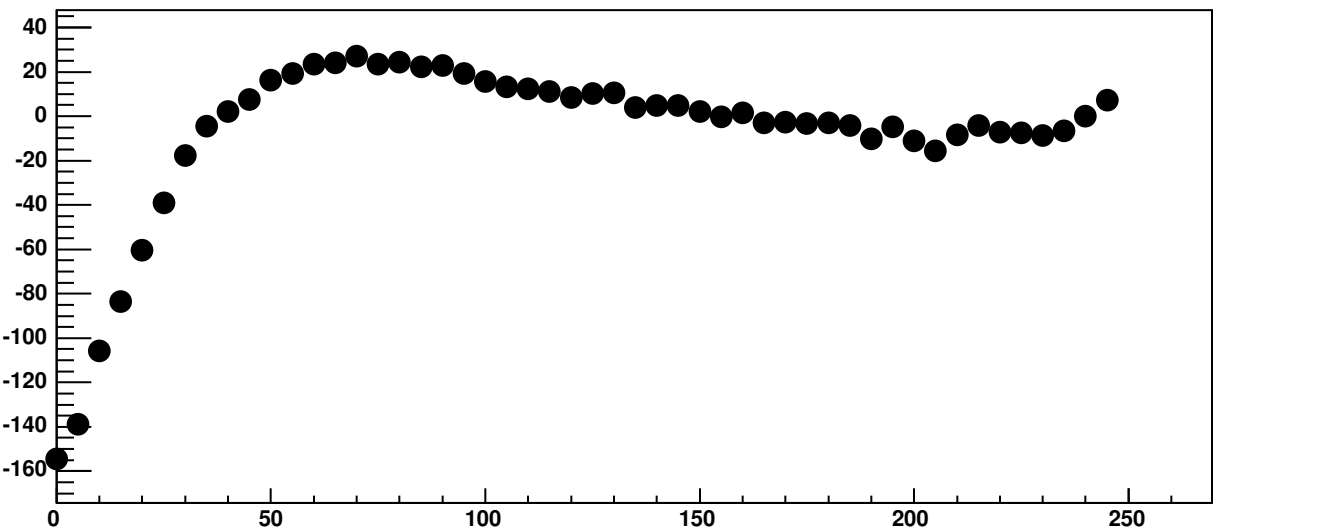
Chip 0, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold



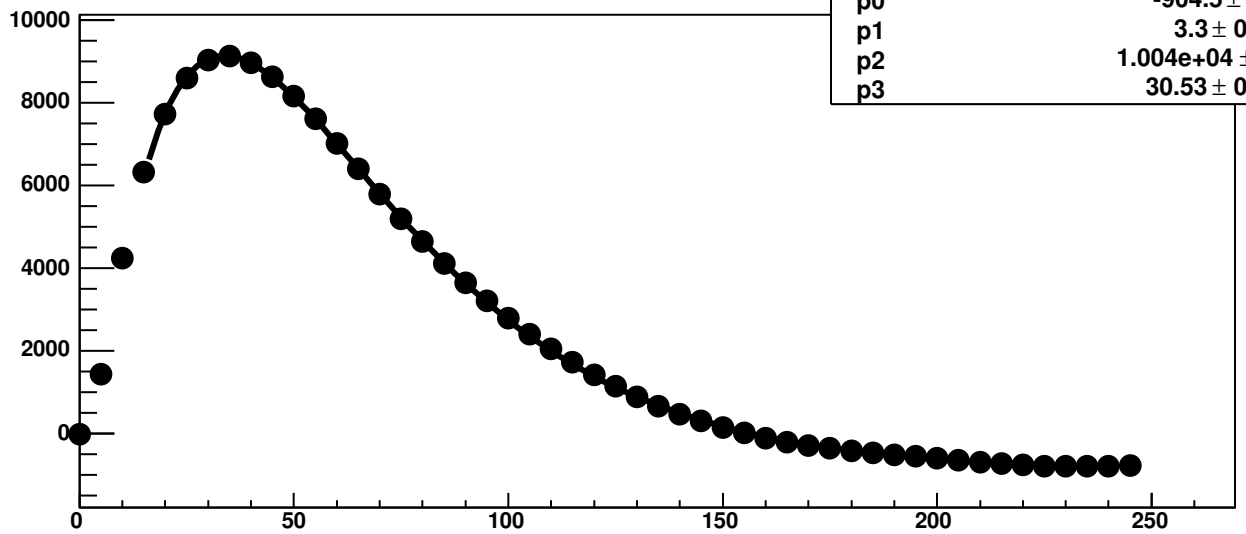
Chip 0, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



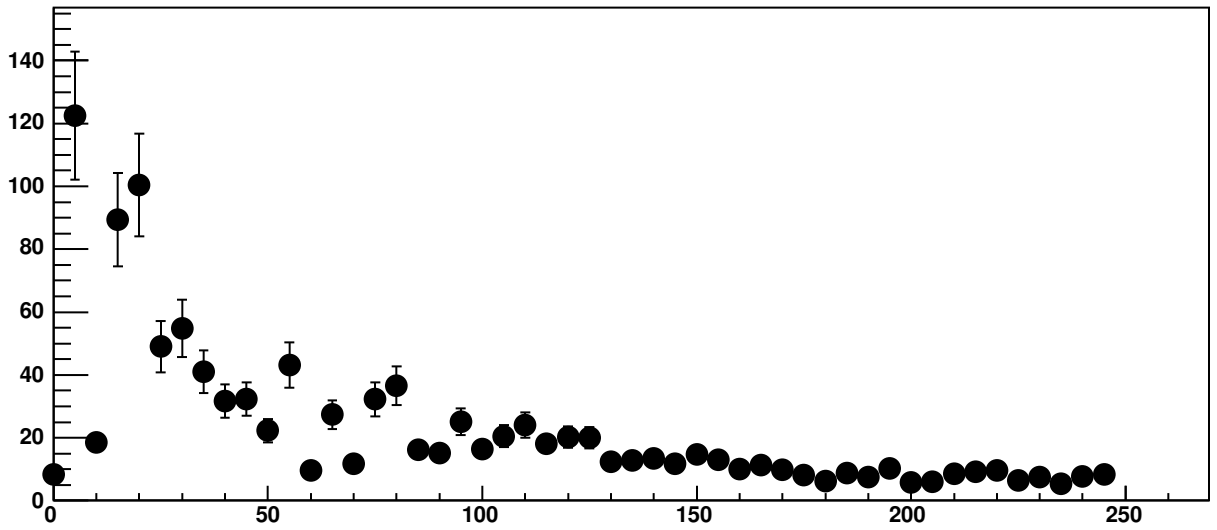
Chip 0, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold



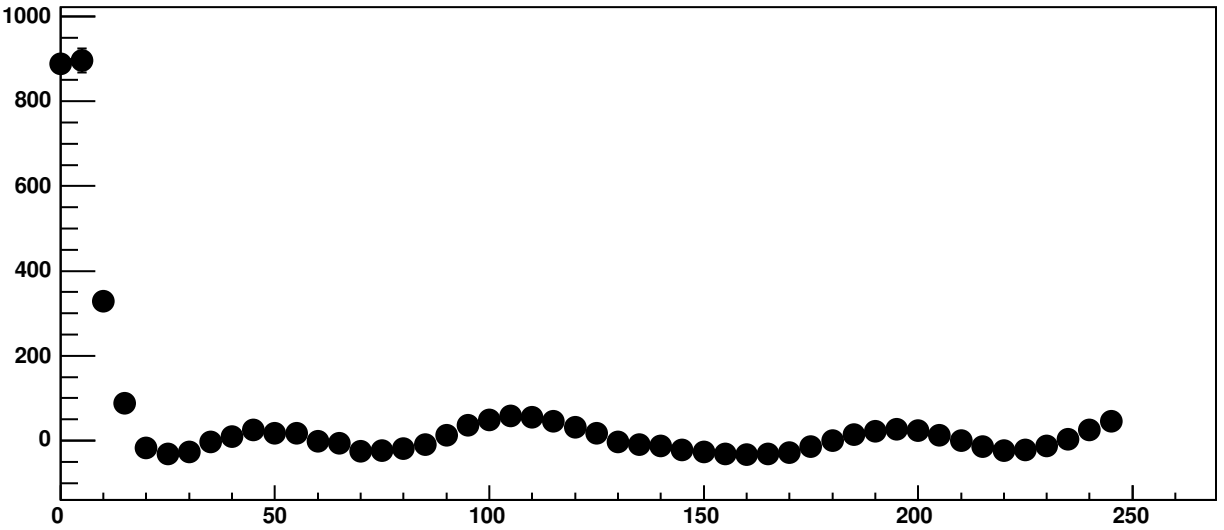
Chip 0, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold

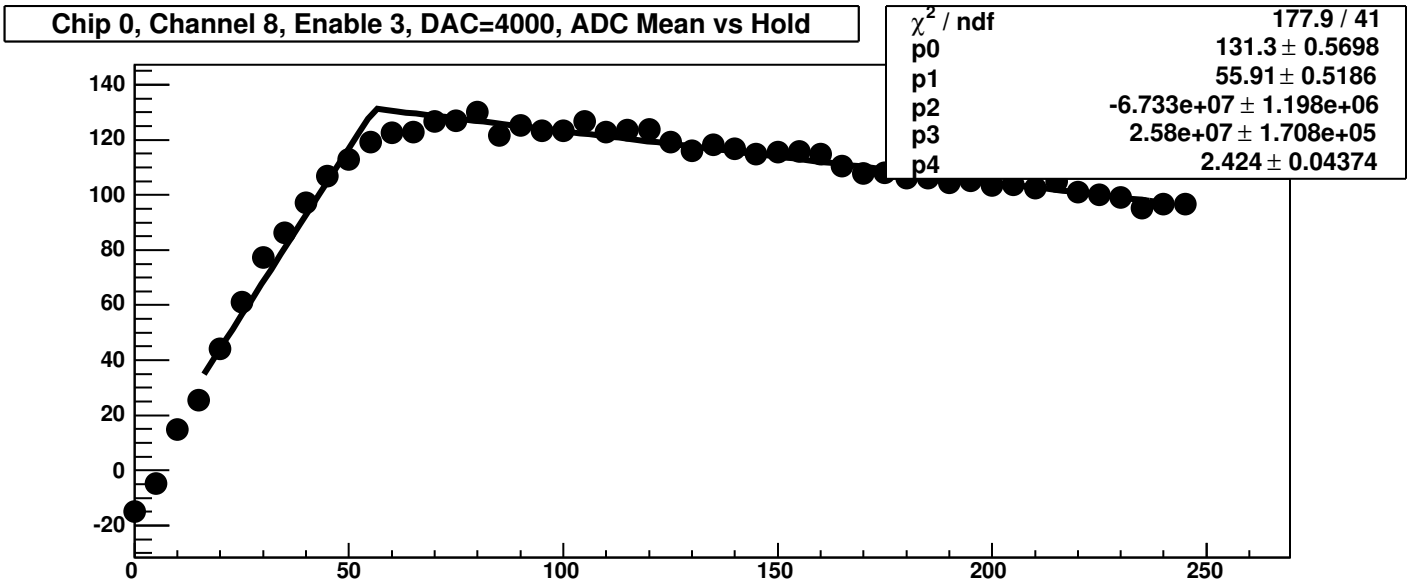


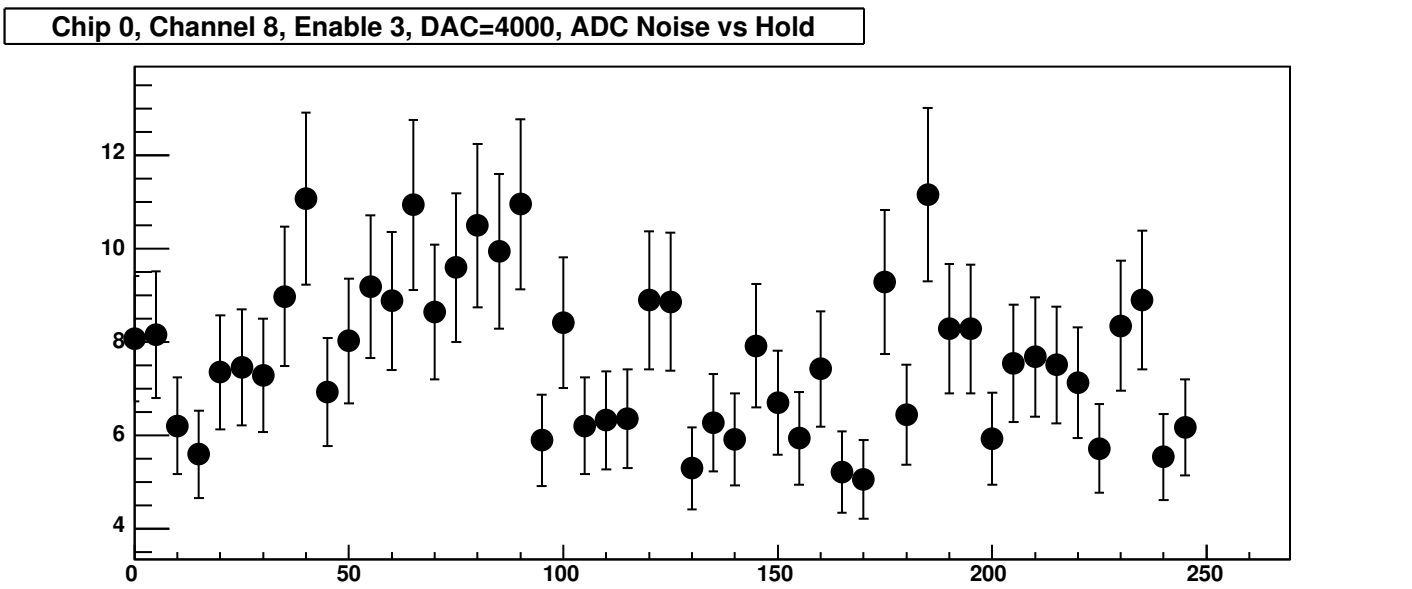
Chip 0, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold

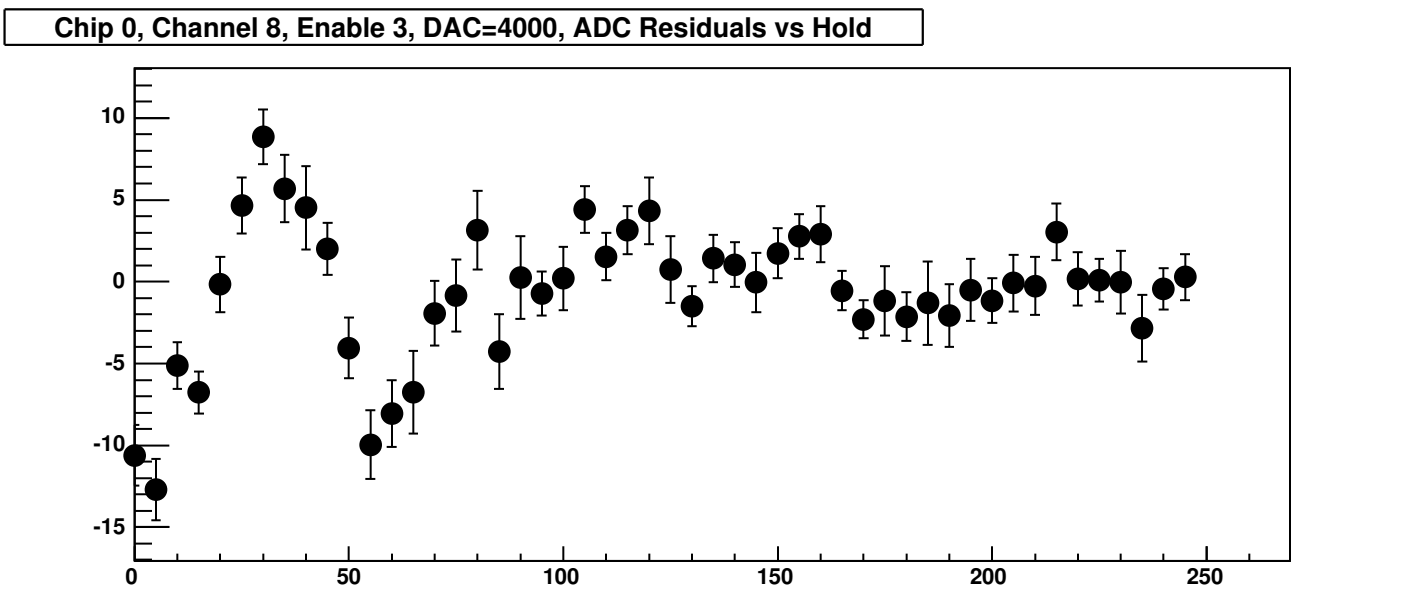


Chip 0, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold

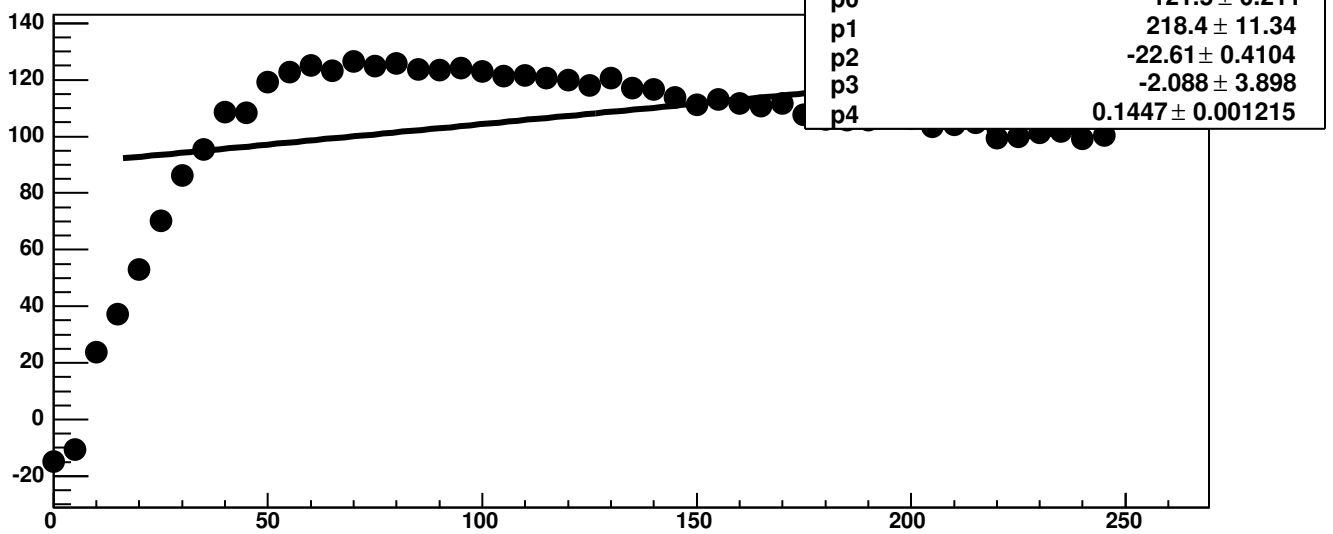



Chip 0, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 177.9 / 41
p0 131.3 ± 0.5698
p1 55.91 ± 0.5186
p2 -6.733e+07 ± 1.198e+06
p3 2.58e+07 ± 1.708e+05
p4 2.424 ± 0.04374

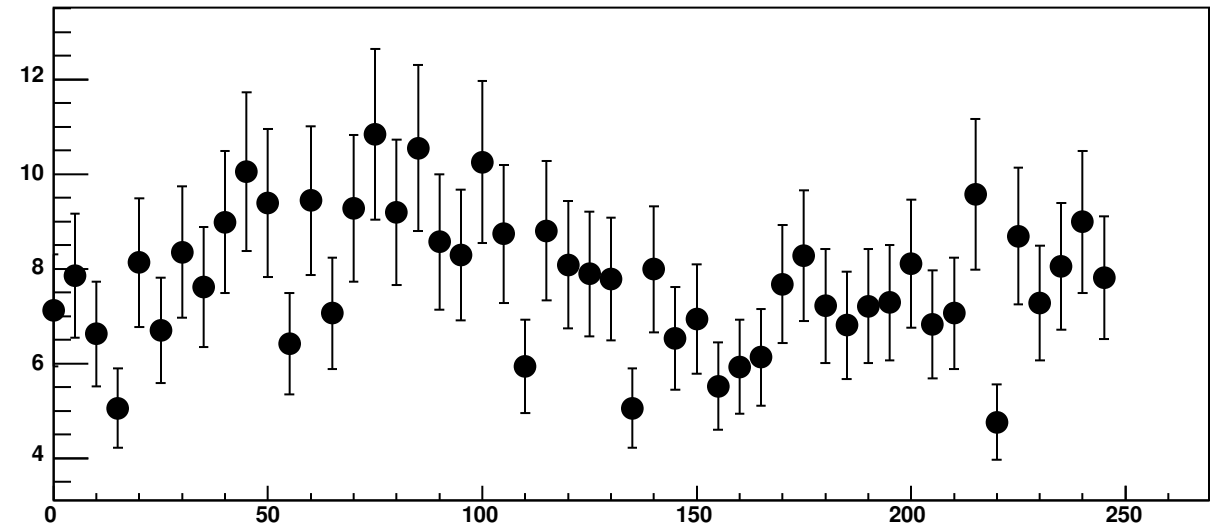

Chip 0, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold


Chip 0, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

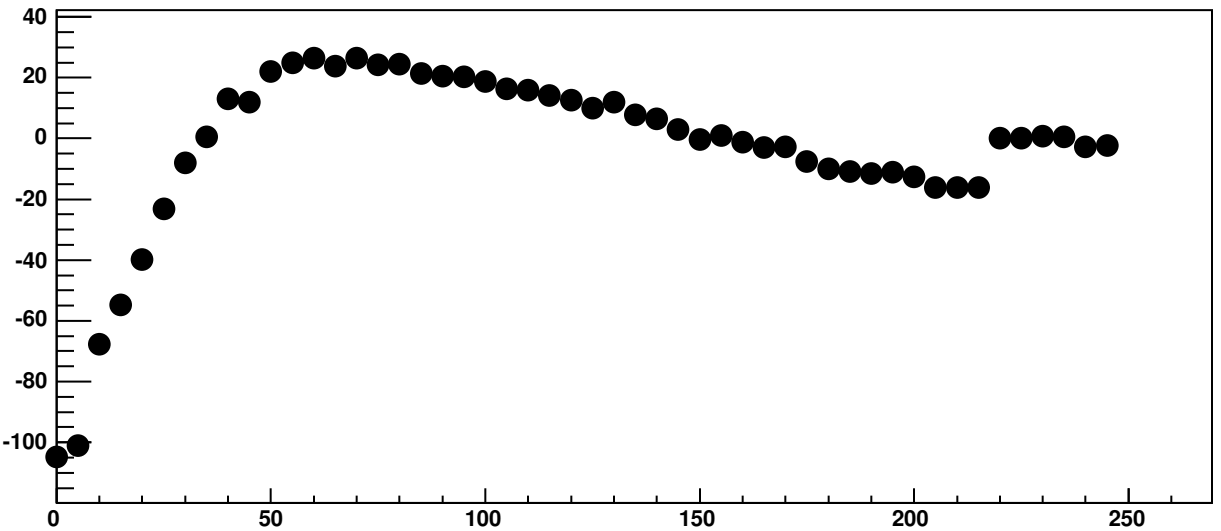
Chip 0, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold



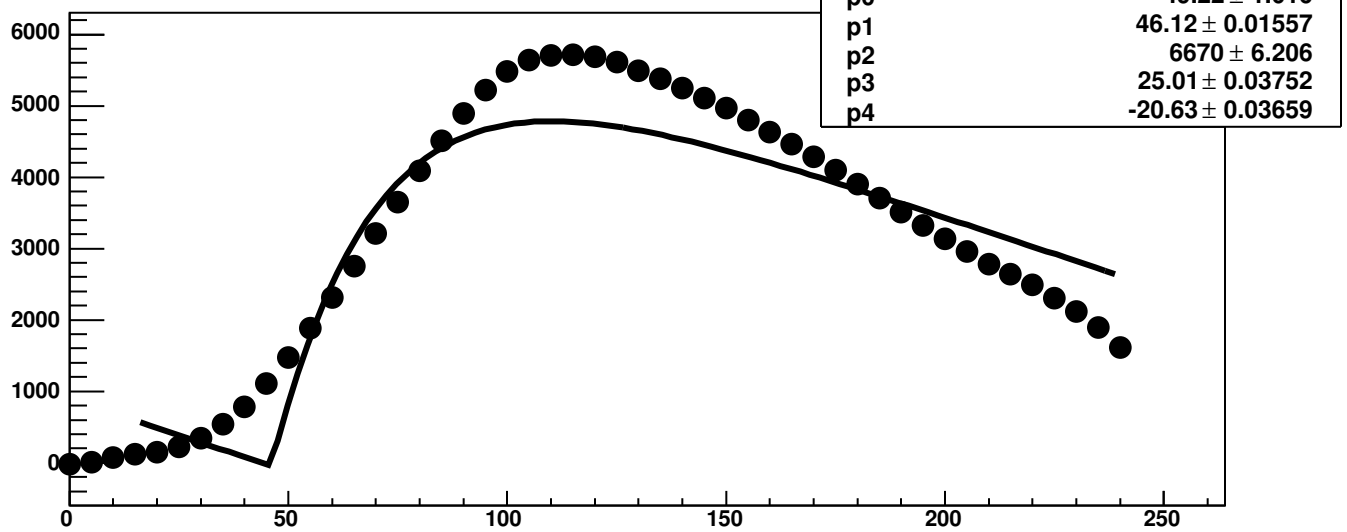
Chip 0, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



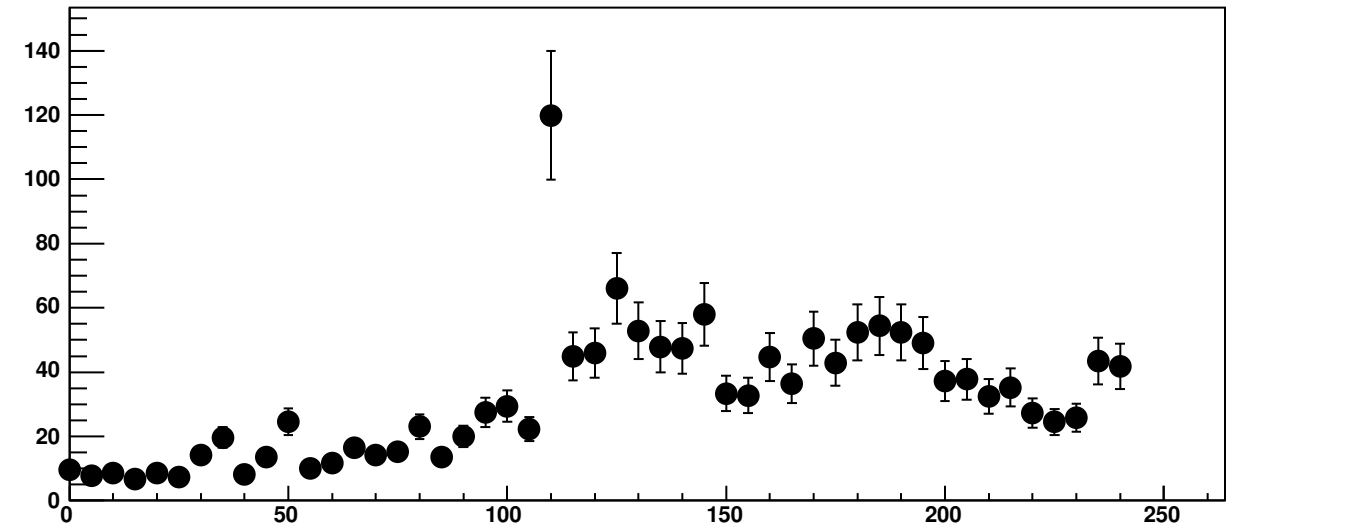
Chip 0, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold



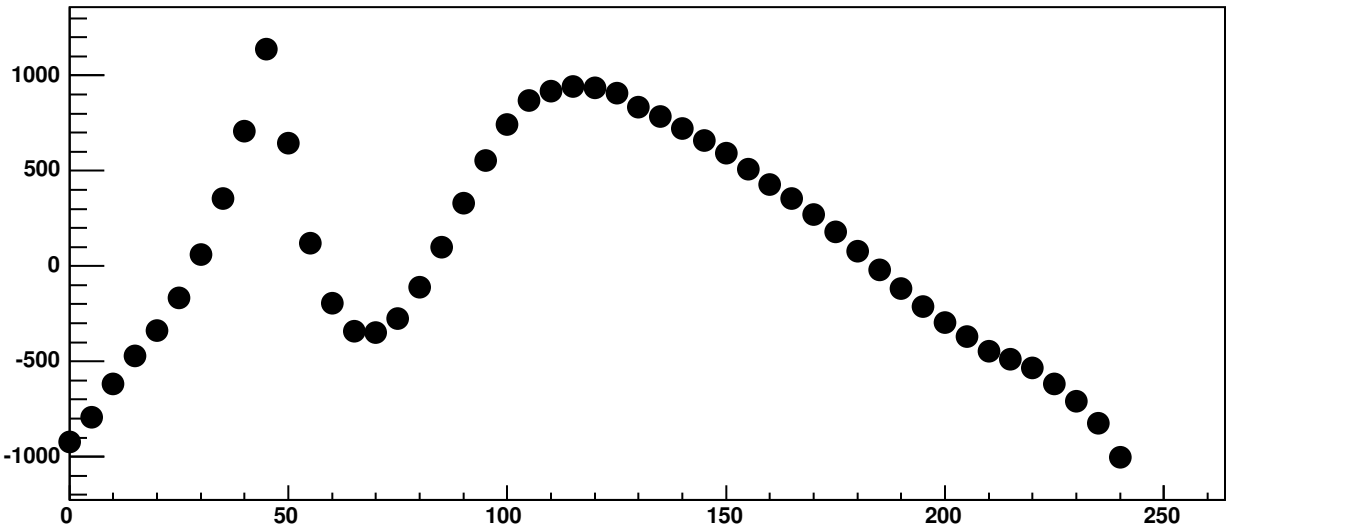
Chip 0, Channel 8, Enable 5, DAC=4000, ADC Mean vs Hold

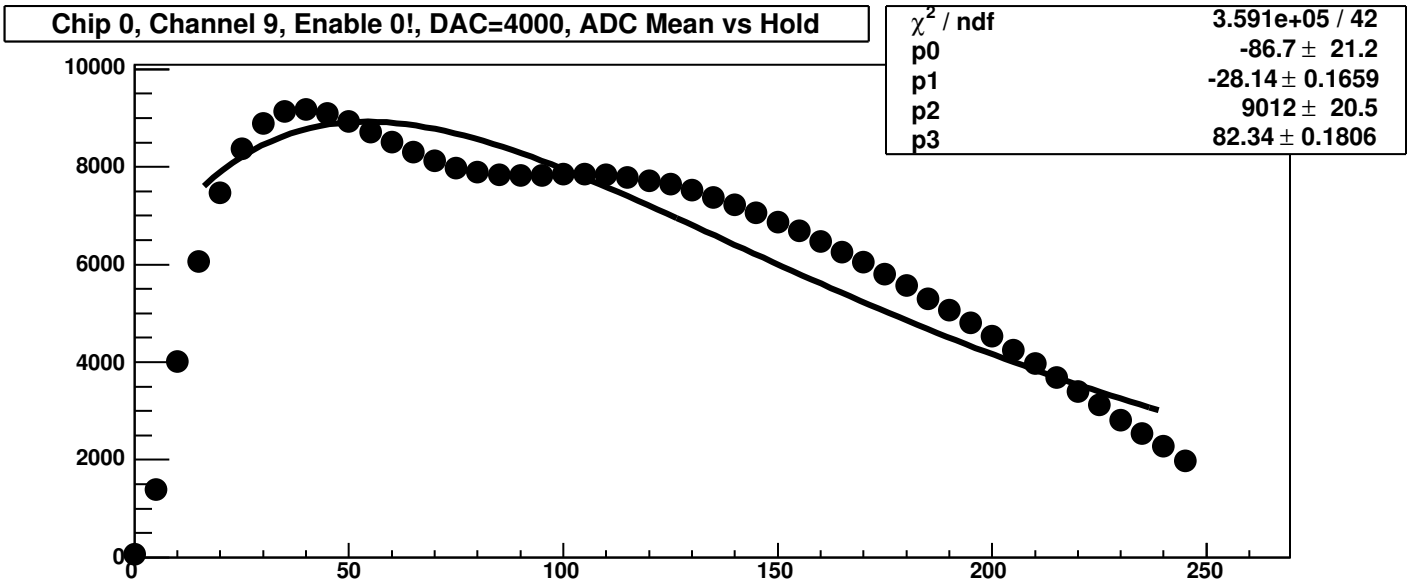


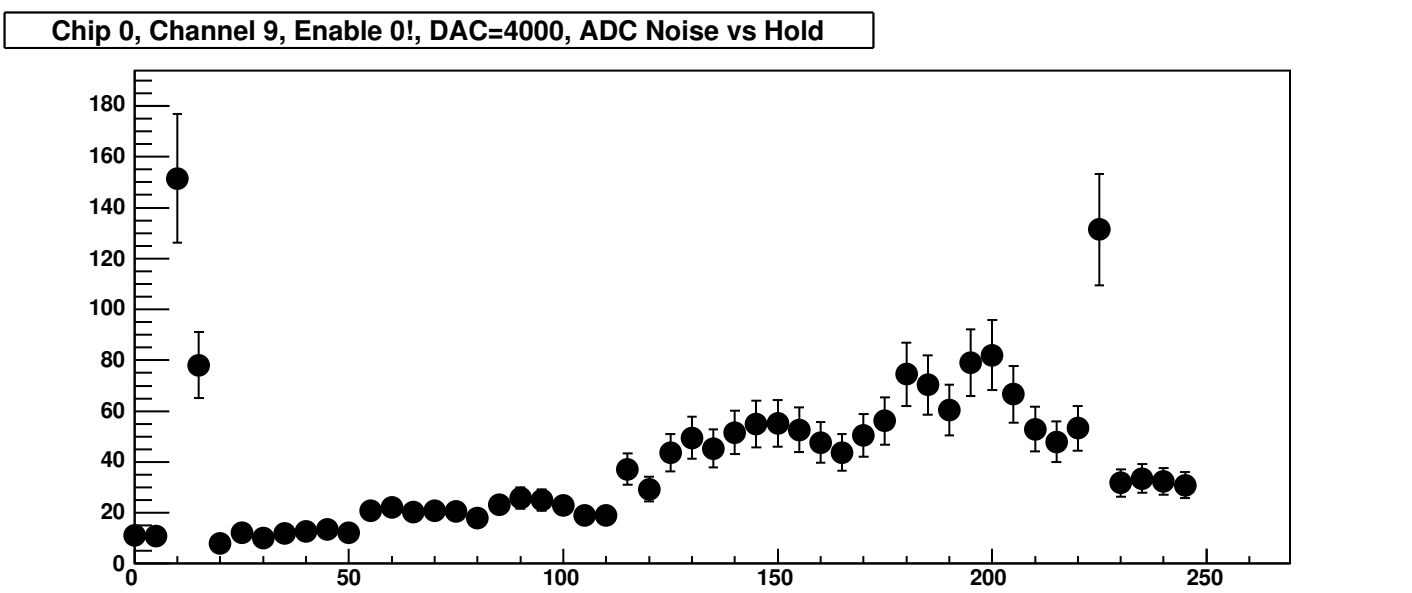
Chip 0, Channel 8, Enable 5, DAC=4000, ADC Noise vs Hold

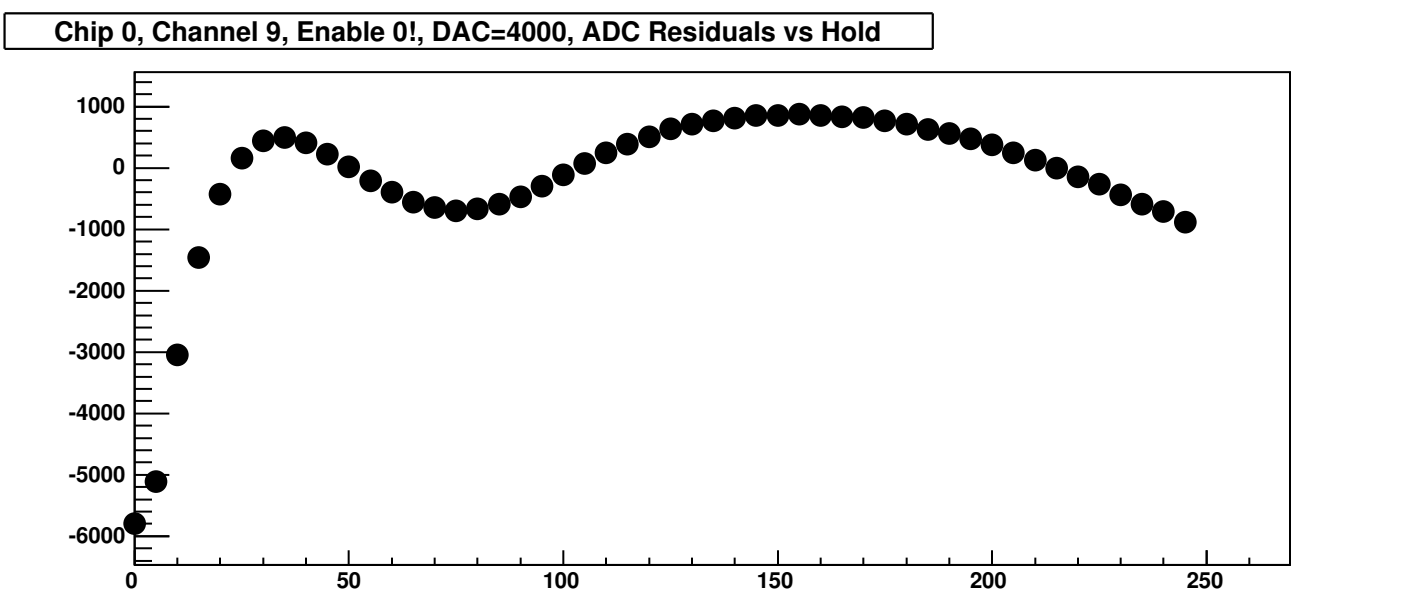


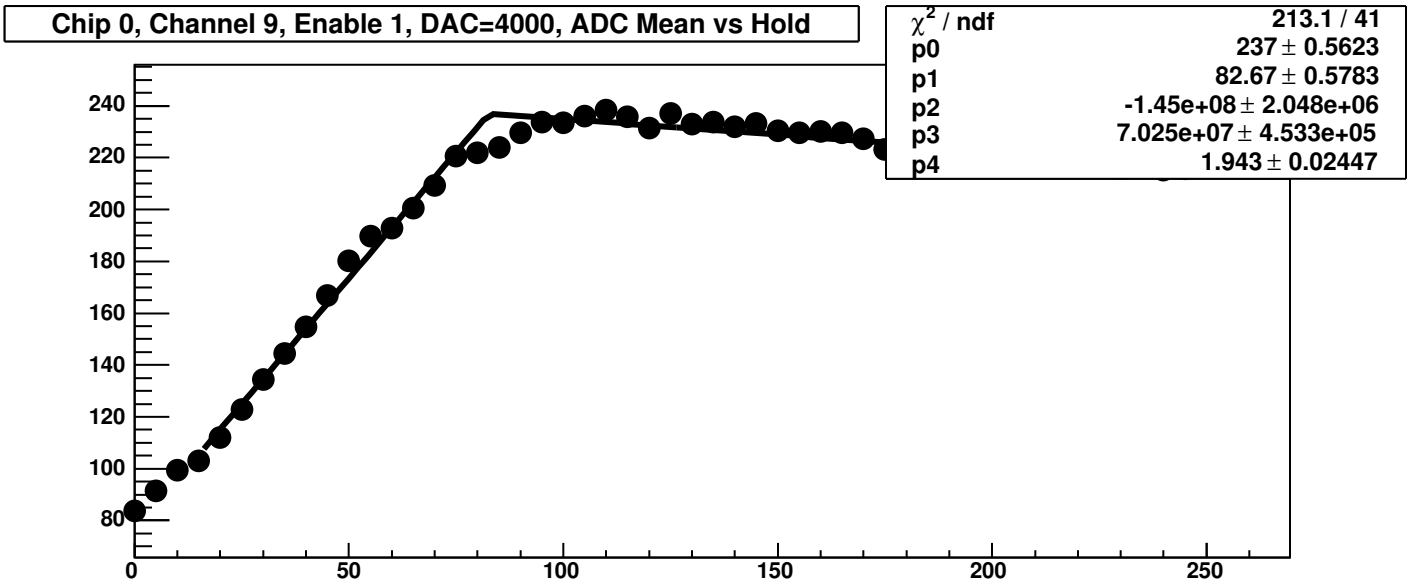
Chip 0, Channel 8, Enable 5, DAC=4000, ADC Residuals vs Hold

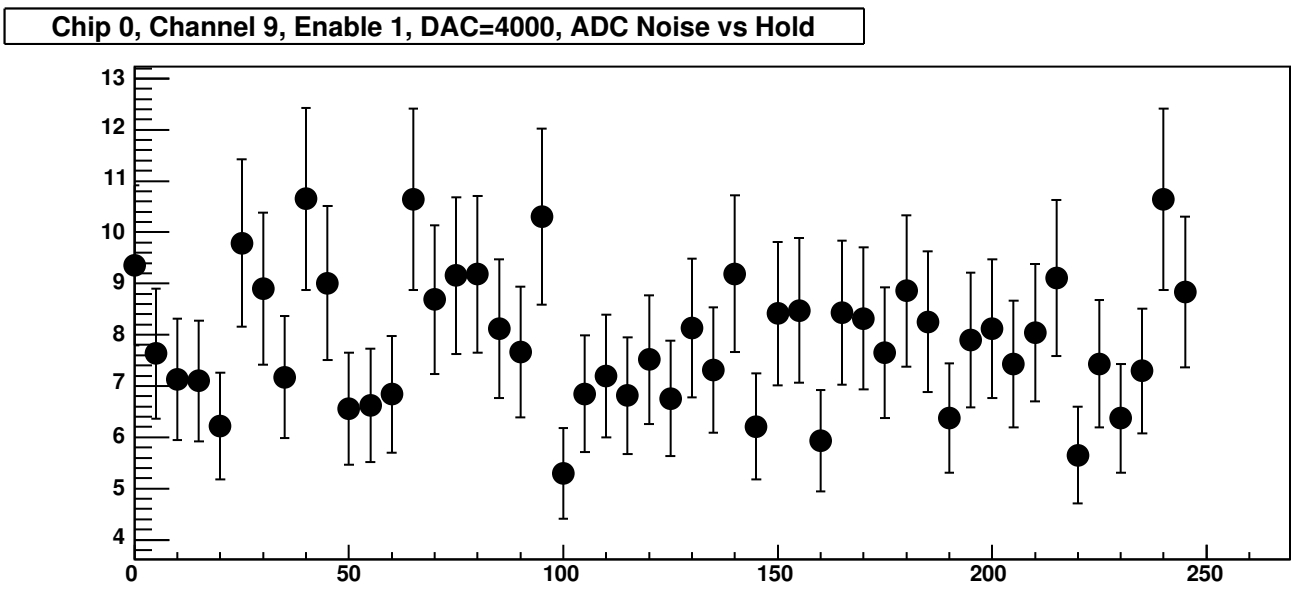


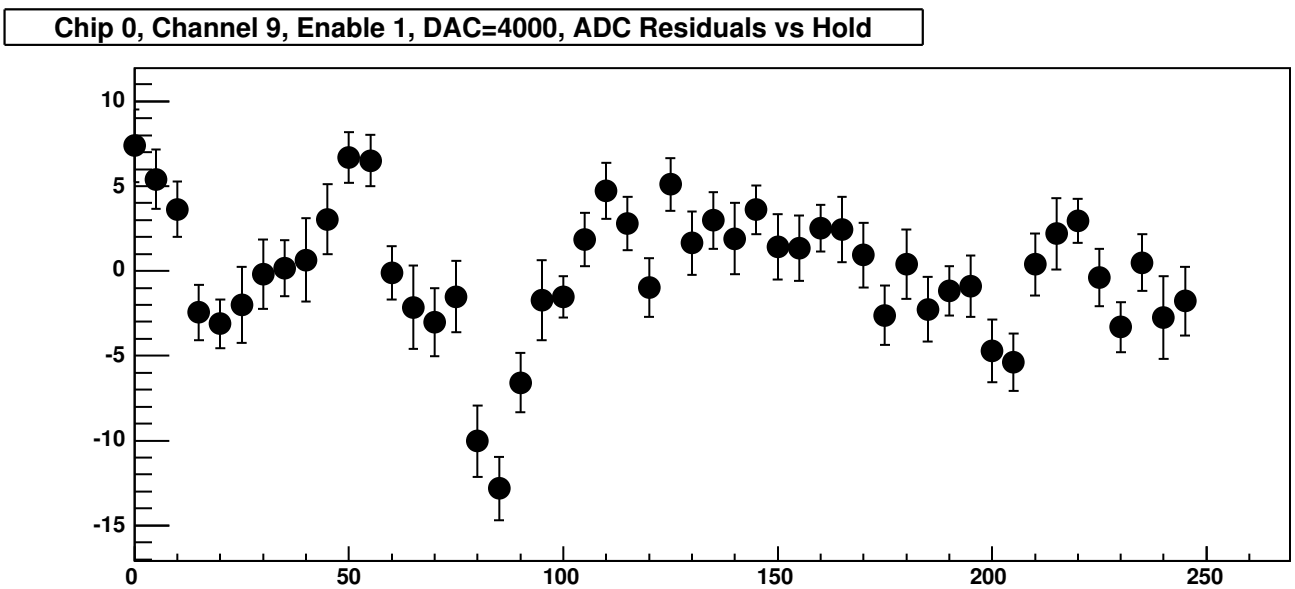

Chip 0, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.591e+05 / 42
p0 -86.7 ± 21.2
p1 -28.14 ± 0.1659
p2 9012 ± 20.5
p3 82.34 ± 0.1806
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 0, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

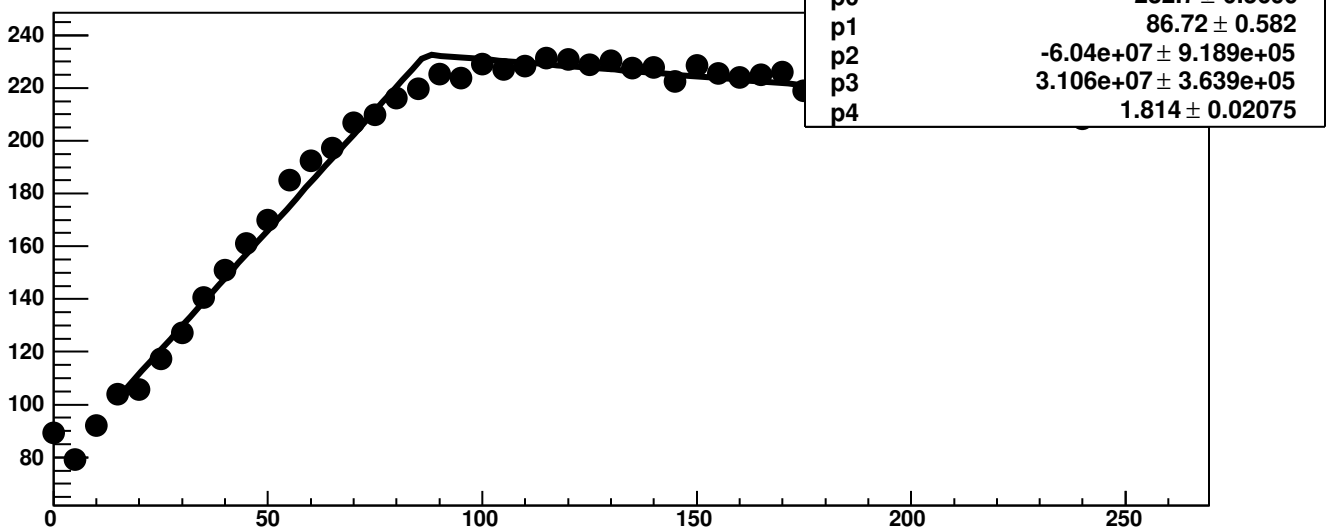

Chip 0, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
50
100
150
200
250


Chip 0, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 213.1 / 41
p0 237 ± 0.5623
p1 82.67 ± 0.5783
p2 -1.45e+08 ± 2.048e+06
p3 7.025e+07 ± 4.533e+05
p4 1.943 ± 0.02447

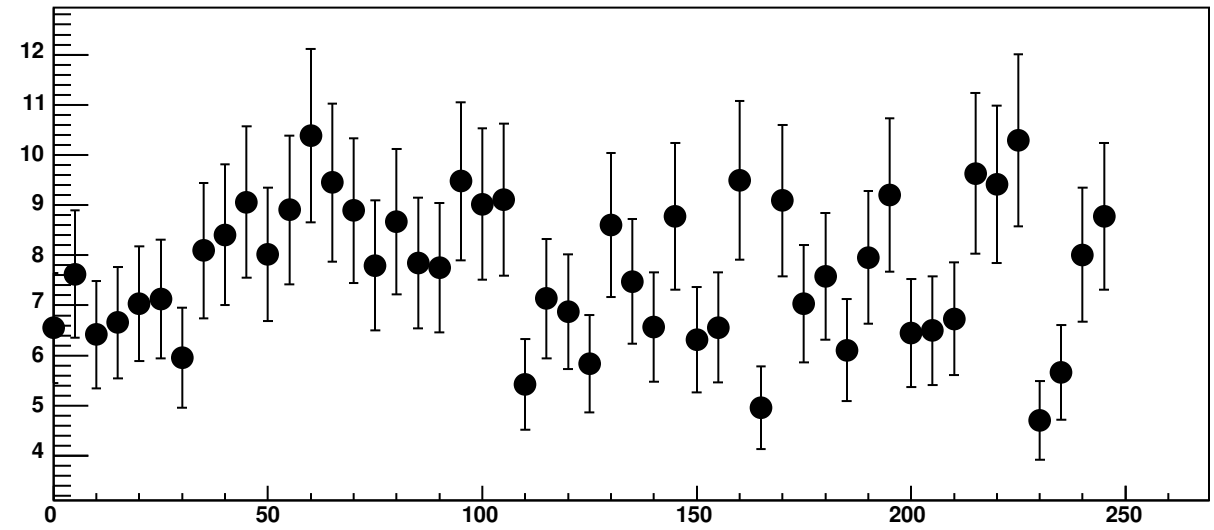

Chip 0, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold


Chip 0, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

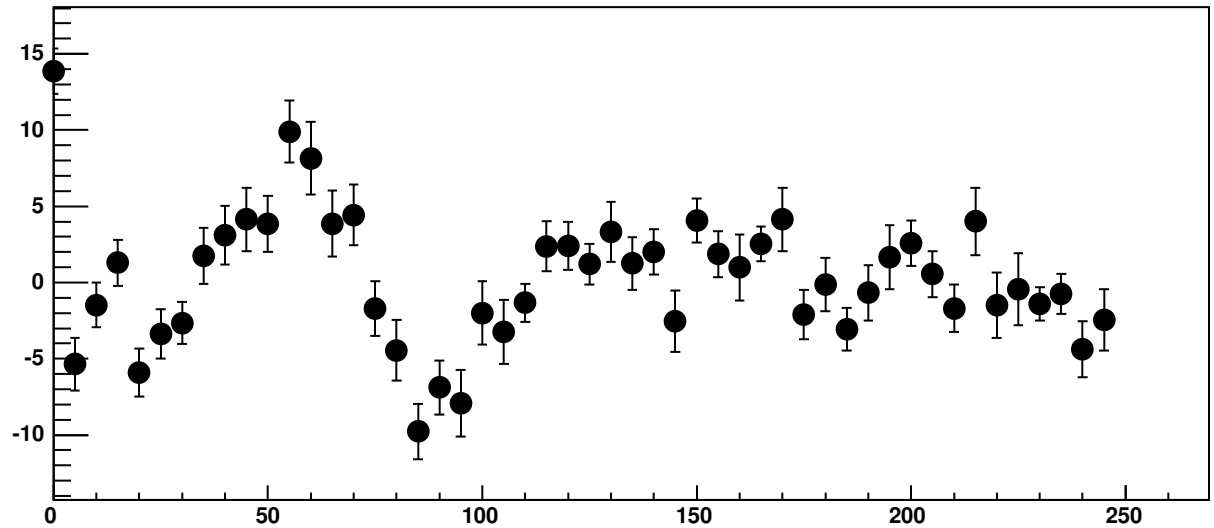
Chip 0, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold



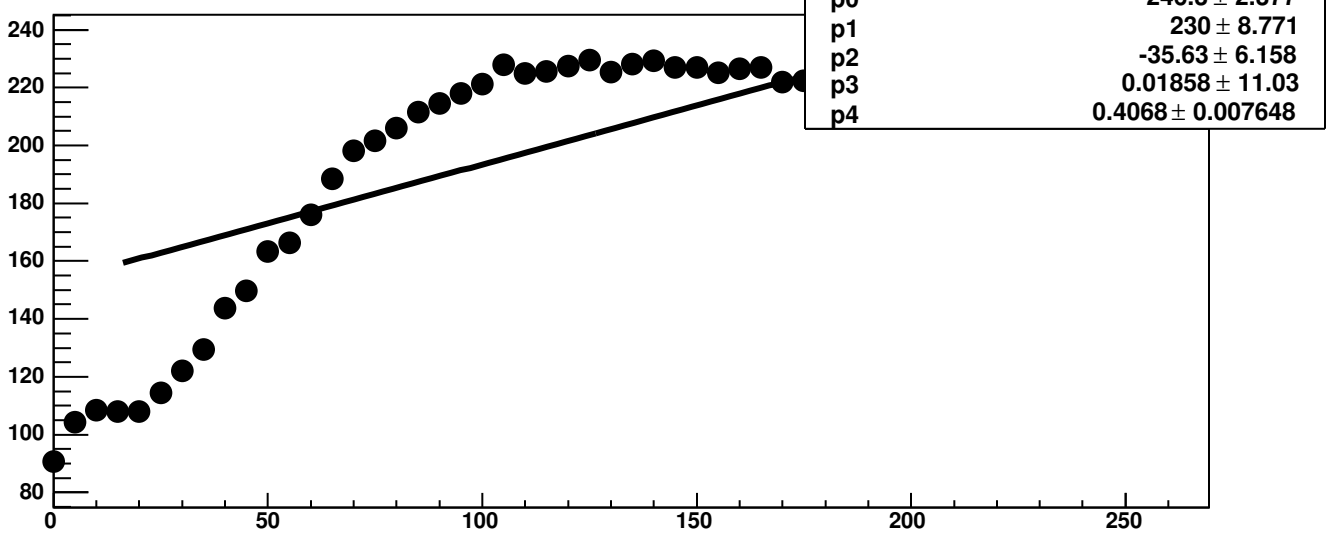
Chip 0, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



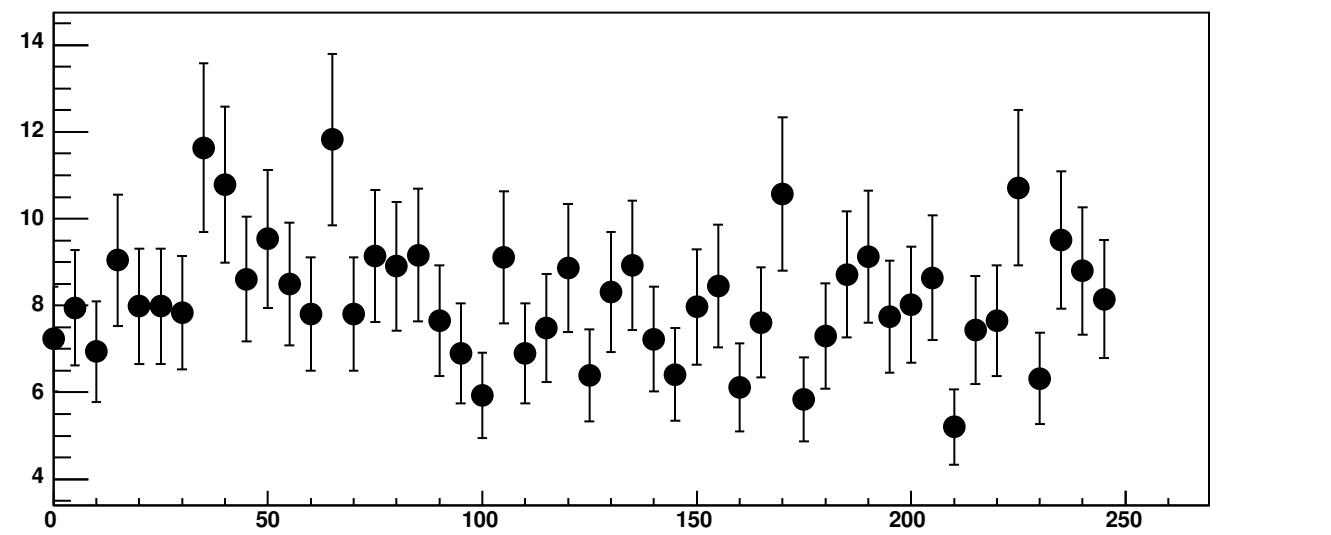
Chip 0, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold



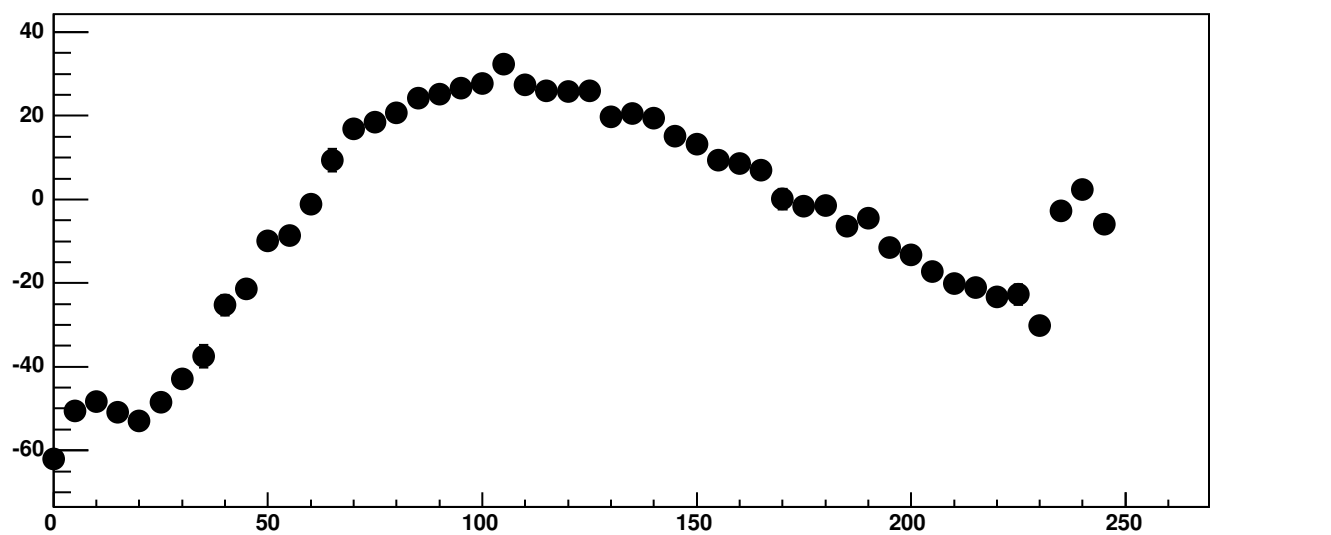
Chip 0, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold



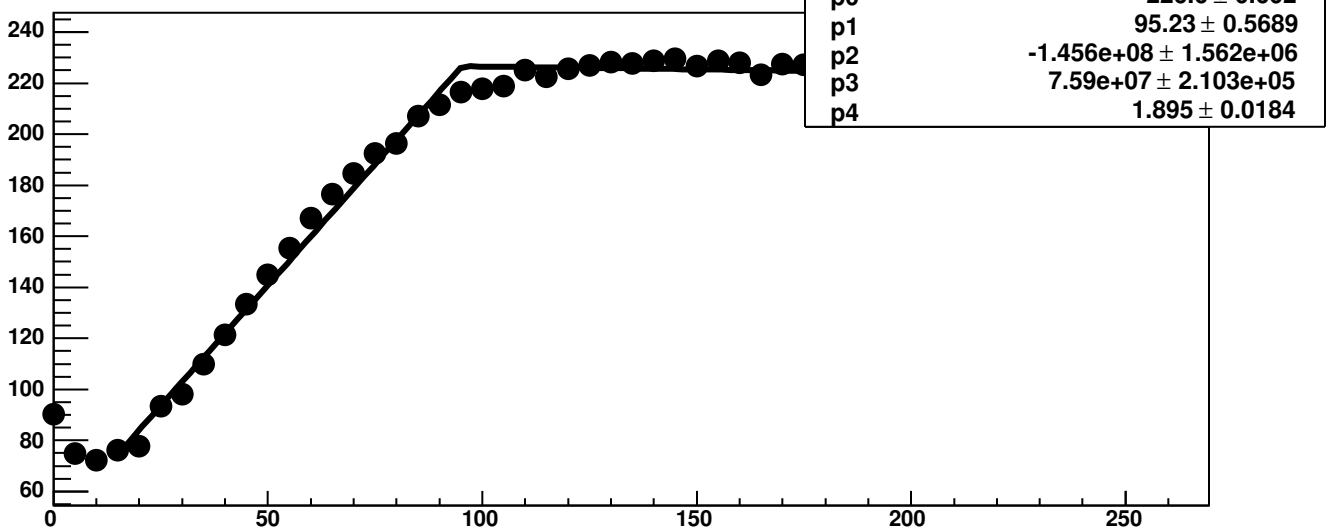
Chip 0, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold



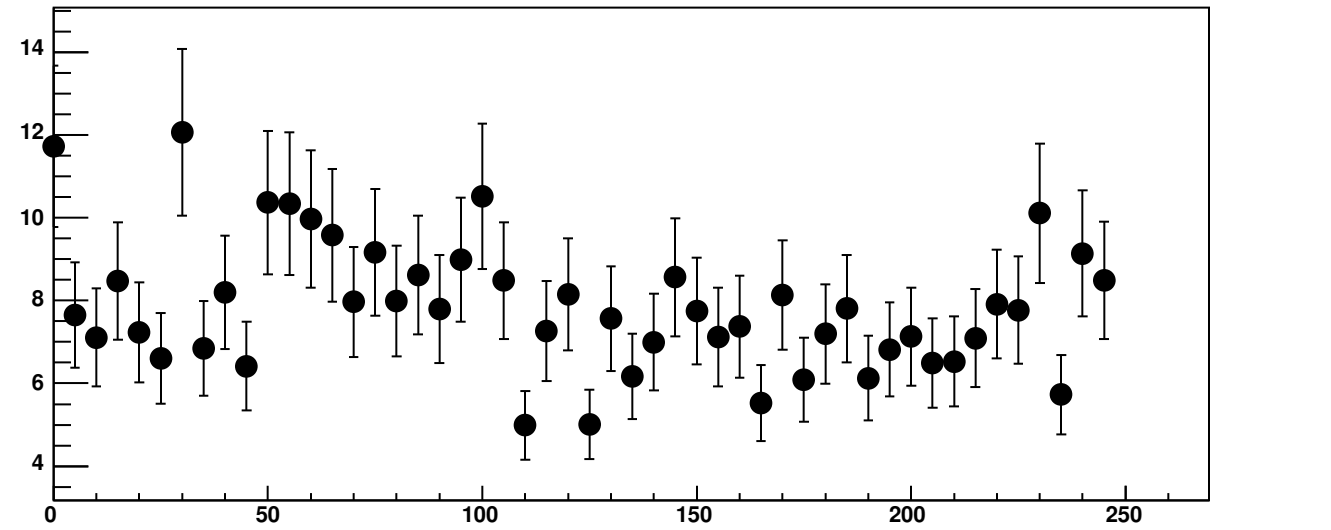
Chip 0, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold



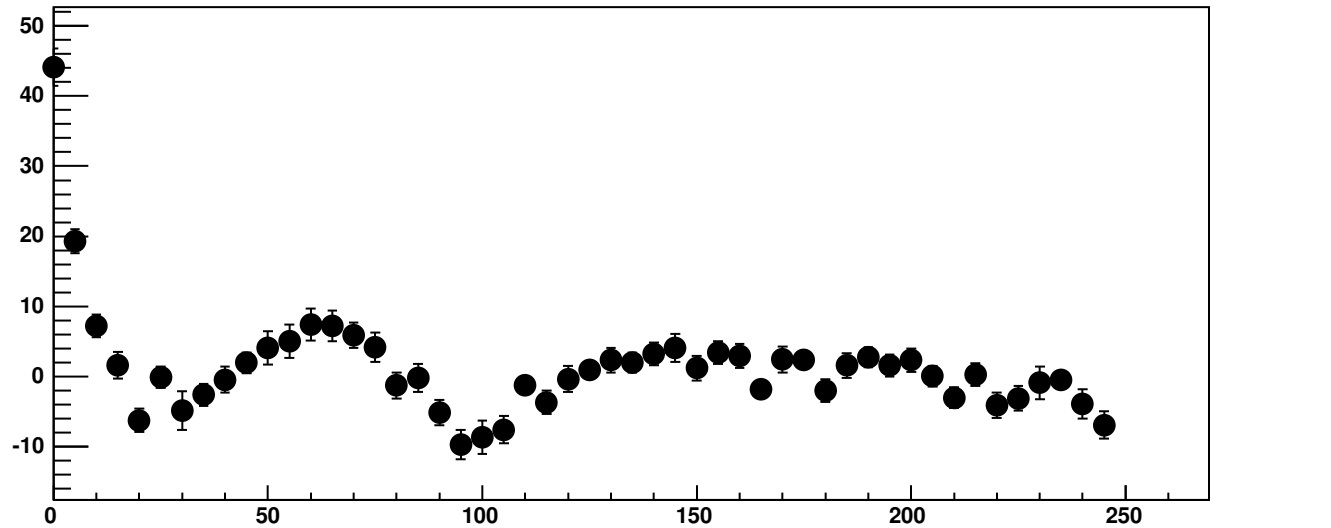
Chip 0, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

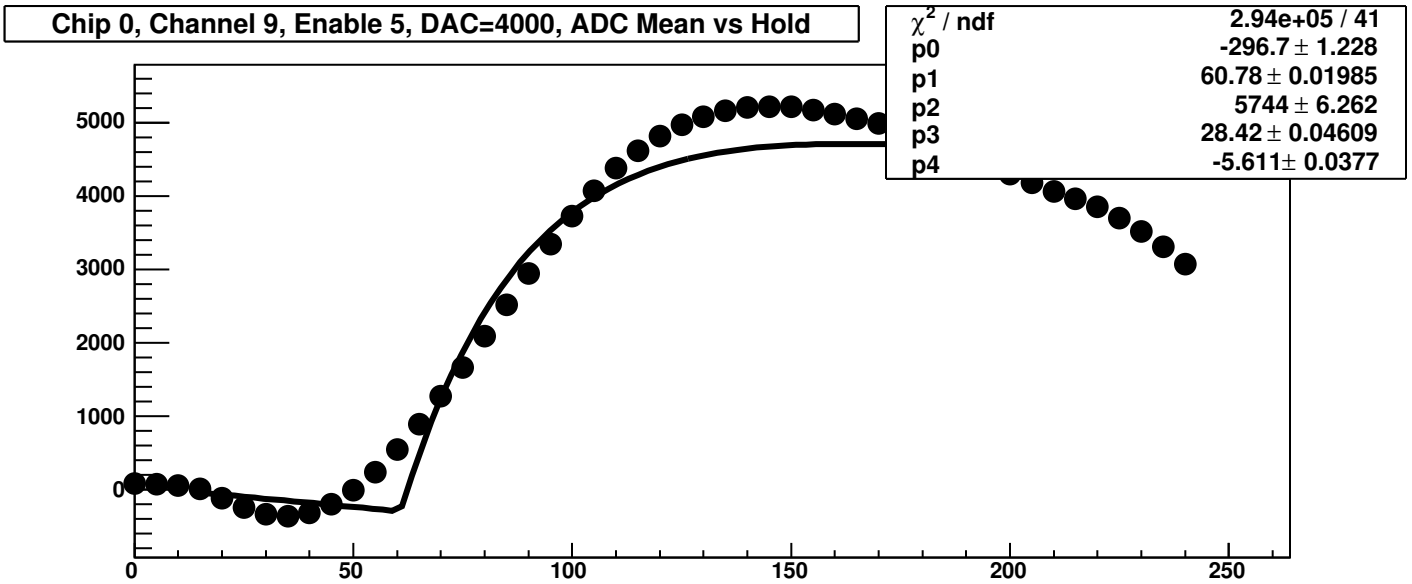


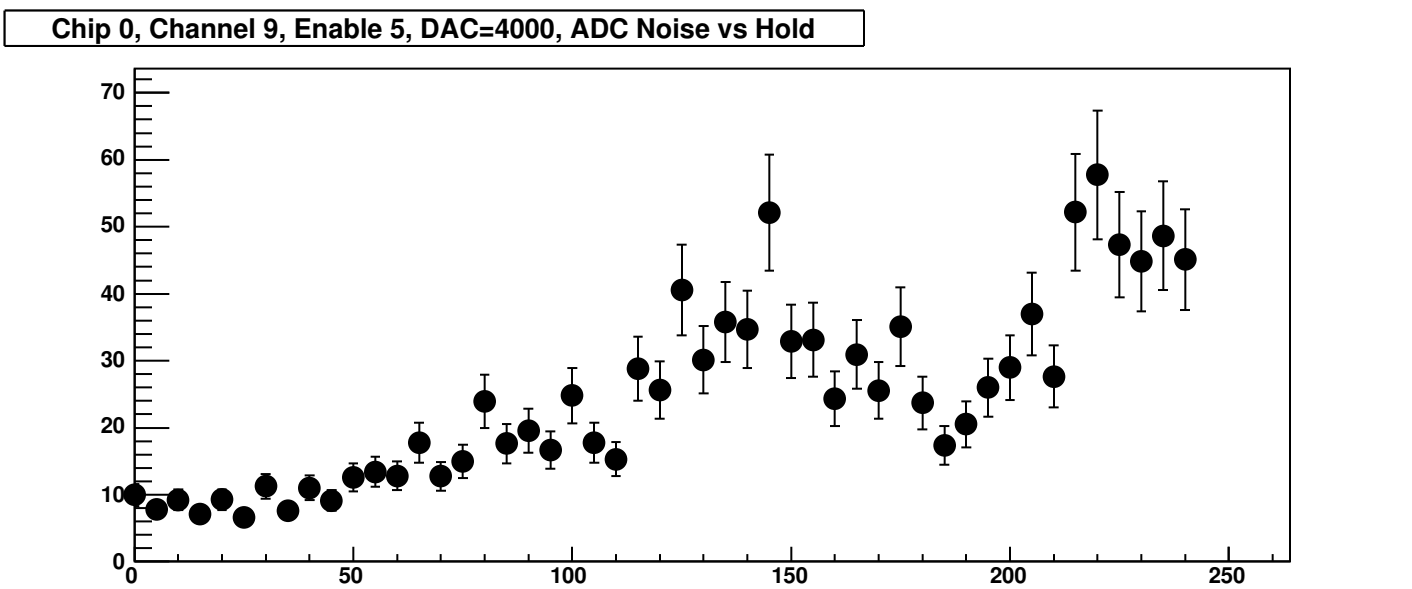
Chip 0, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold

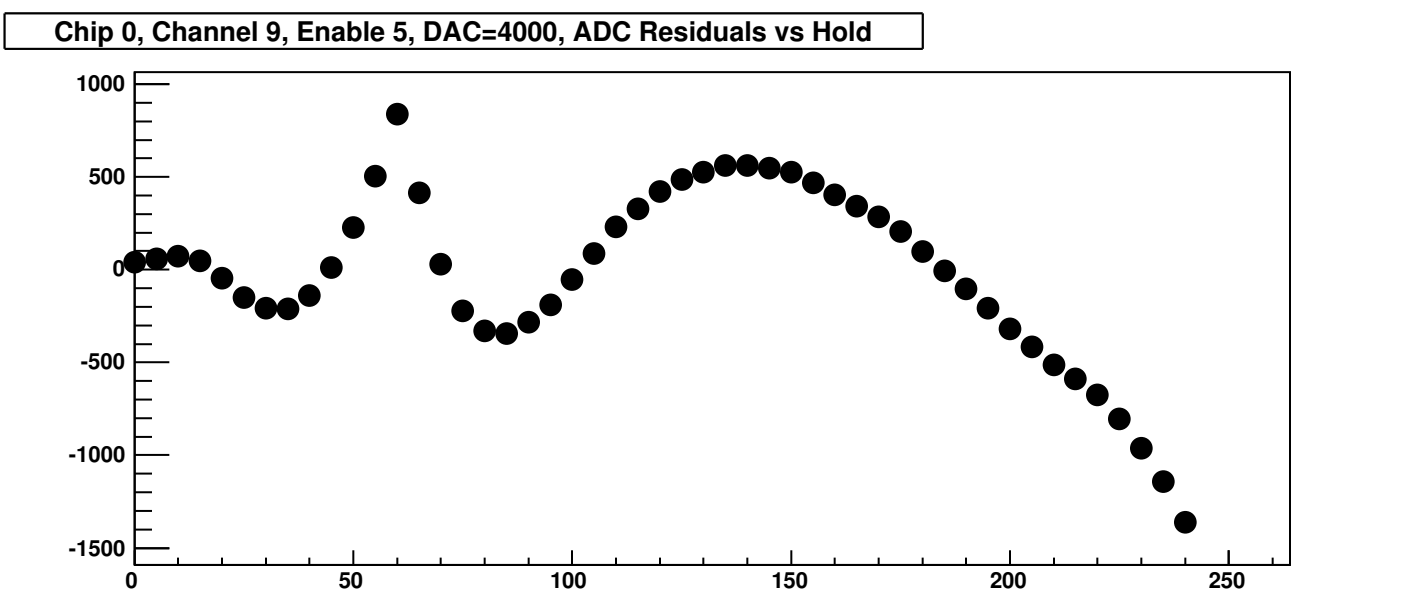


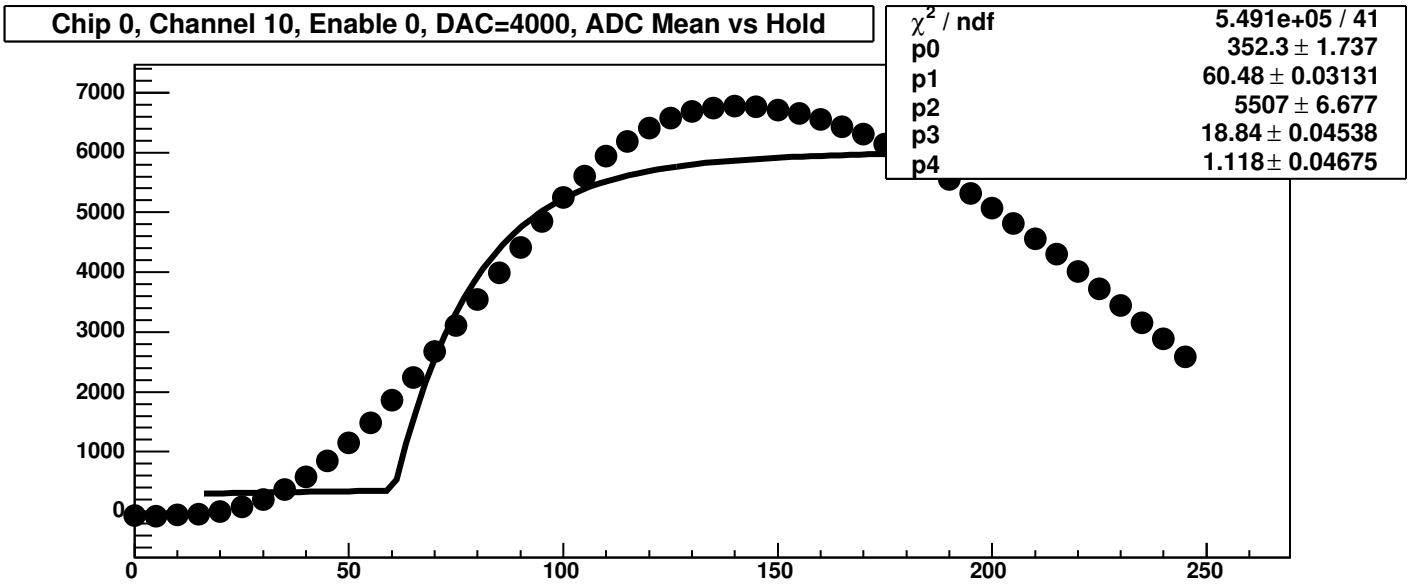
Chip 0, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

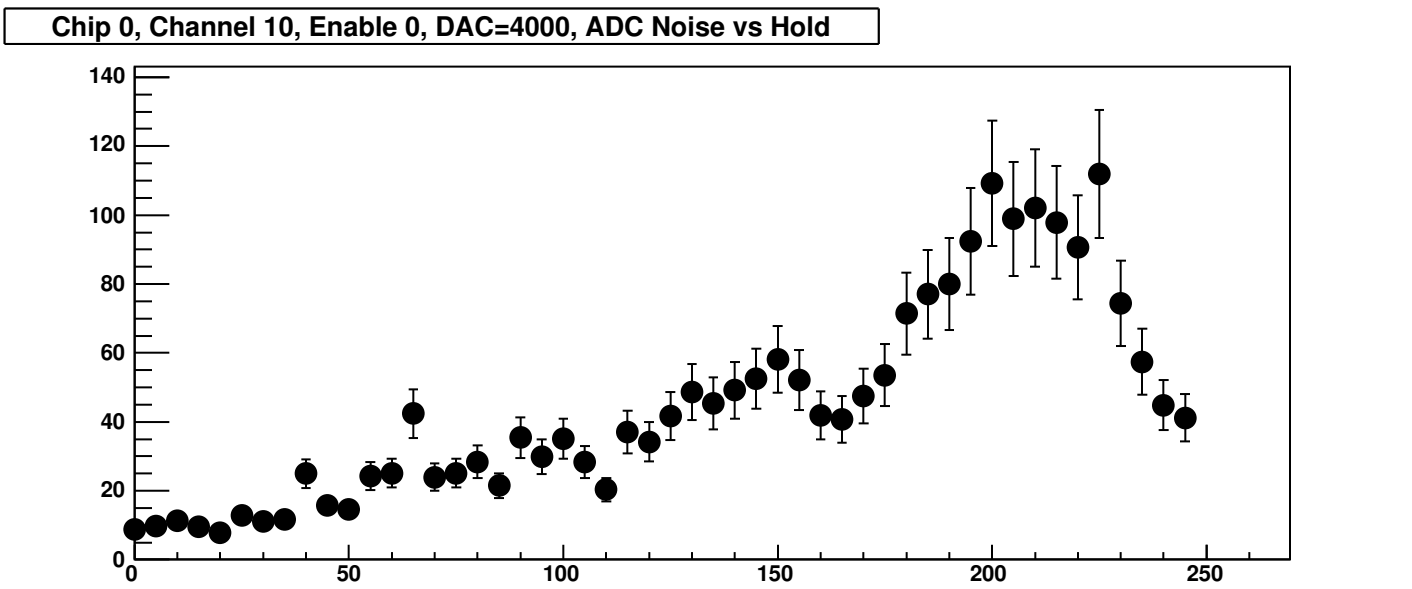


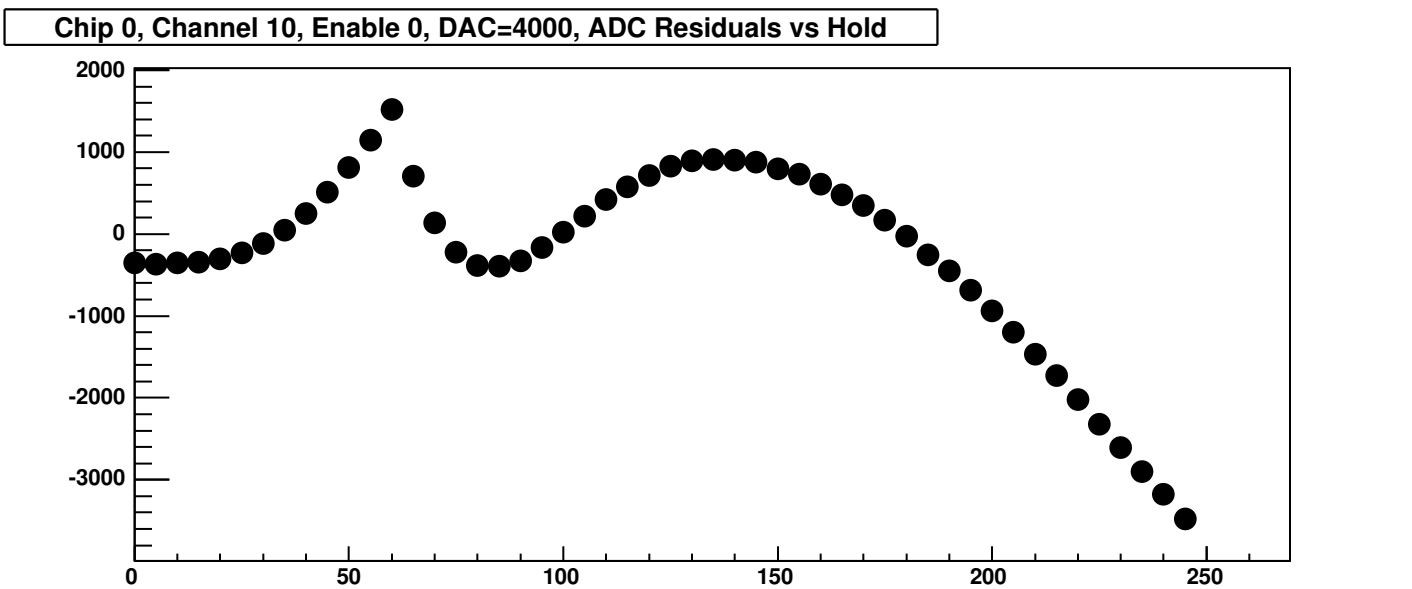

Chip 0, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.94e+05 / 41
p0 -296.7 ± 1.228
p1 60.78 ± 0.01985
p2 5744 ± 6.262
p3 28.42 ± 0.04609
p4 -5.611± 0.0377
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

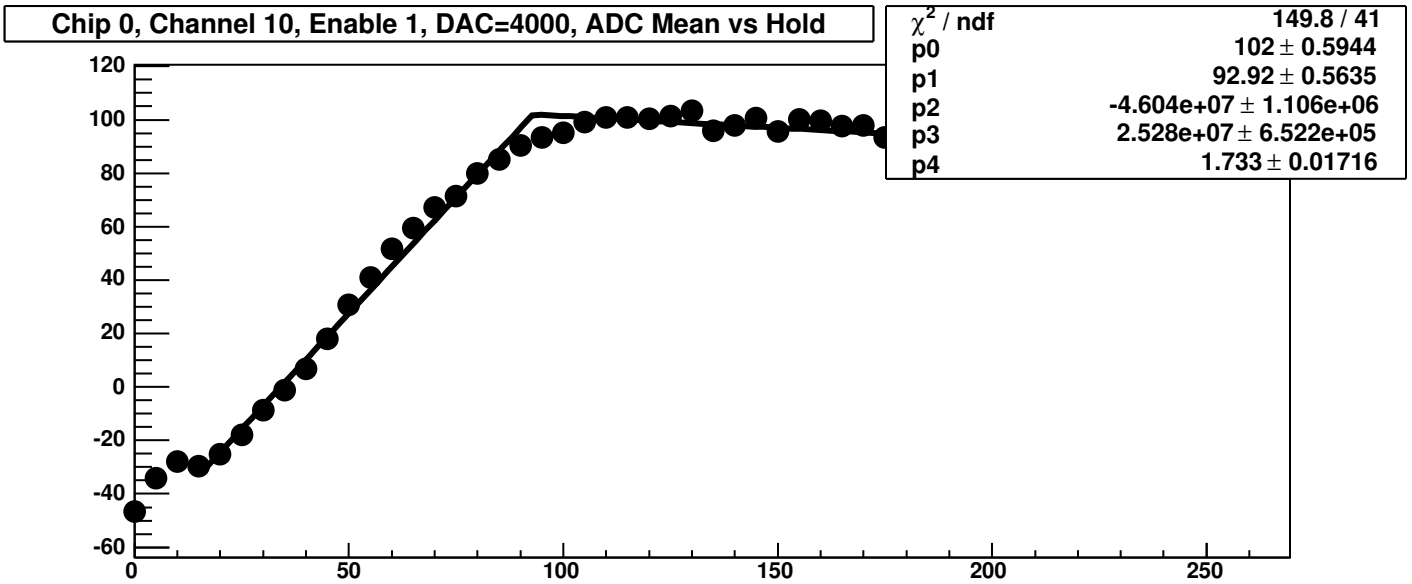

Chip 0, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold
70
60
50
40
30
20
10
0
0
50
100
150
200
250

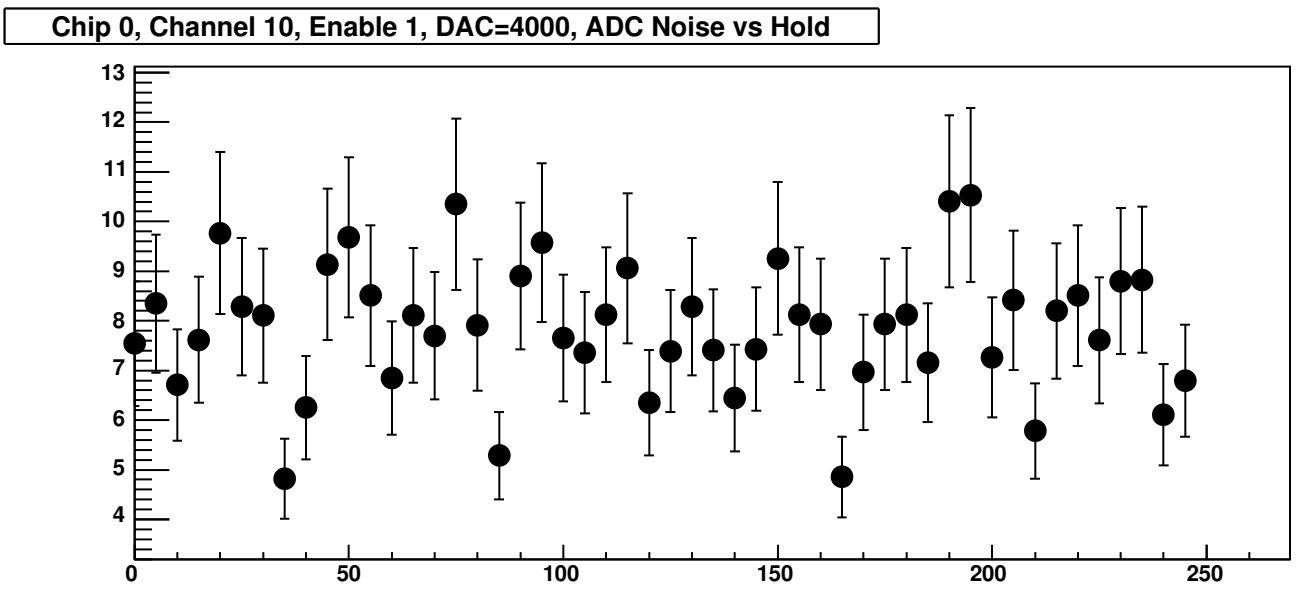

Chip 0, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

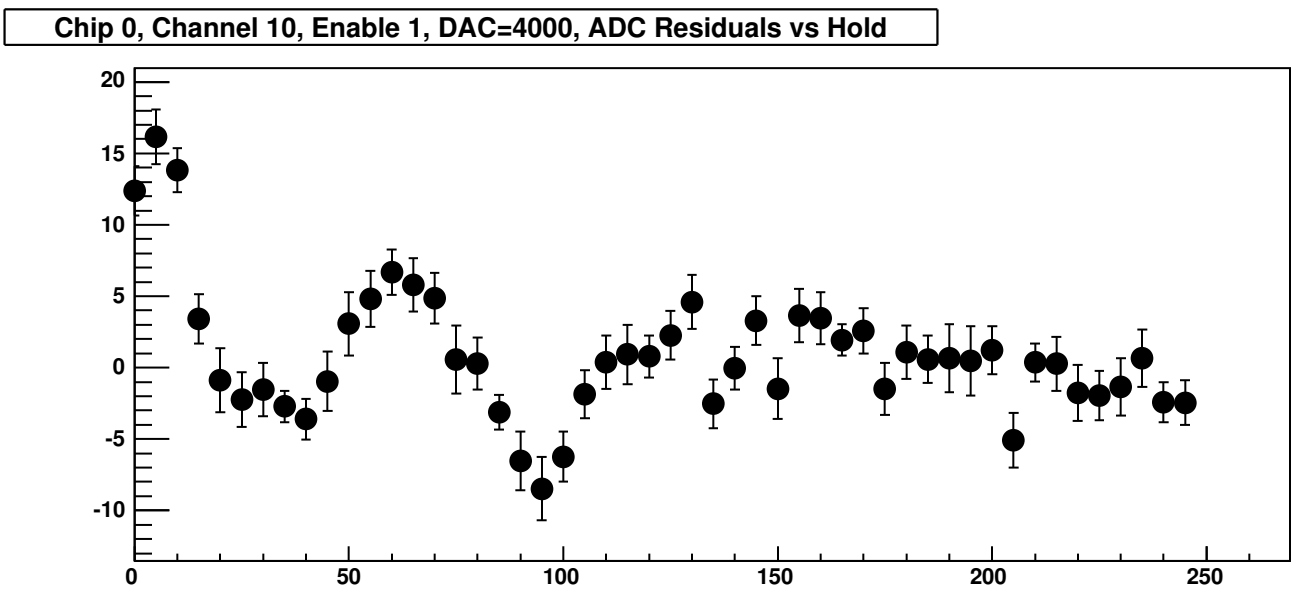

Chip 0, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.491e+05 / 41
p0 352.3 ± 1.737
p1 60.48 ± 0.03131
p2 5507 ± 6.677
p3 18.84 ± 0.04538
p4 1.118 ± 0.04675
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 0, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250

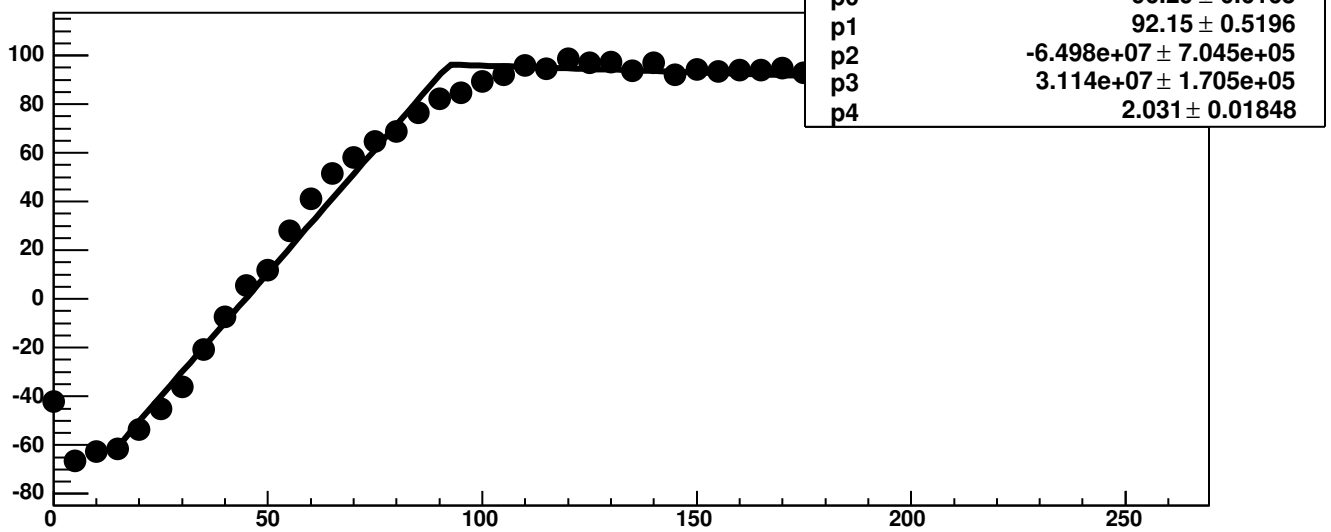

Chip 0, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250


Chip 0, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 149.8 / 41
p0 102 ± 0.5944
p1 92.92 ± 0.5635
p2 -4.604e+07 ± 1.106e+06
p3 2.528e+07 ± 6.522e+05
p4 1.733 ± 0.01716

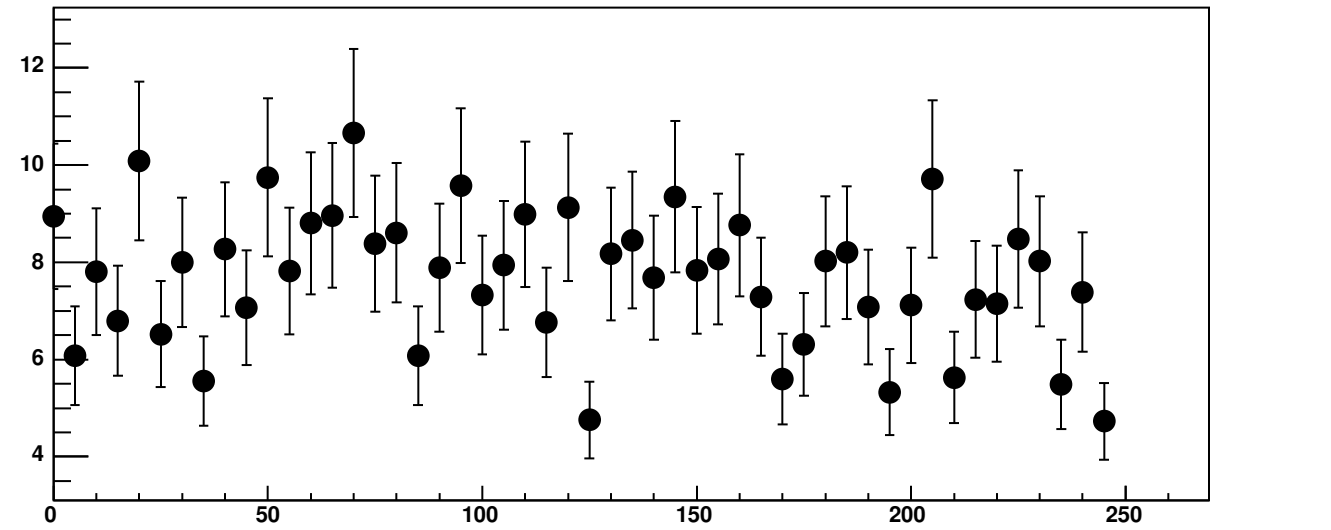

Chip 0, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold


Chip 0, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

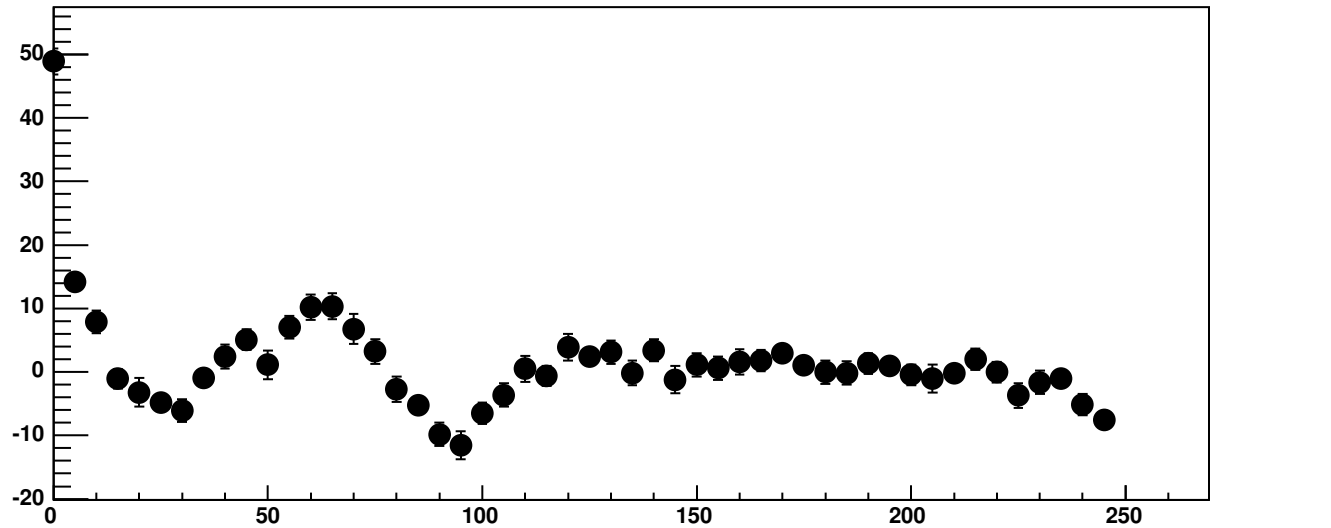
Chip 0, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold



Chip 0, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold

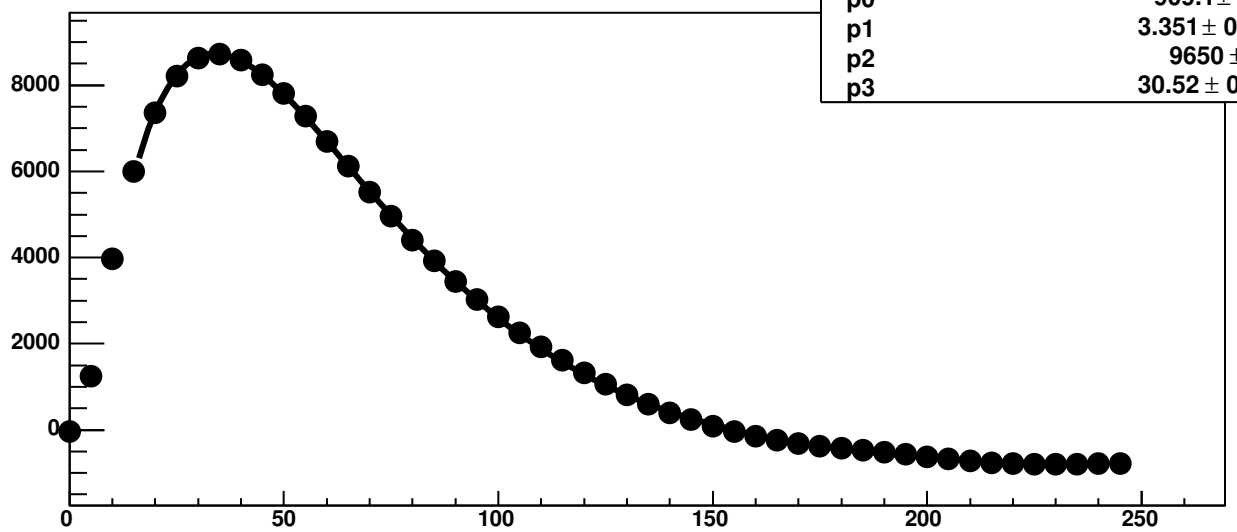


Chip 0, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

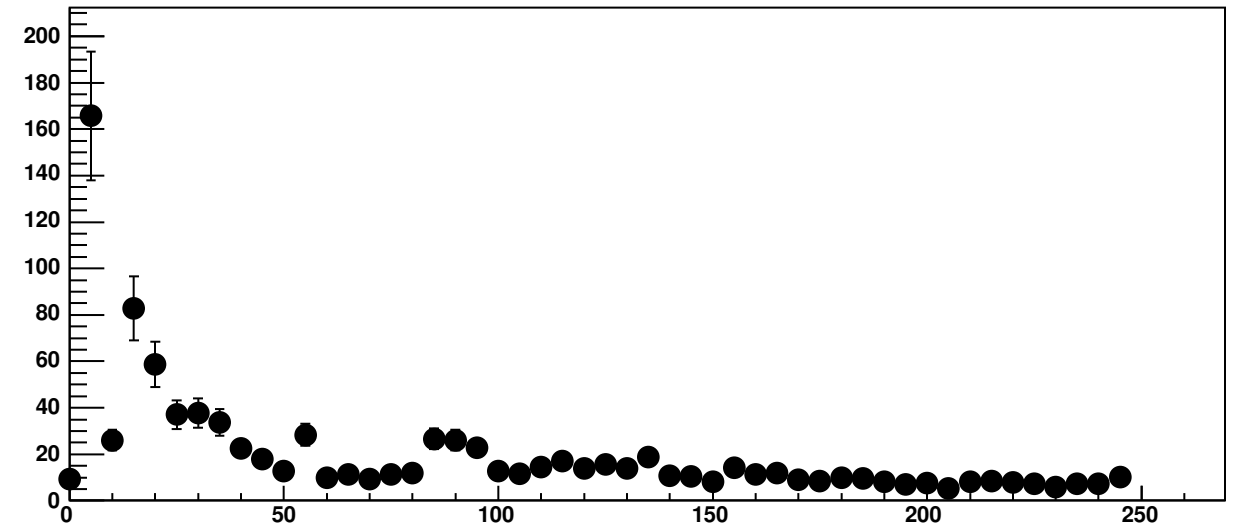


Chip 0, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold

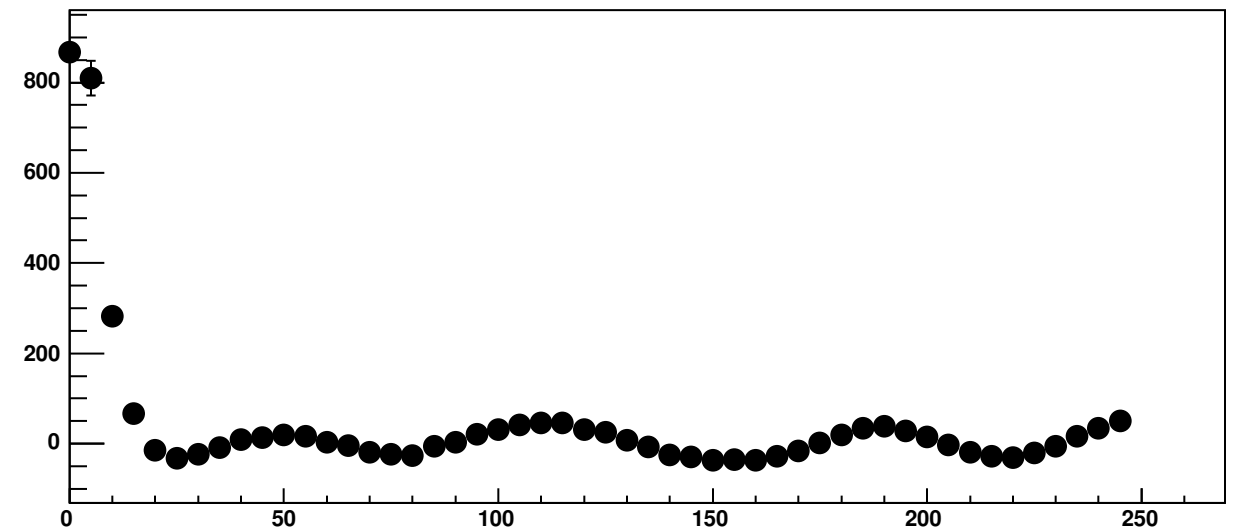
χ^2 / ndf	4722 / 42
p0	-909.1 ± 0.6269
p1	3.351 ± 0.02676
p2	9650 ± 2.389
p3	30.52 ± 0.01061



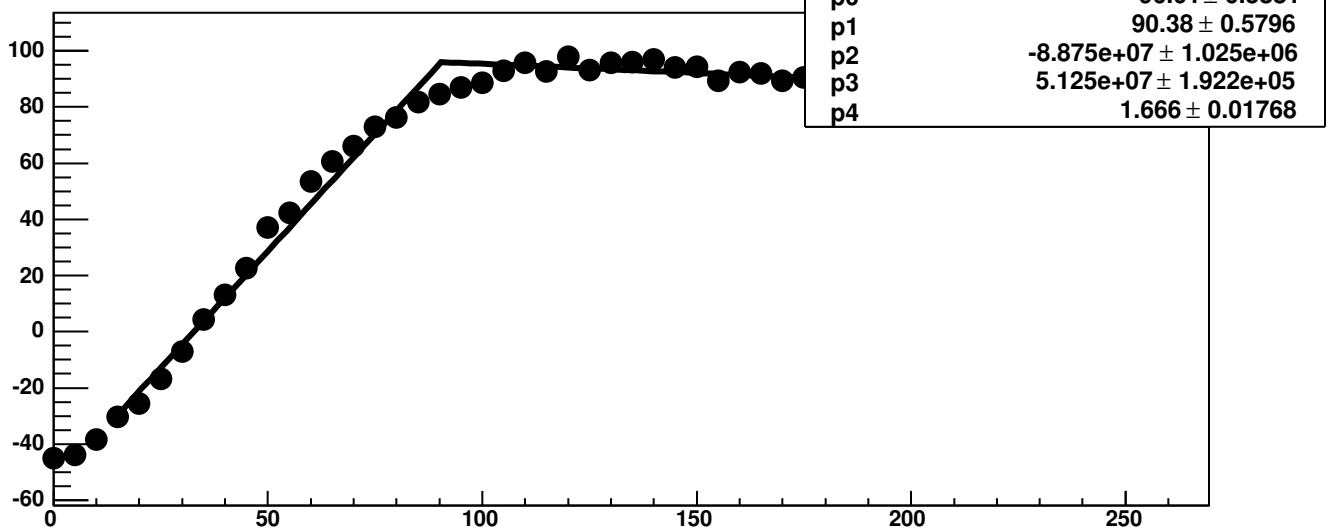
Chip 0, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold



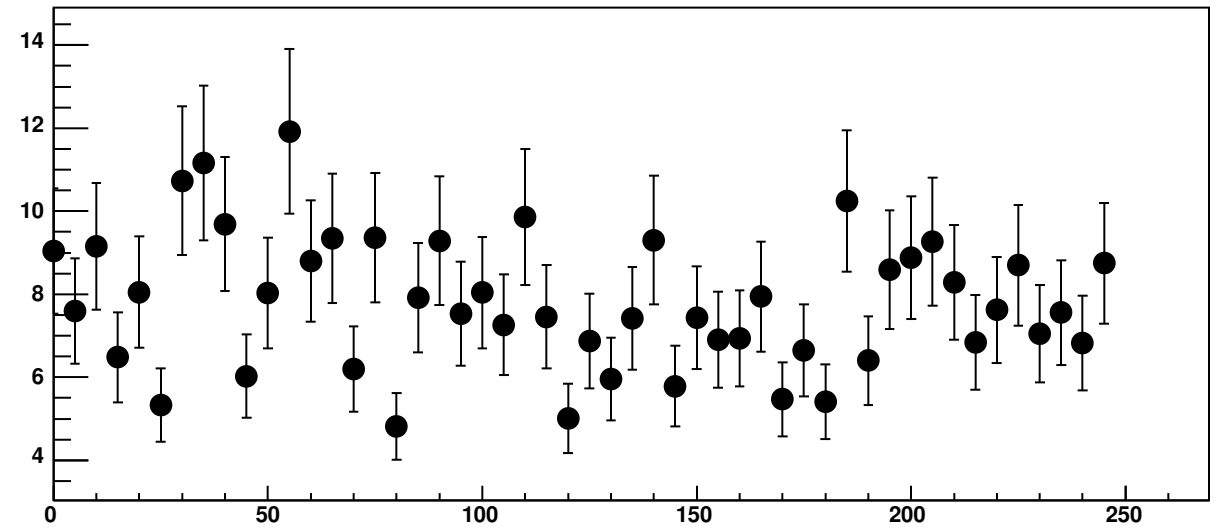
Chip 0, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold



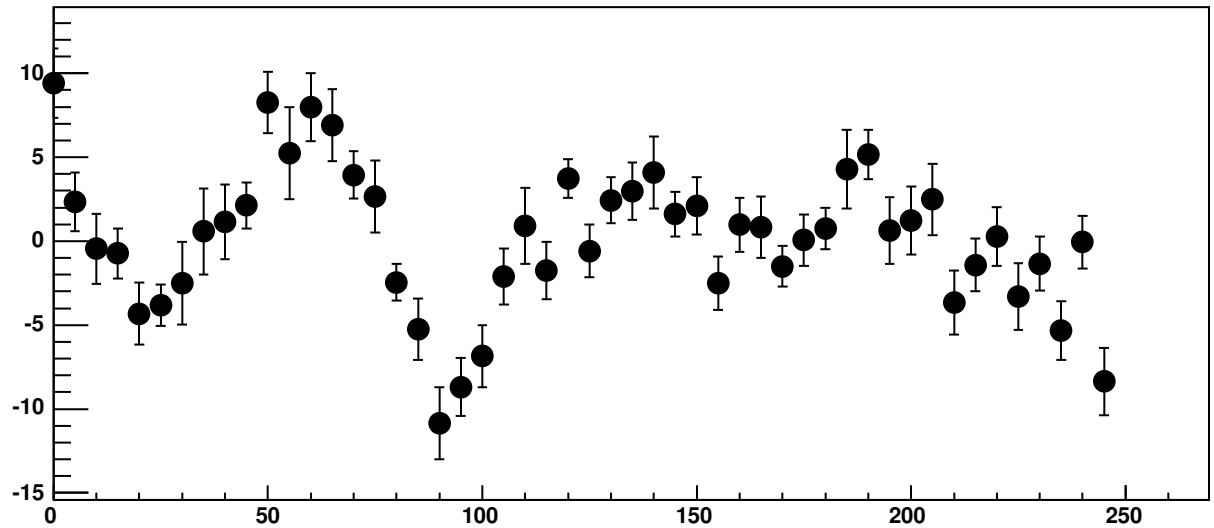
Chip 0, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold



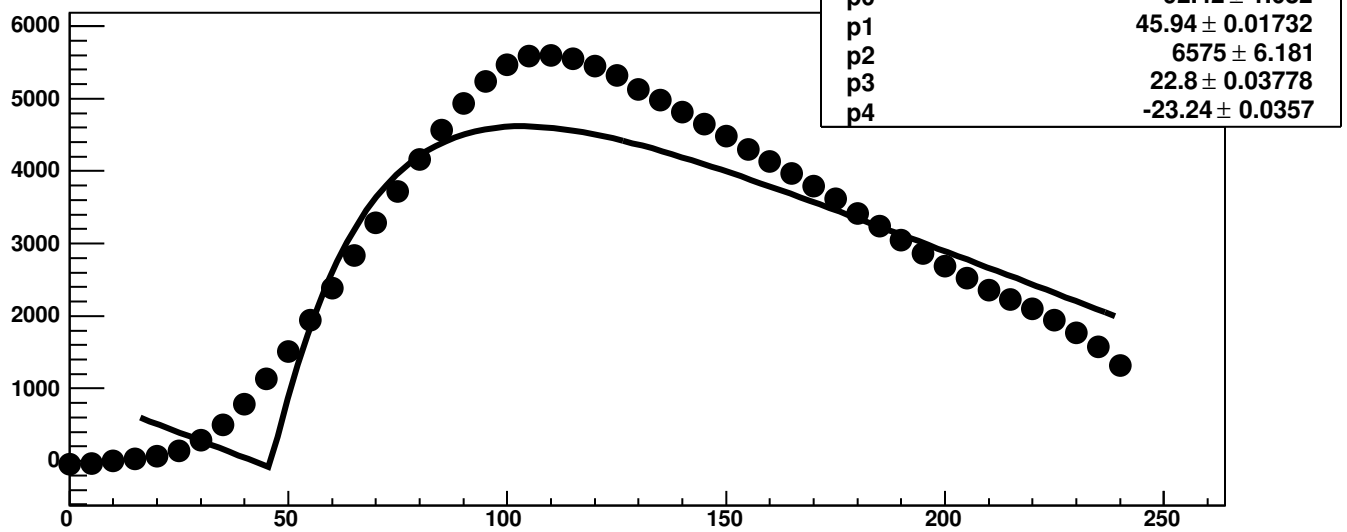
Chip 0, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



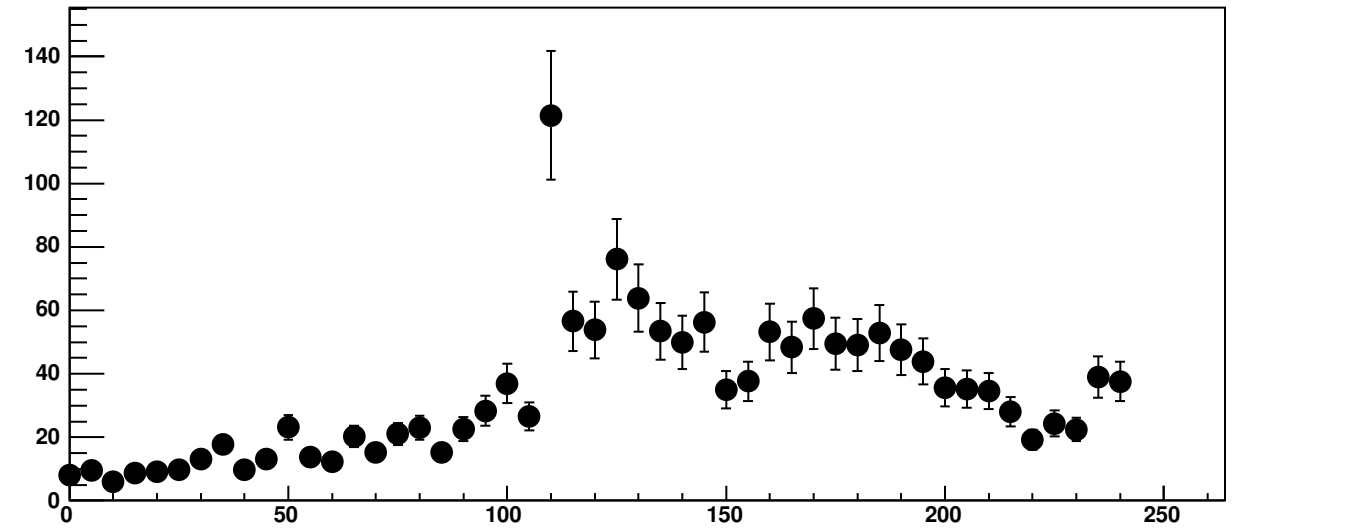
Chip 0, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold



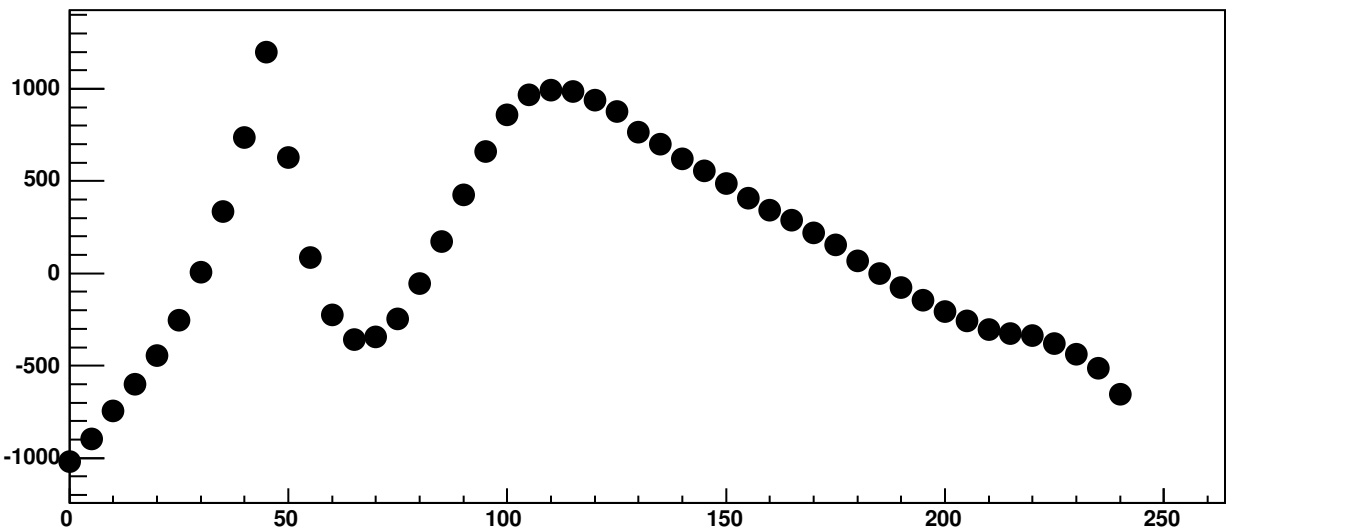
Chip 0, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold



Chip 0, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold

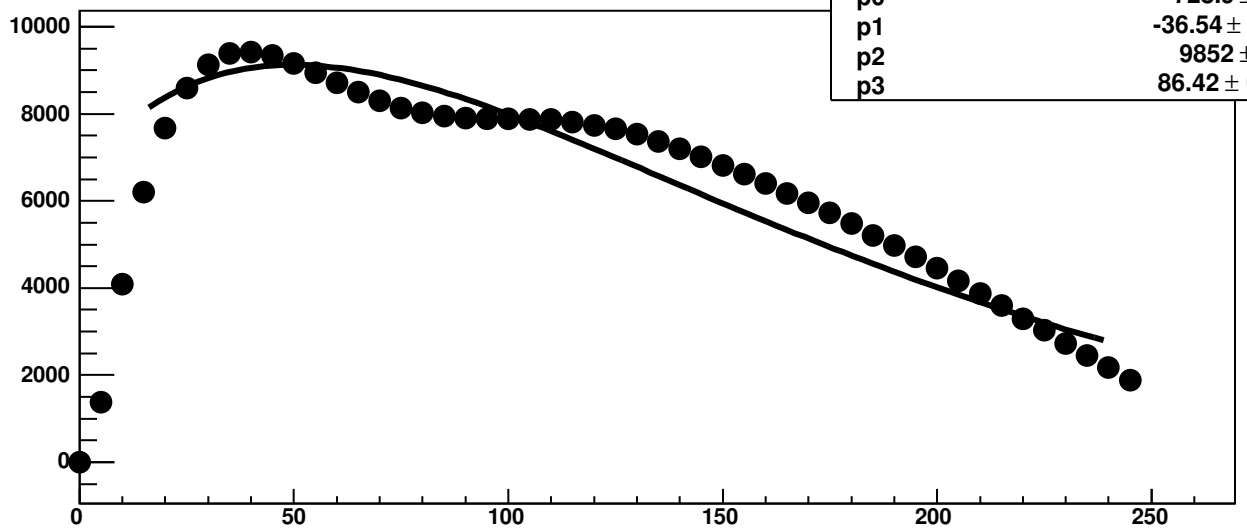


Chip 0, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

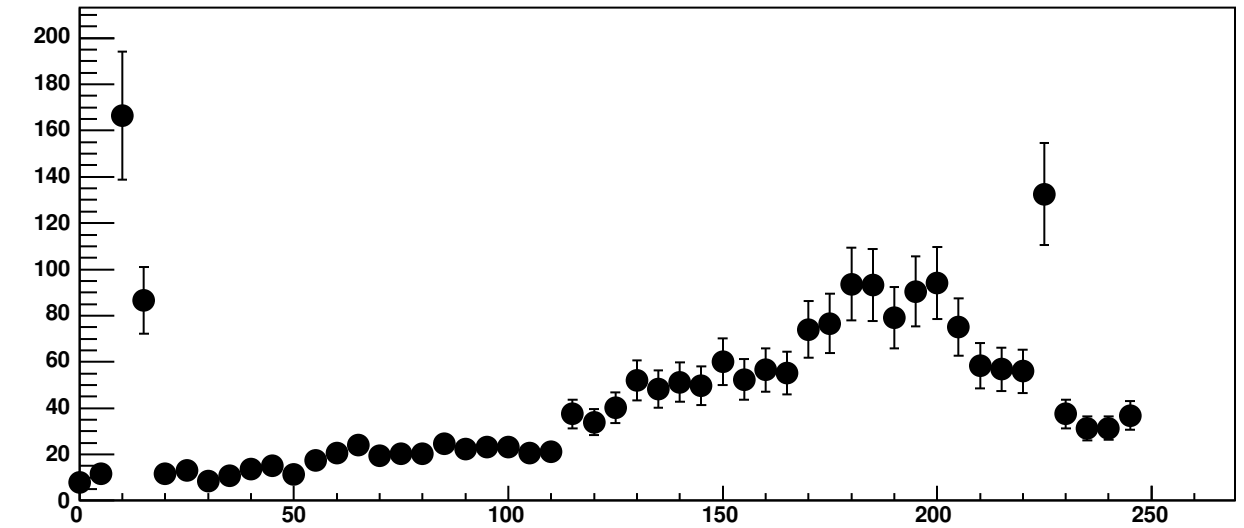


Chip 0, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold

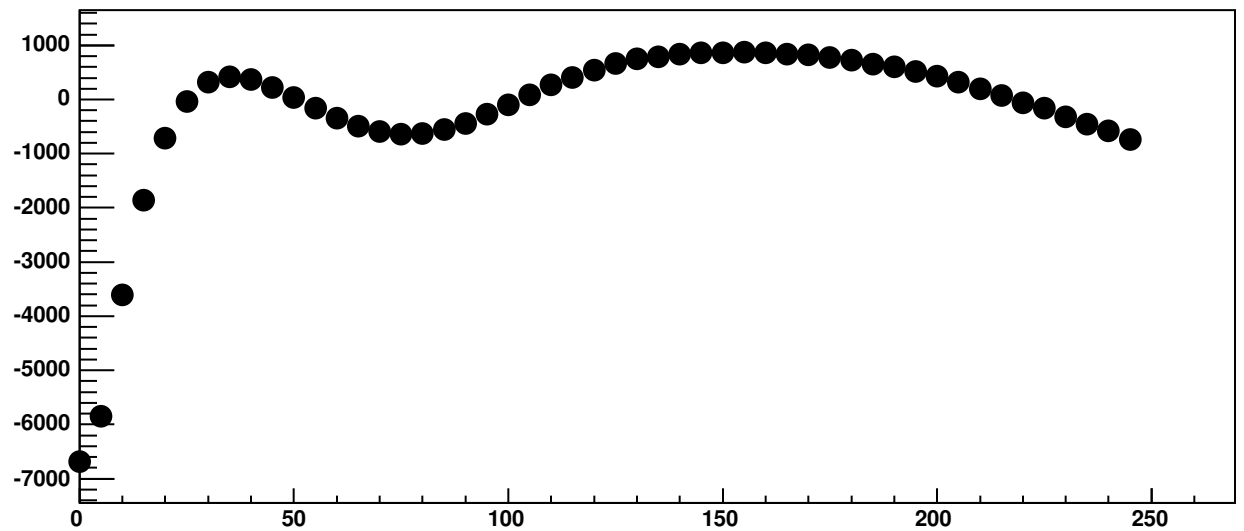
χ^2 / ndf	3.166e+05 / 42
p0	-728.9 ± 25.57
p1	-36.54 ± 0.2222
p2	9852 ± 24.75
p3	86.42 ± 0.2216

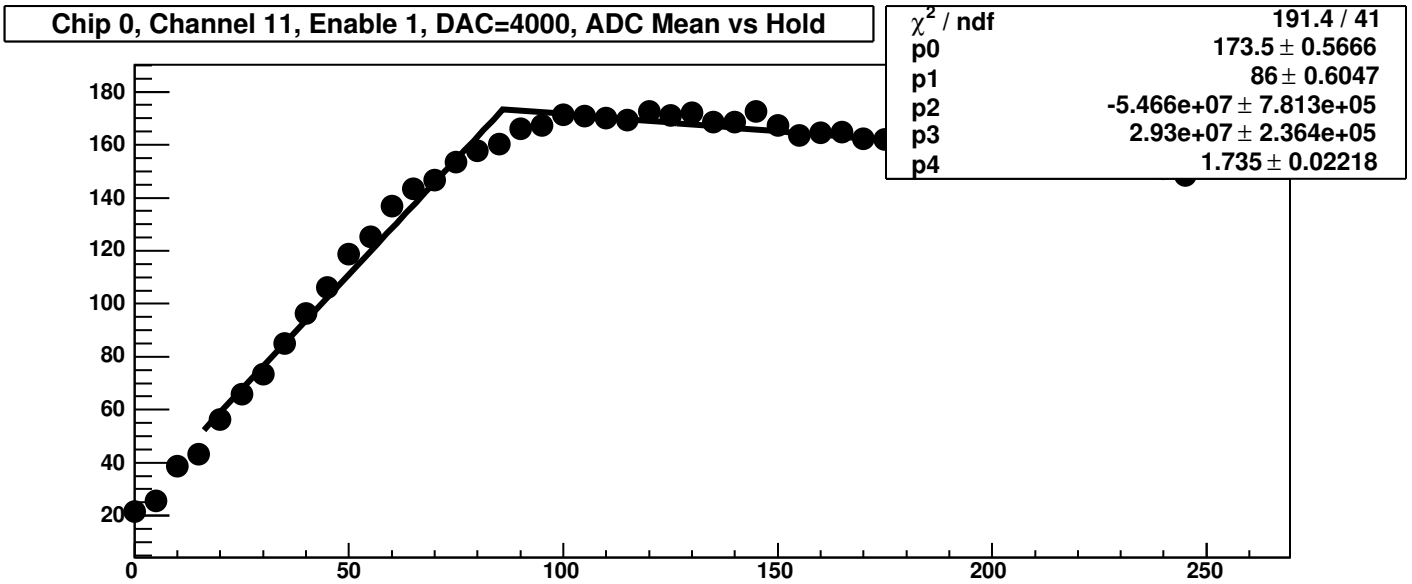


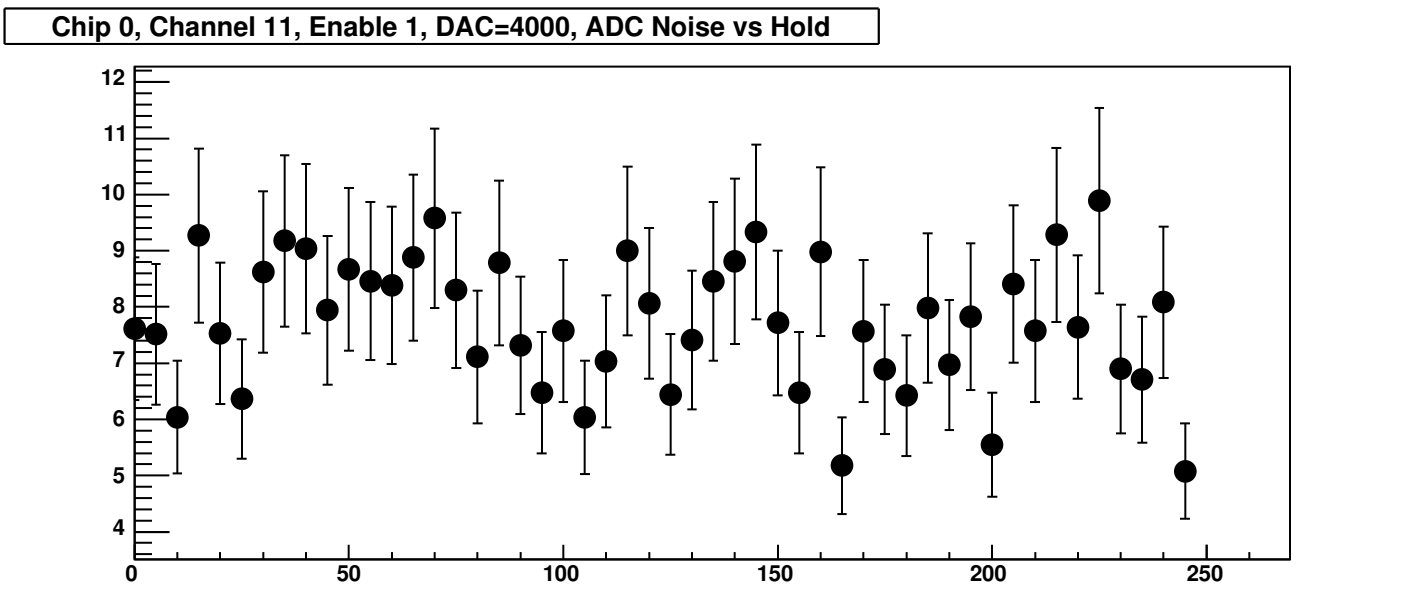
Chip 0, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold

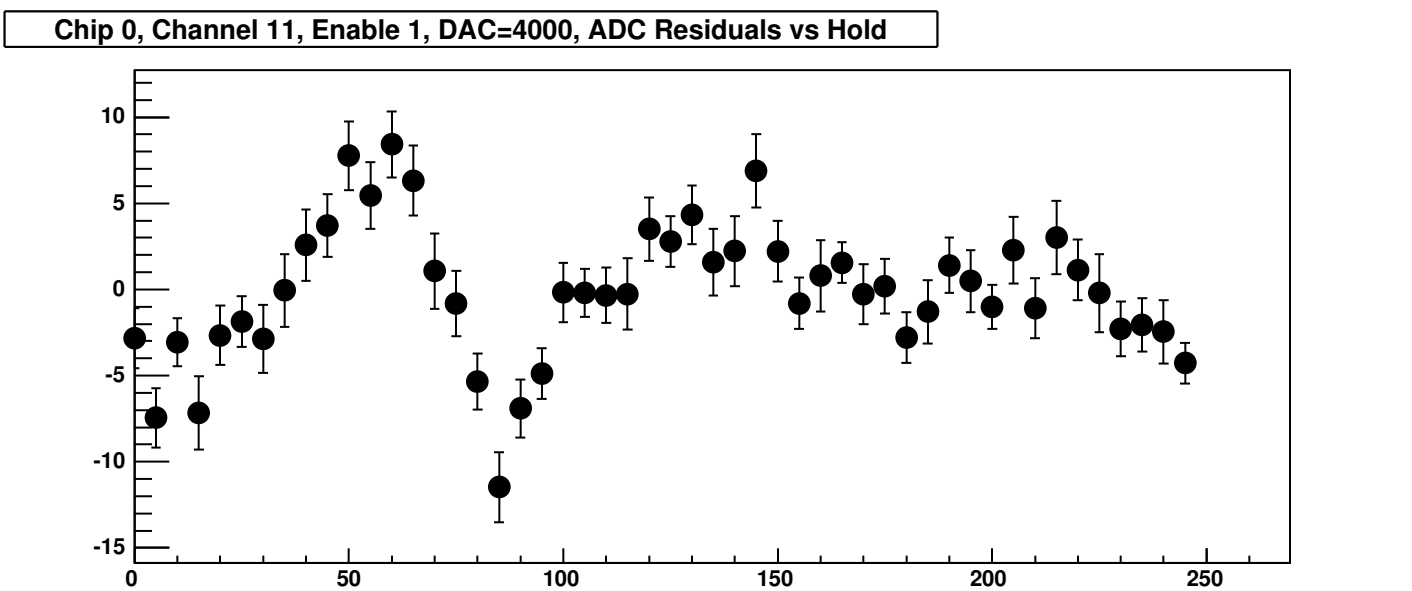


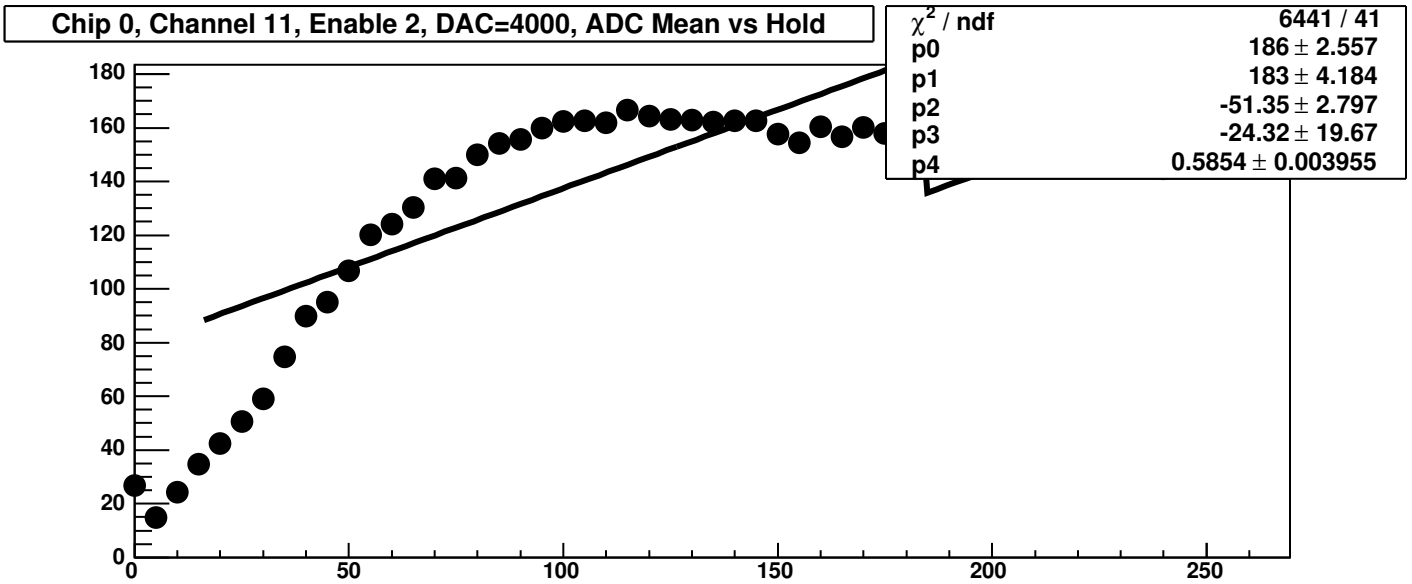
Chip 0, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold

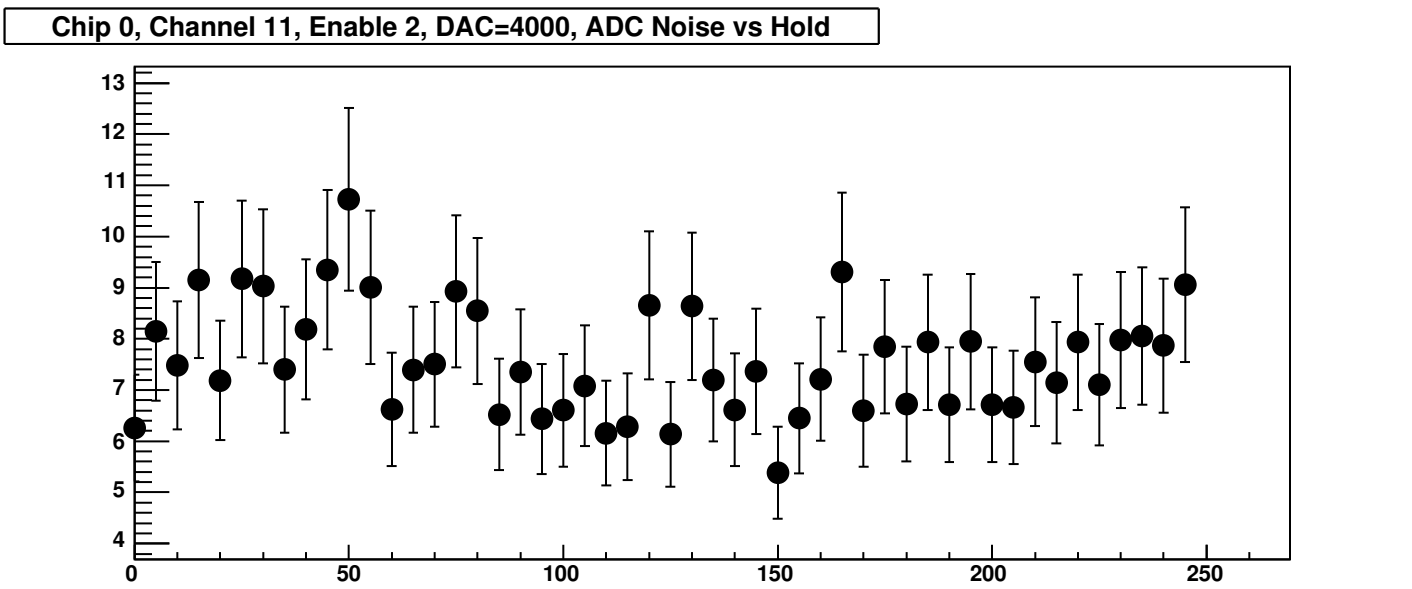


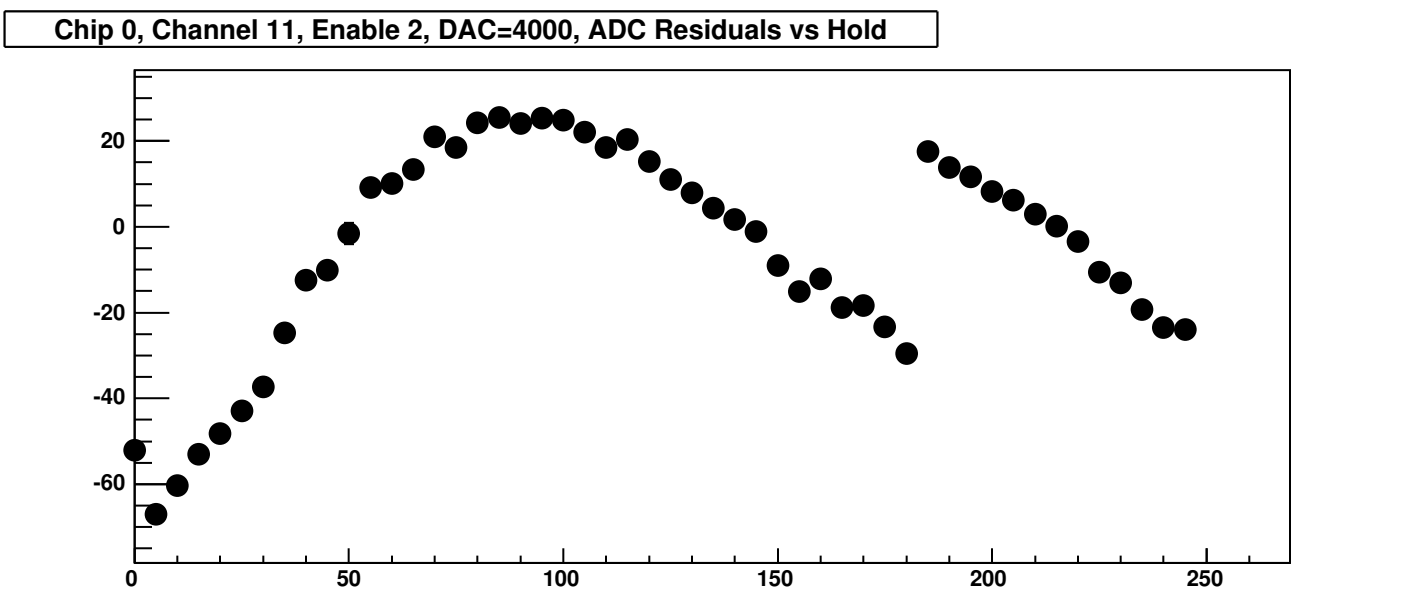

Chip 0, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 191.4 / 41
p0 173.5 ± 0.5666
p1 86 ± 0.6047
p2 -5.466e+07 ± 7.813e+05
p3 2.93e+07 ± 2.364e+05
p4 1.735 ± 0.02218
180
160
140
120
100
80
60
40
20
0
50
100
150
200
250

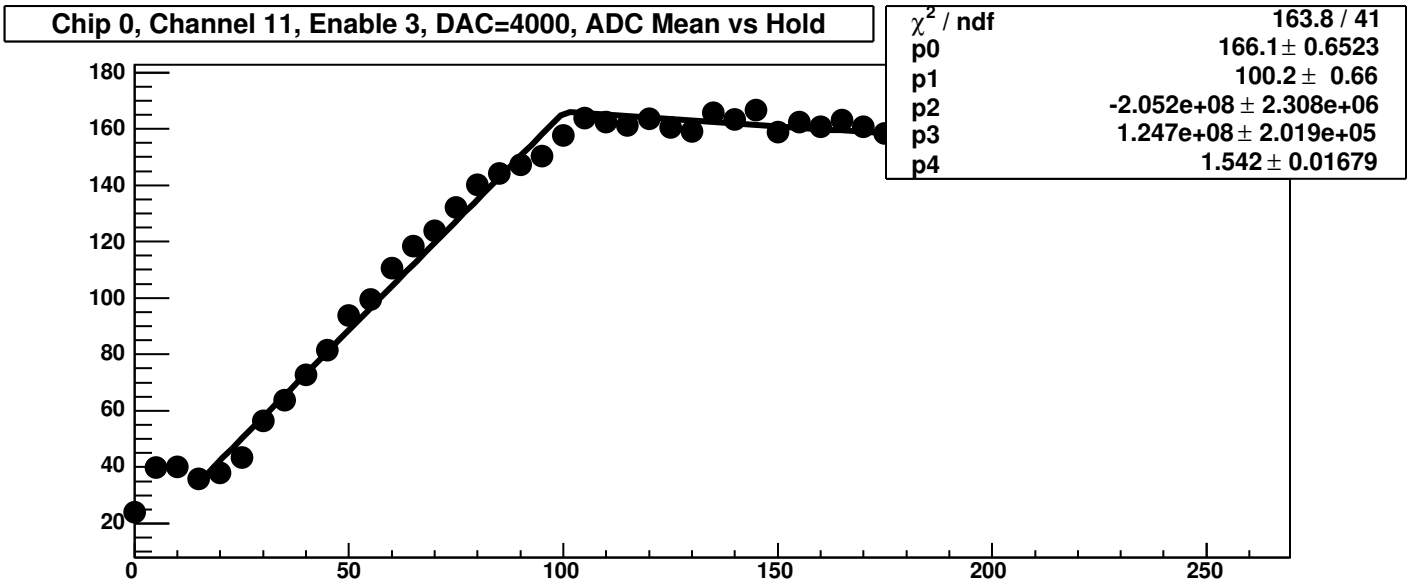

Chip 0, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

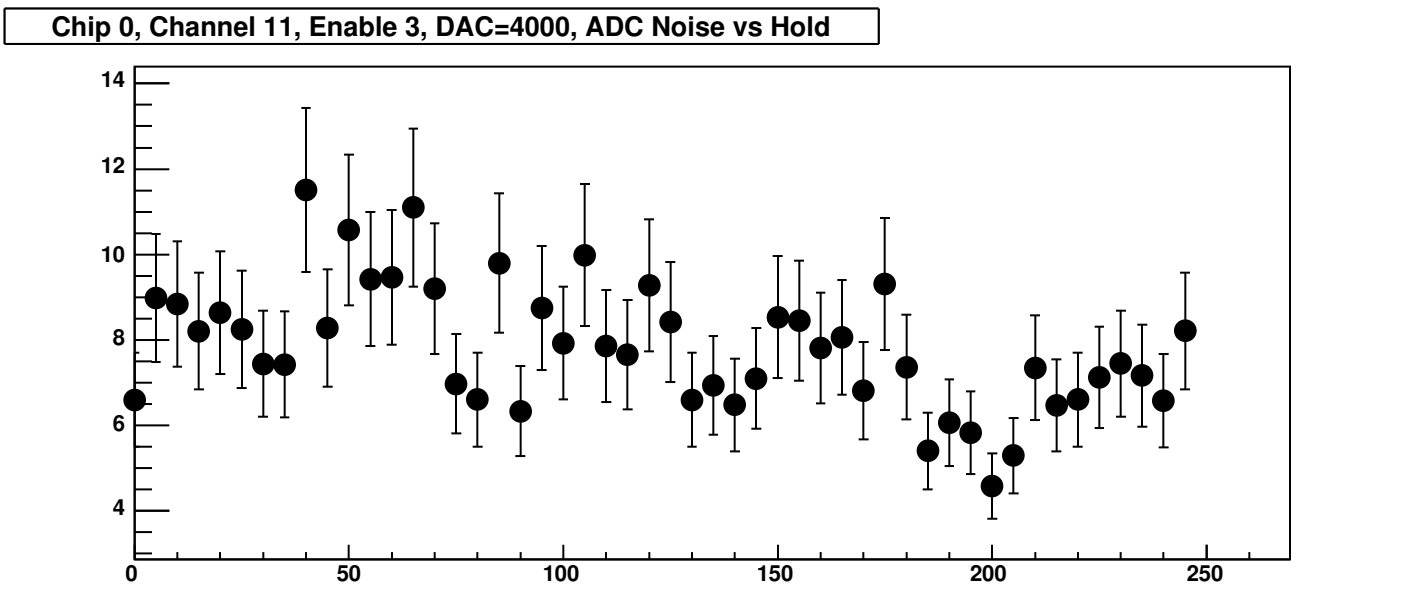

Chip 0, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
0
50
100
150
200
250

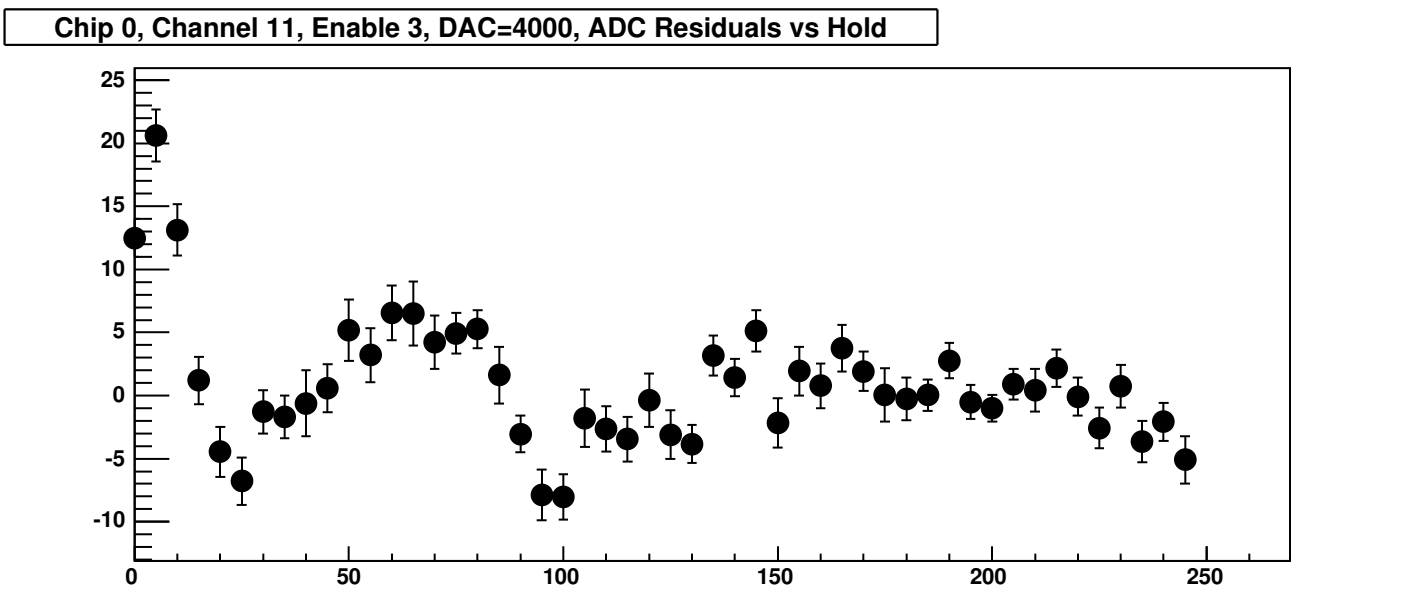

Chip 0, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 6441 / 41
p0 186 ± 2.557
p1 183 ± 4.184
p2 -51.35 ± 2.797
p3 -24.32 ± 19.67
p4 0.5854 ± 0.003955
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

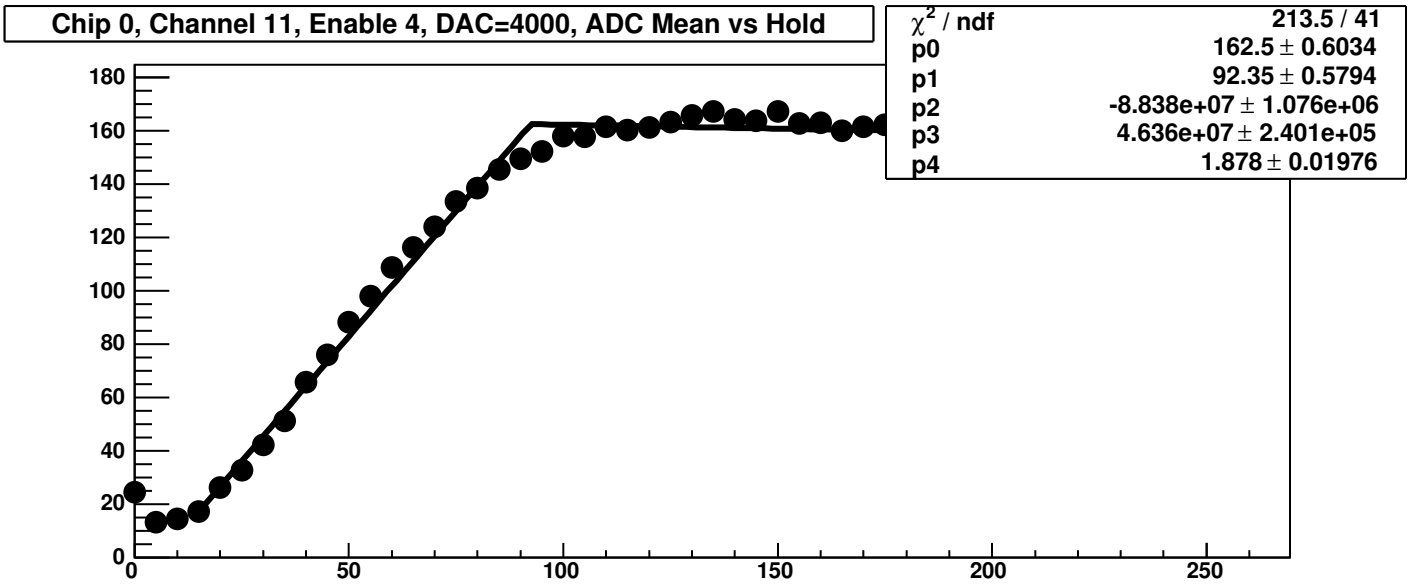

Chip 0, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

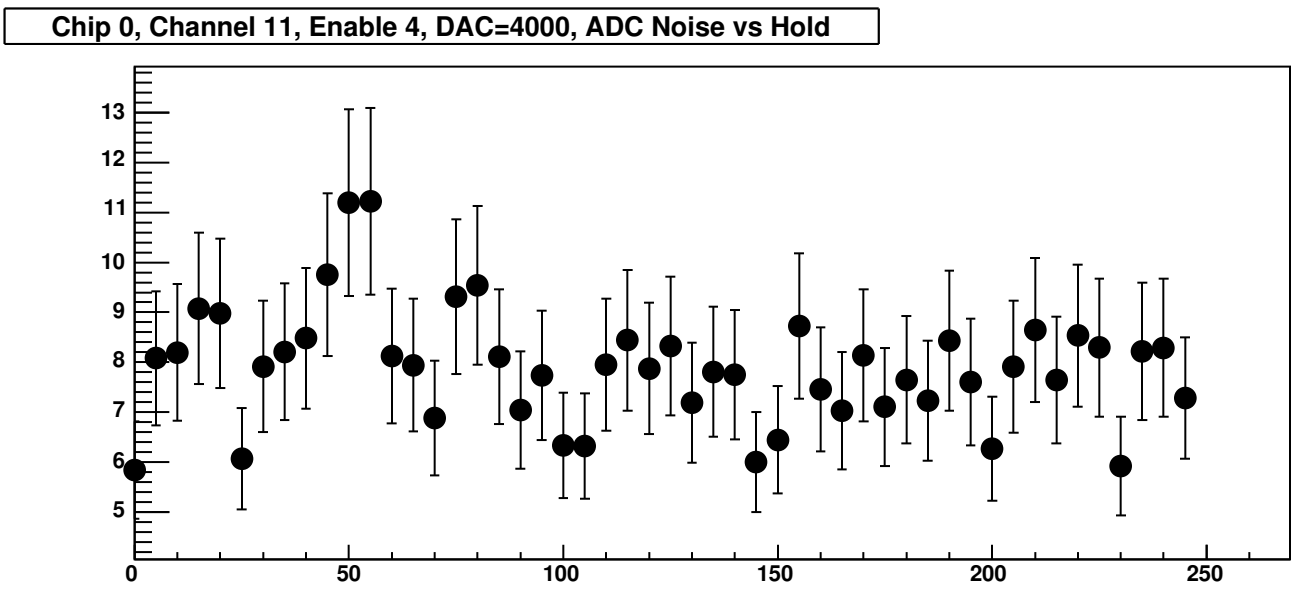

Chip 0, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
0
50
100
150
200
250

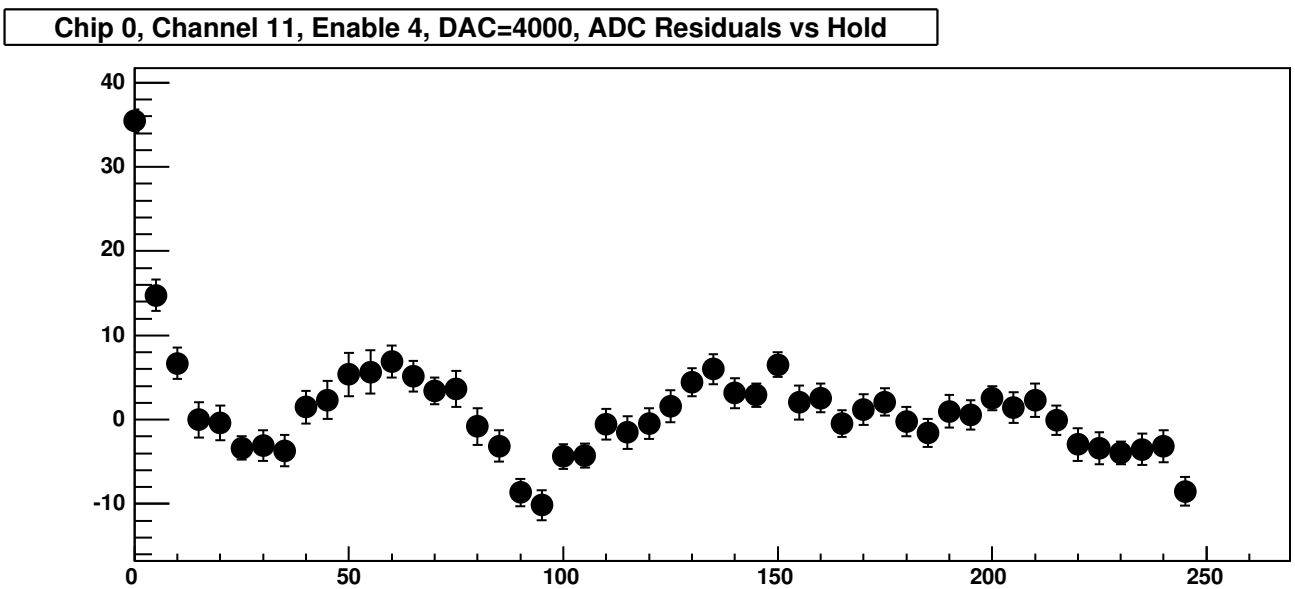

Chip 0, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 163.8 / 41
p0 166.1 ± 0.6523
p1 100.2 ± 0.66
p2 -2.052e+08 ± 2.308e+06
p3 1.247e+08 ± 2.019e+05
p4 1.542 ± 0.01679

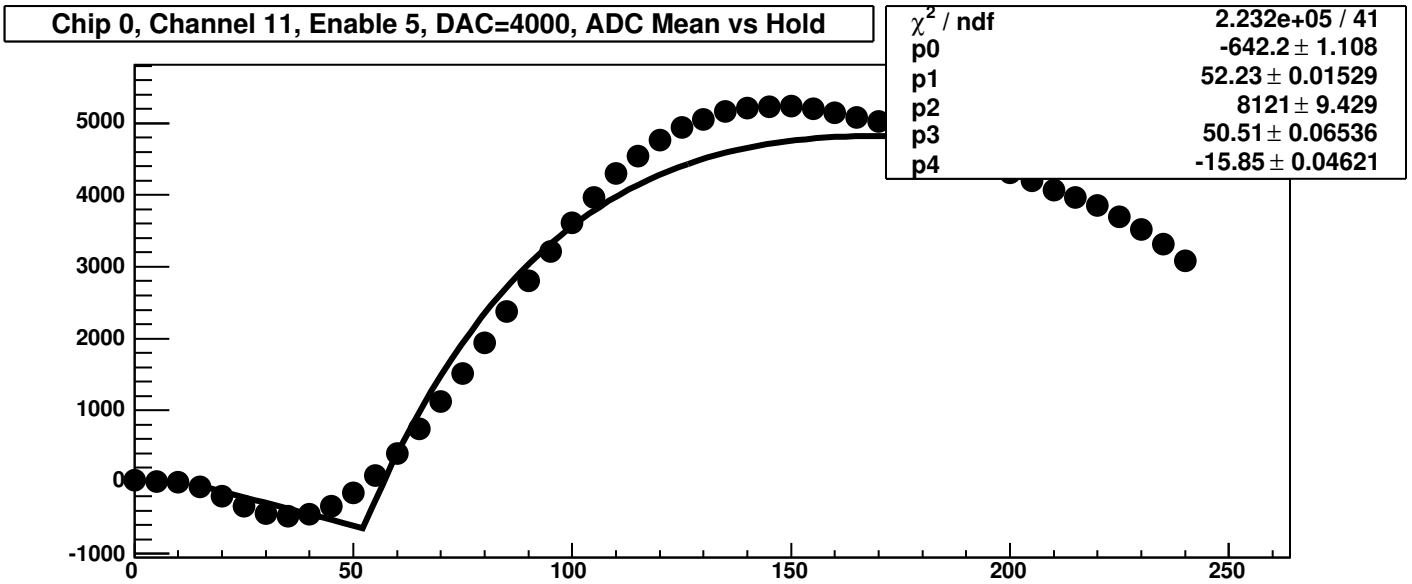

Chip 0, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold

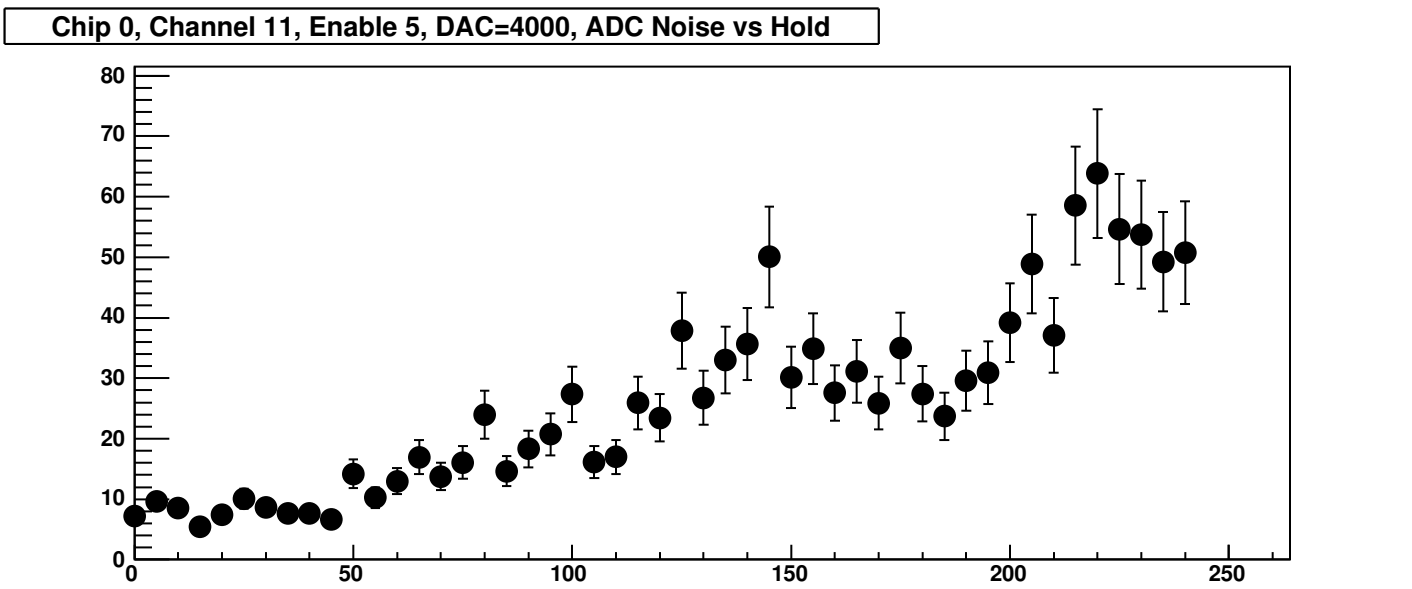

Chip 0, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold

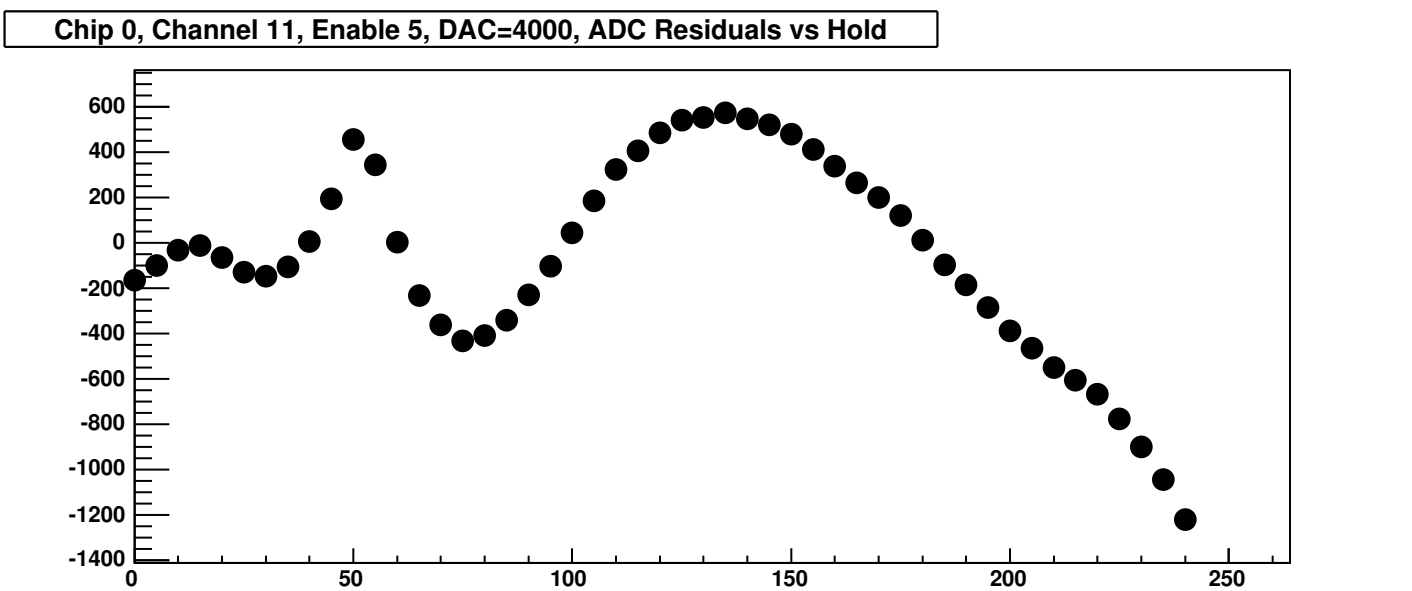

Chip 0, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 213.5 / 41
p0 162.5 ± 0.6034
p1 92.35 ± 0.5794
p2 -8.838e+07 ± 1.076e+06
p3 4.636e+07 ± 2.401e+05
p4 1.878 ± 0.01976

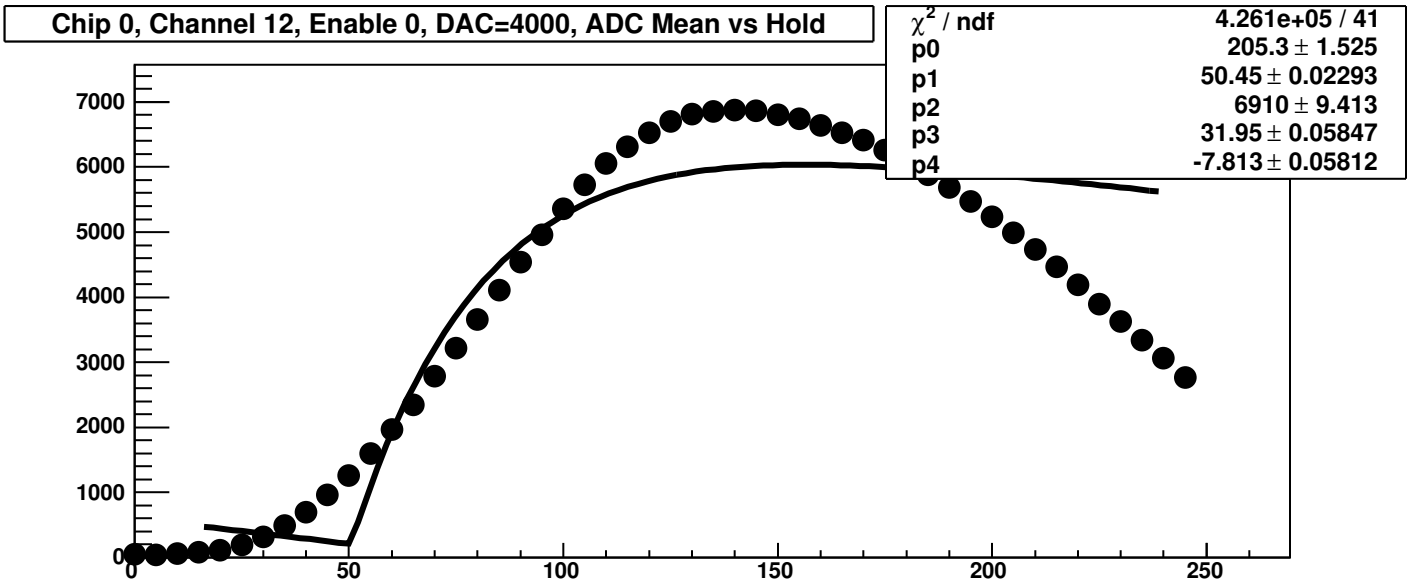

Chip 0, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold

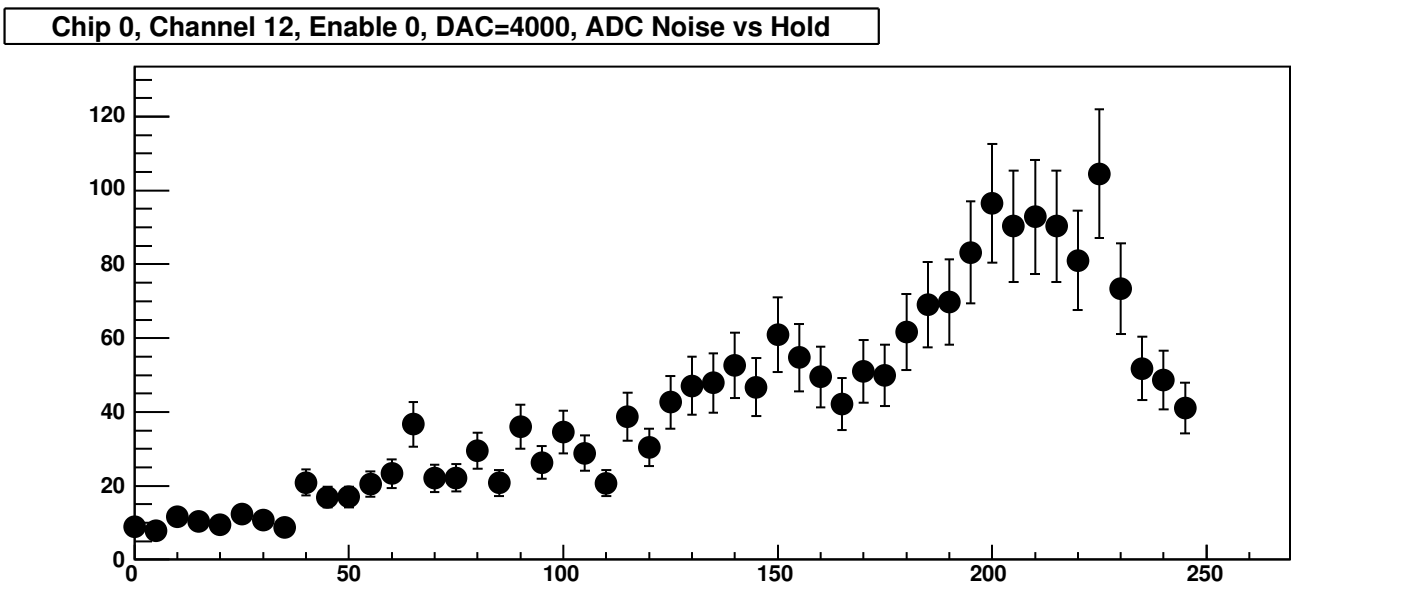

Chip 0, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

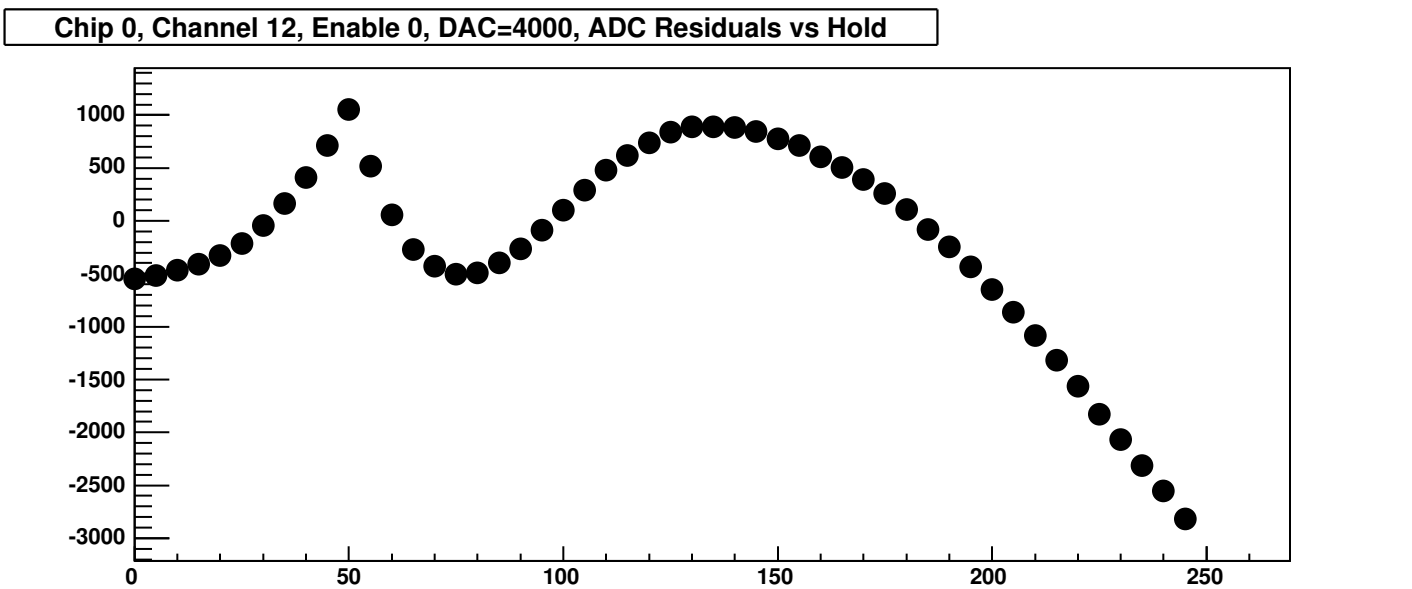

Chip 0, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.232e+05 / 41
p0 -642.2 ± 1.108
p1 52.23 ± 0.01529
p2 8121 ± 9.429
p3 50.51 ± 0.06536
p4 -15.85 ± 0.04621
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

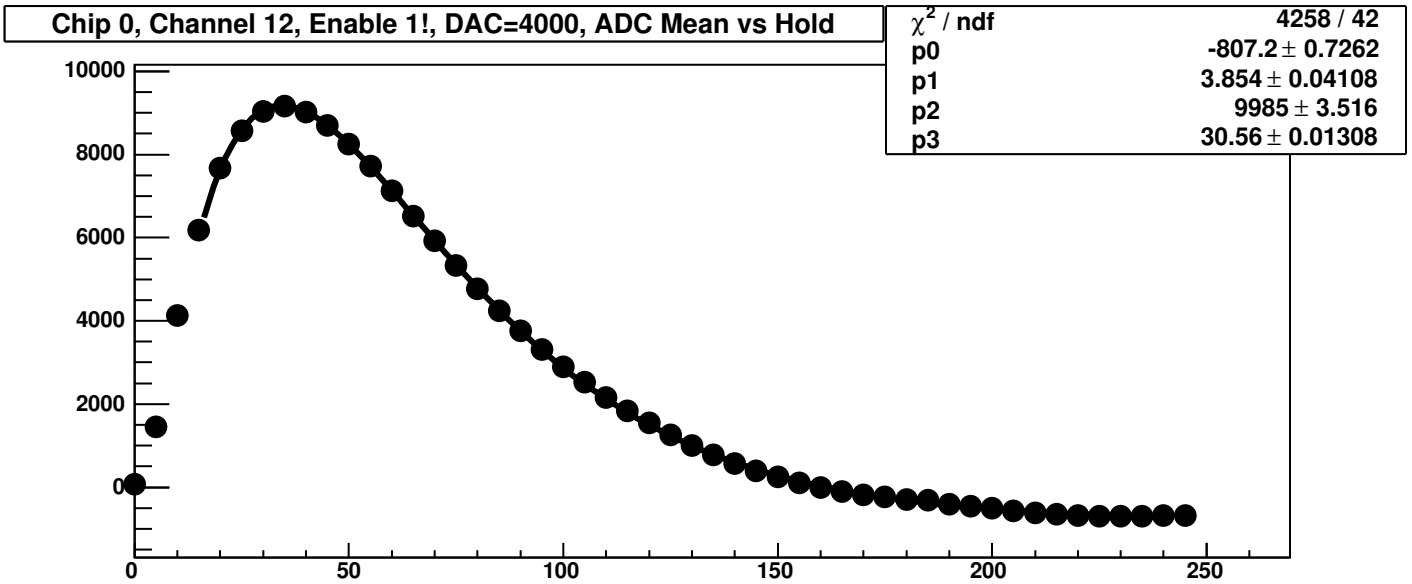

Chip 0, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250

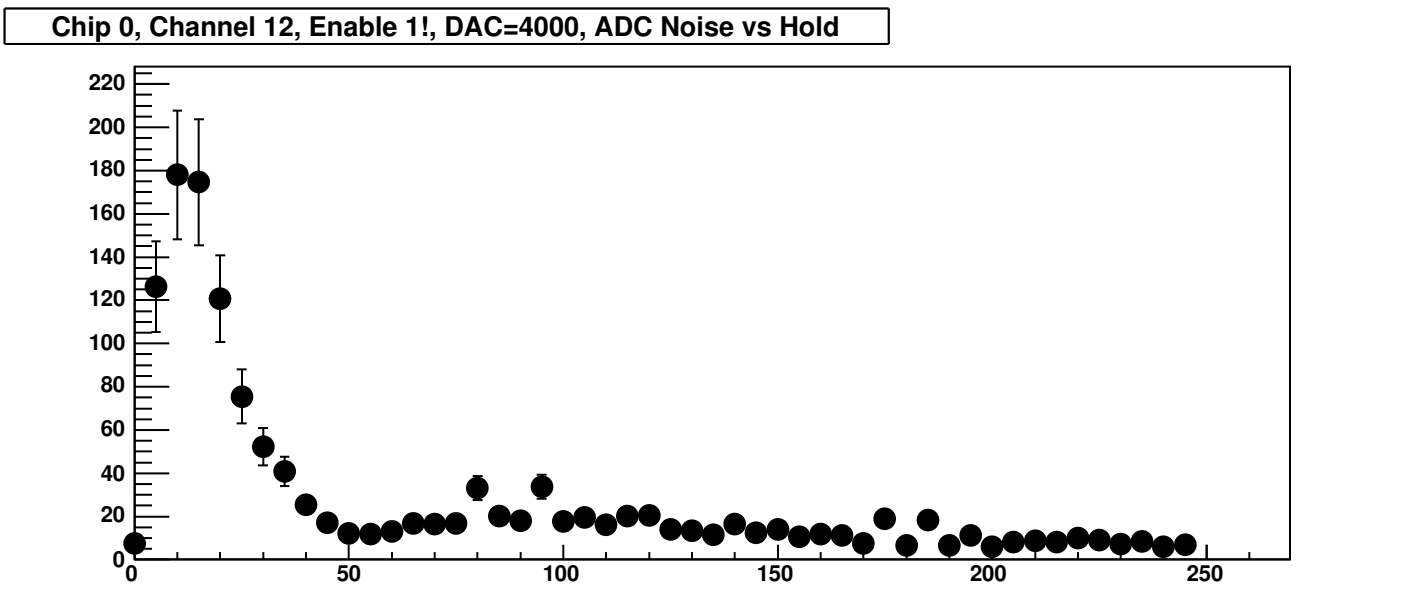

Chip 0, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold
600
400
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
50
100
150
200
250

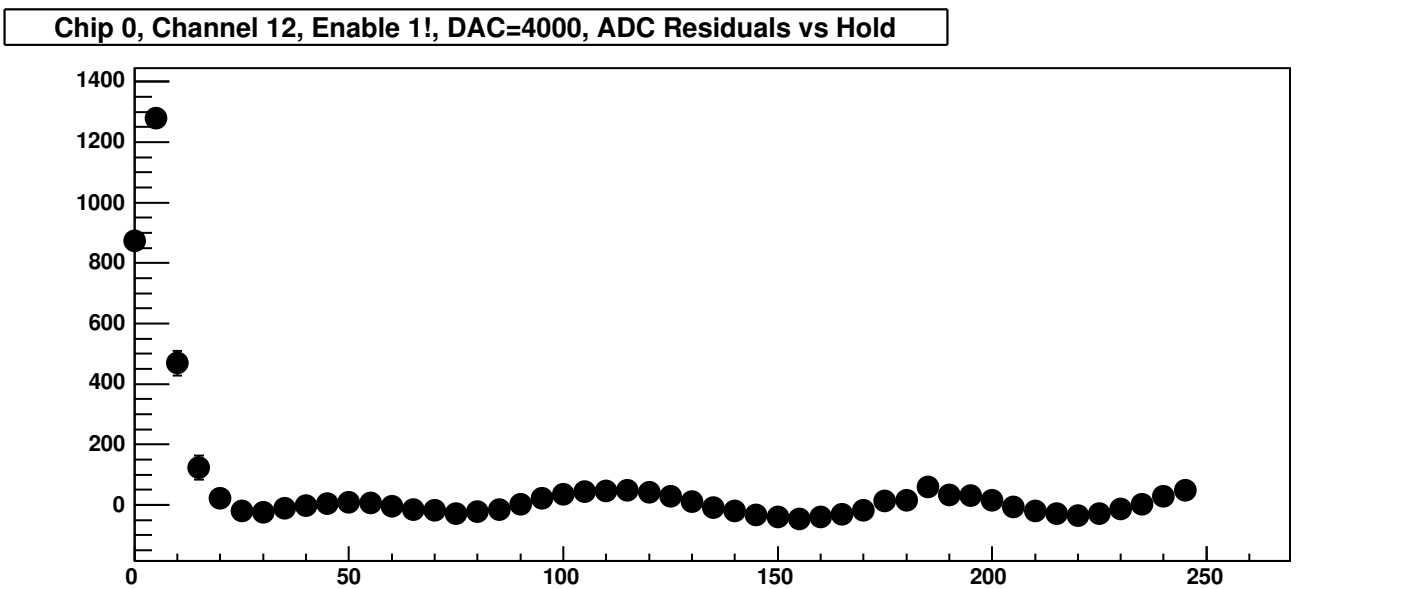

Chip 0, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.261e+05 / 41
p0 205.3 ± 1.525
p1 50.45 ± 0.02293
p2 6910 ± 9.413
p3 31.95 ± 0.05847
p4 -7.813 ± 0.05812
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

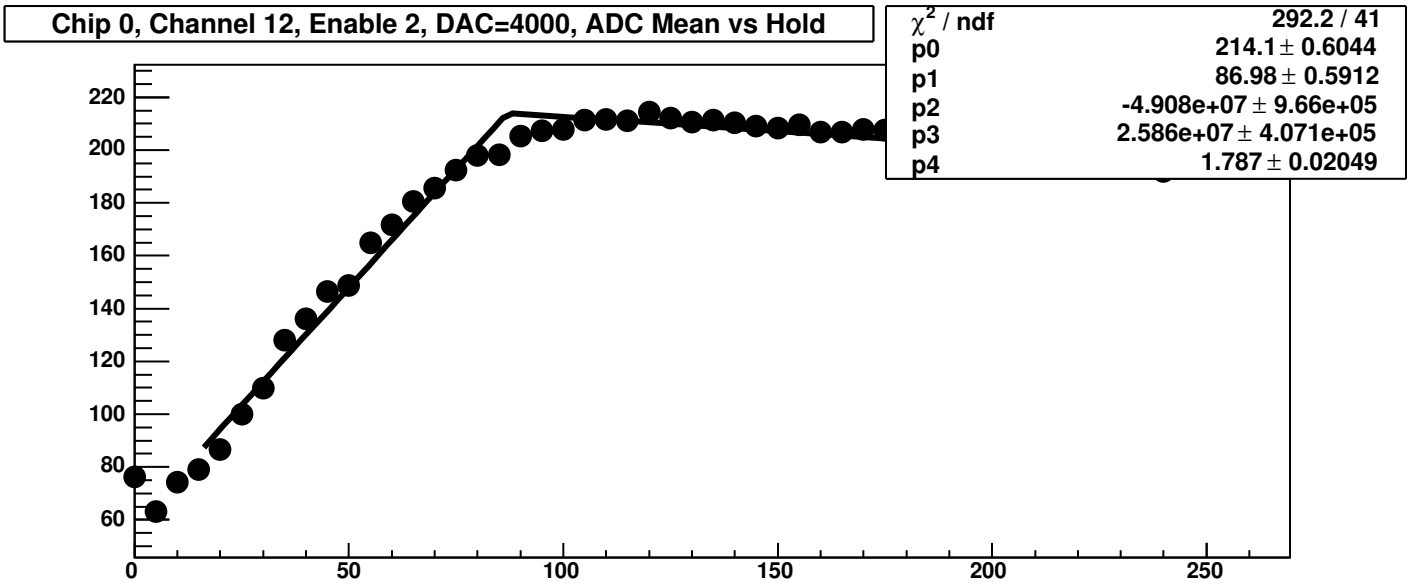

Chip 0, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250

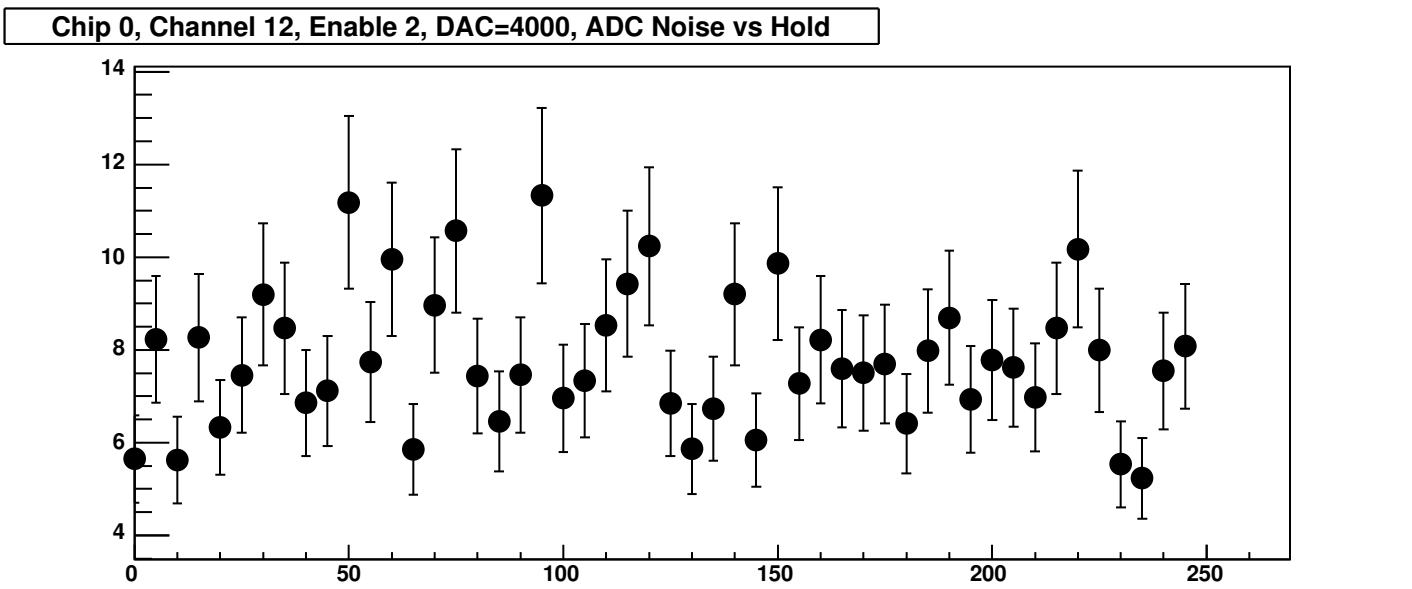

Chip 0, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250

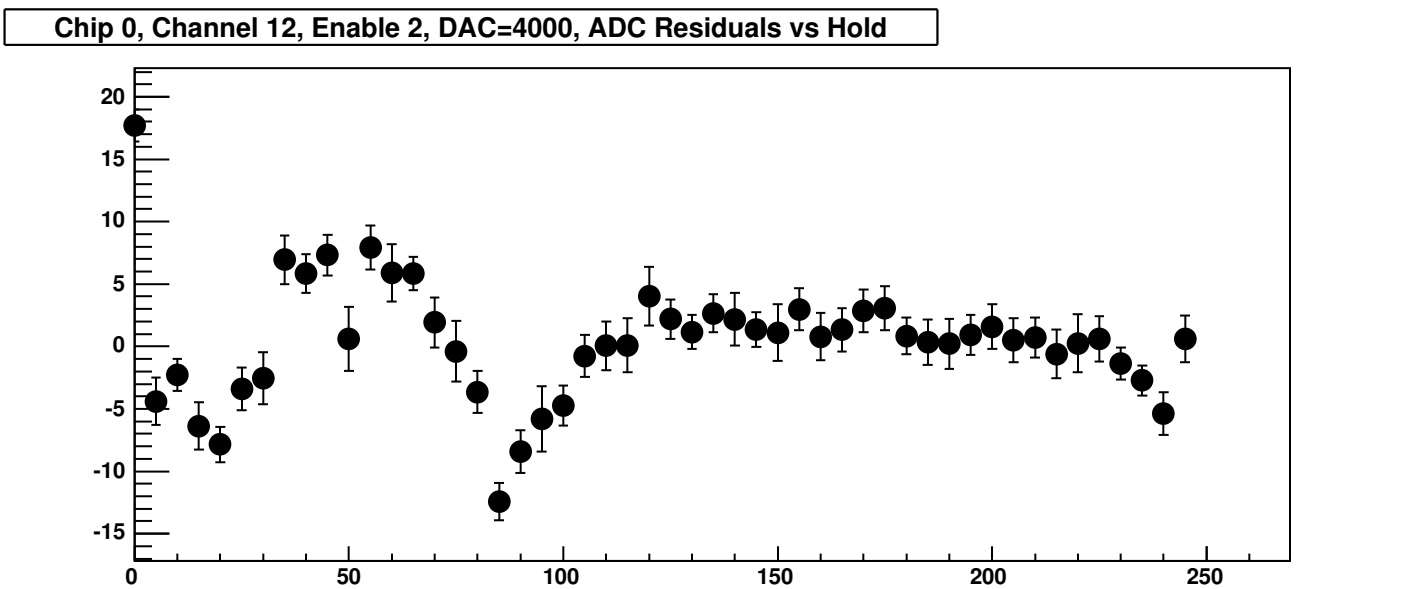

Chip 0, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 4258 / 42
p0 -807.2 ± 0.7262
p1 3.854 ± 0.04108
p2 9985 ± 3.516
p3 30.56 ± 0.01308
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 0, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

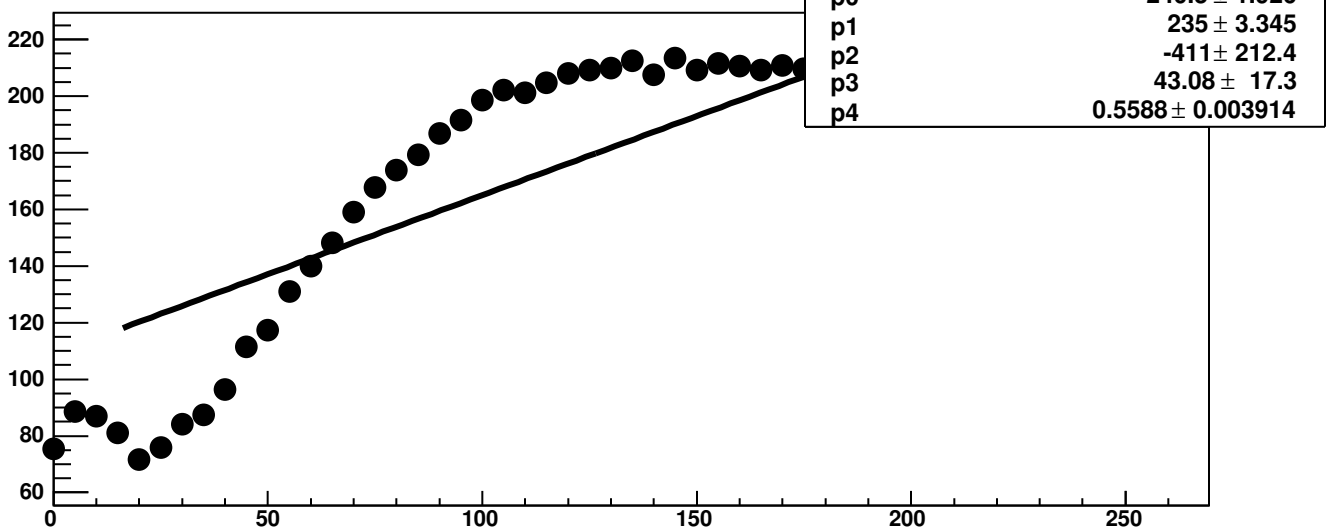

Chip 0, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 0, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 292.2 / 41
p0 214.1 ± 0.6044
p1 86.98 ± 0.5912
p2 -4.908e+07 ± 9.66e+05
p3 2.586e+07 ± 4.071e+05
p4 1.787 ± 0.02049
220
200
180
160
140
120
100
80
60
0
50
100
150
200
250

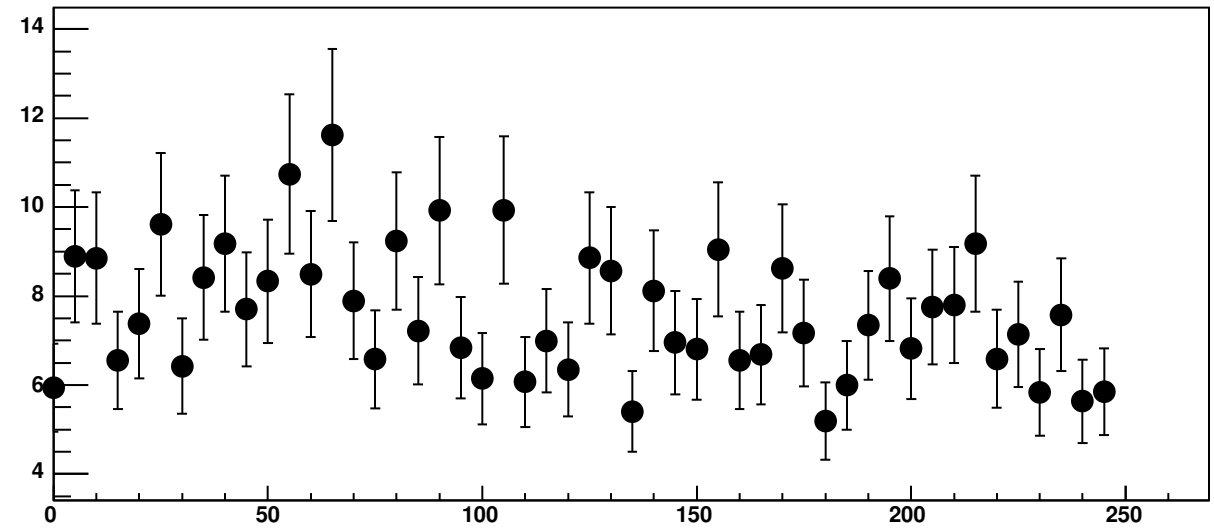

Chip 0, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 0, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

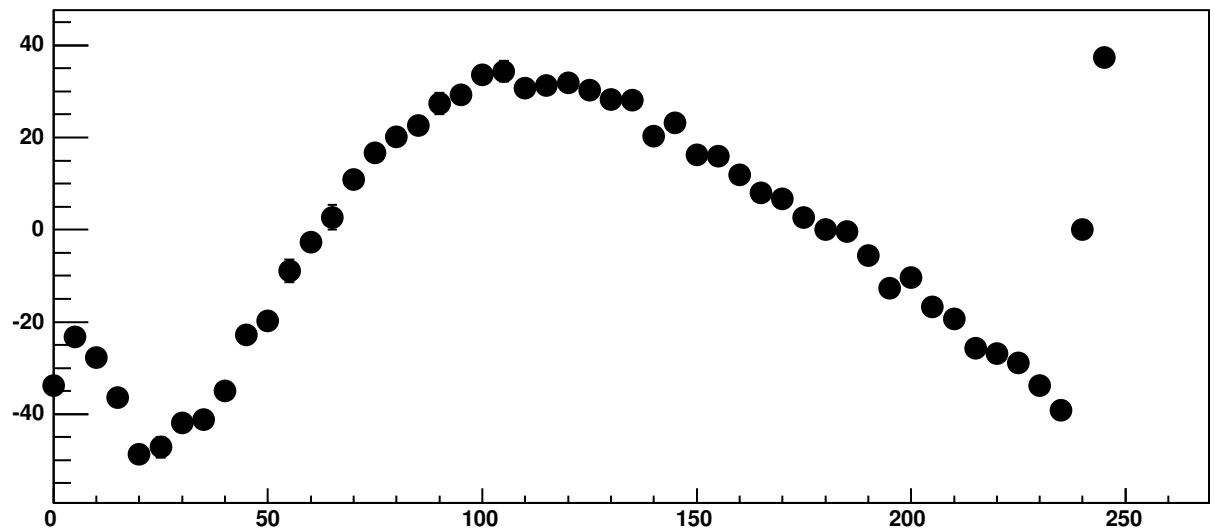
Chip 0, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

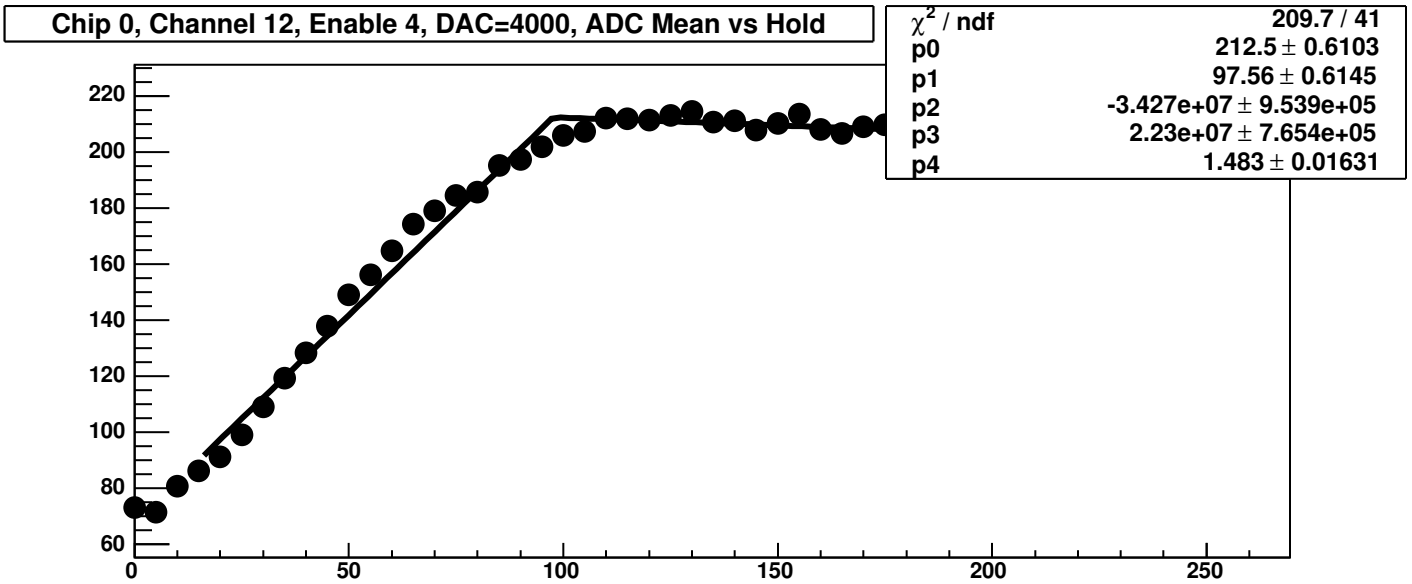


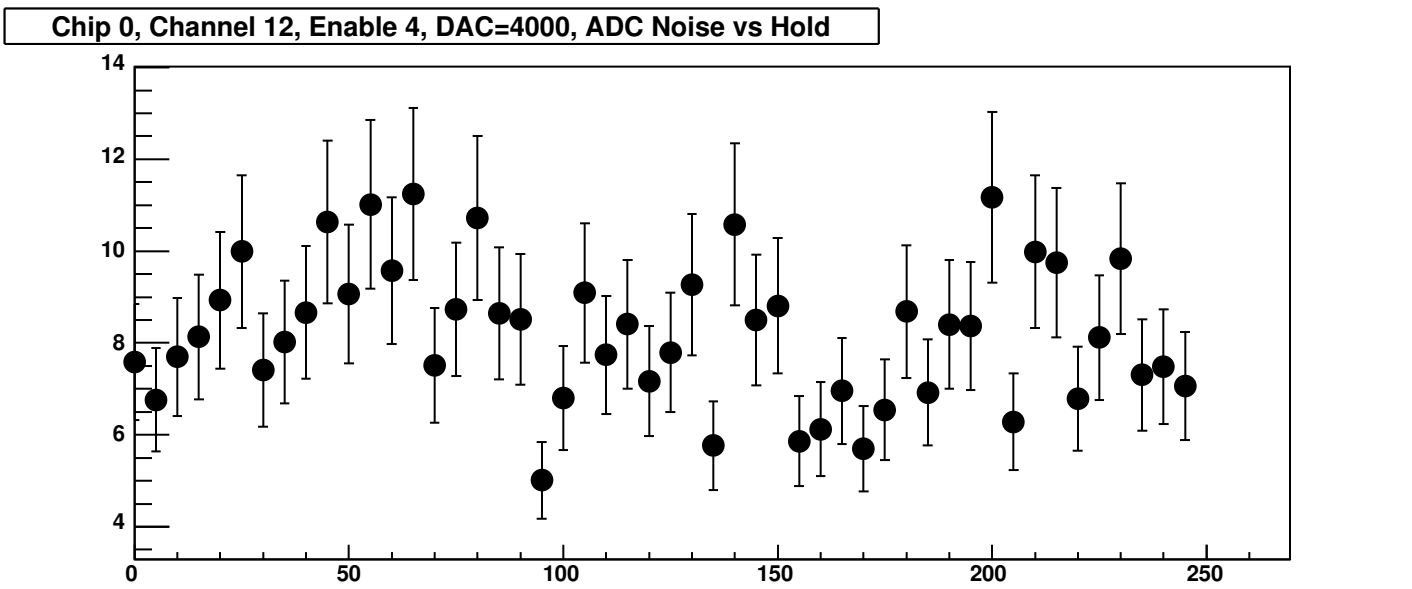
Chip 0, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold

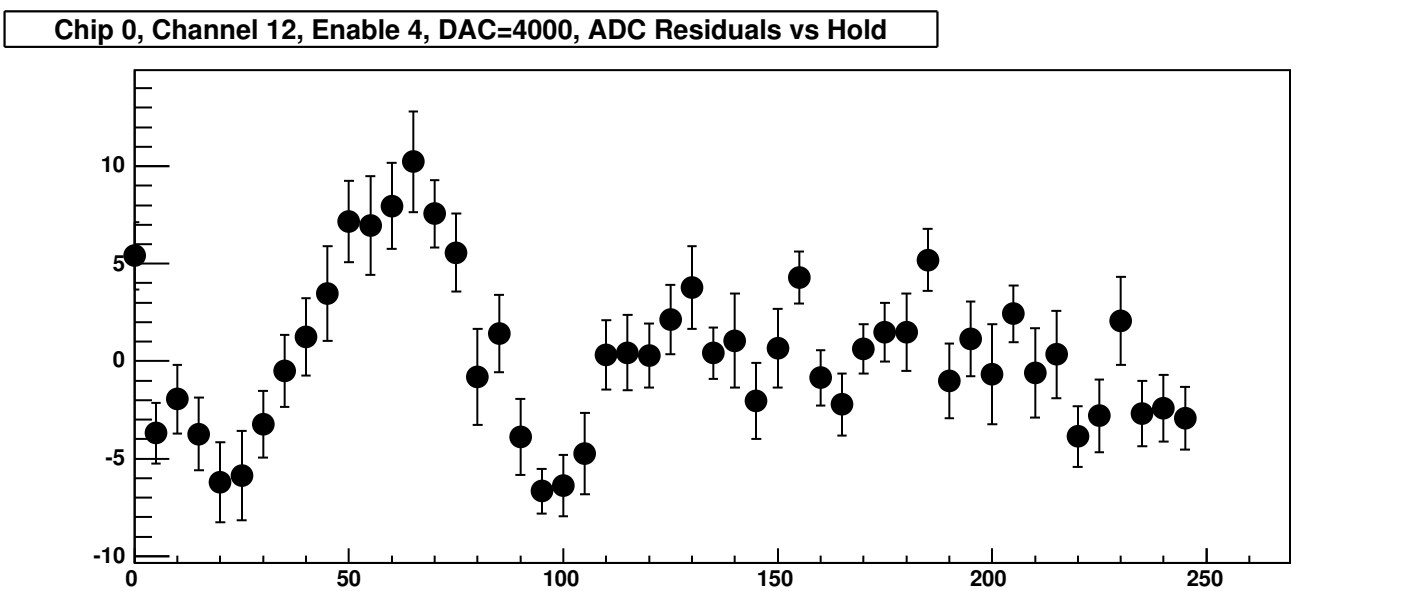


Chip 0, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

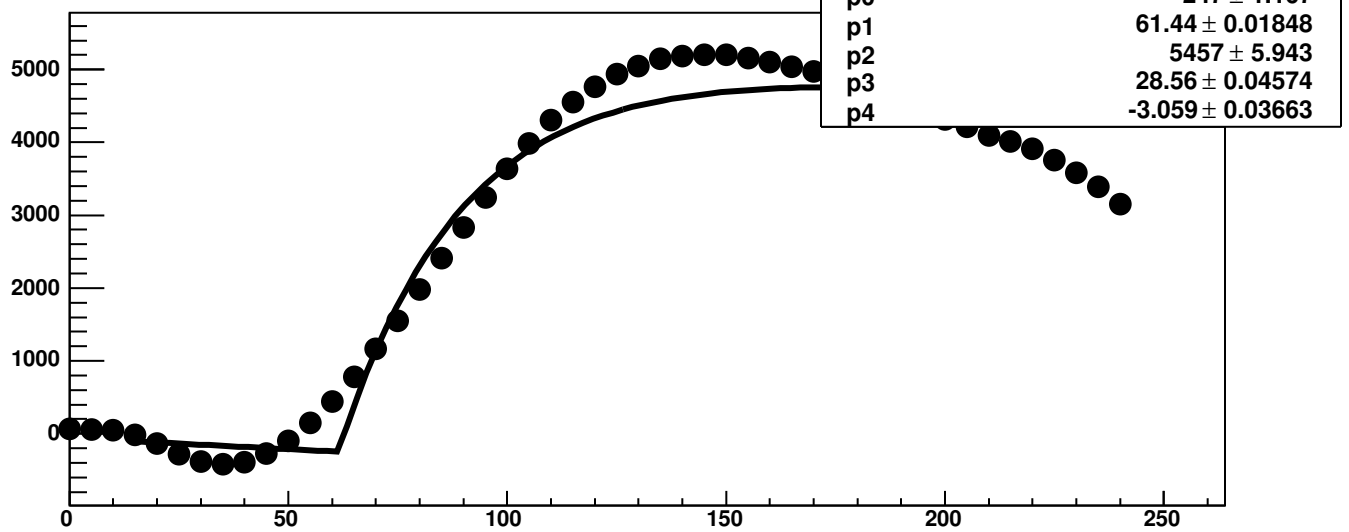



Chip 0, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 209.7 / 41
p0 212.5 ± 0.6103
p1 97.56 ± 0.6145
p2 -3.427e+07 ± 9.539e+05
p3 2.23e+07 ± 7.654e+05
p4 1.483 ± 0.01631
220
200
180
160
140
120
100
80
60
0
50
100
150
200
250

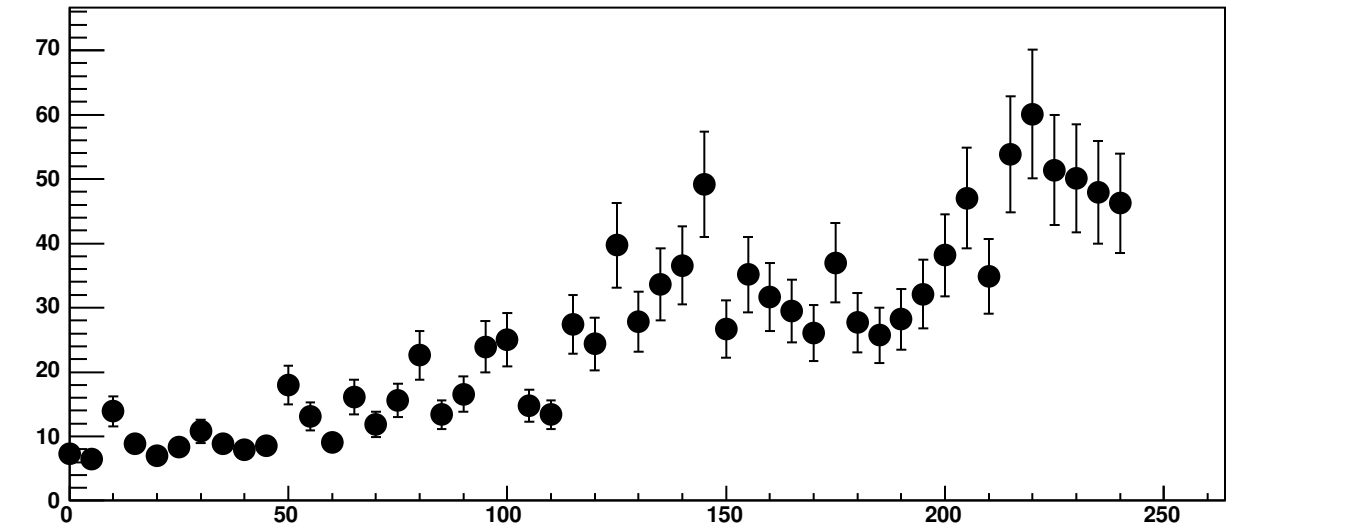

Chip 0, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 0, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250

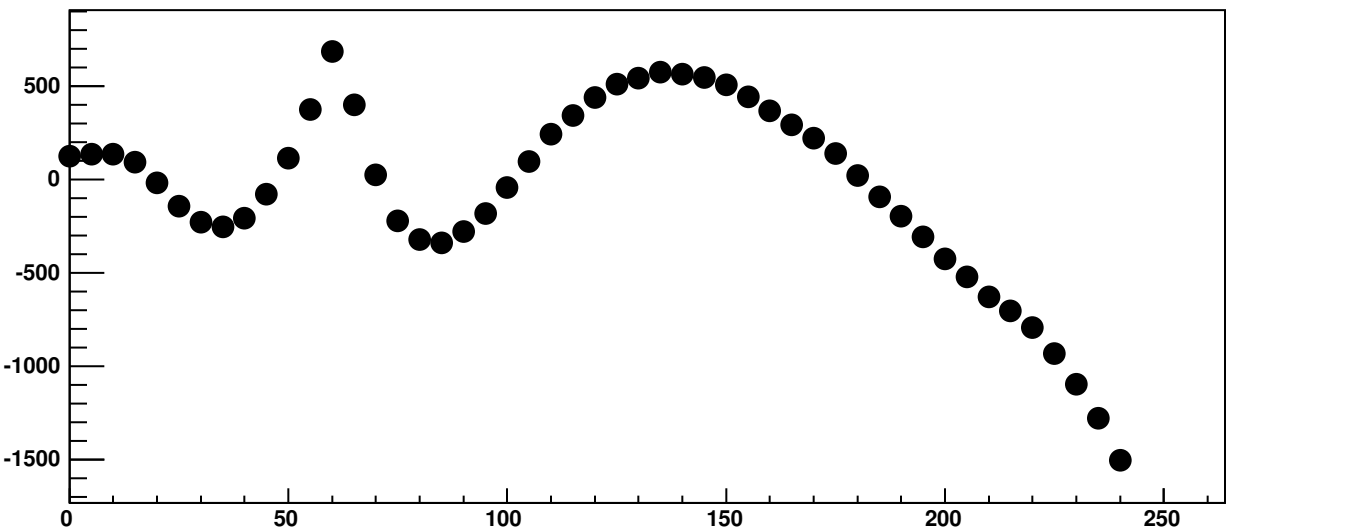
Chip 0, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold

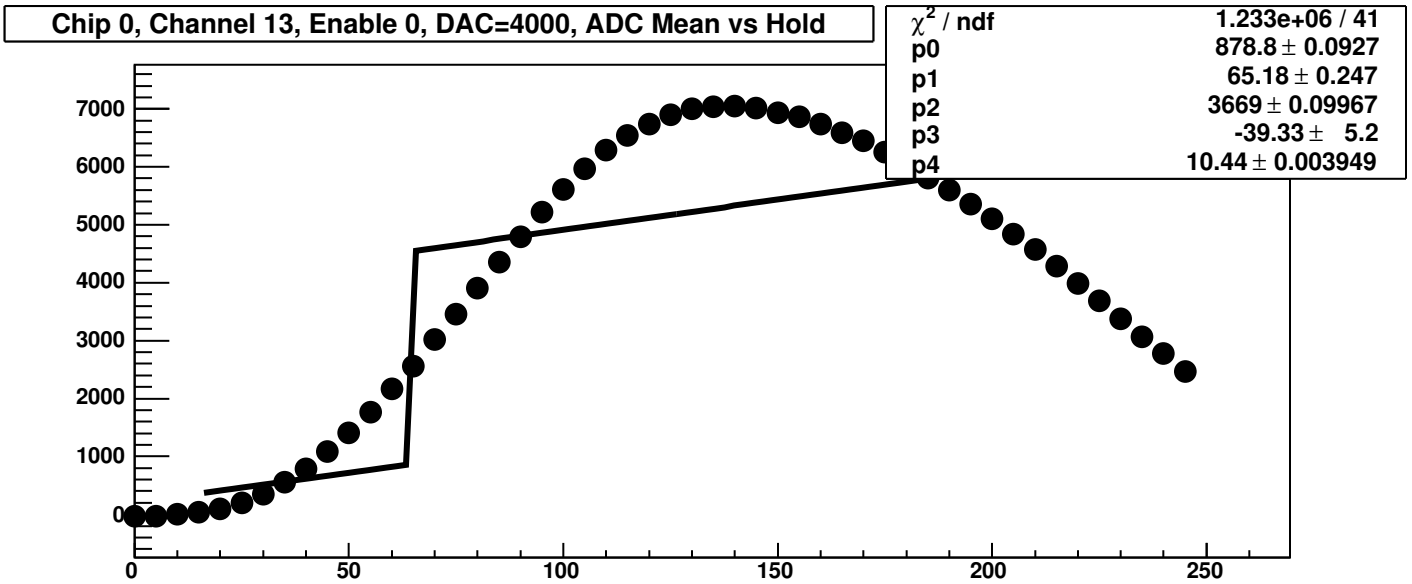


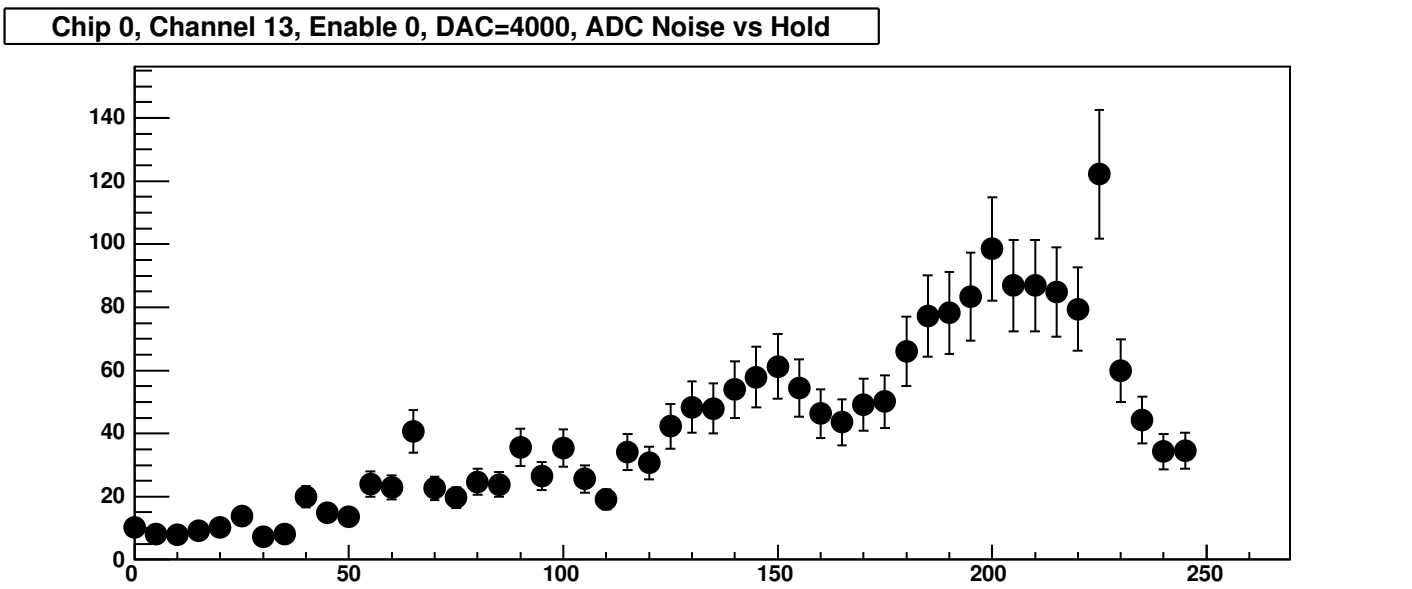
Chip 0, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold

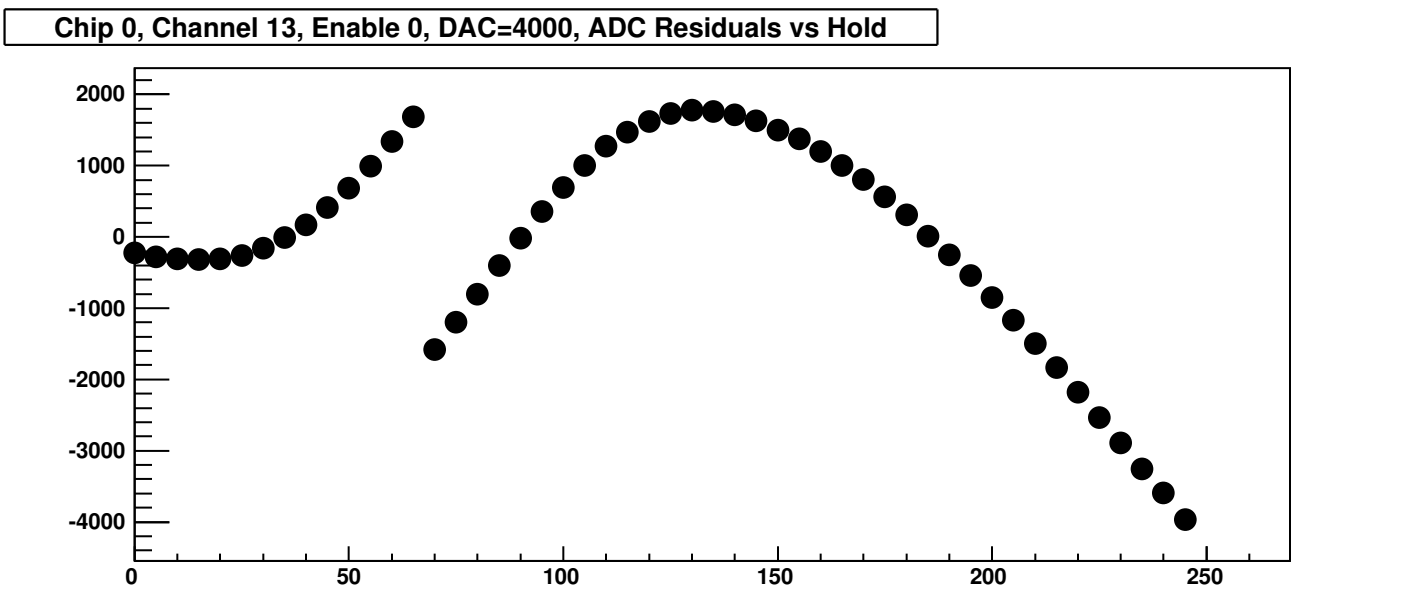


Chip 0, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold

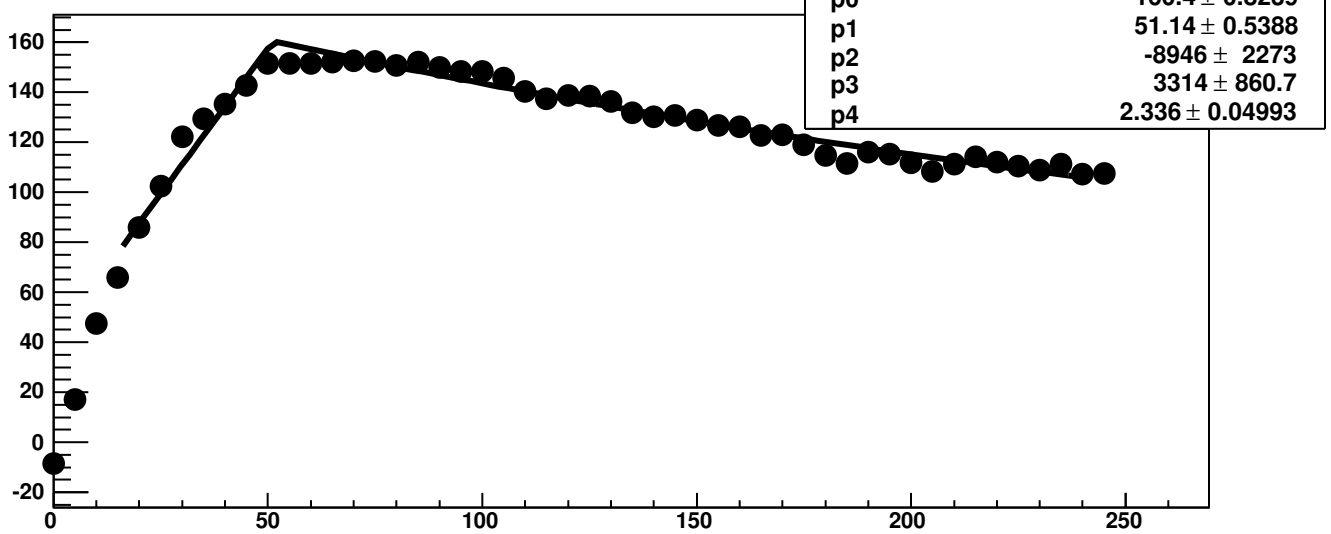



Chip 0, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.233e+06 / 41
p0 878.8 ± 0.0927
p1 65.18 ± 0.247
p2 3669 ± 0.09967
p3 -39.33 ± 5.2
p4 10.44 ± 0.003949
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

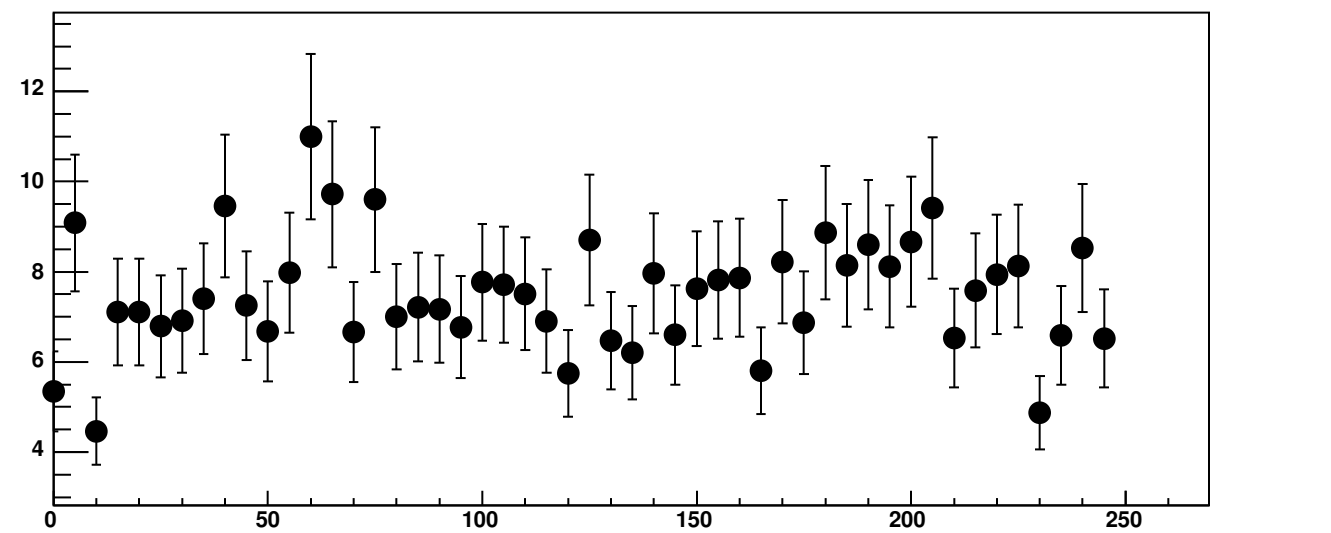

Chip 0, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

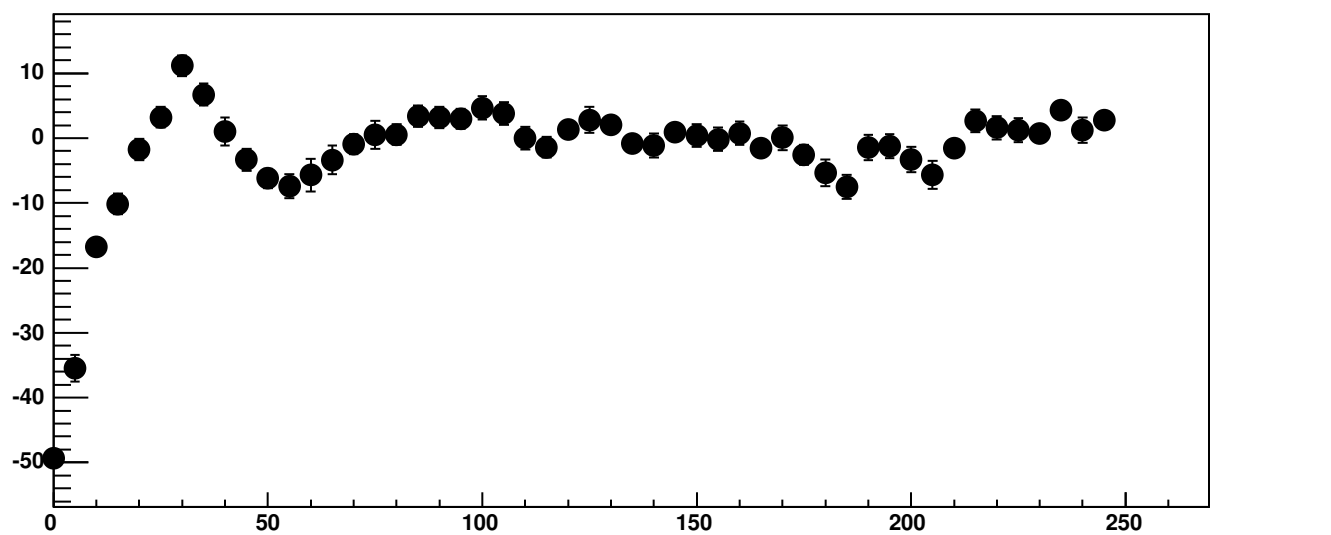
Chip 0, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold



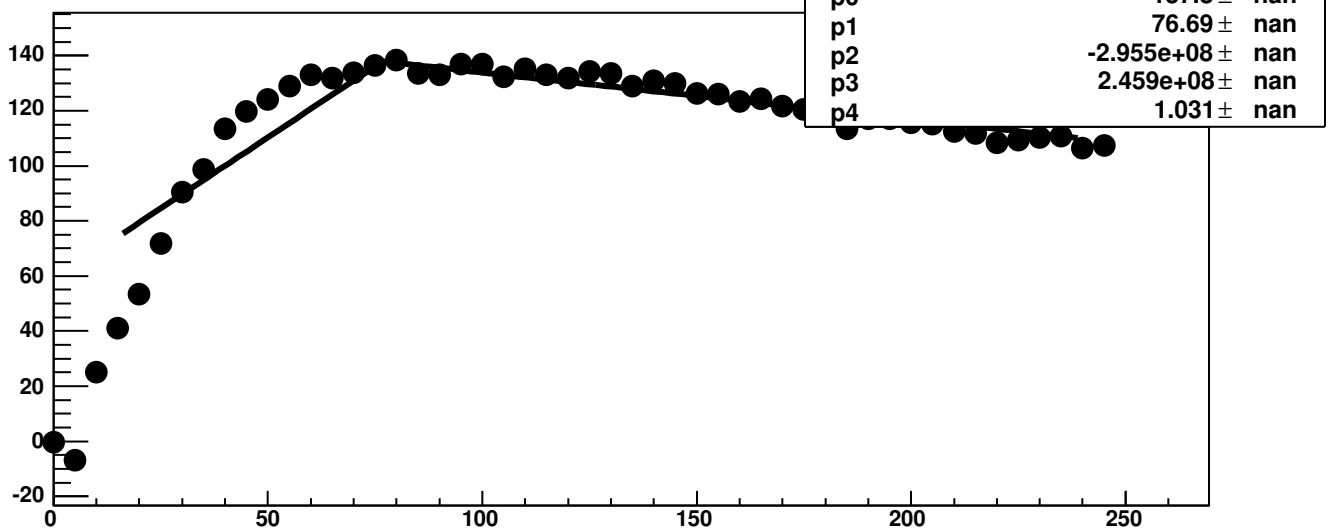
Chip 0, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold



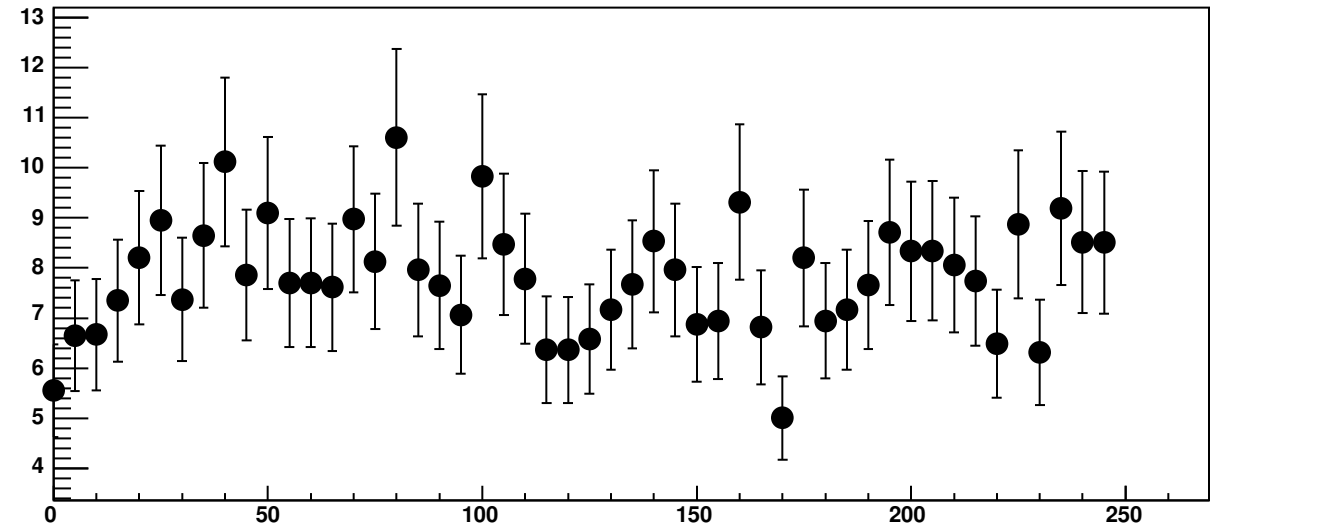
Chip 0, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold



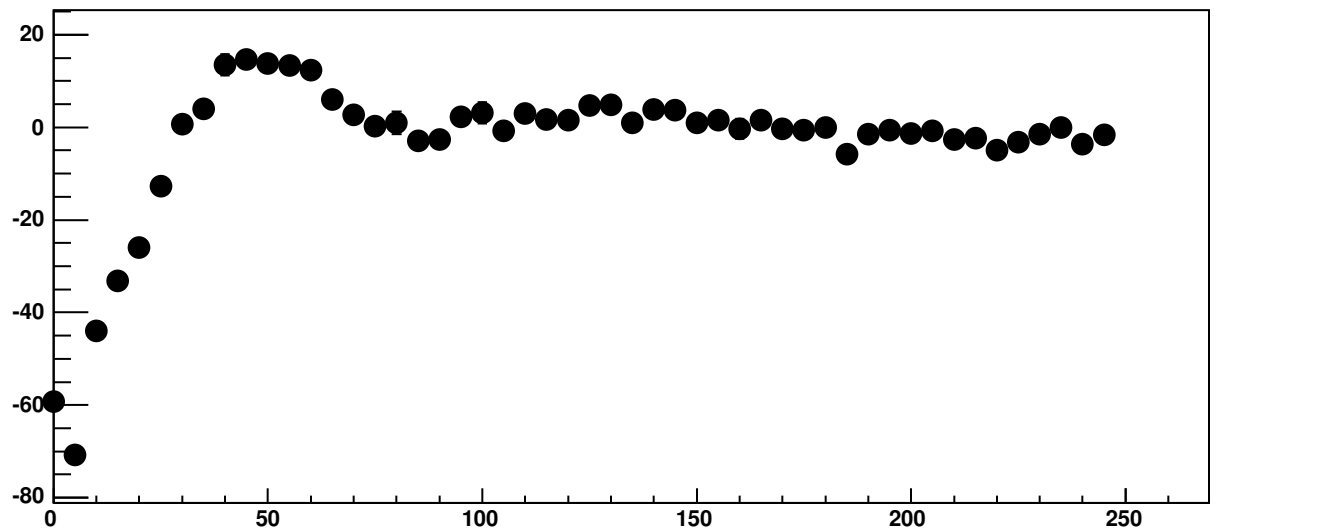
Chip 0, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold



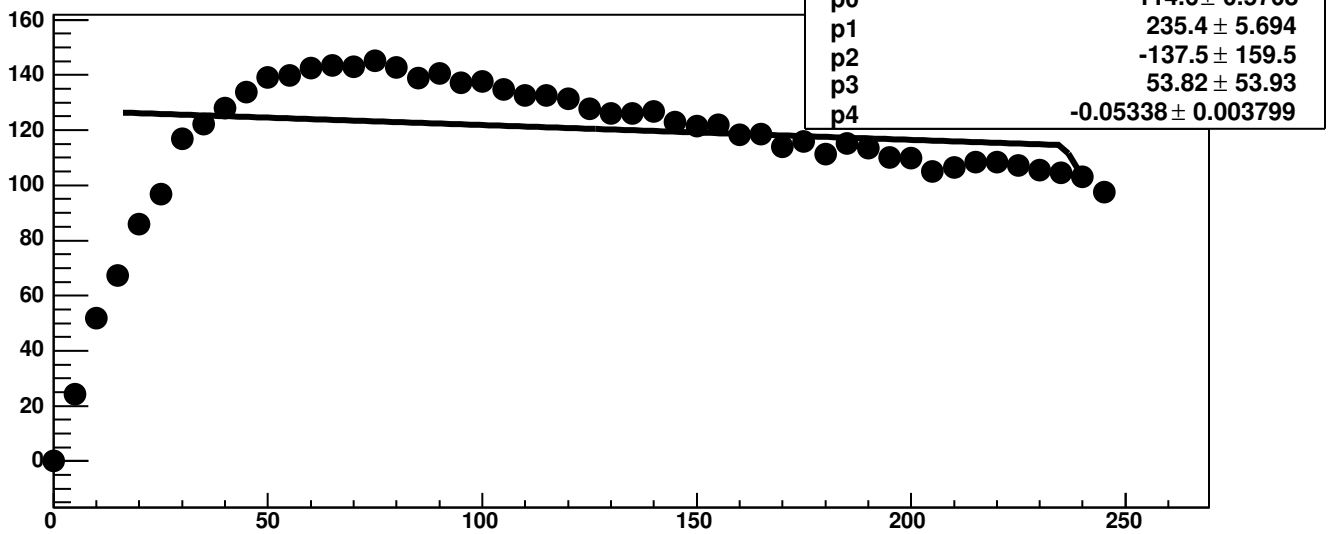
Chip 0, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



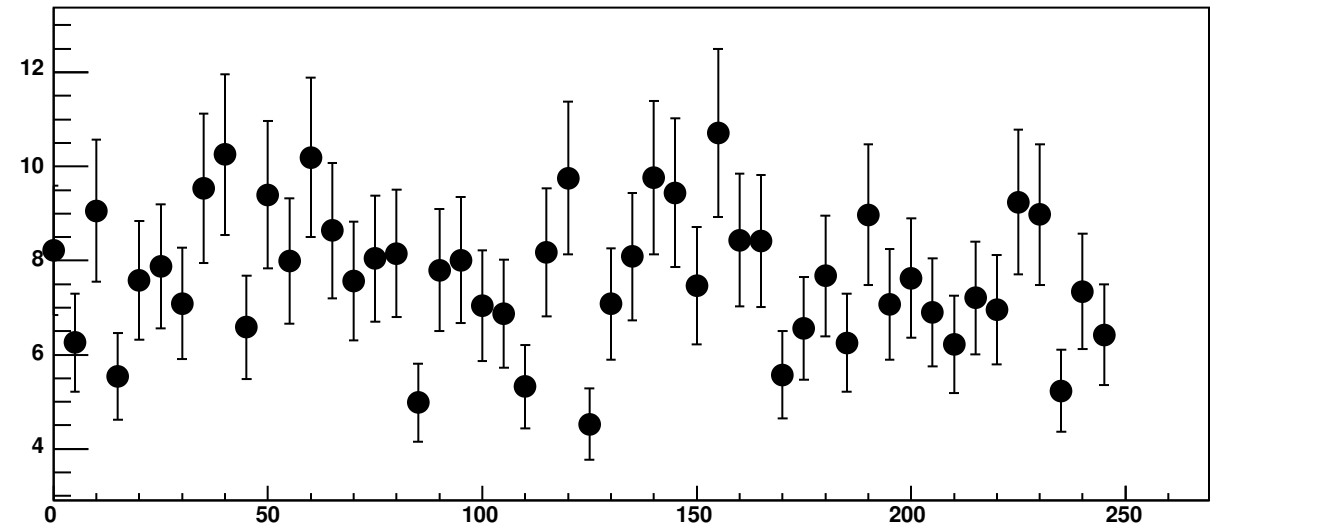
Chip 0, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold



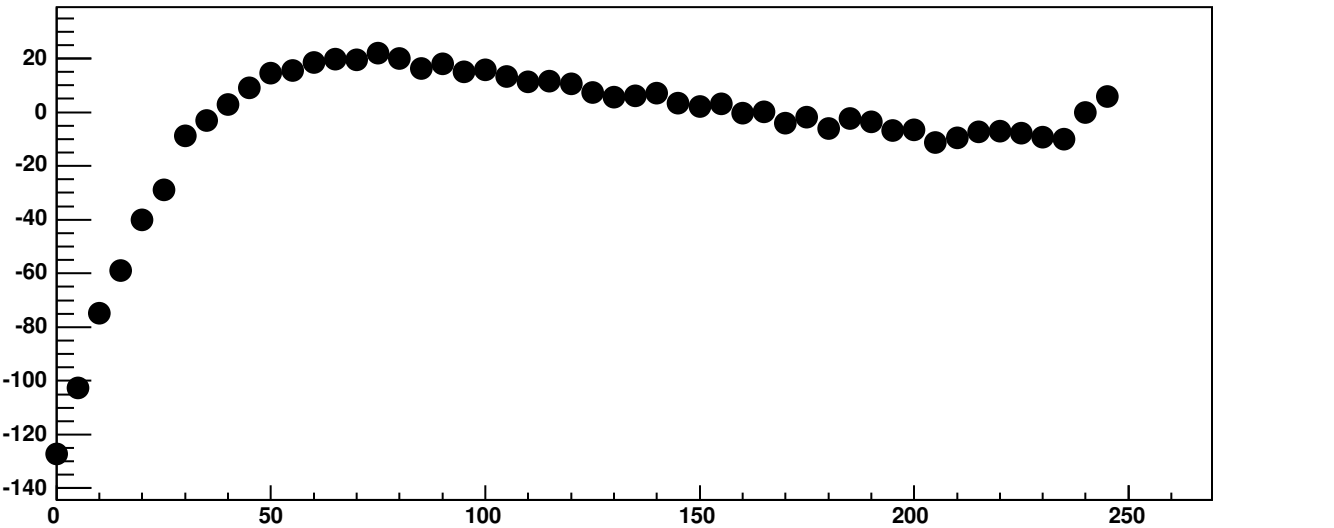
Chip 0, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold



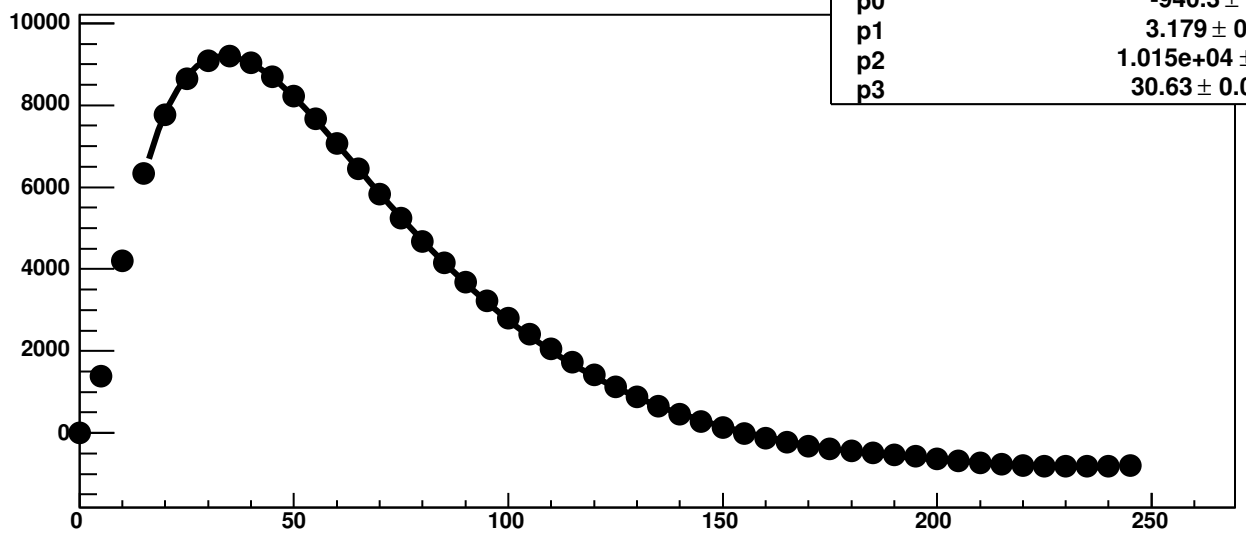
Chip 0, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



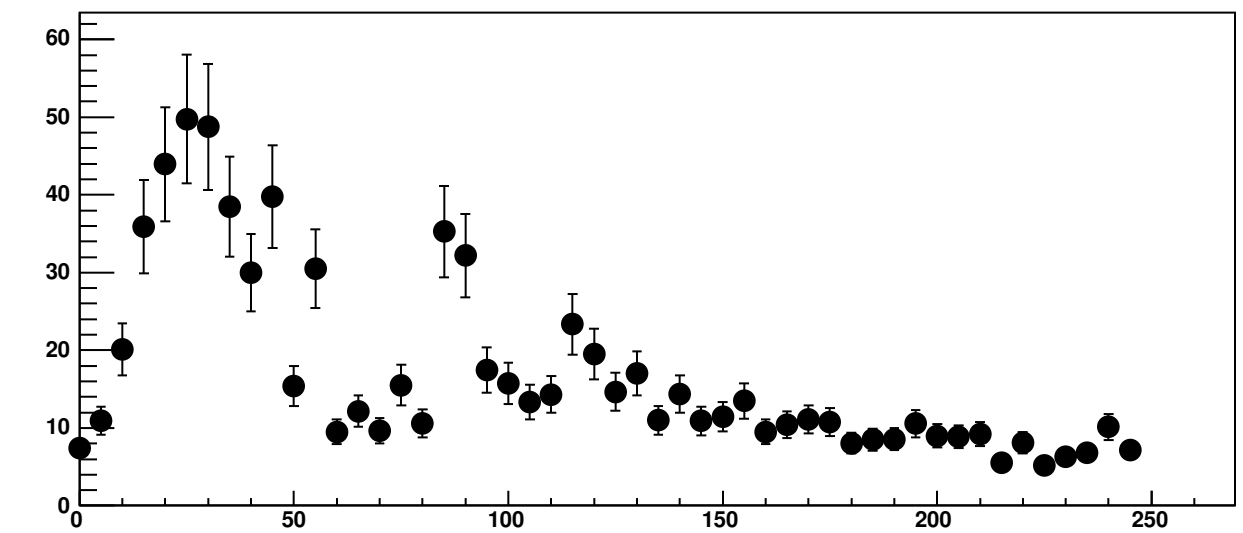
Chip 0, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold



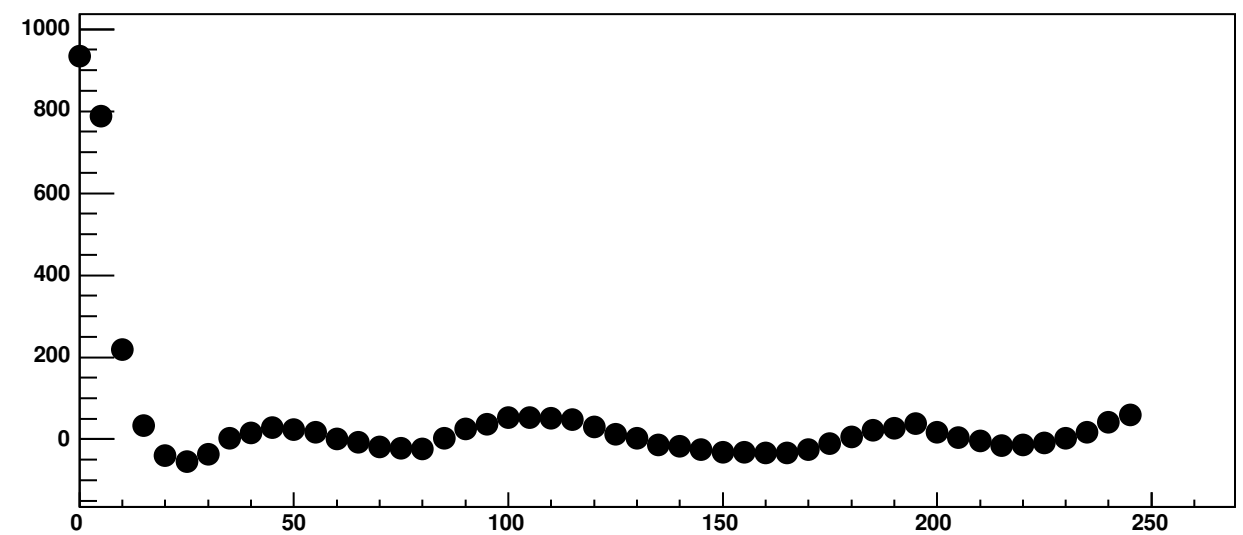
Chip 0, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold



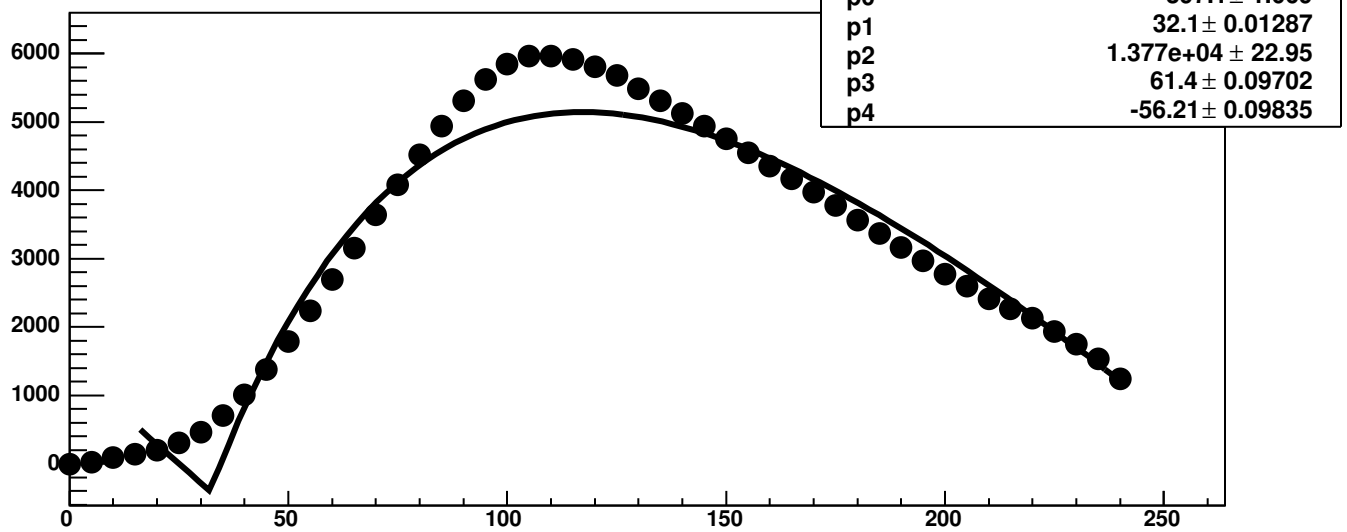
Chip 0, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold



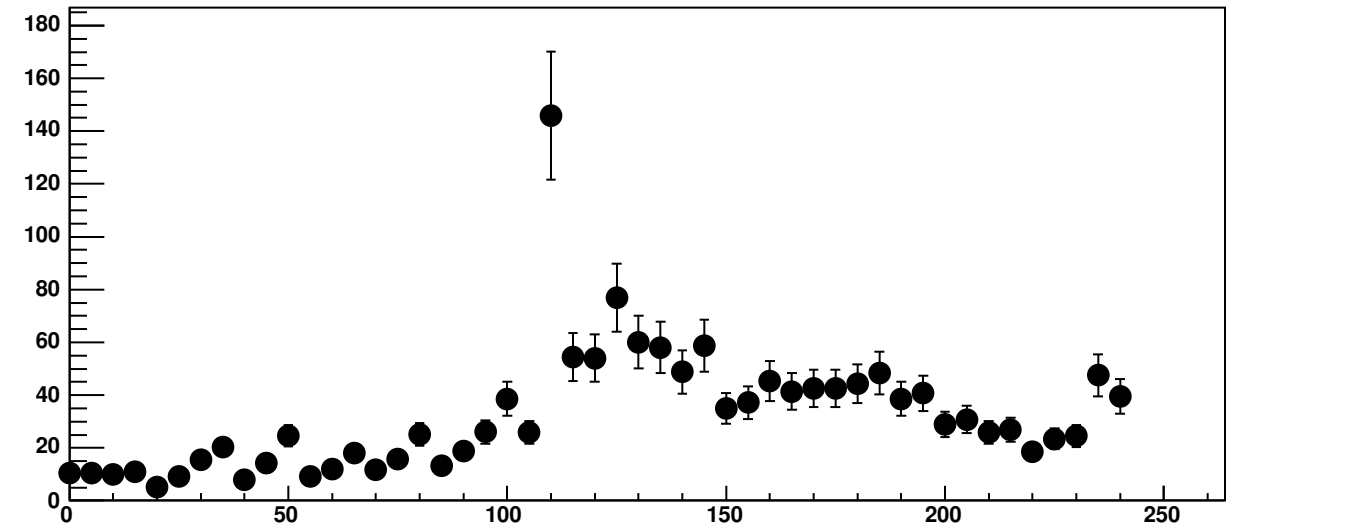
Chip 0, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold



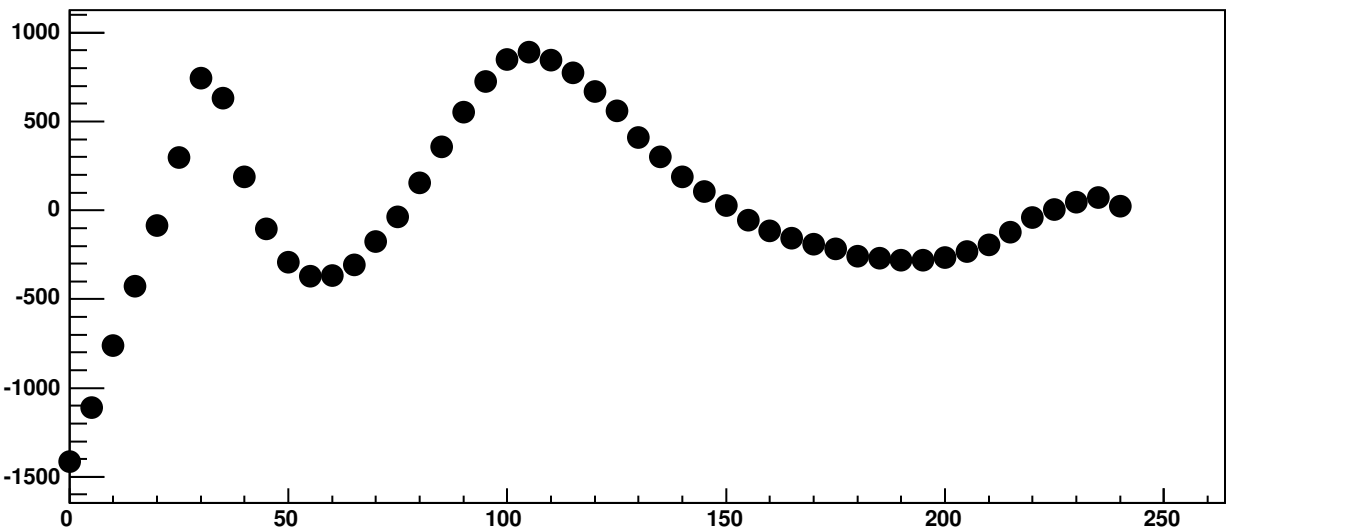
Chip 0, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

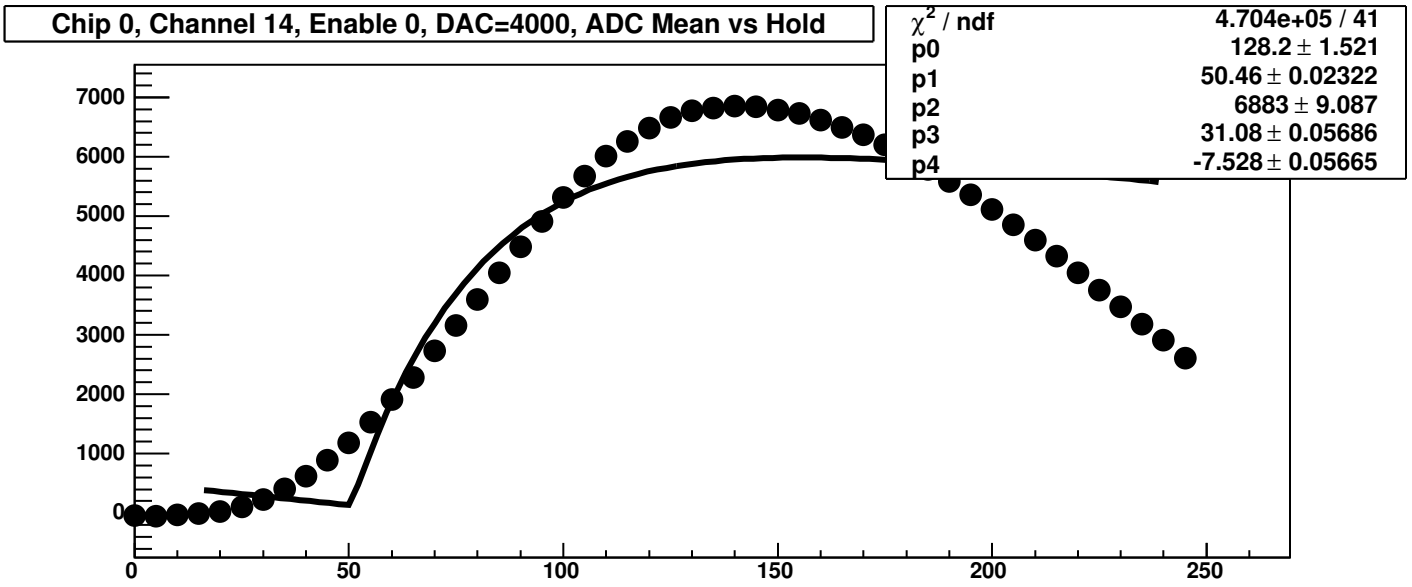


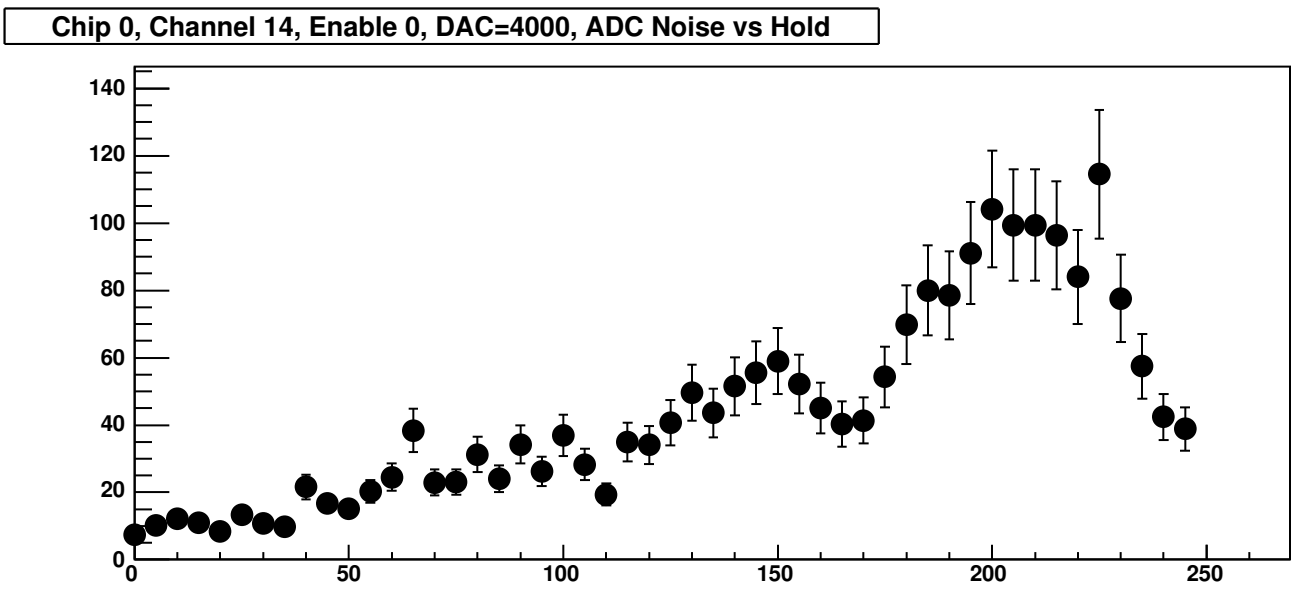
Chip 0, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold

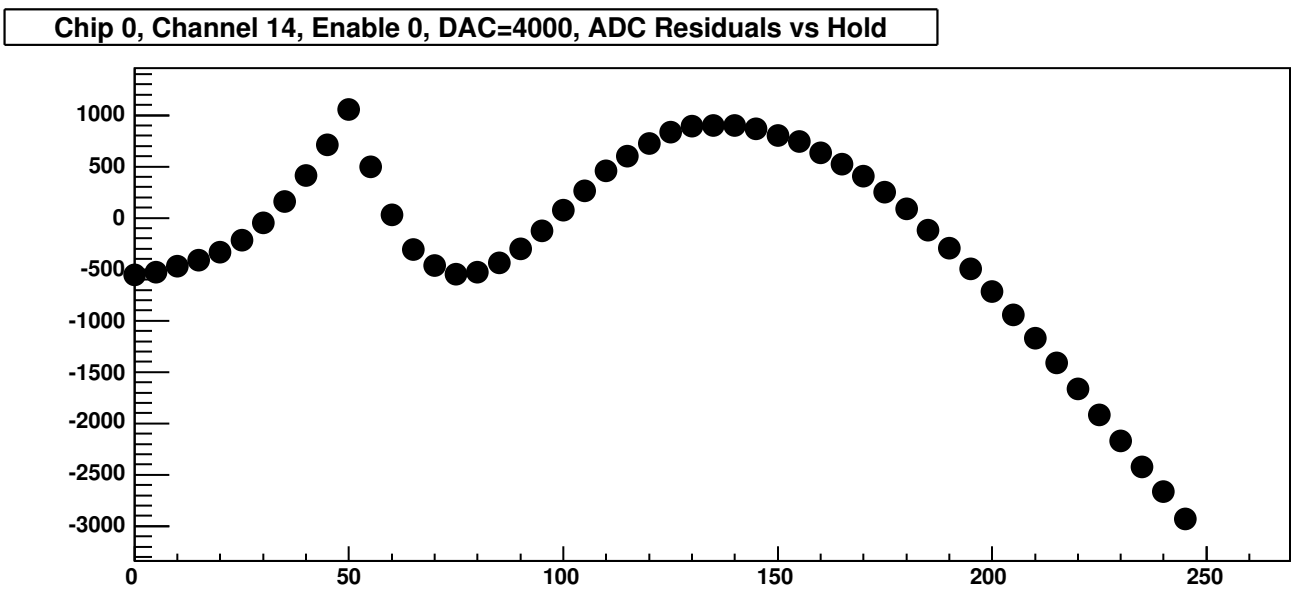


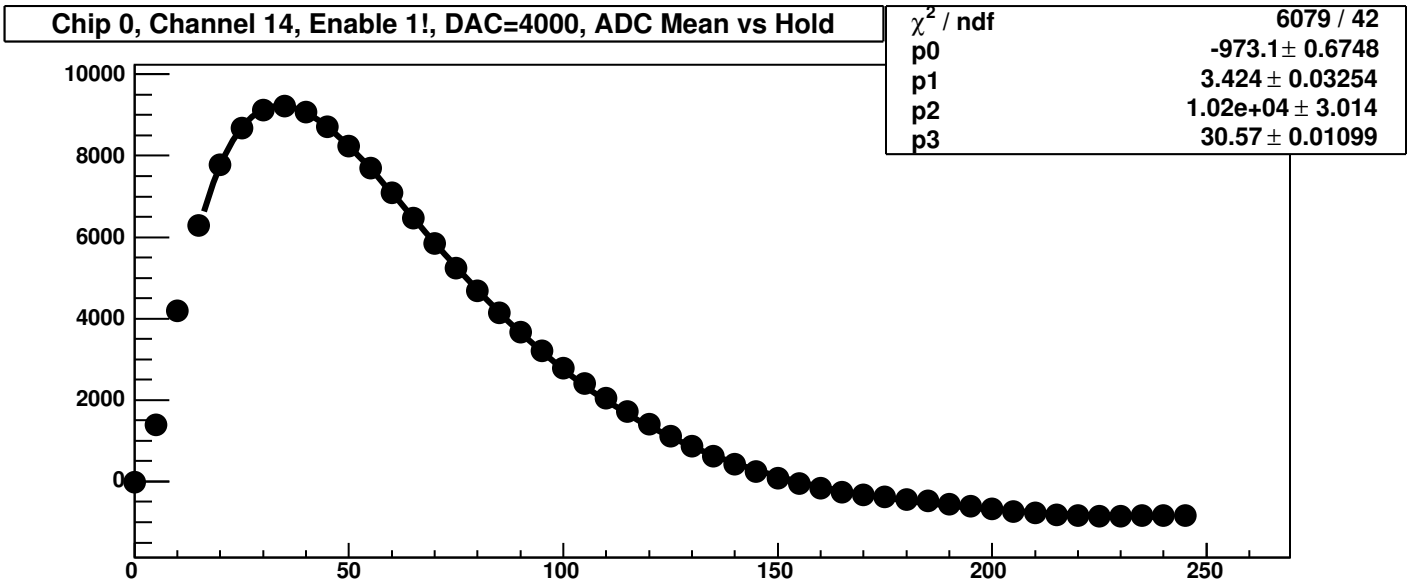
Chip 0, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold

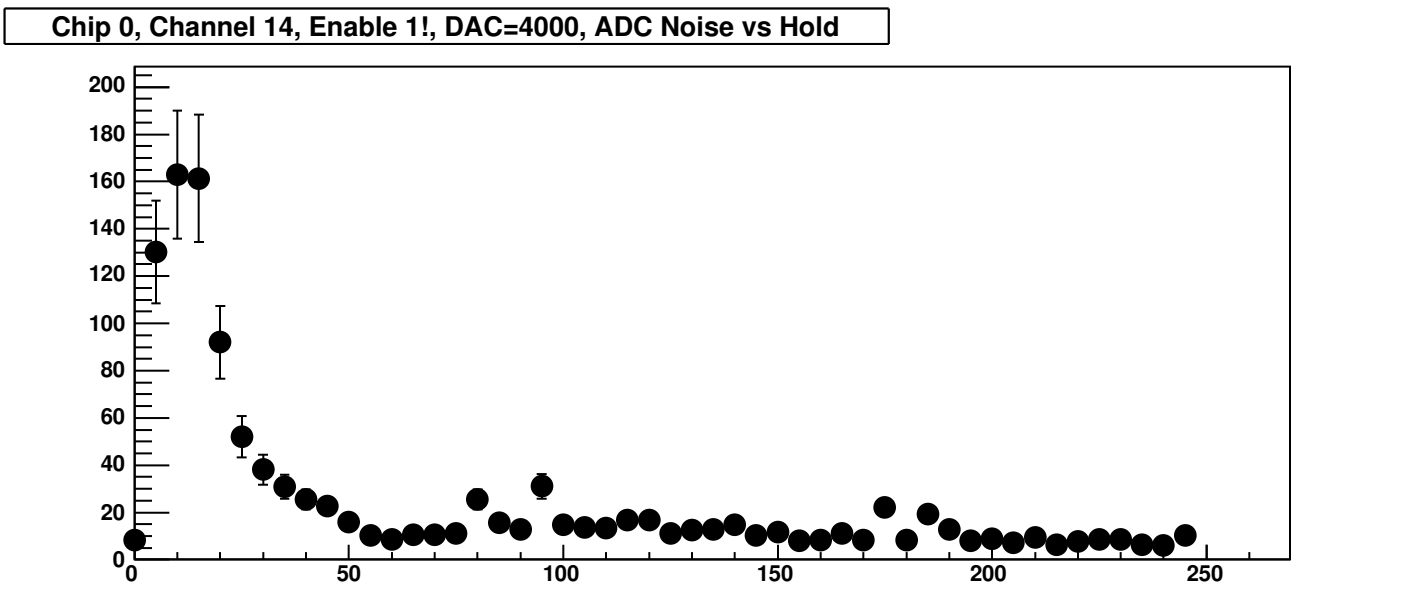


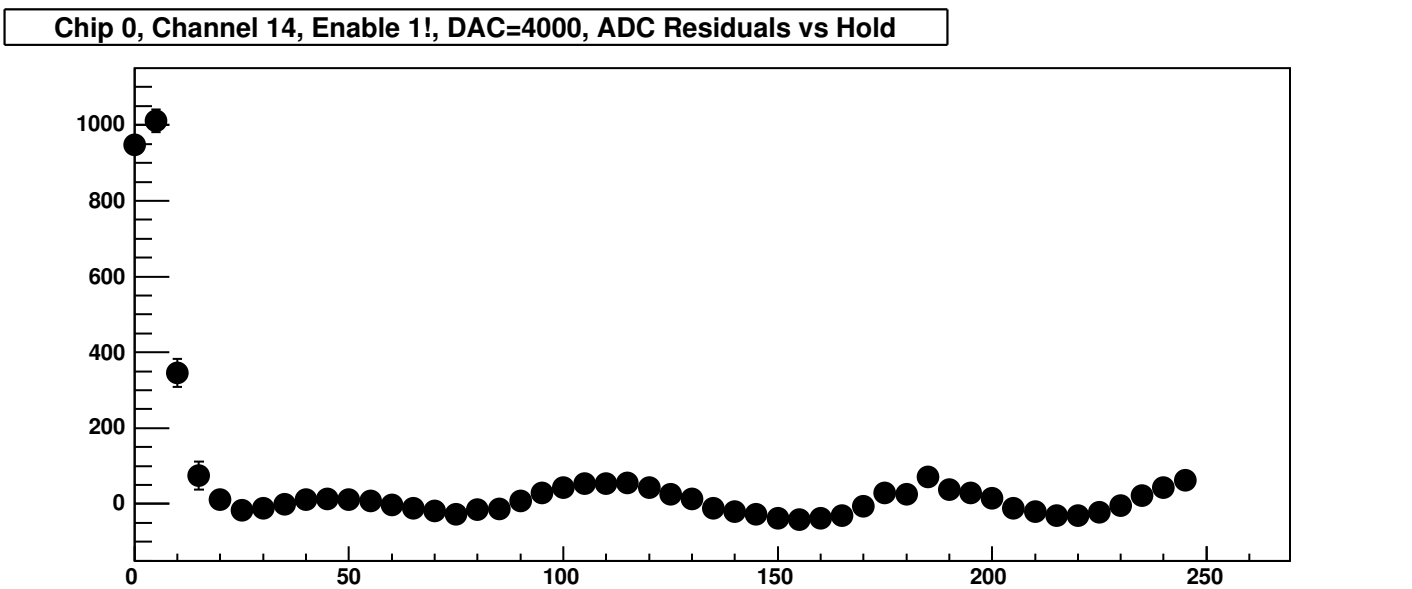

Chip 0, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.704e+05 / 41
p0 128.2 ± 1.521
p1 50.46 ± 0.02322
p2 6883 ± 9.087
p3 31.08 ± 0.05686
p4 -7.528 ± 0.05665
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 0, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250

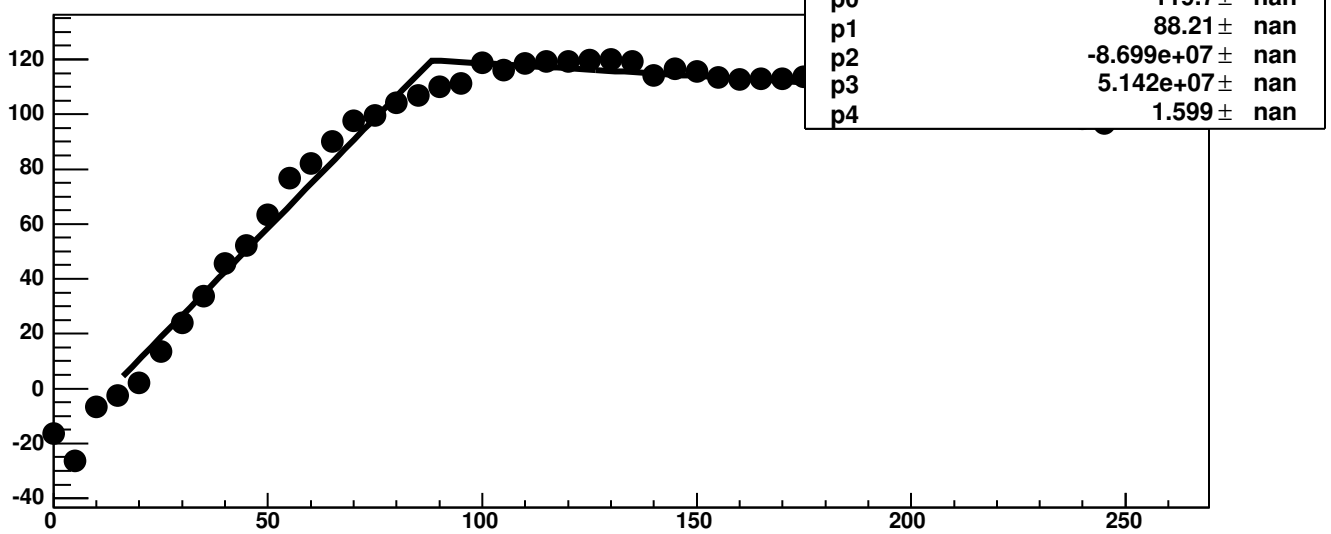

Chip 0, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250


Chip 0, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 6079 / 42
p0 -973.1 ± 0.6748
p1 3.424 ± 0.03254
p2 1.02e+04 ± 3.014
p3 30.57 ± 0.01099
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

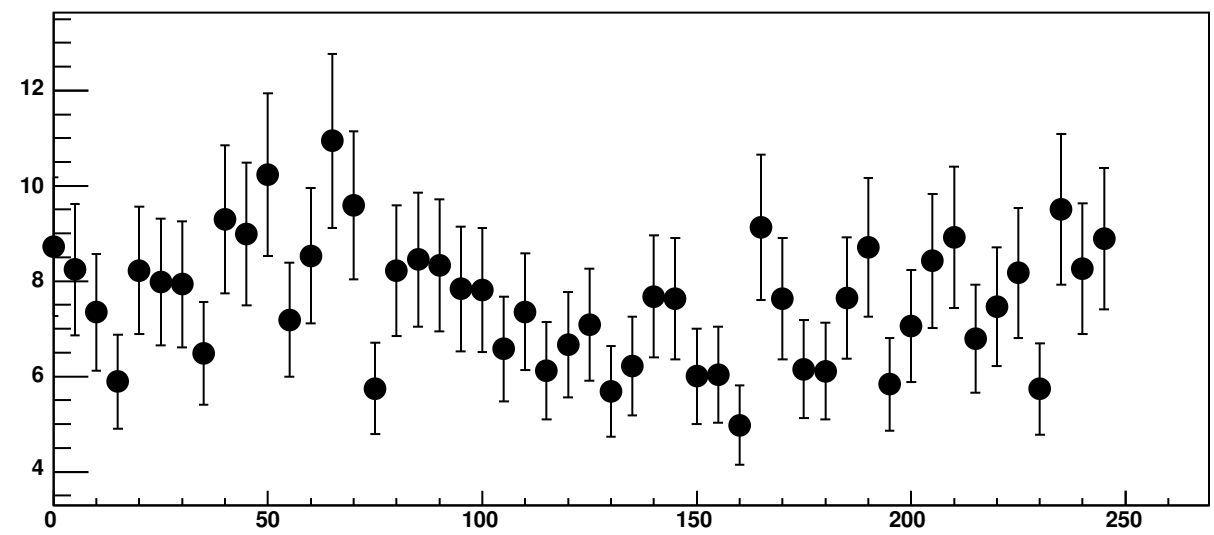

Chip 0, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold
1000
800
600
400
200
0
0
50
100
150
200
250

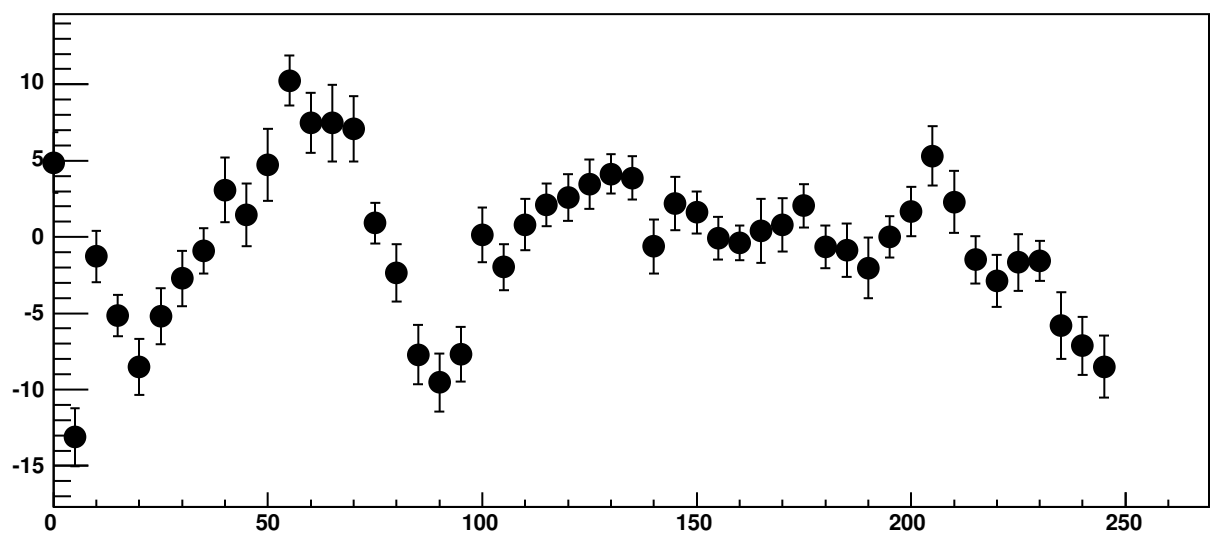
Chip 0, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold



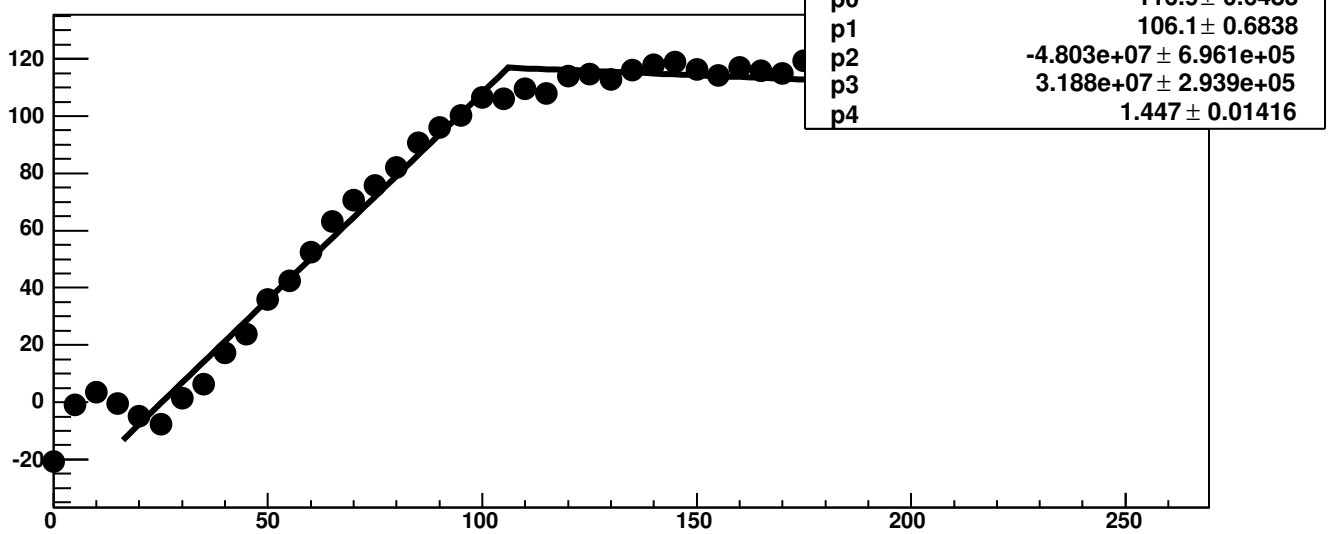
Chip 0, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



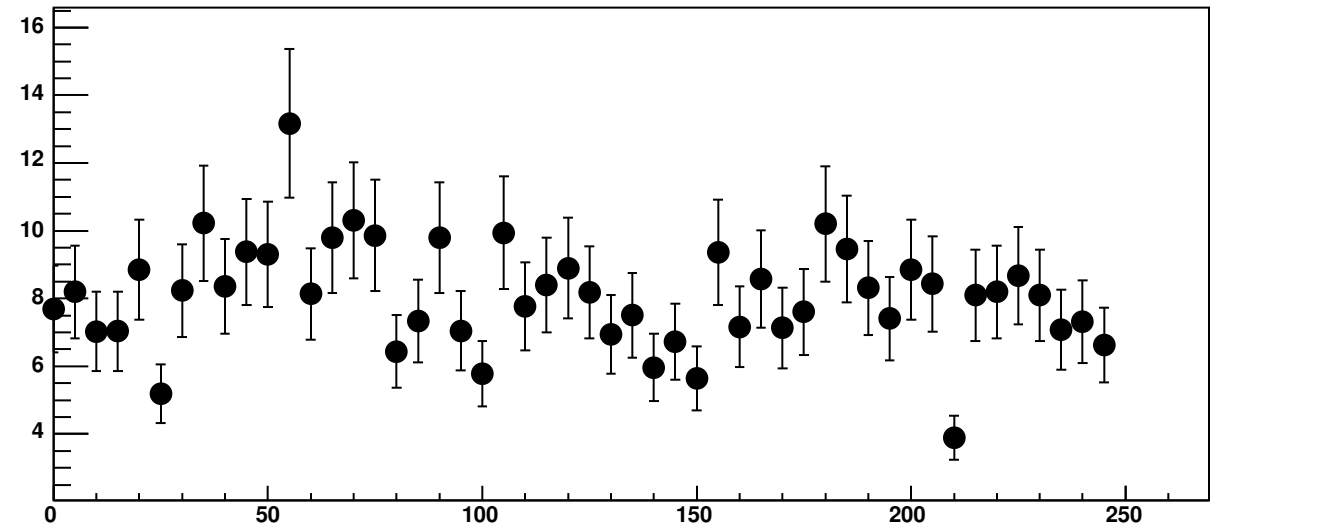
Chip 0, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold



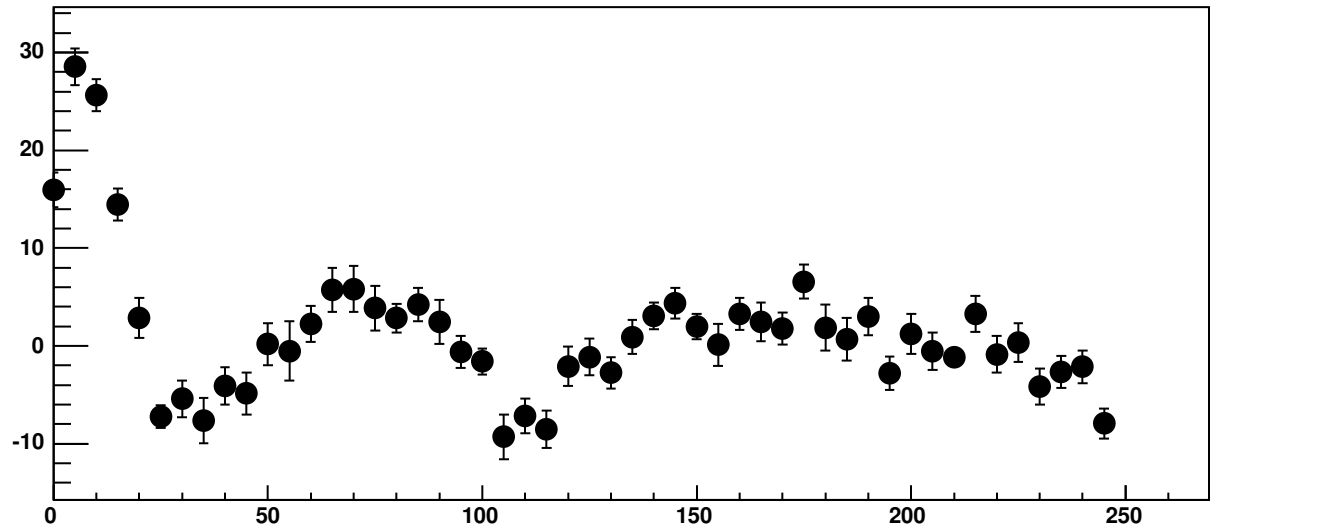
Chip 0, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

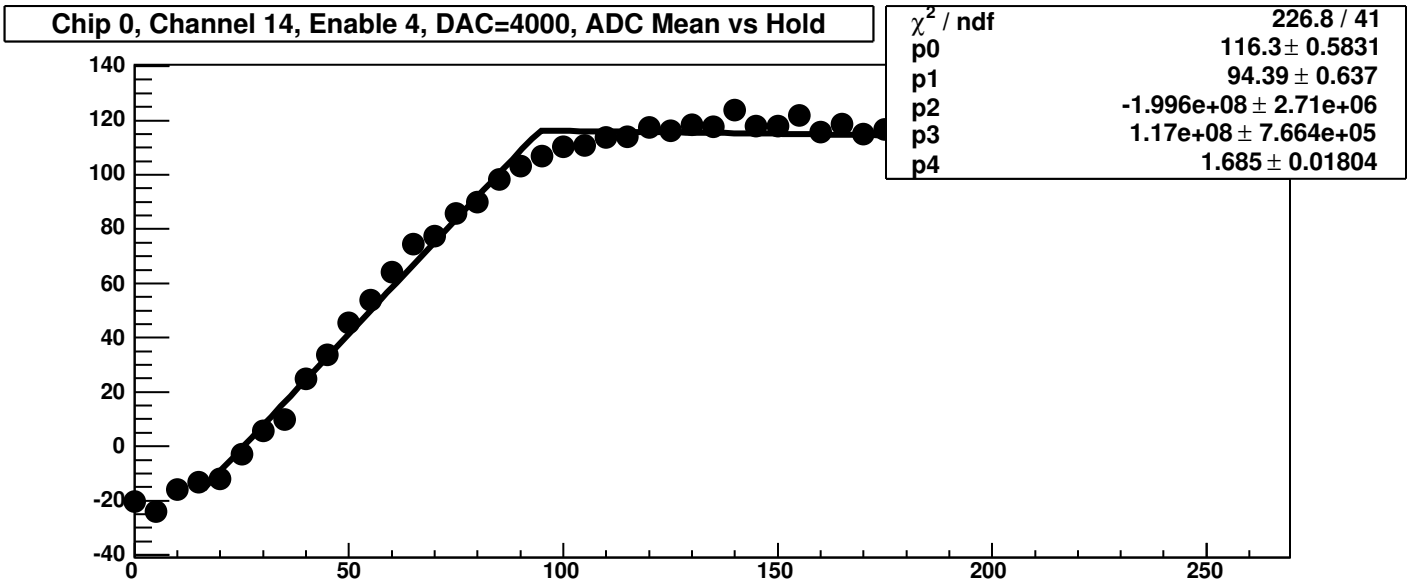


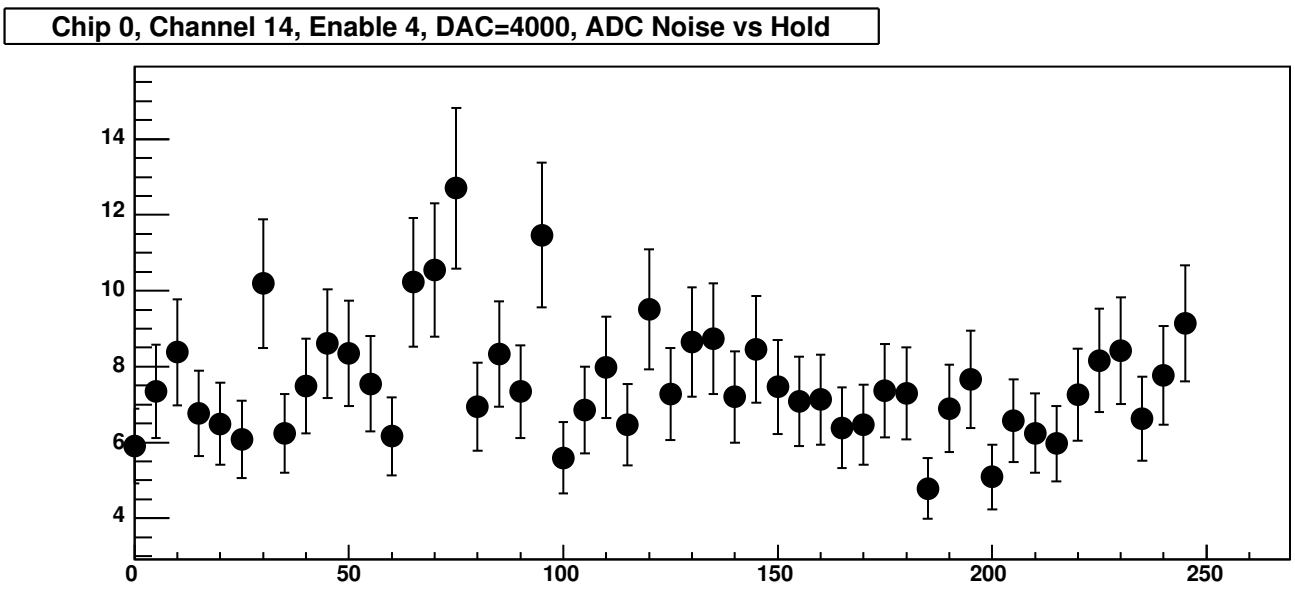
Chip 0, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold

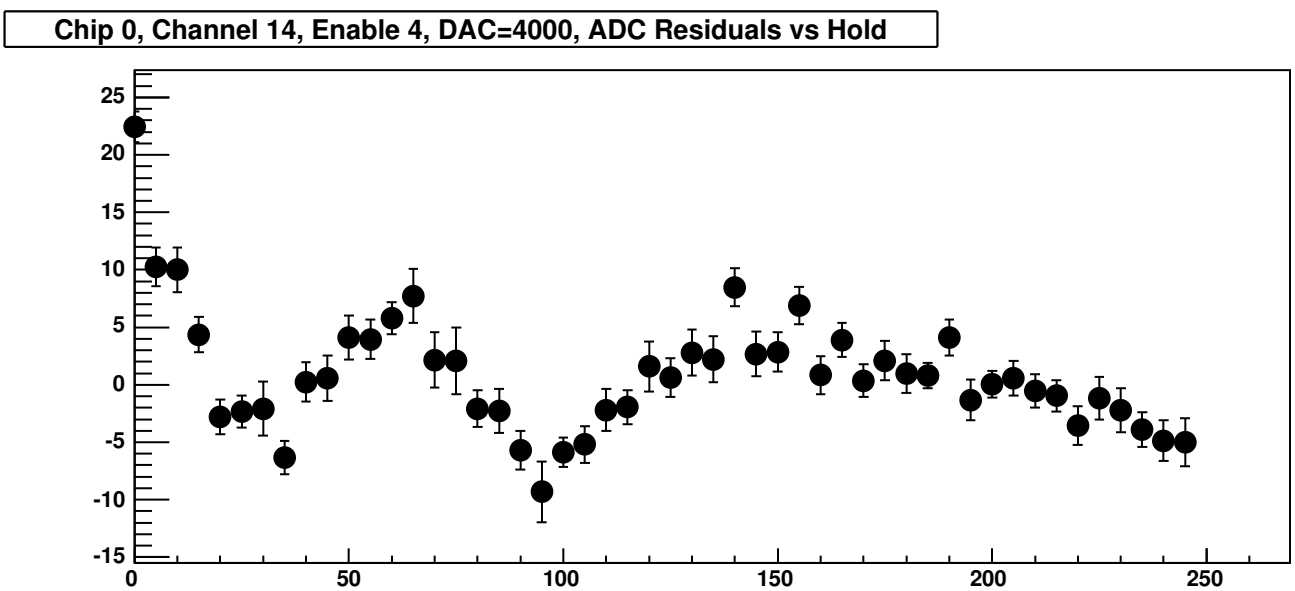


Chip 0, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

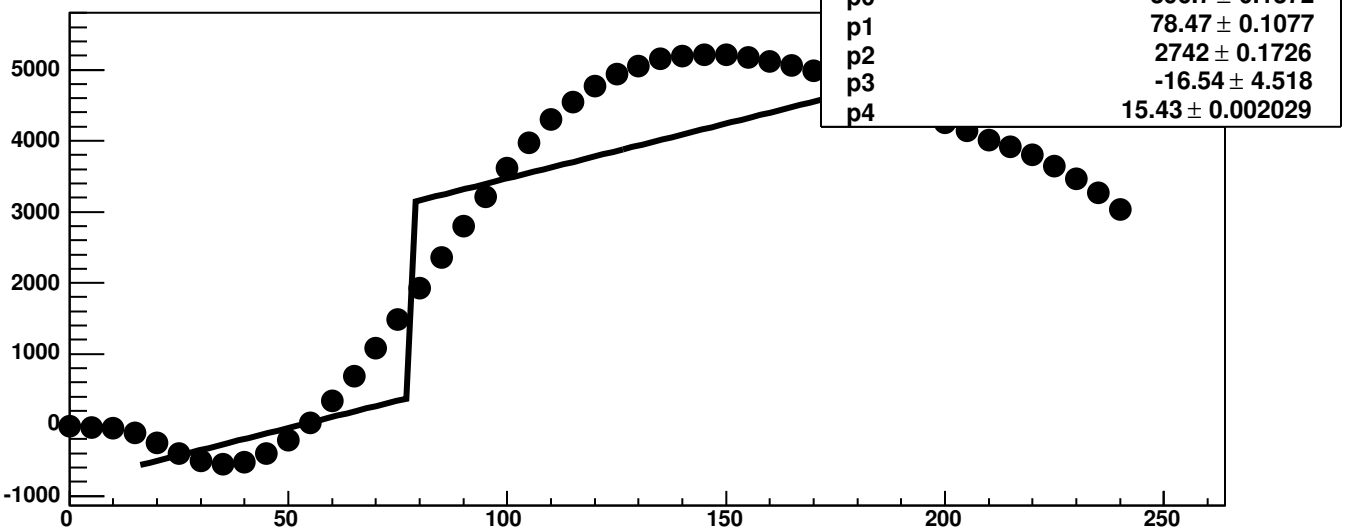



Chip 0, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 226.8 / 41
p0 116.3 ± 0.5831
p1 94.39 ± 0.637
p2 -1.996e+08 ± 2.71e+06
p3 1.17e+08 ± 7.664e+05
p4 1.685 ± 0.01804

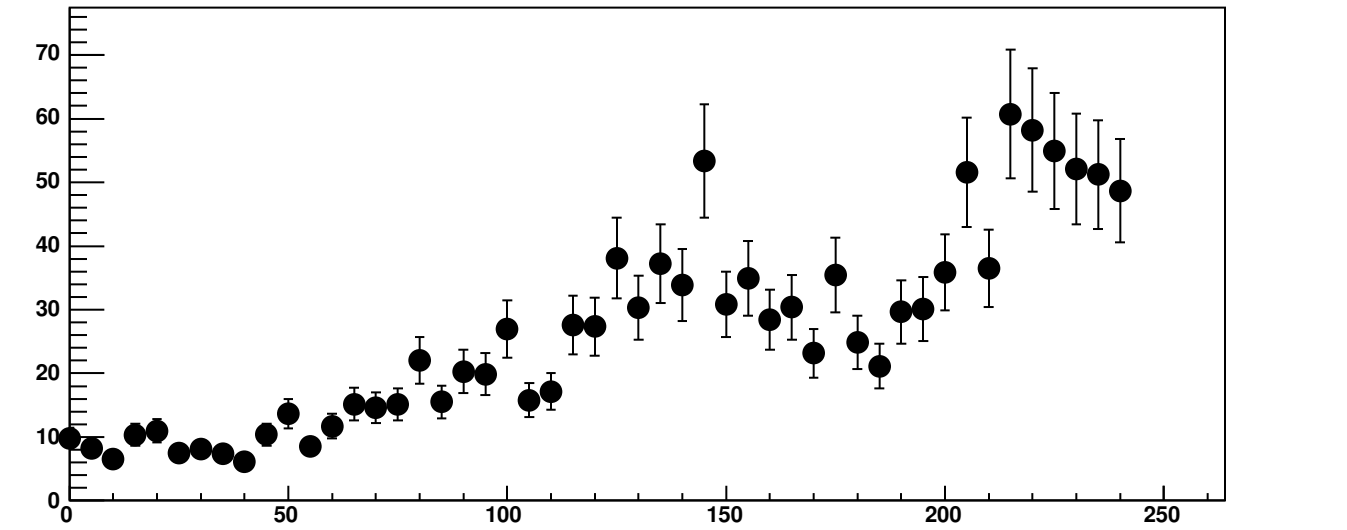

Chip 0, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold


Chip 0, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold

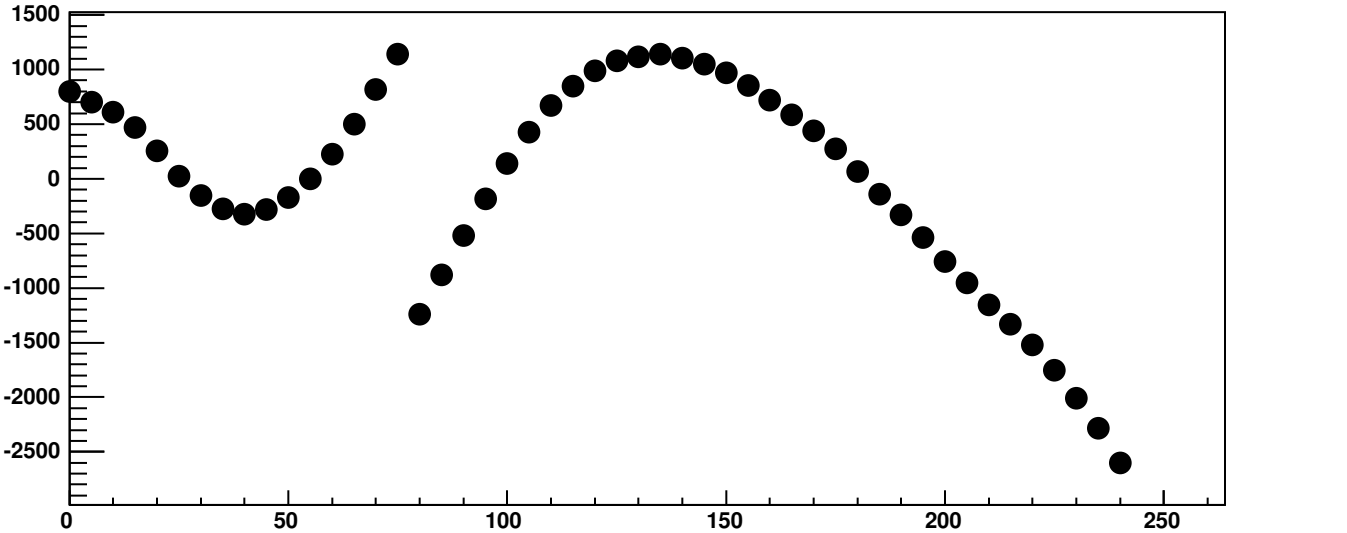
Chip 0, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold



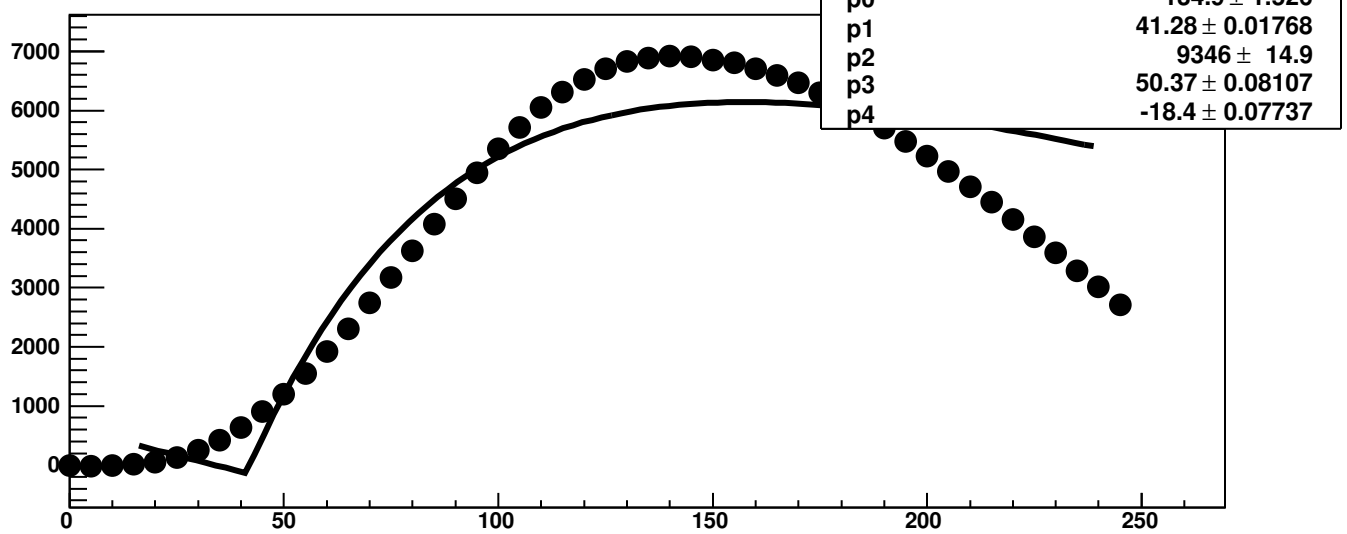
Chip 0, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold



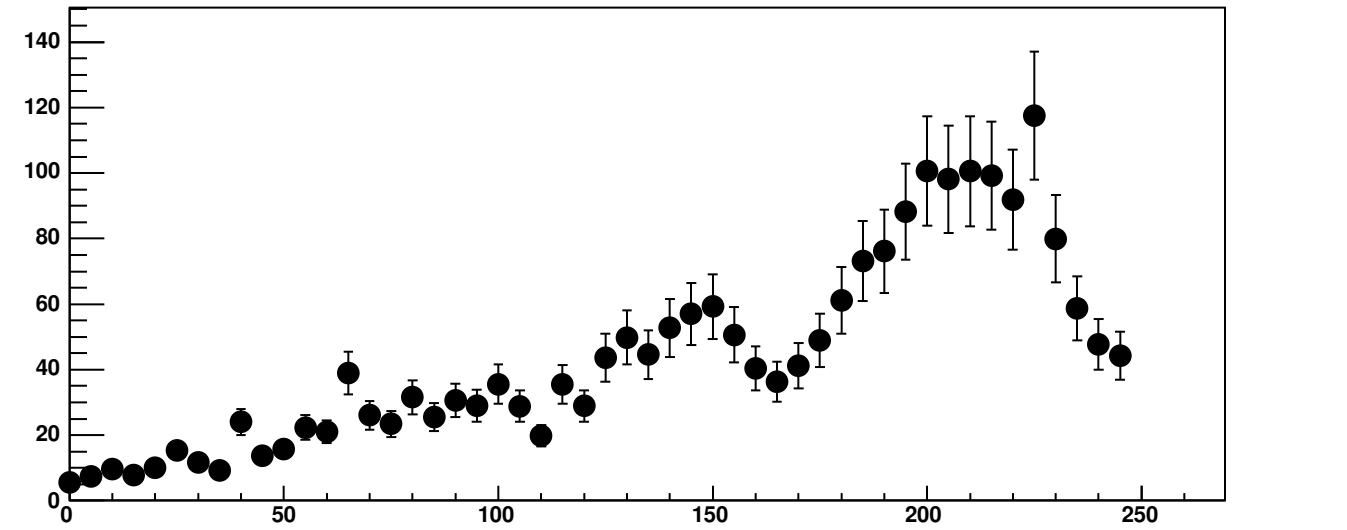
Chip 0, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold



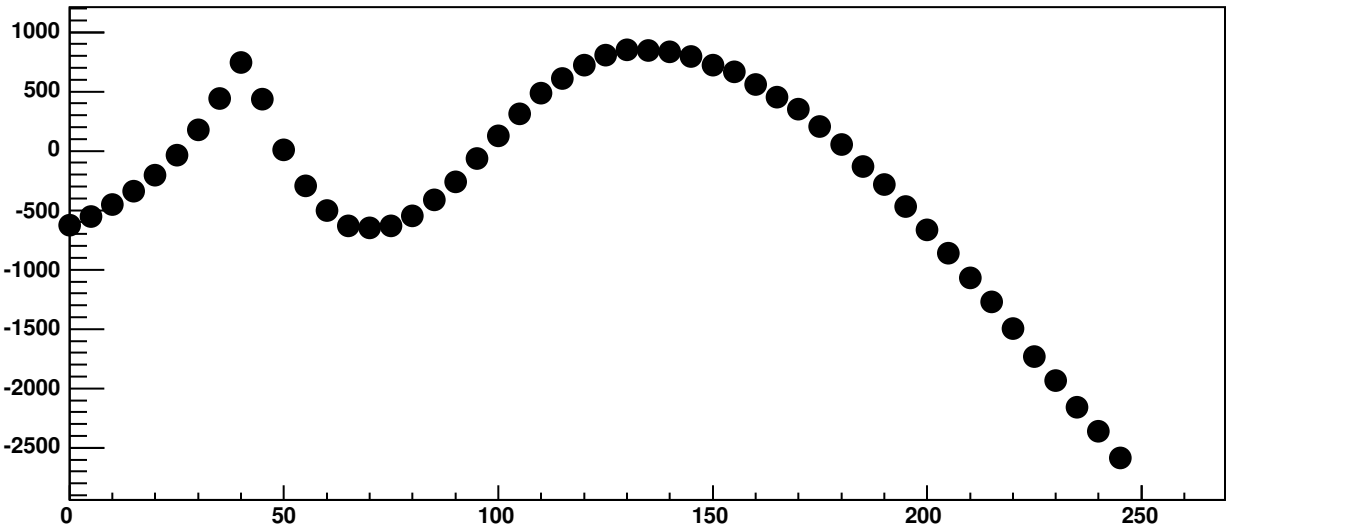
Chip 0, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold



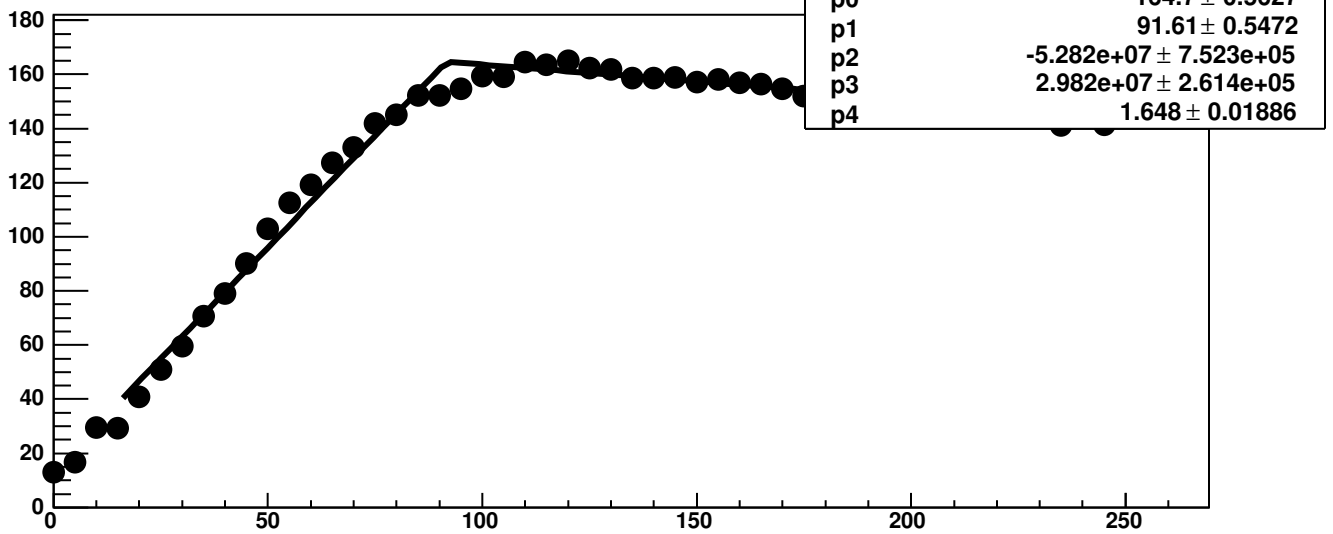
Chip 0, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



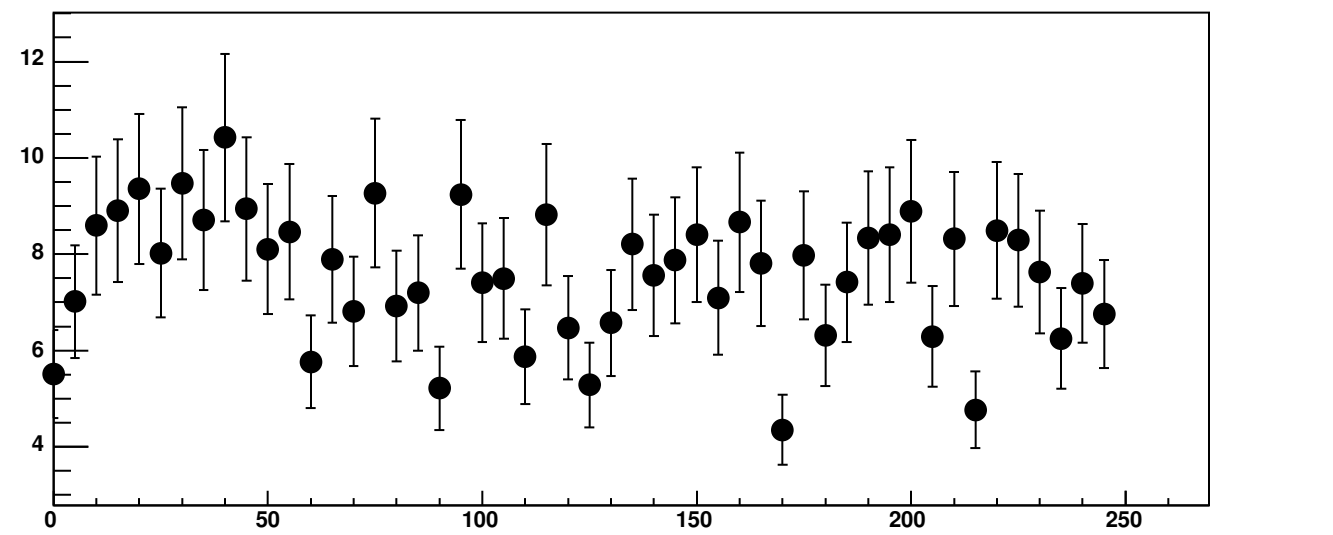
Chip 0, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold



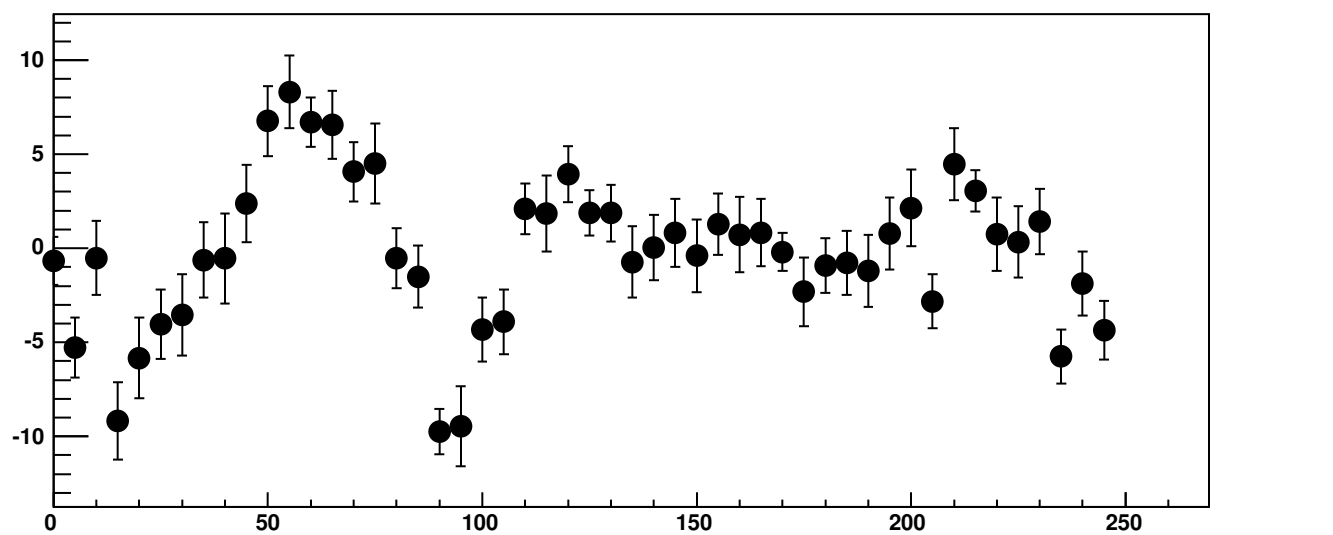
Chip 0, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 0, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold

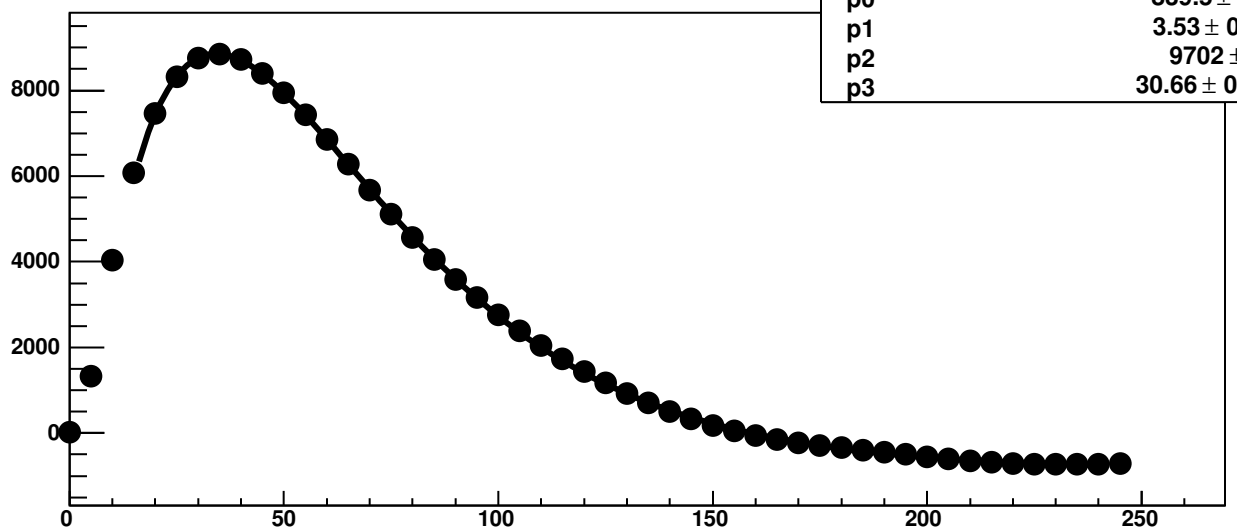


Chip 0, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

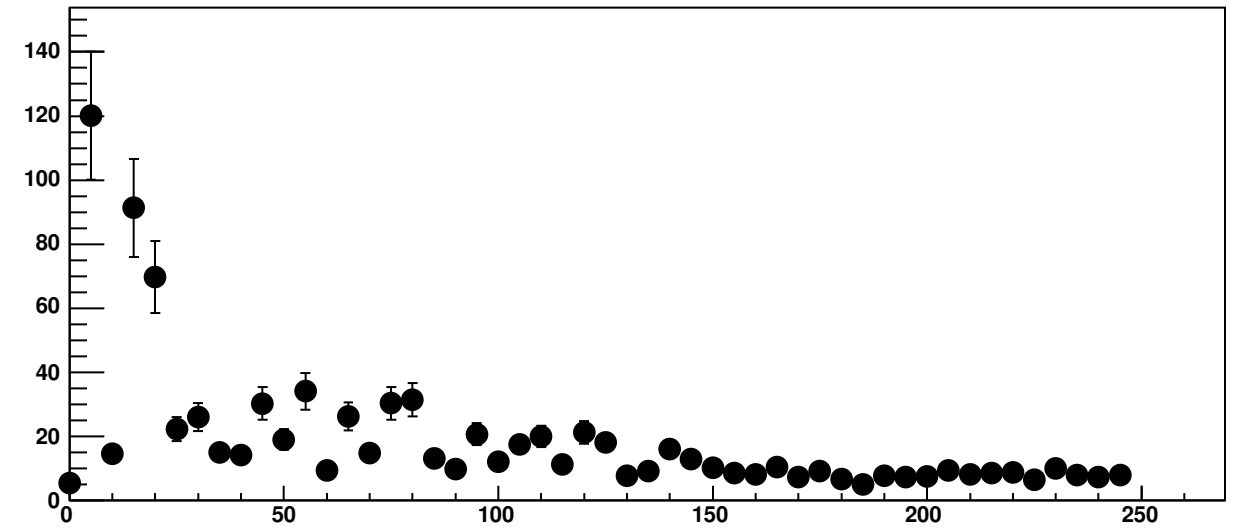


Chip 0, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold

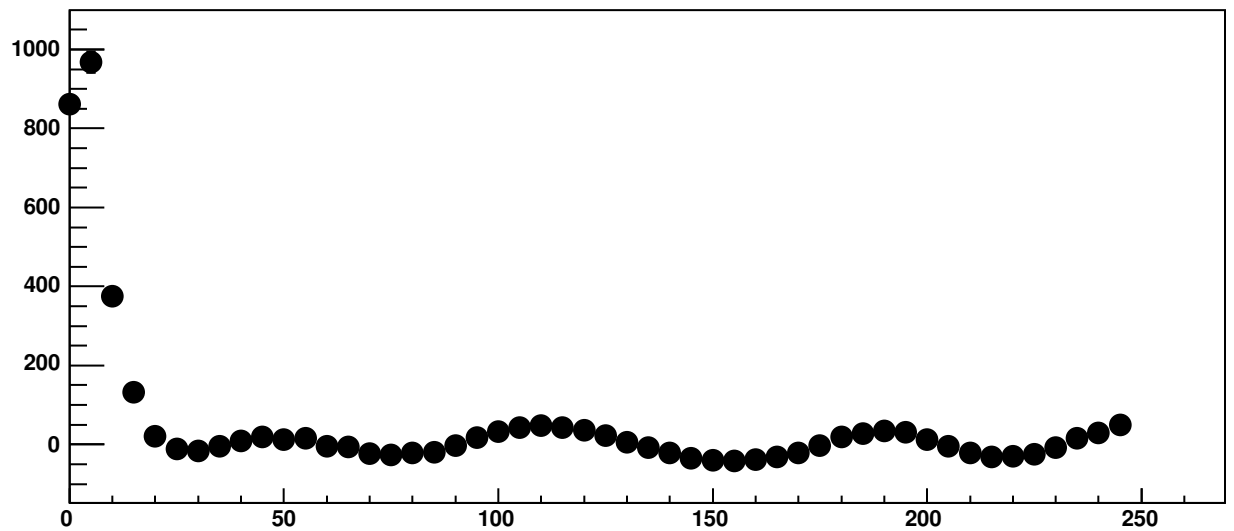
χ^2 / ndf	5384 / 42
p0	-839.5 ± 0.6658
p1	3.53 ± 0.02481
p2	9702 ± 1.793
p3	30.66 ± 0.01044



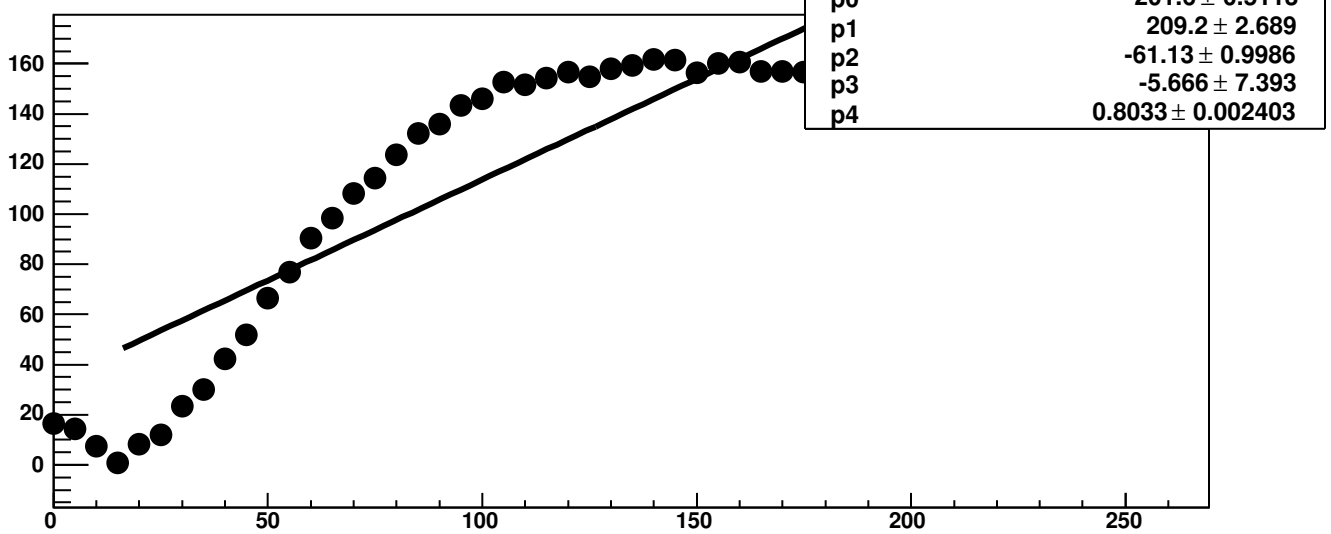
Chip 0, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold



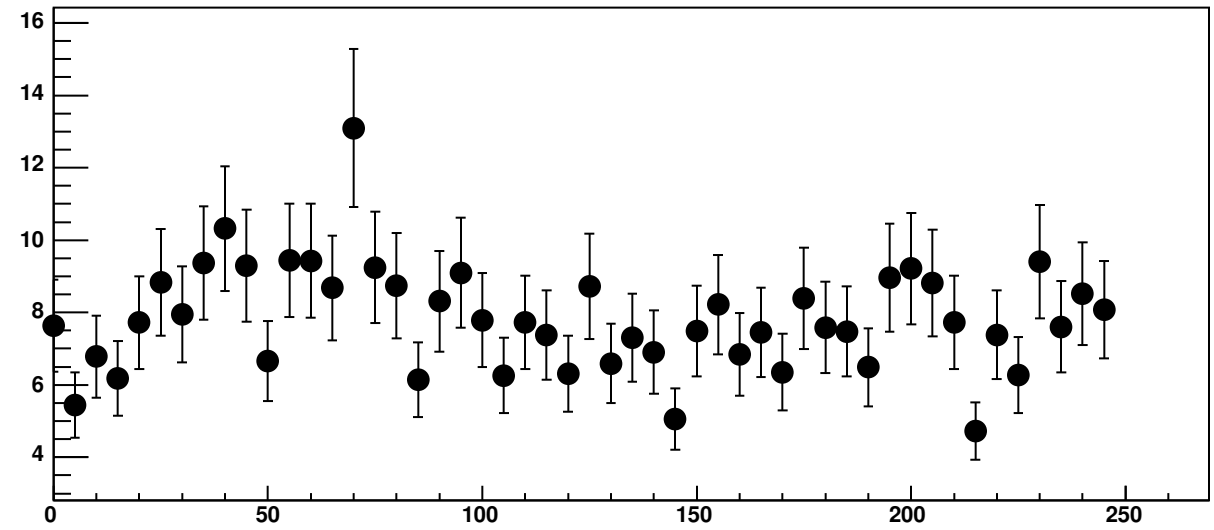
Chip 0, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold



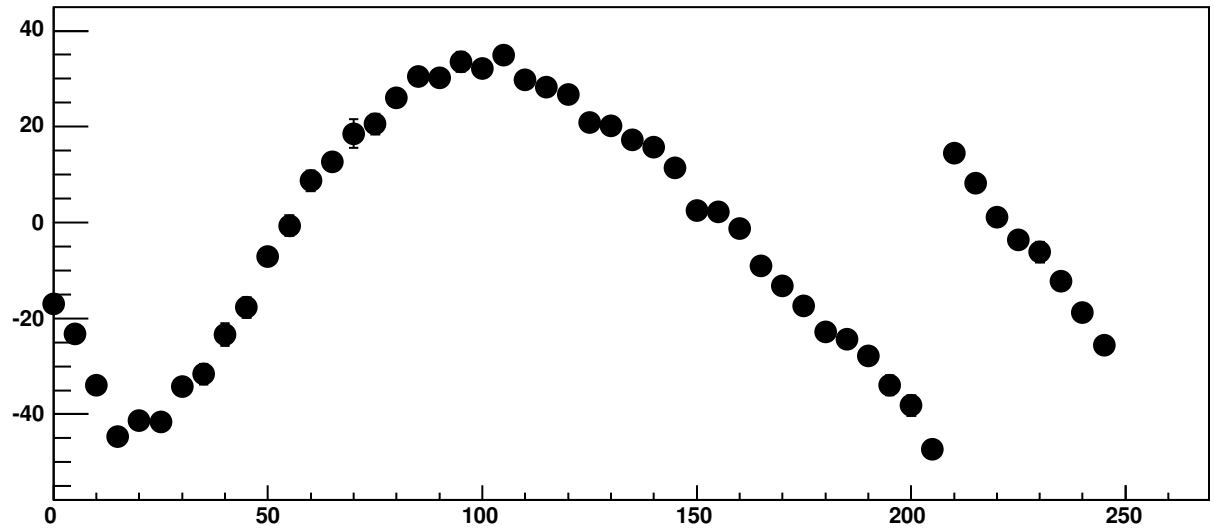
Chip 0, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

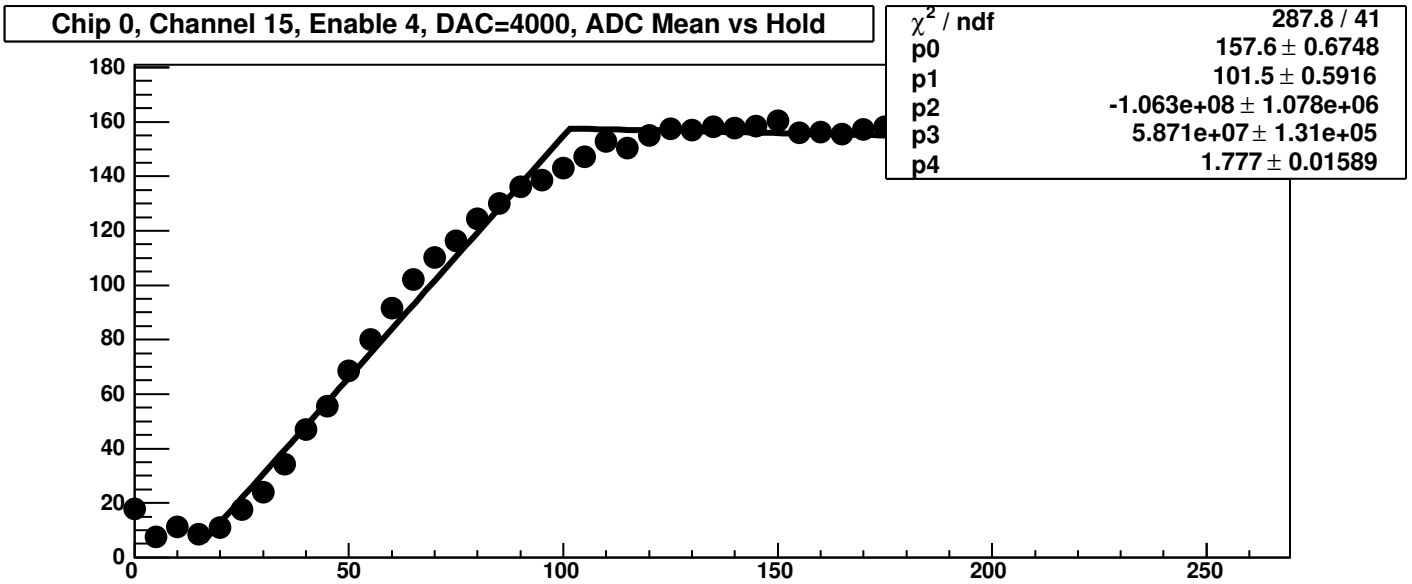


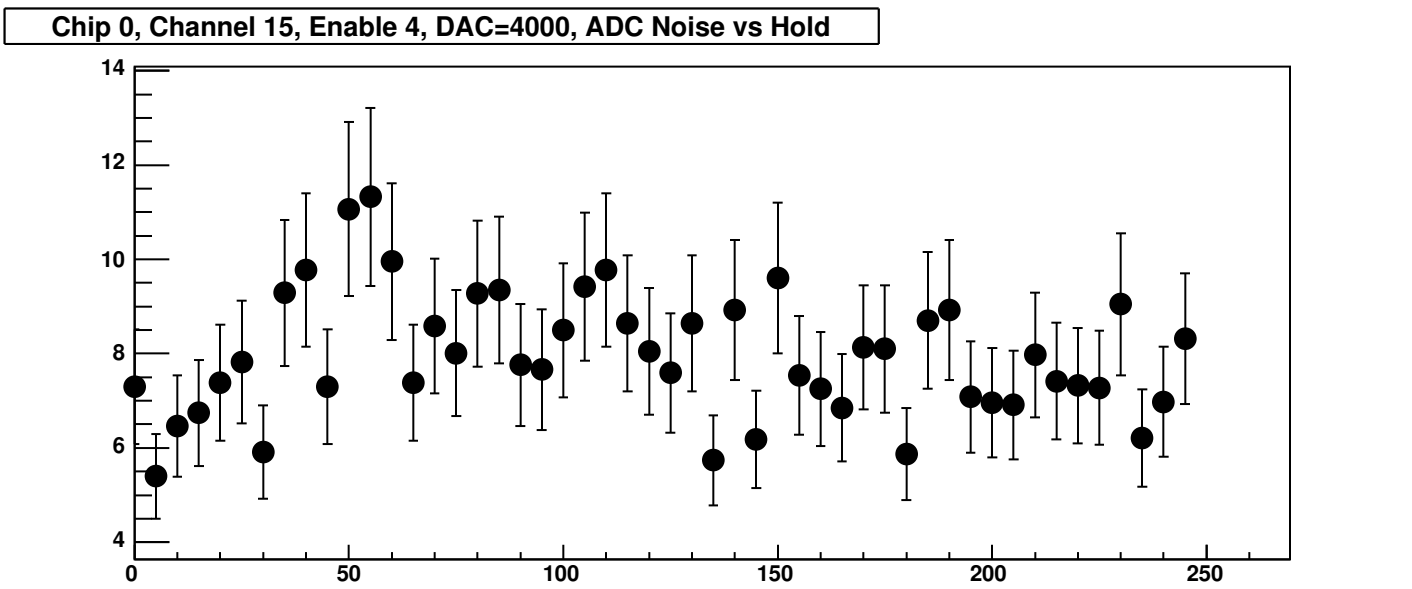
Chip 0, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold

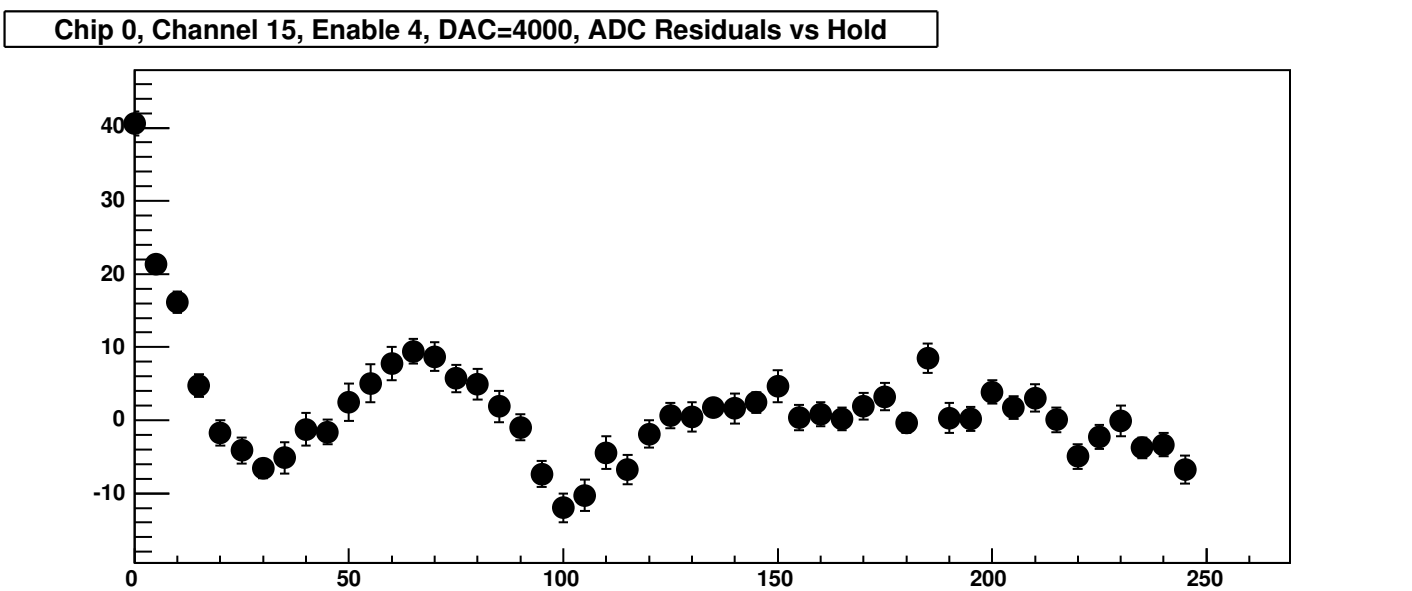


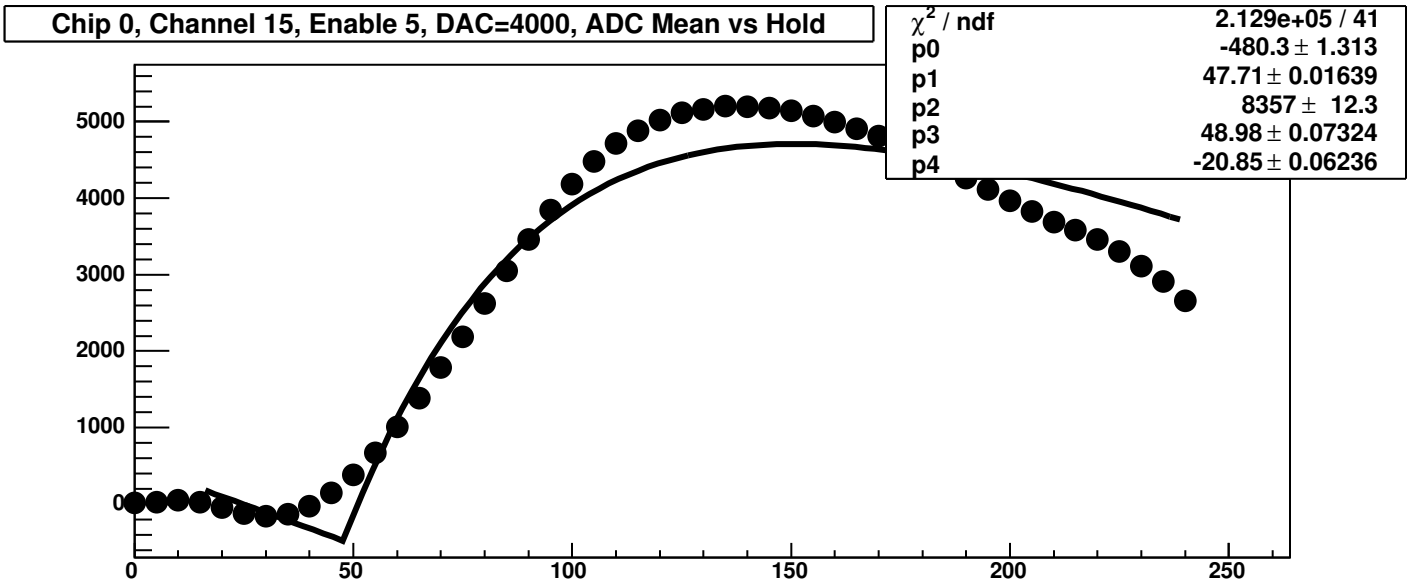
Chip 0, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold

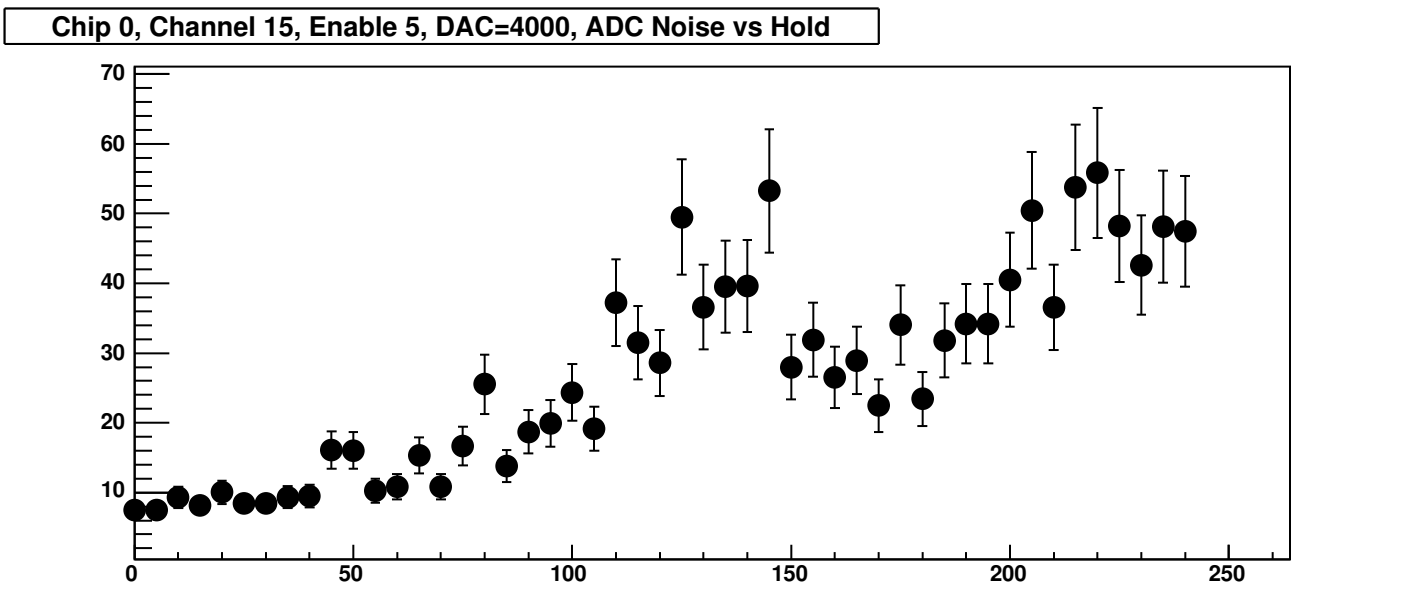


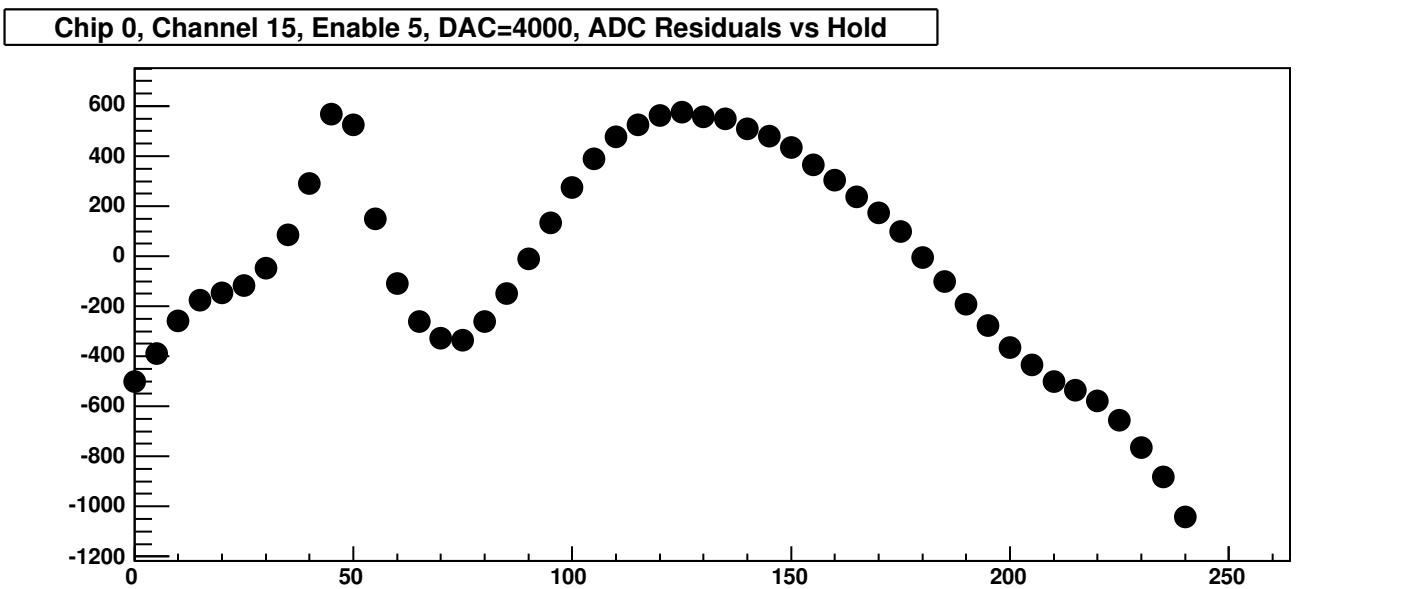

Chip 0, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 287.8 / 41
p0 157.6 ± 0.6748
p1 101.5 ± 0.5916
p2 -1.063e+08 ± 1.078e+06
p3 5.871e+07 ± 1.31e+05
p4 1.777 ± 0.01589

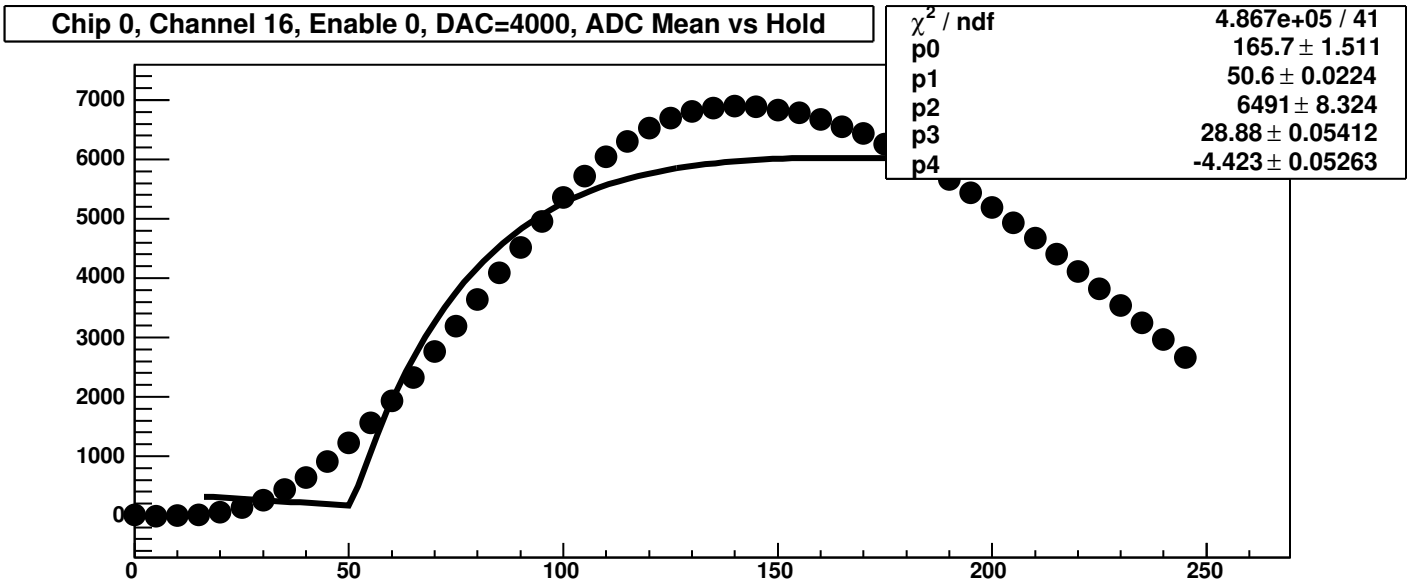

Chip 0, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold

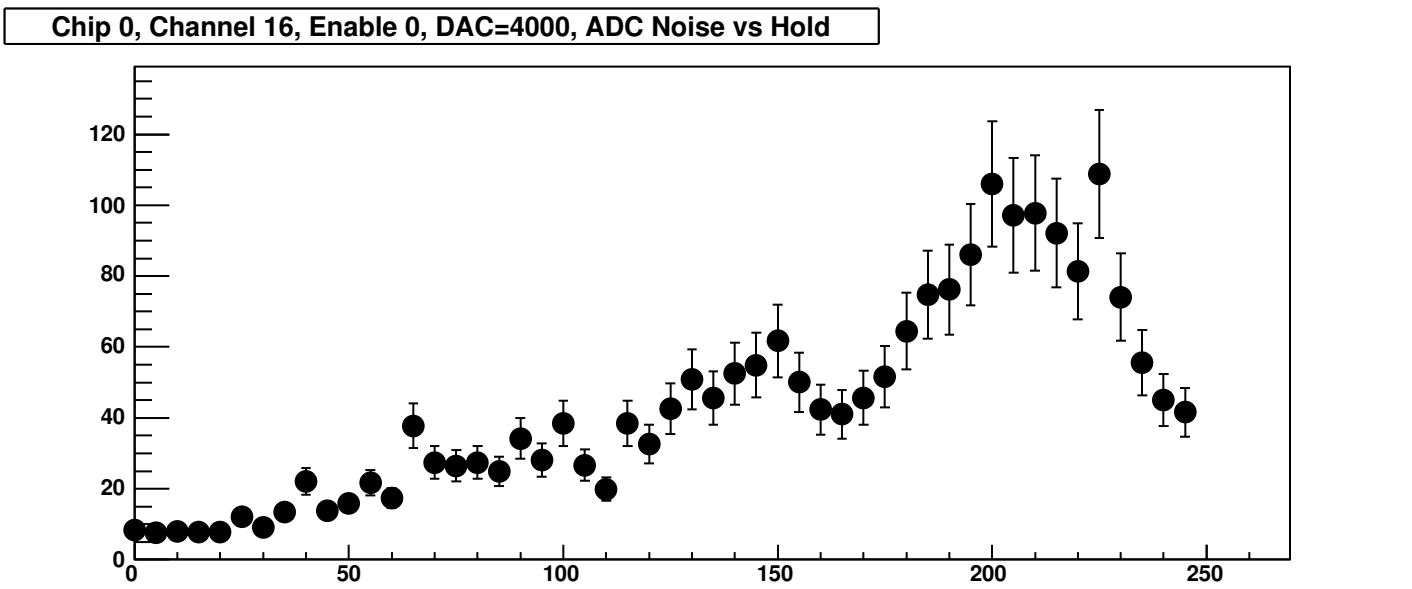

Chip 0, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

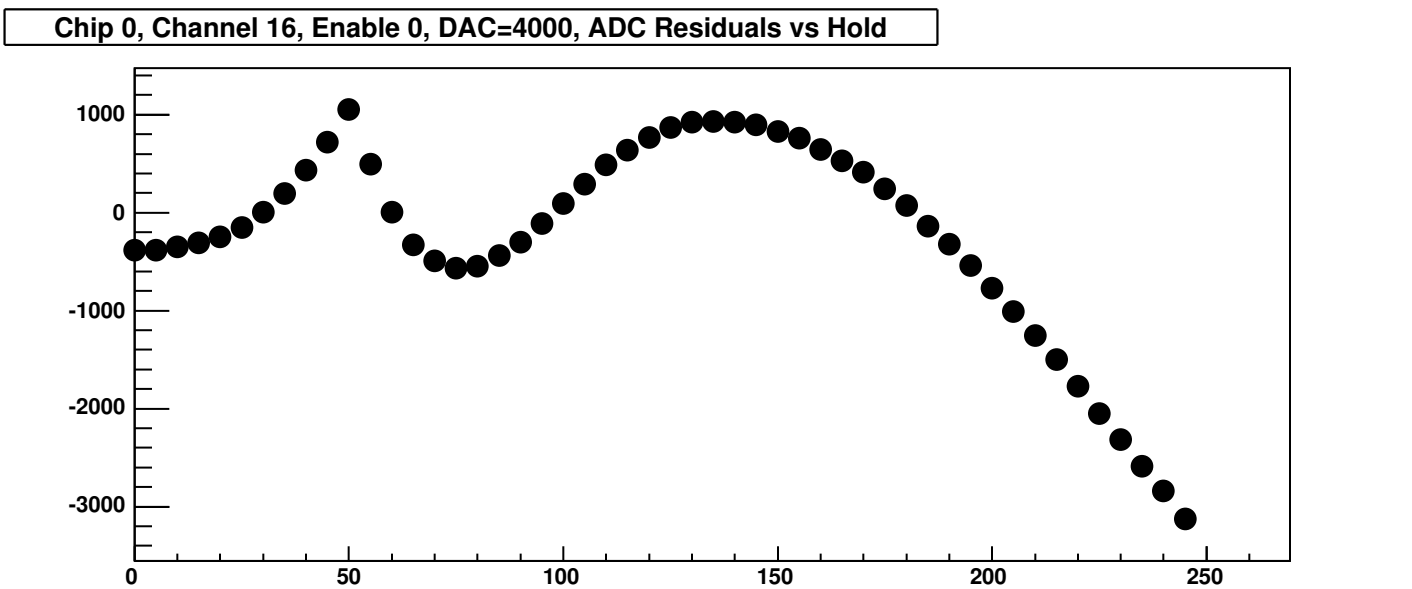

Chip 0, Channel 15, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.129e+05 / 41
p0 -480.3 ± 1.313
p1 47.71 ± 0.01639
p2 8357 ± 12.3
p3 48.98 ± 0.07324
p4 -20.85 ± 0.06236
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 0, Channel 15, Enable 5, DAC=4000, ADC Noise vs Hold
70
60
50
40
30
20
10
0
50
100
150
200
250

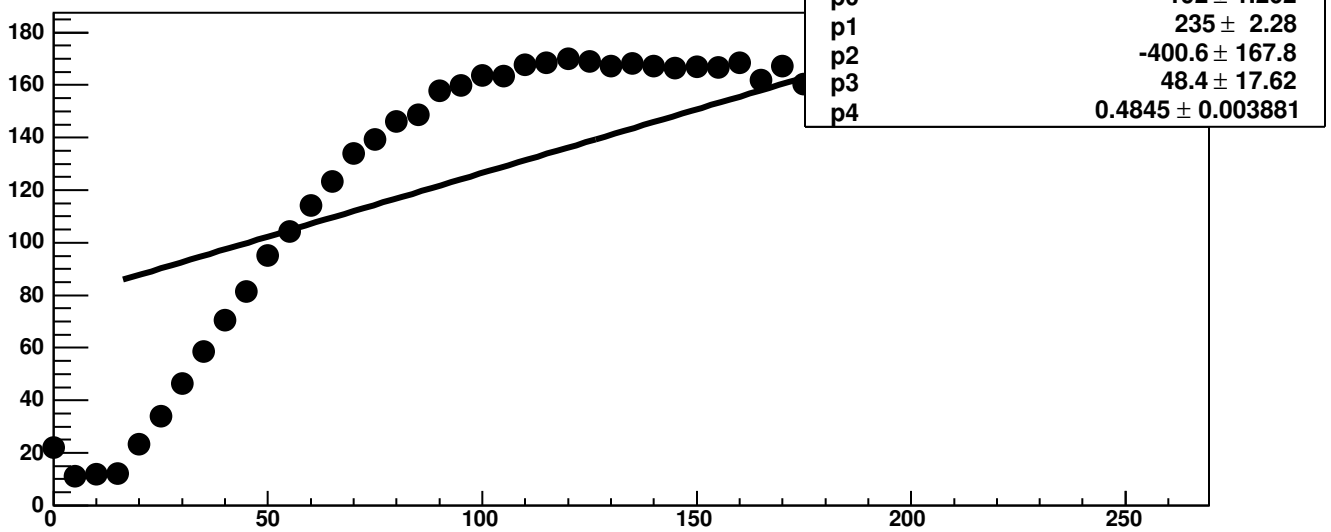

Chip 0, Channel 15, Enable 5, DAC=4000, ADC Residuals vs Hold
600
400
200
0
-200
-400
-600
-800
-1000
-1200
0
50
100
150
200
250


Chip 0, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.867e+05 / 41
p0 165.7 ± 1.511
p1 50.6 ± 0.0224
p2 6491 ± 8.324
p3 28.88 ± 0.05412
p4 -4.423 ± 0.05263
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

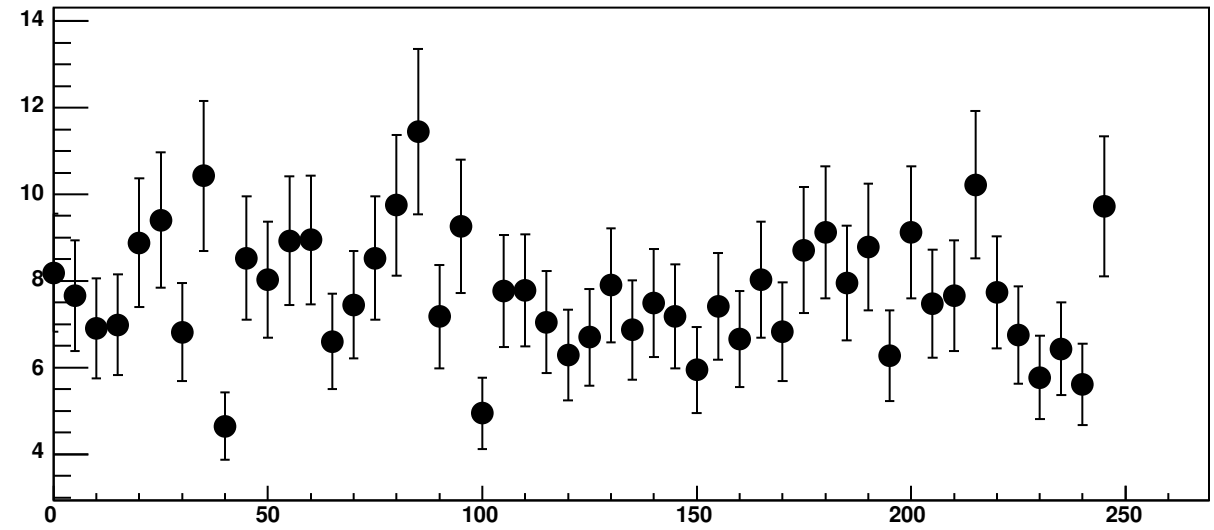

Chip 0, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

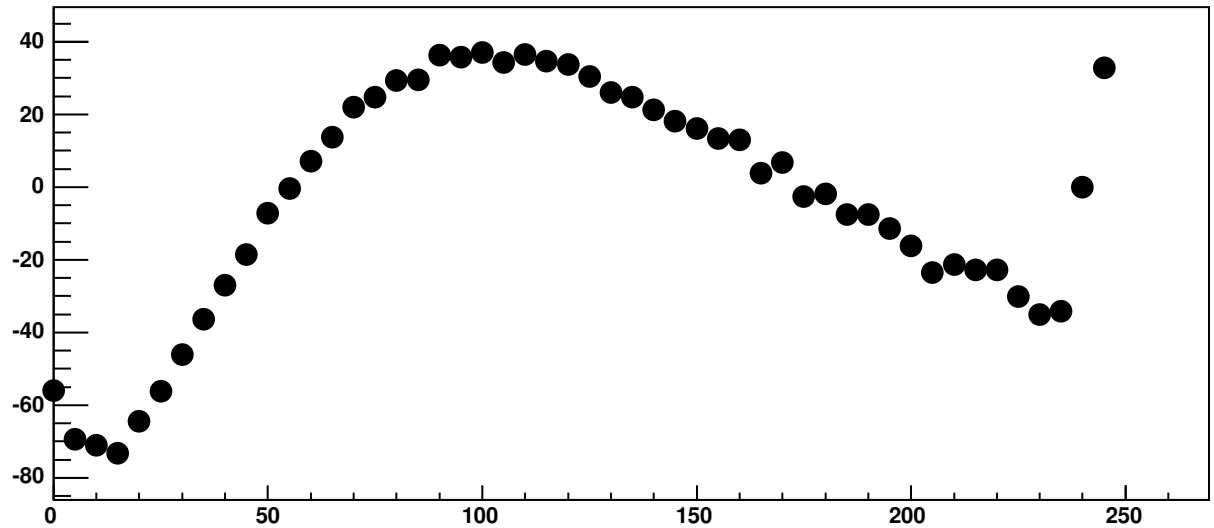
Chip 0, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold



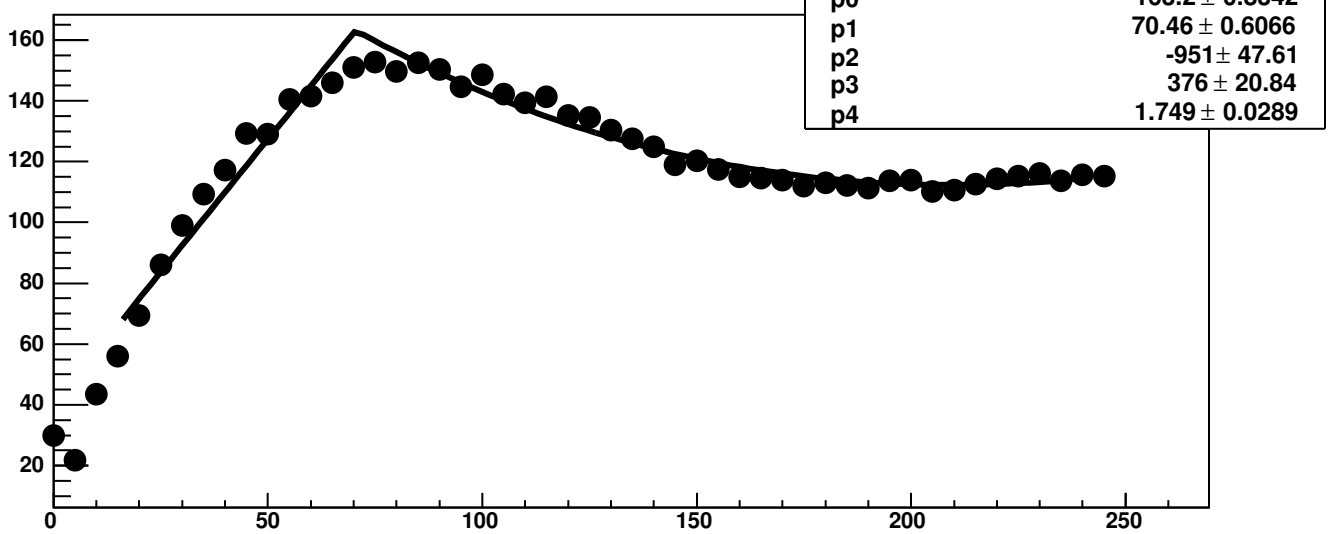
Chip 0, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



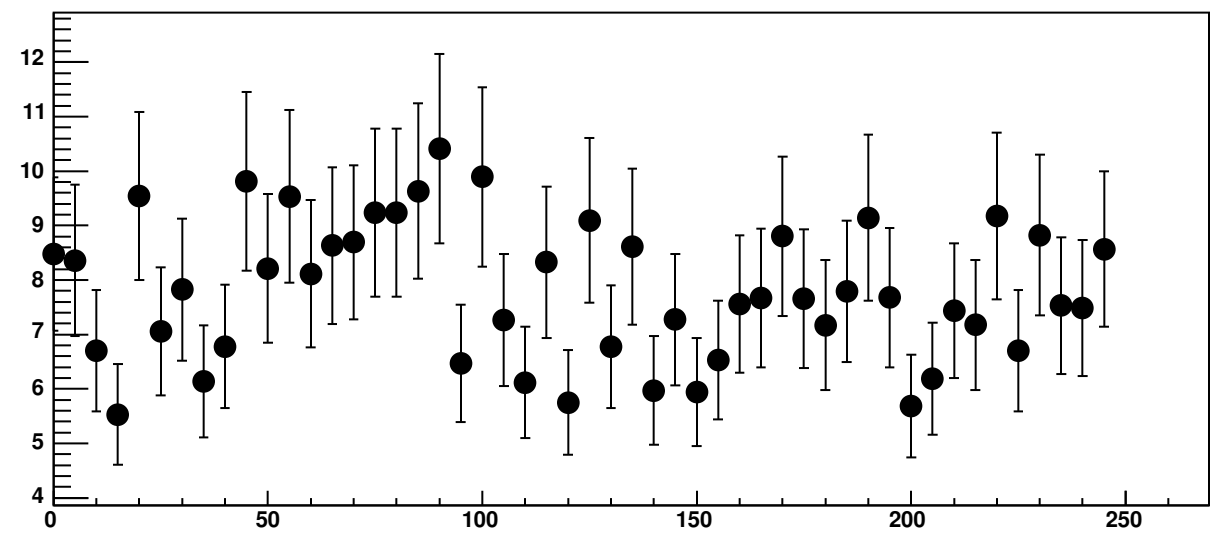
Chip 0, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold



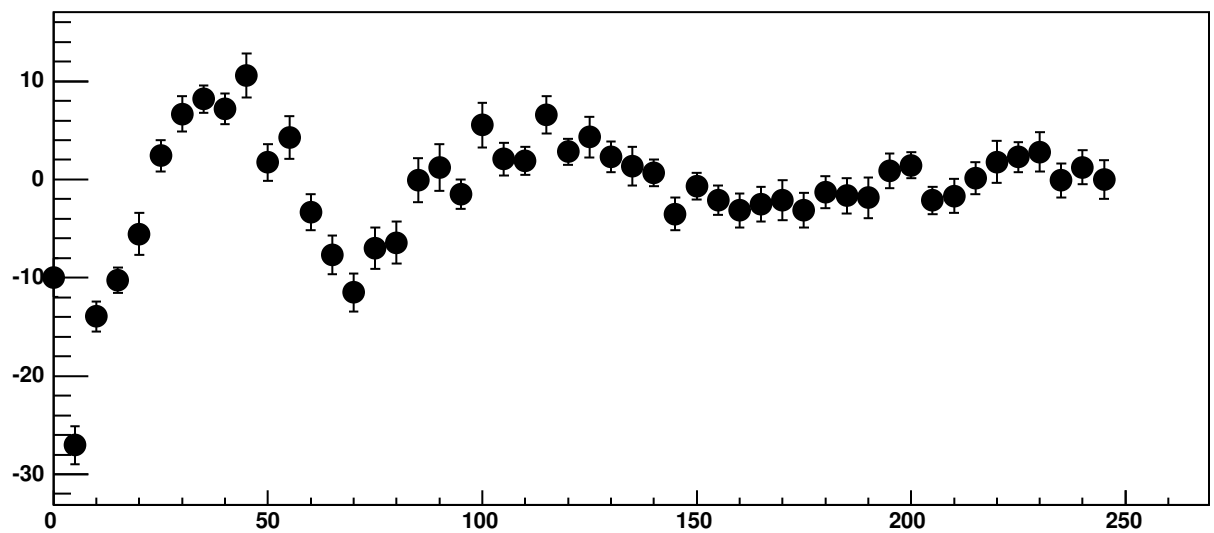
Chip 0, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold

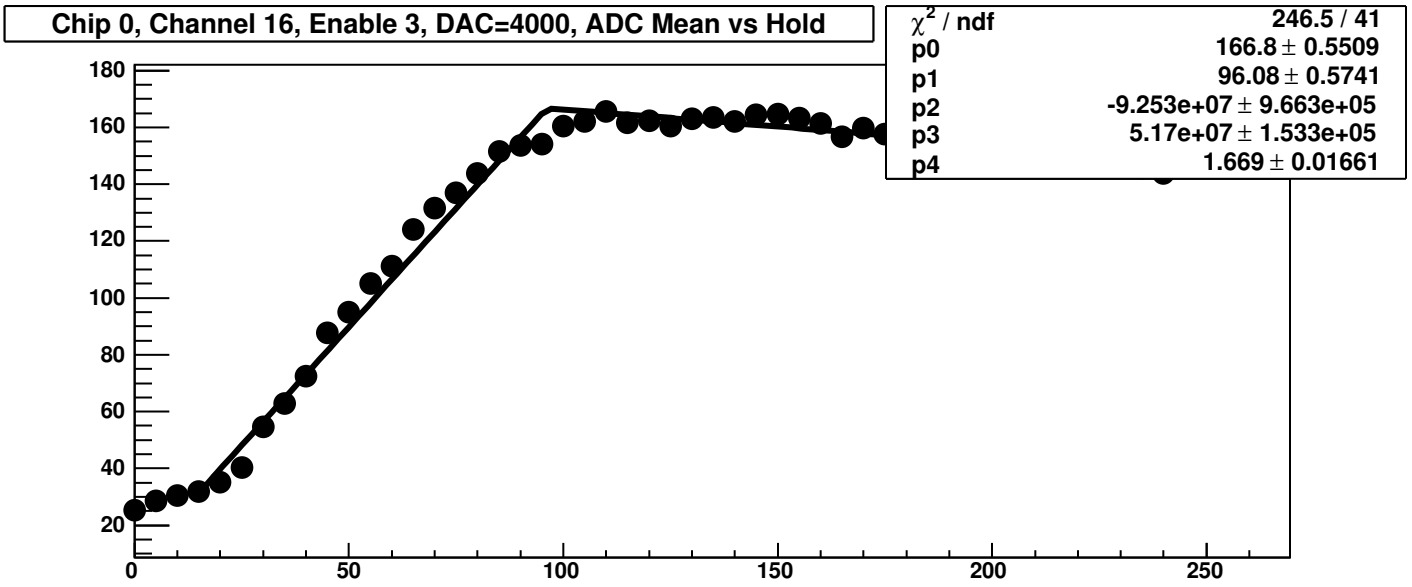


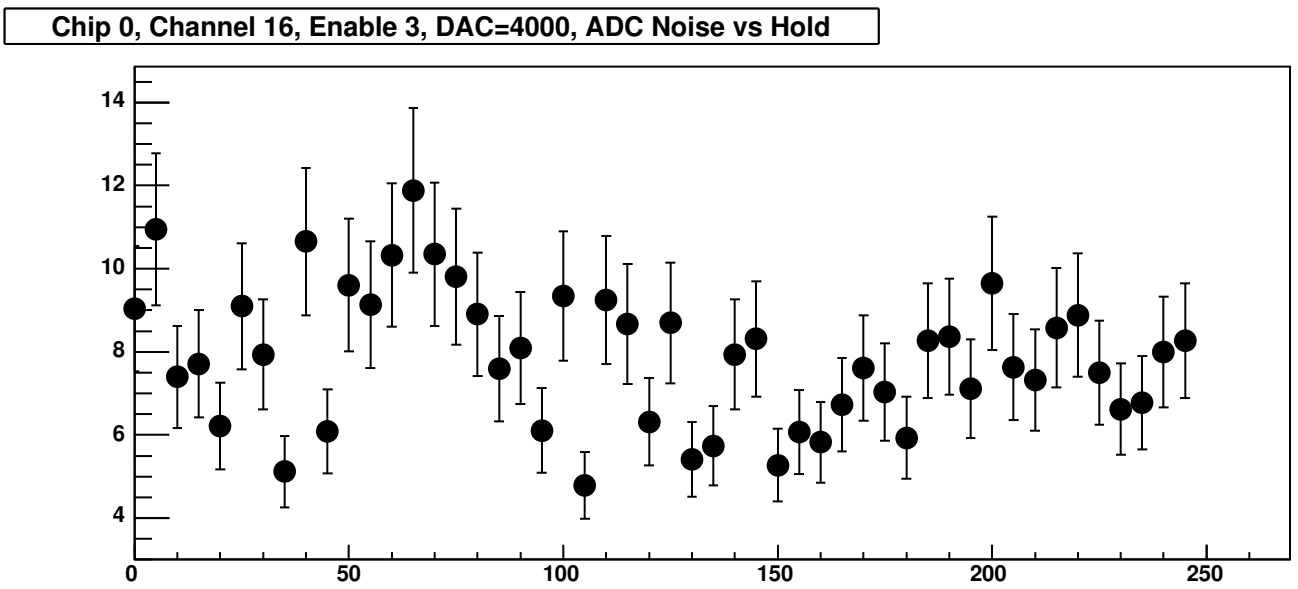
Chip 0, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold

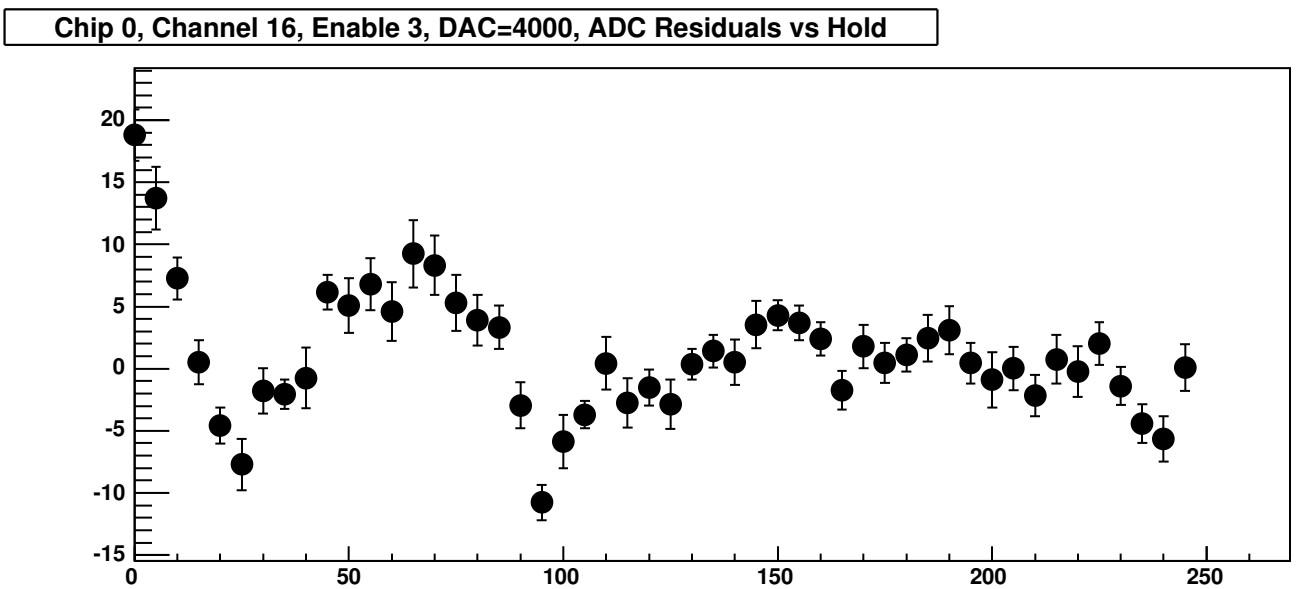


Chip 0, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold

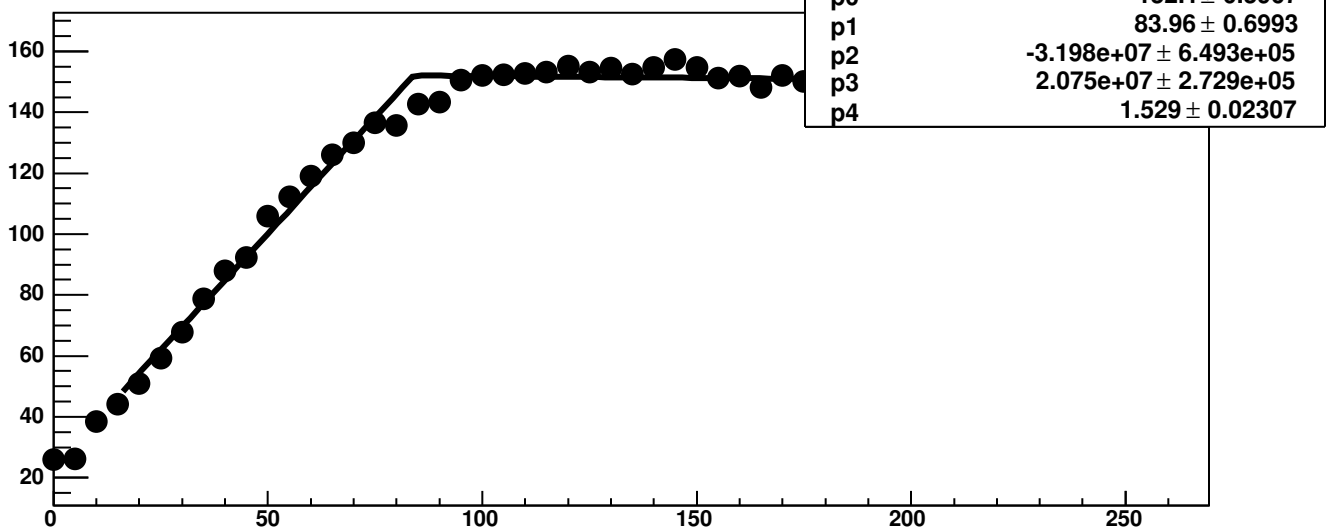



Chip 0, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 246.5 / 41
p0 166.8 ± 0.5509
p1 96.08 ± 0.5741
p2 -9.253e+07 ± 9.663e+05
p3 5.17e+07 ± 1.533e+05
p4 1.669 ± 0.01661

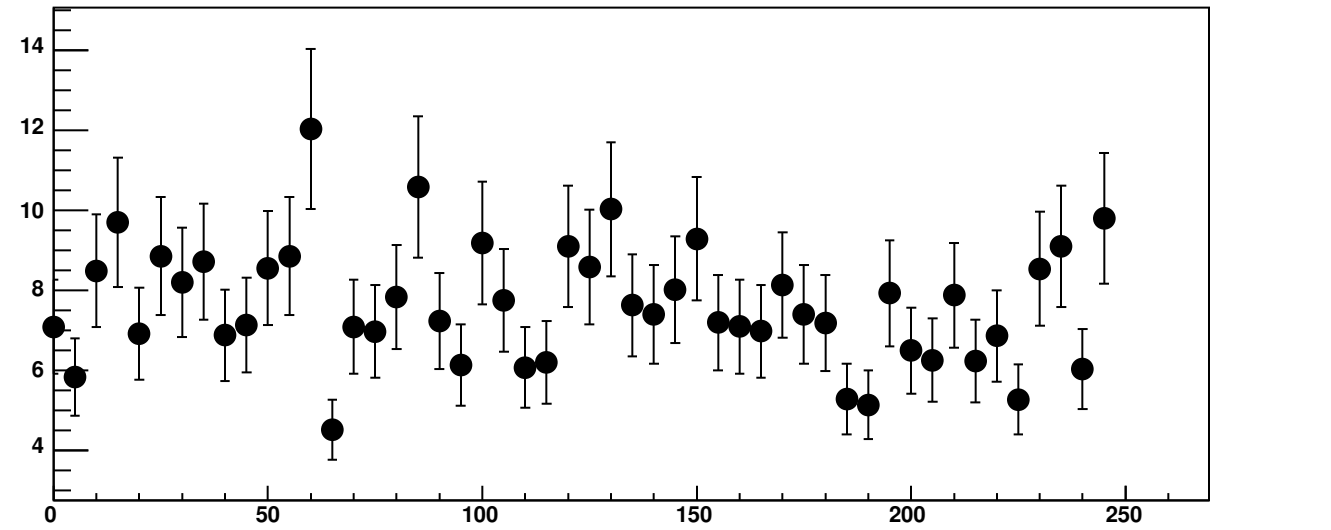

Chip 0, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold


Chip 0, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

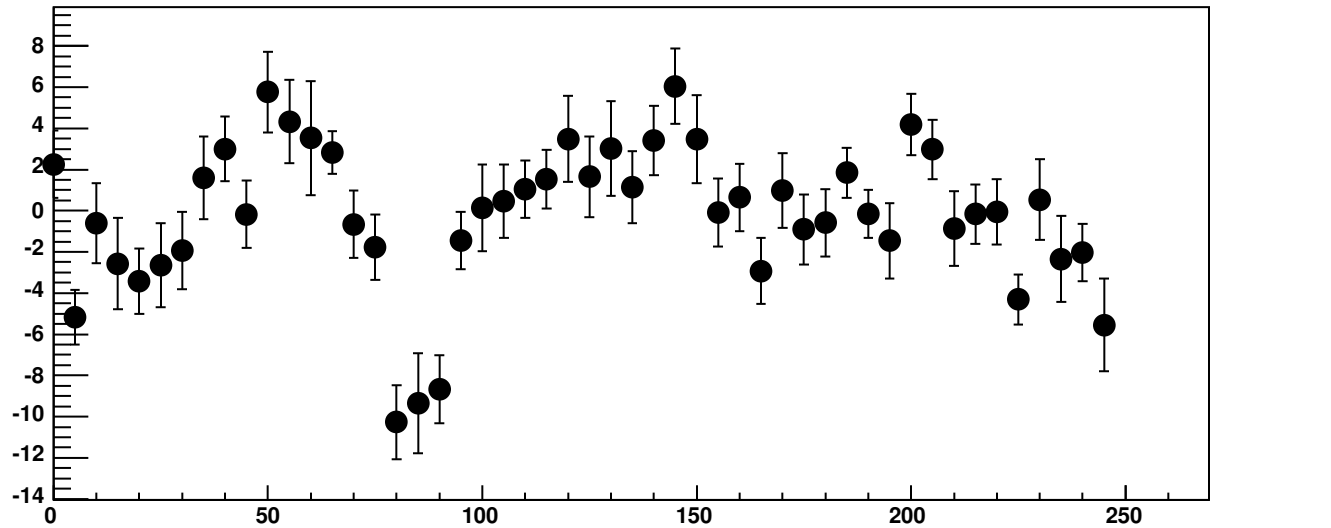
Chip 0, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

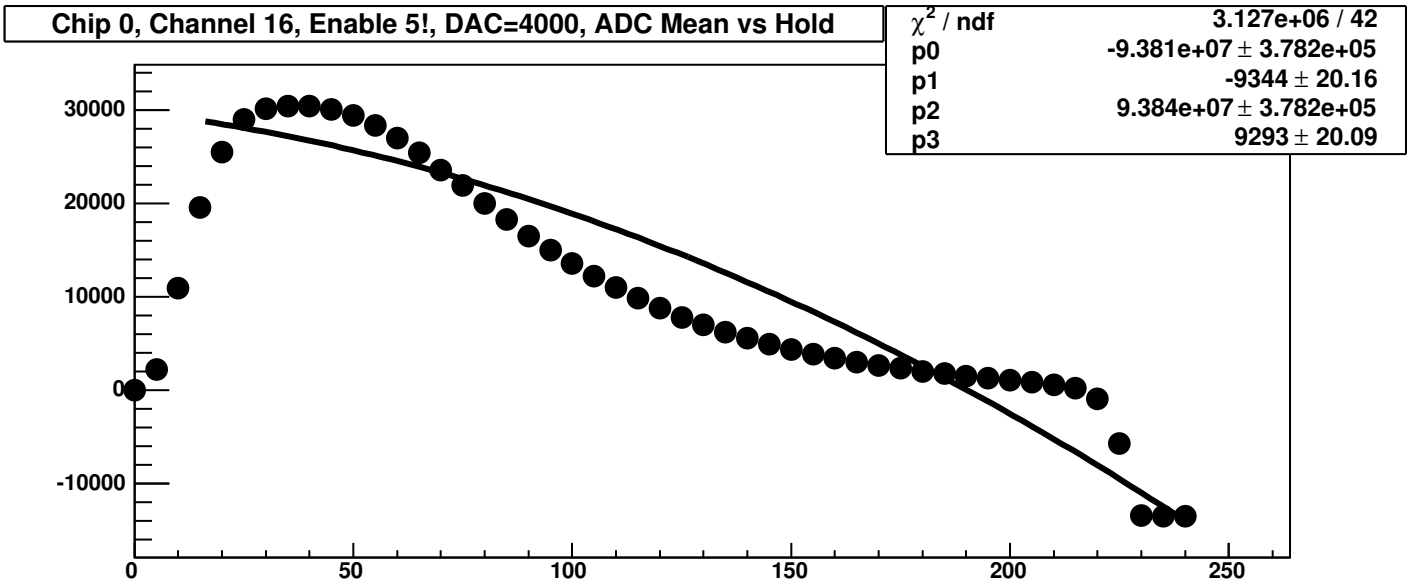


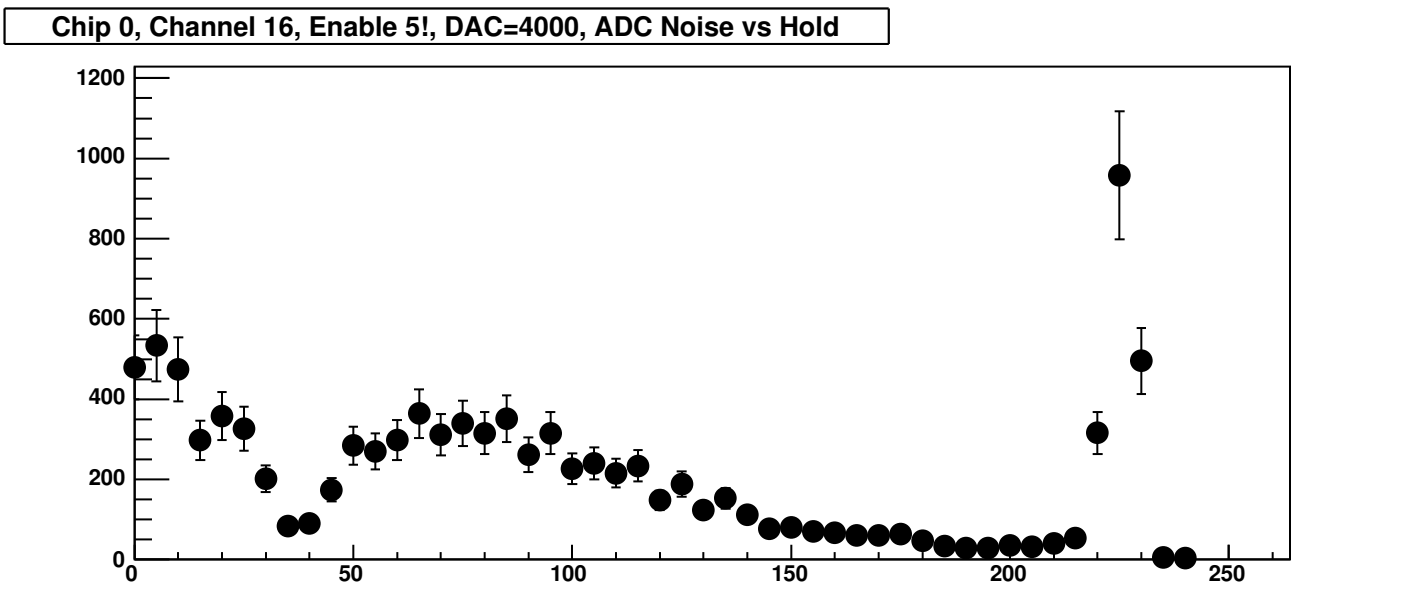
Chip 0, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold

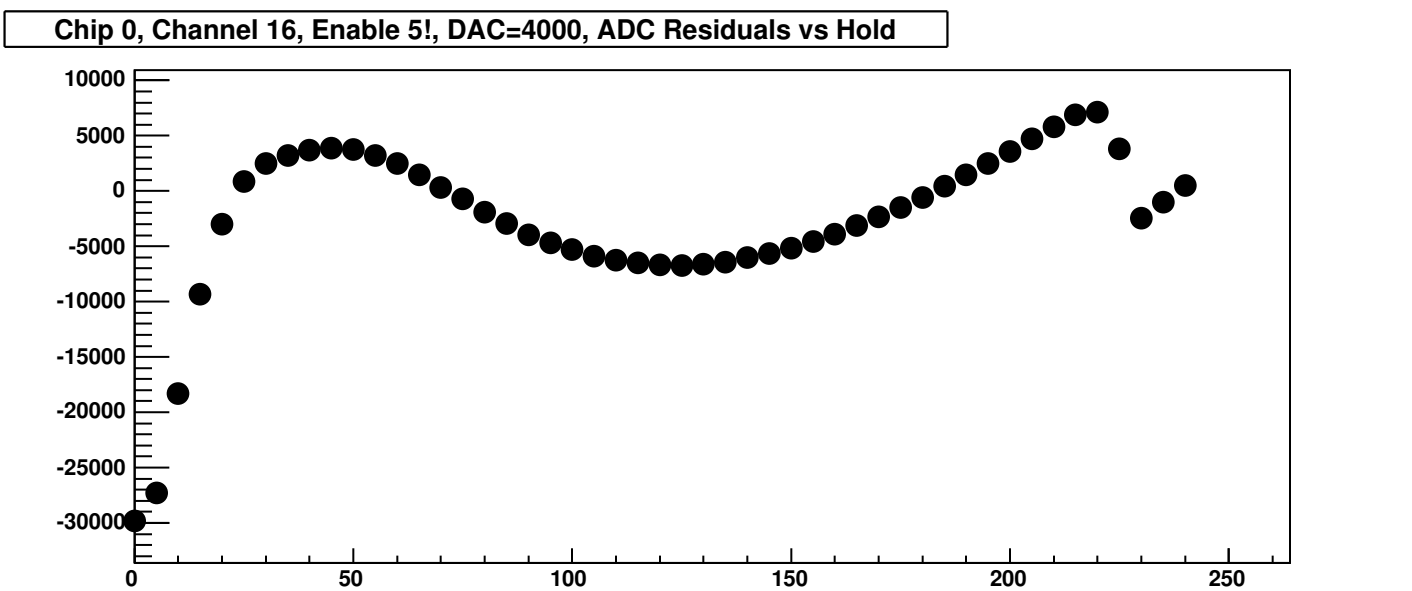


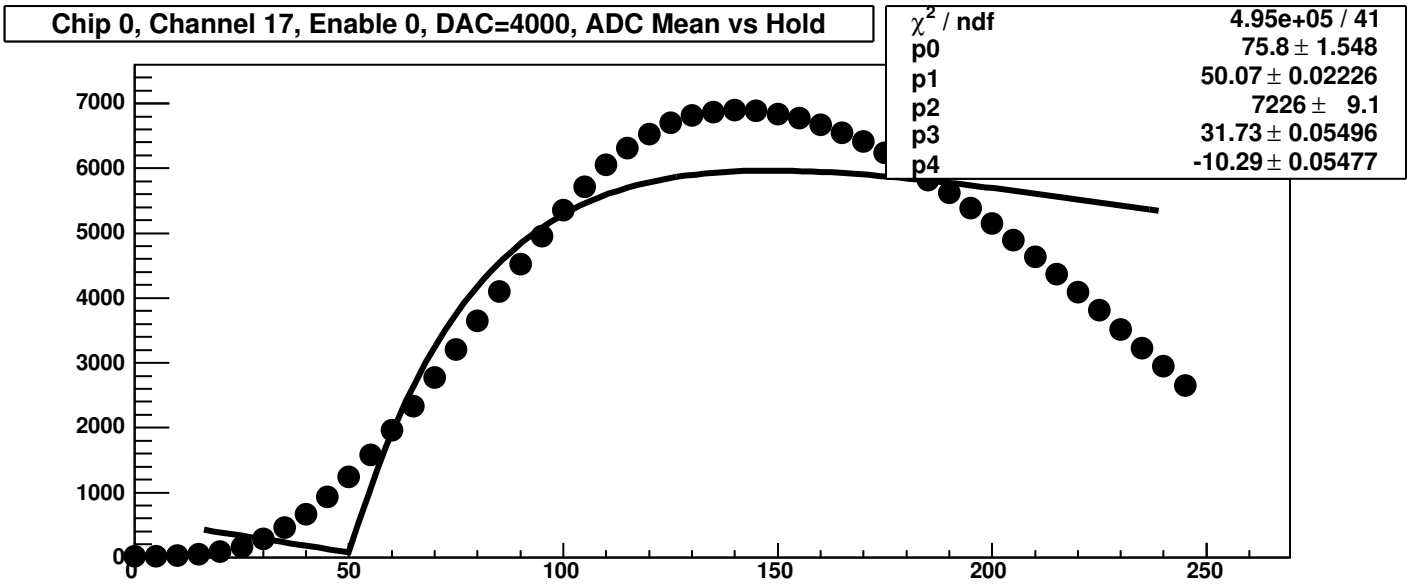
Chip 0, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

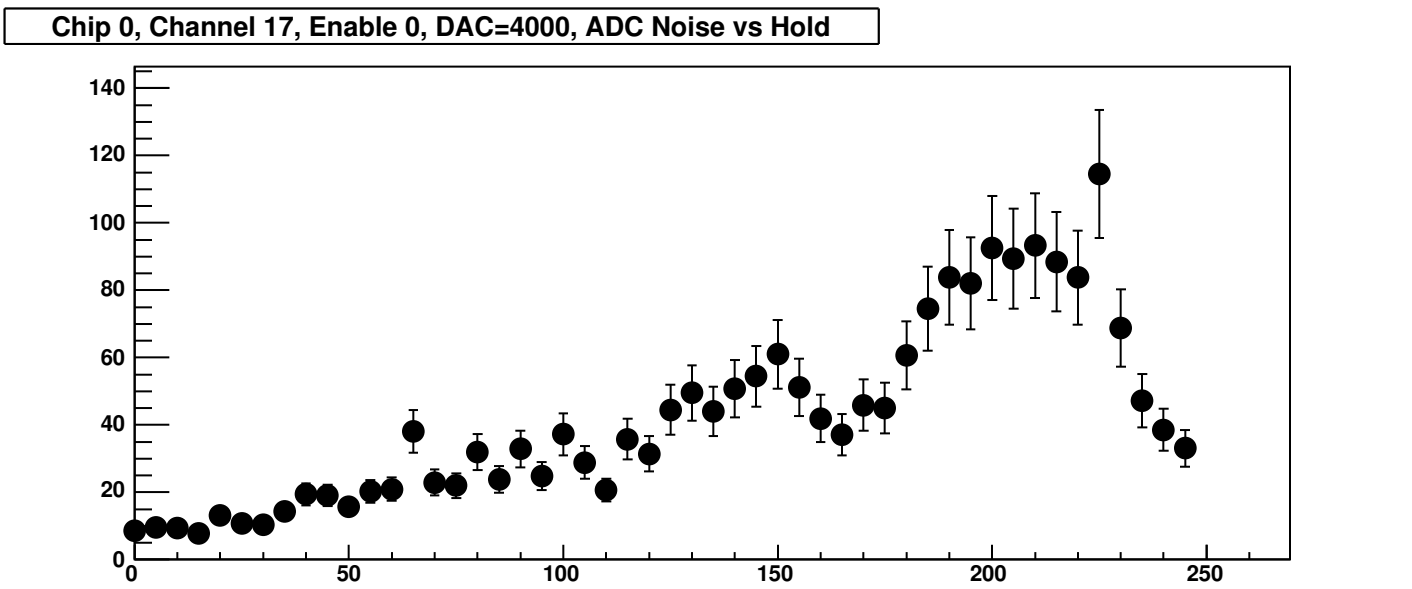


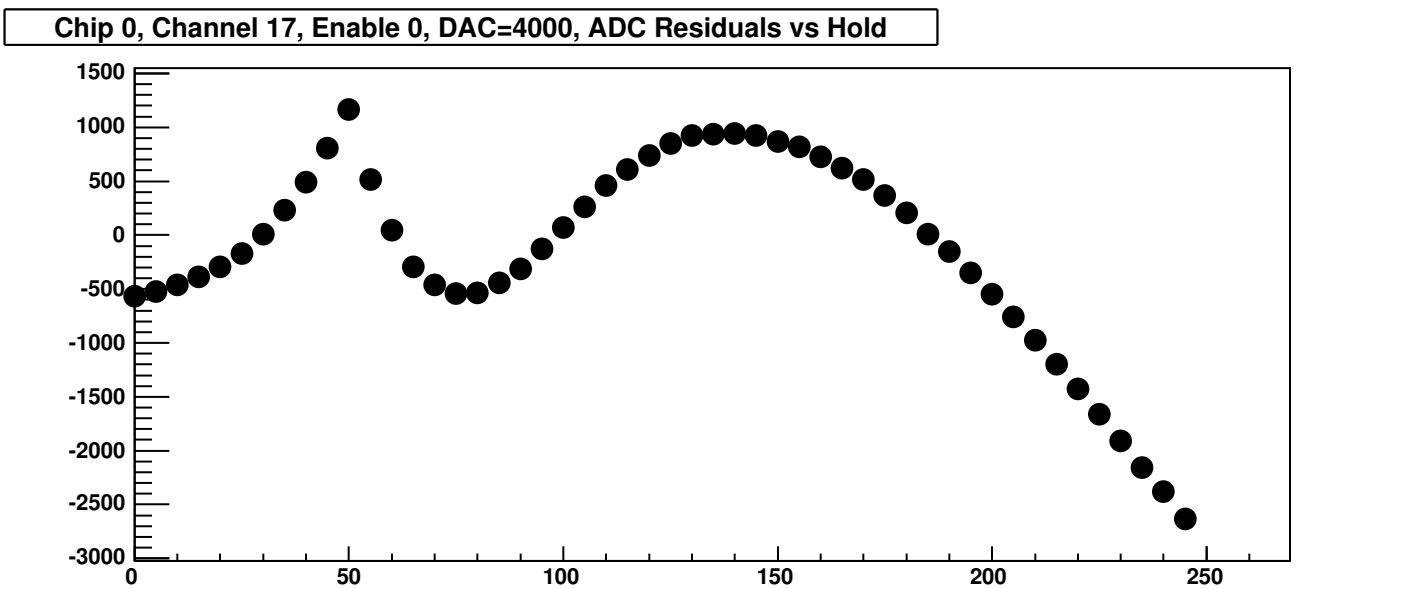

Chip 0, Channel 16, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.127e+06 / 42
p0 -9.381e+07 ± 3.782e+05
p1 -9344 ± 20.16
p2 9.384e+07 ± 3.782e+05
p3 9293 ± 20.09
30000
20000
10000
0
-10000
0
50
100
150
200
250


Chip 0, Channel 16, Enable 5!, DAC=4000, ADC Noise vs Hold
1200
1000
800
600
400
200
0
0
50
100
150
200
250

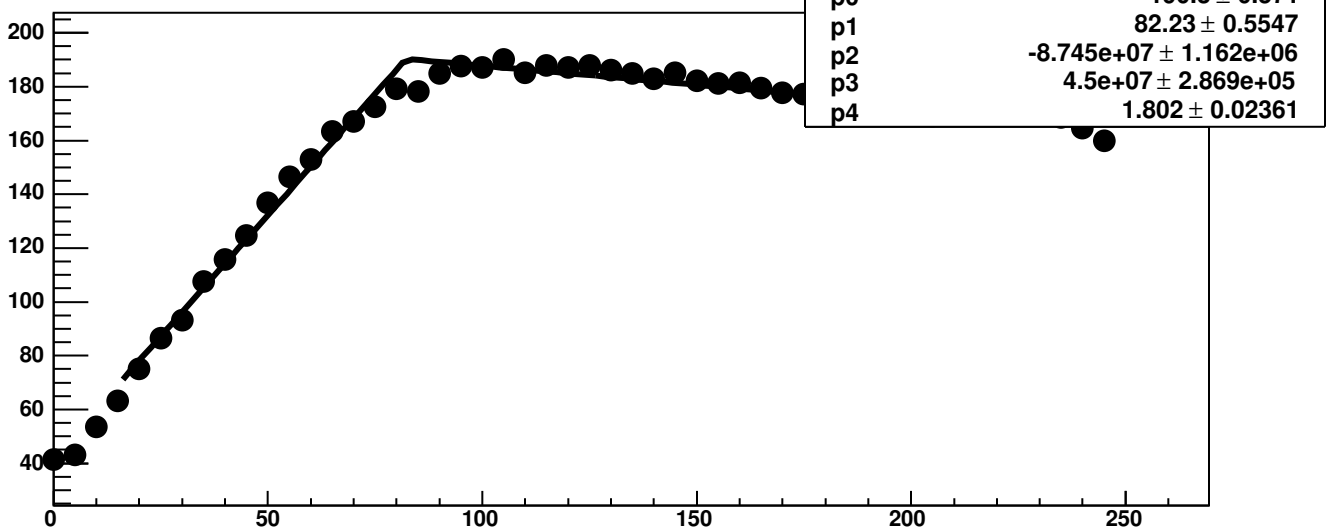

Chip 0, Channel 16, Enable 5!, DAC=4000, ADC Residuals vs Hold
10000
5000
0
-5000
-10000
-15000
-20000
-25000
-30000
0
50
100
150
200
250


Chip 0, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.95e+05 / 41
p0 75.8 ± 1.548
p1 50.07 ± 0.02226
p2 7226 ± 9.1
p3 31.73 ± 0.05496
p4 -10.29 ± 0.05477
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

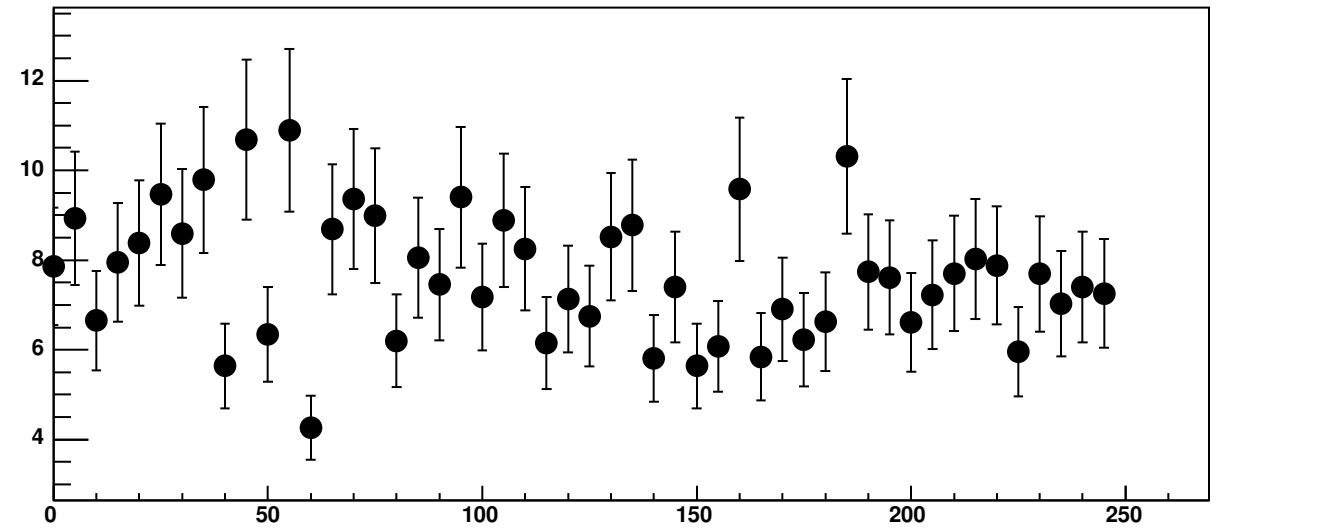

Chip 0, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250

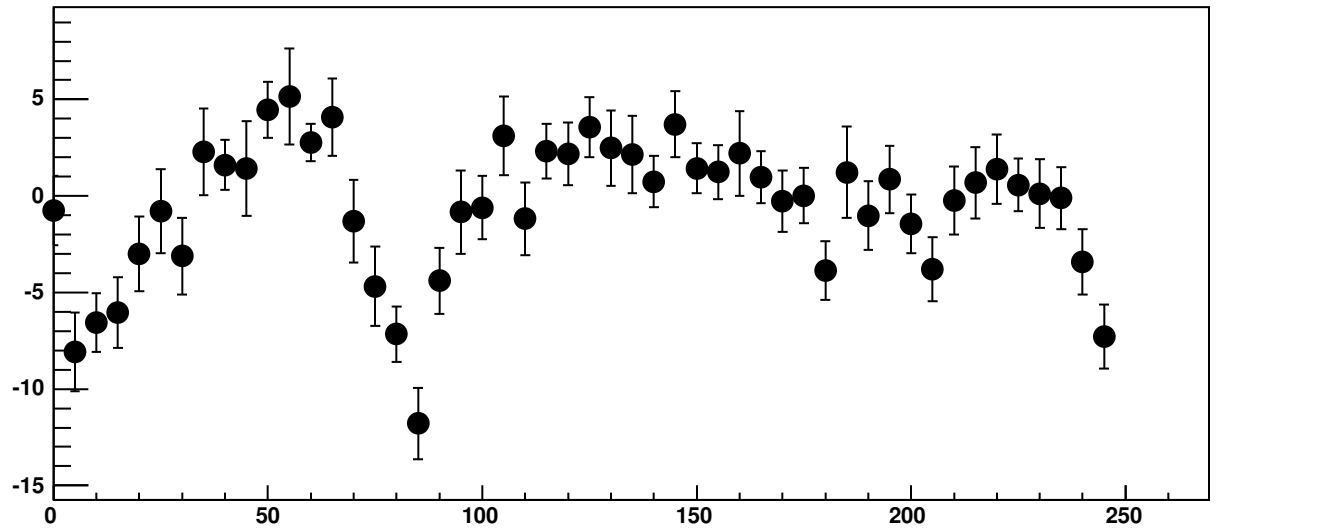
Chip 0, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 0, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold

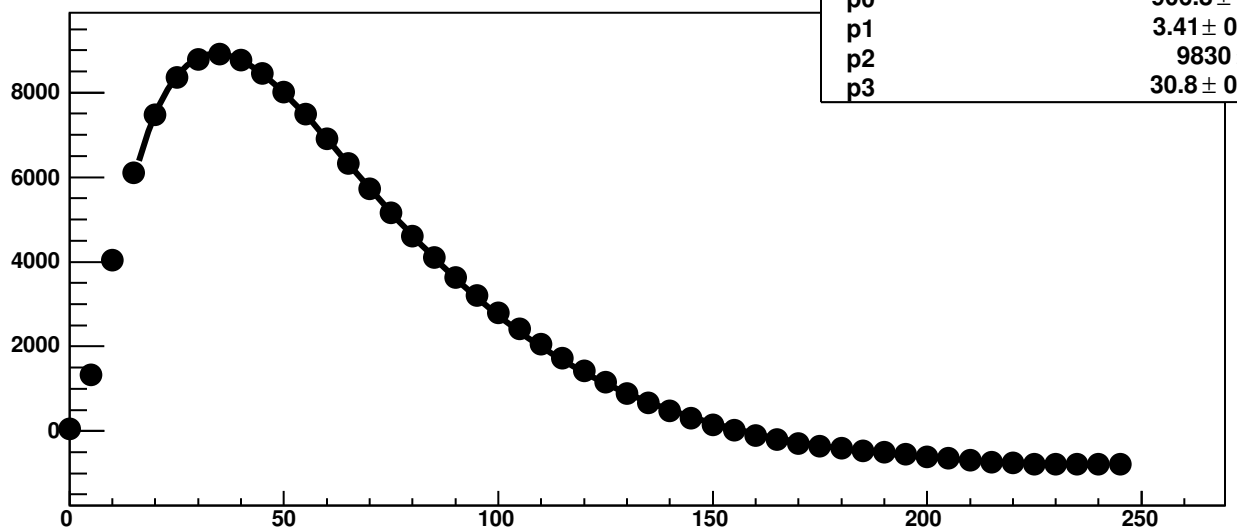


Chip 0, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

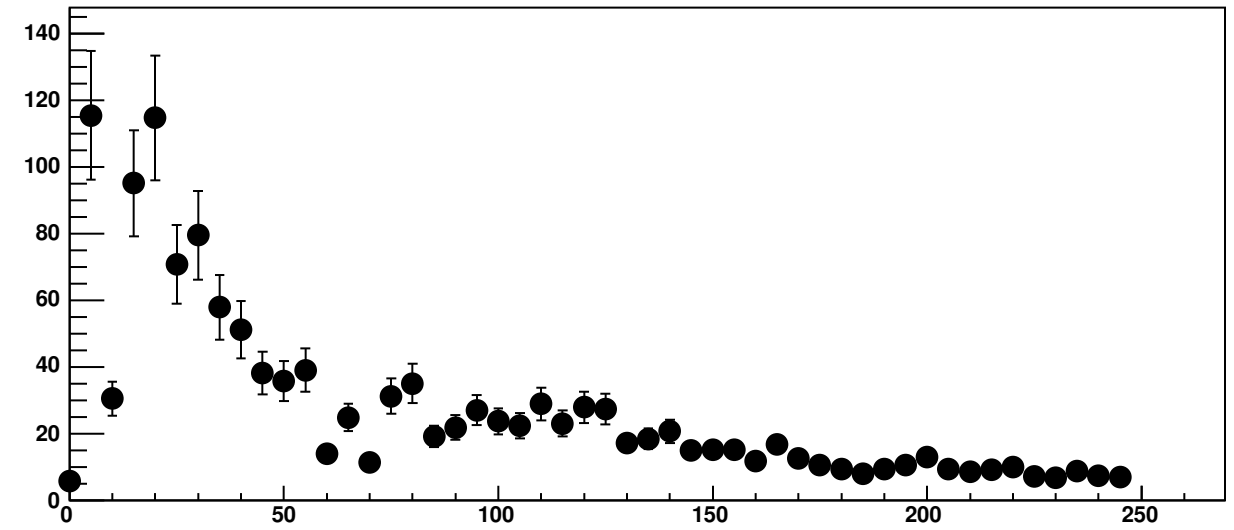


Chip 0, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold

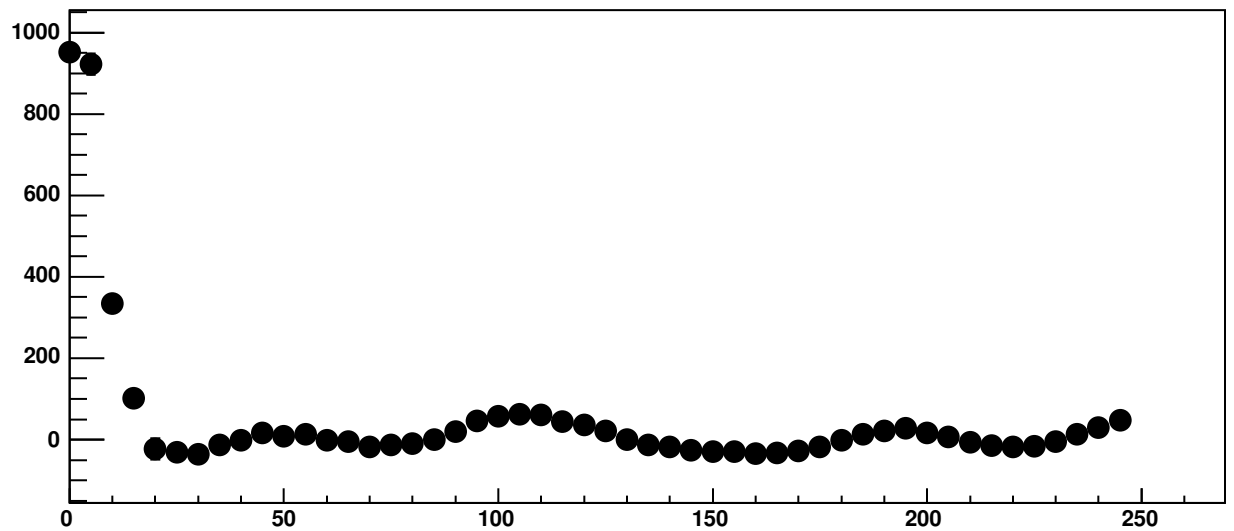
χ^2 / ndf	2103 / 42
p0	-906.8 ± 0.8227
p1	3.41 ± 0.03953
p2	9830 ± 4.14
p3	30.8 ± 0.01547



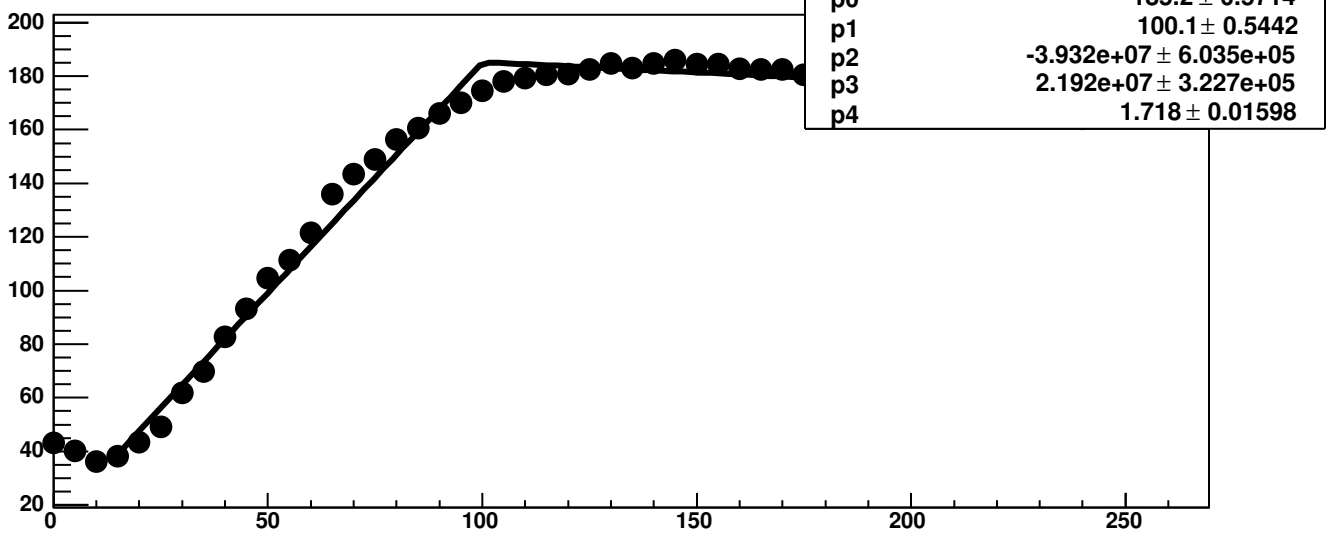
Chip 0, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold



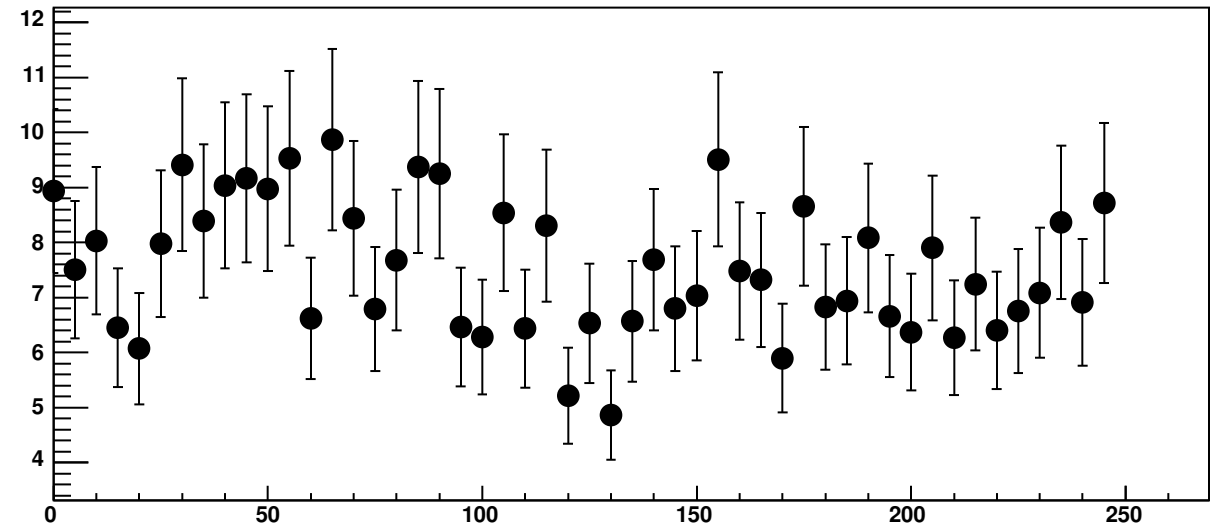
Chip 0, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold



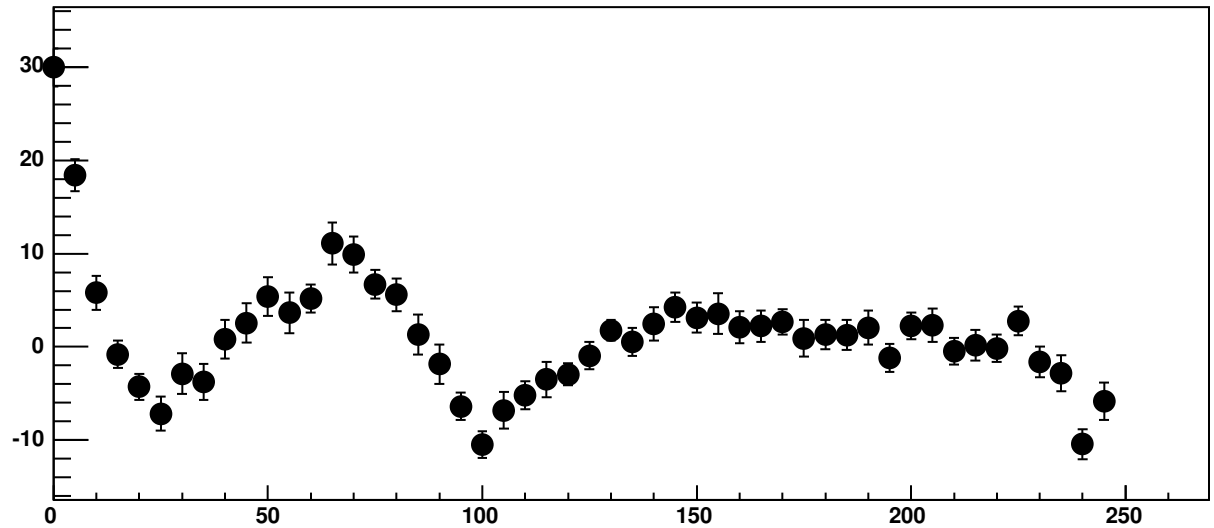
Chip 0, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



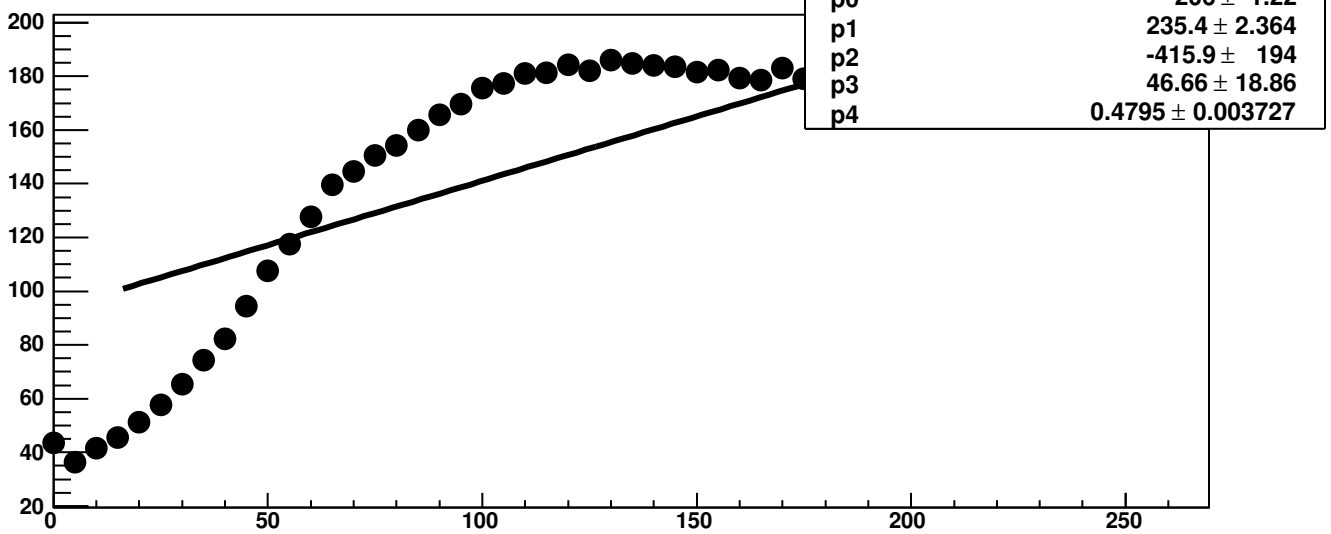
Chip 0, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



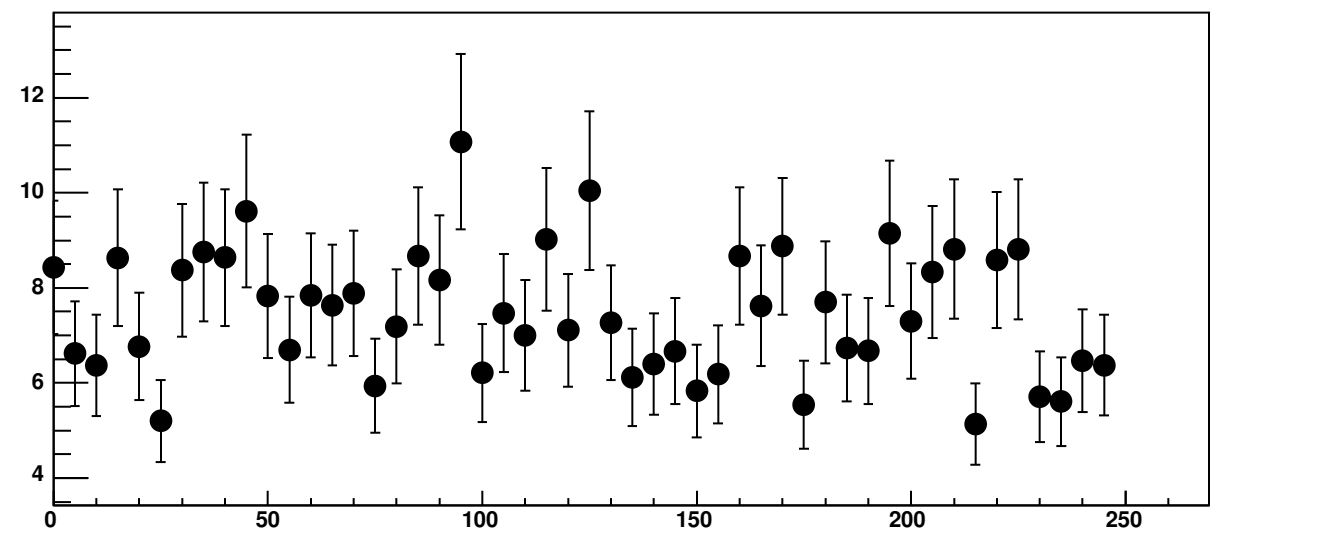
Chip 0, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold



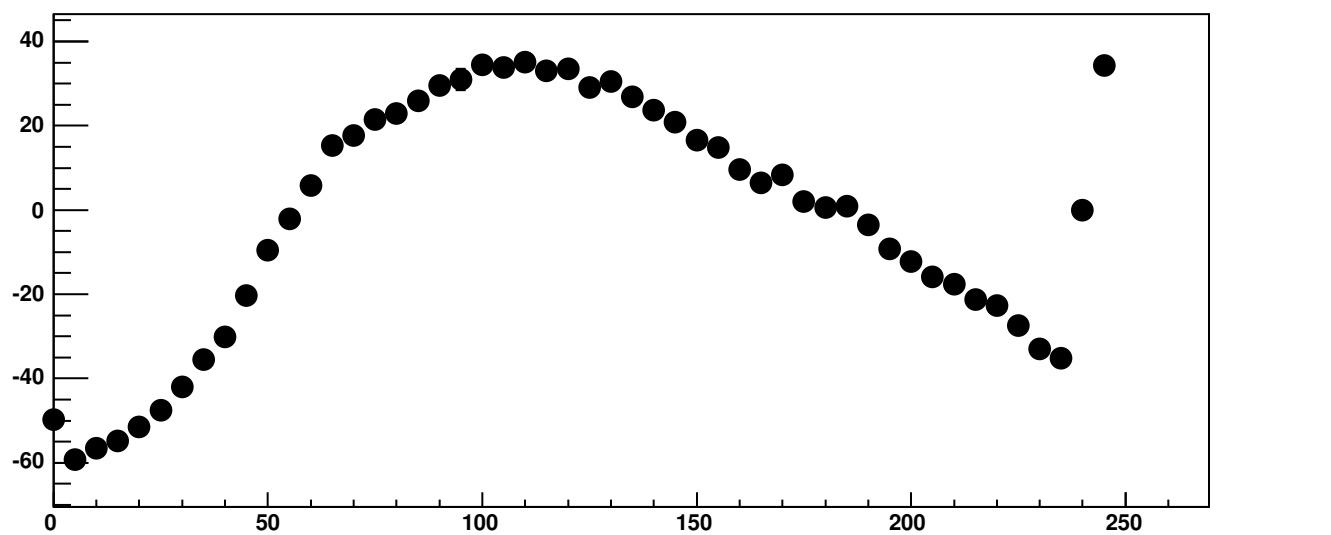
Chip 0, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



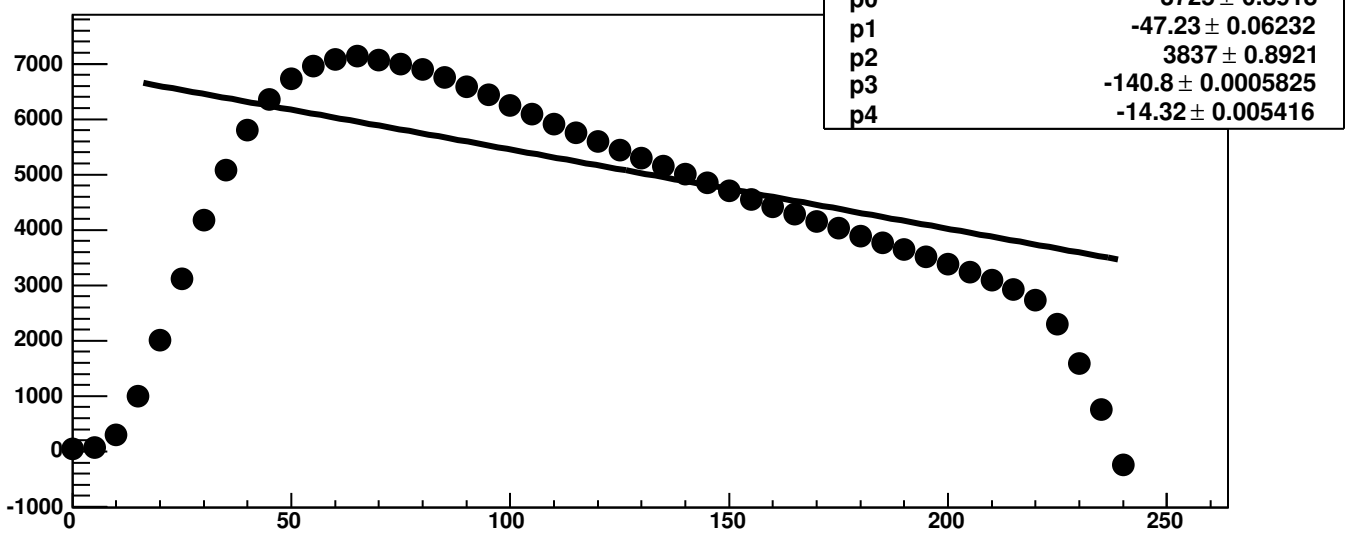
Chip 0, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



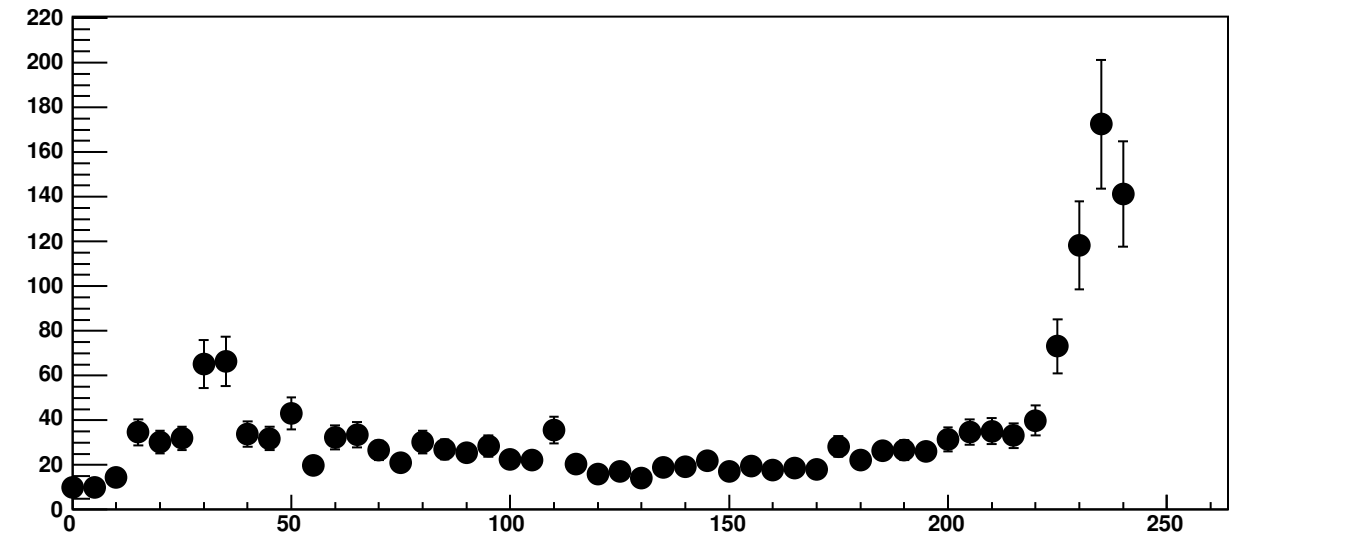
Chip 0, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold



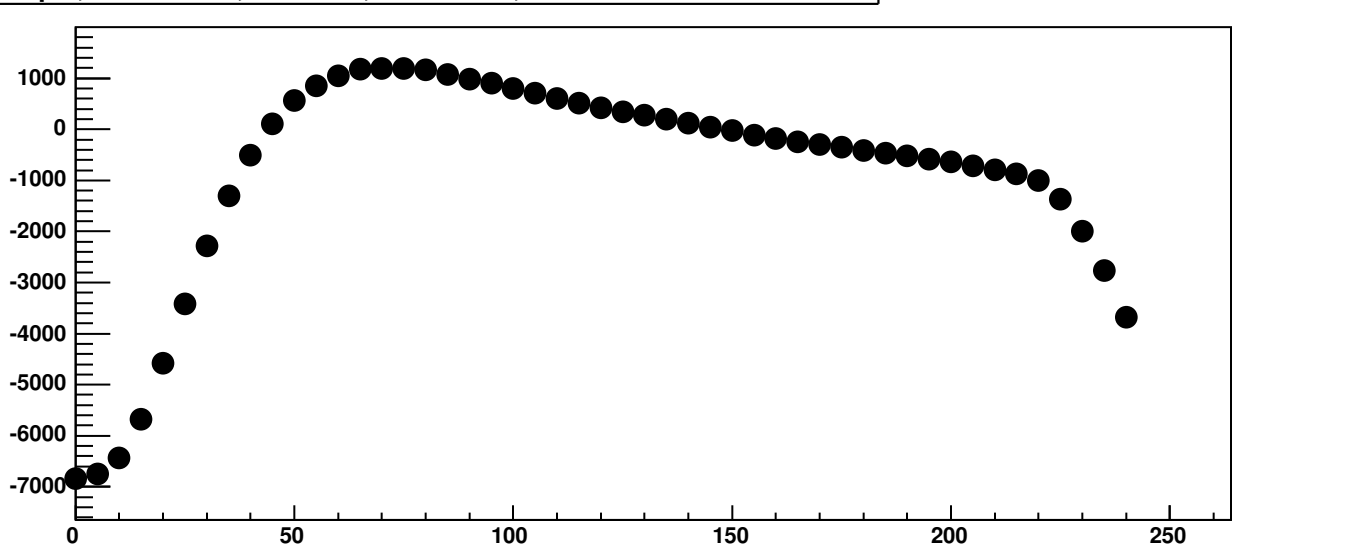
Chip 0, Channel 17, Enable 5, DAC=4000, ADC Mean vs Hold

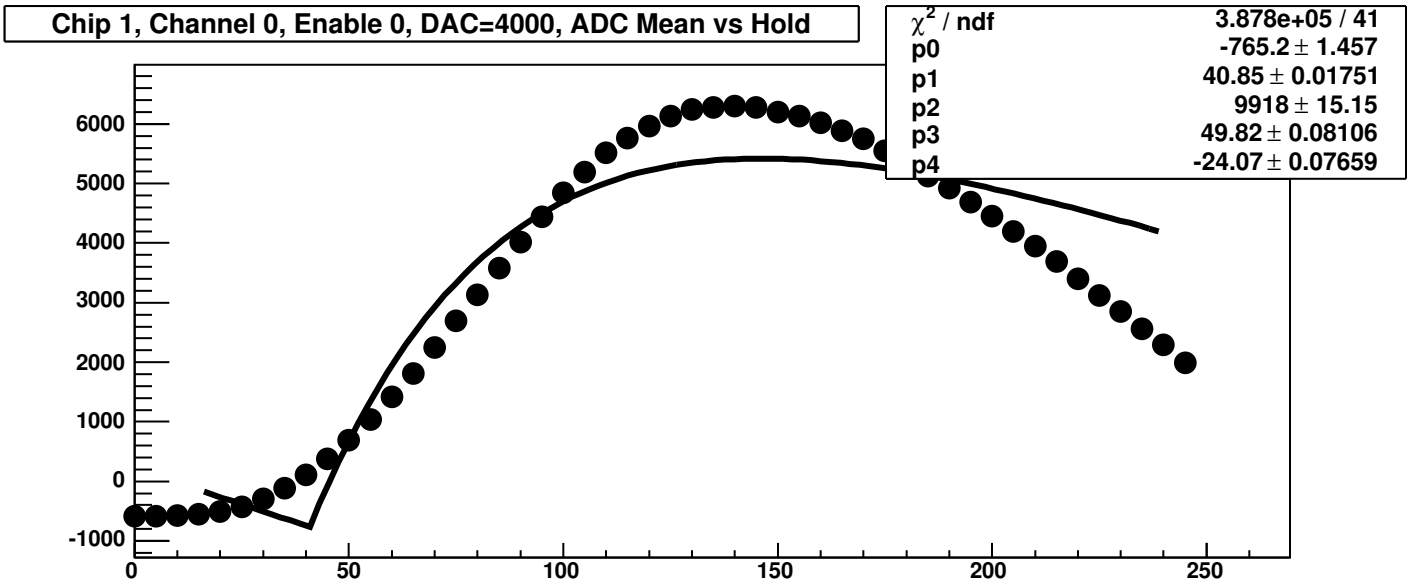


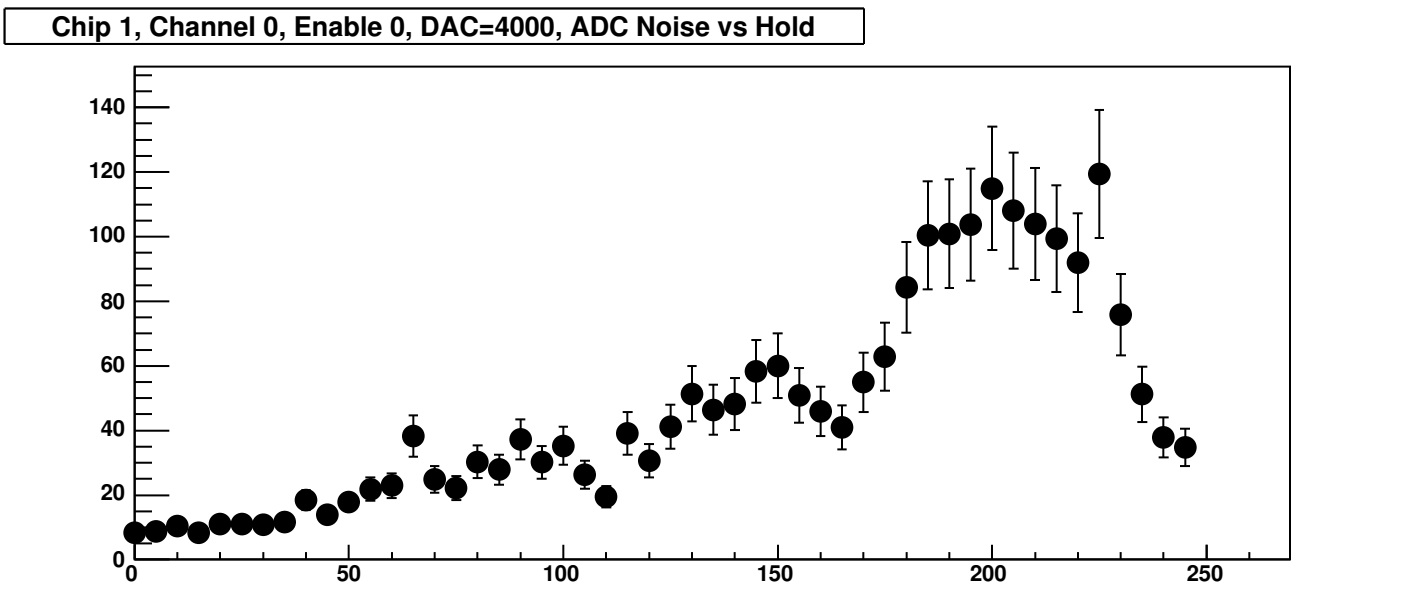
Chip 0, Channel 17, Enable 5, DAC=4000, ADC Noise vs Hold

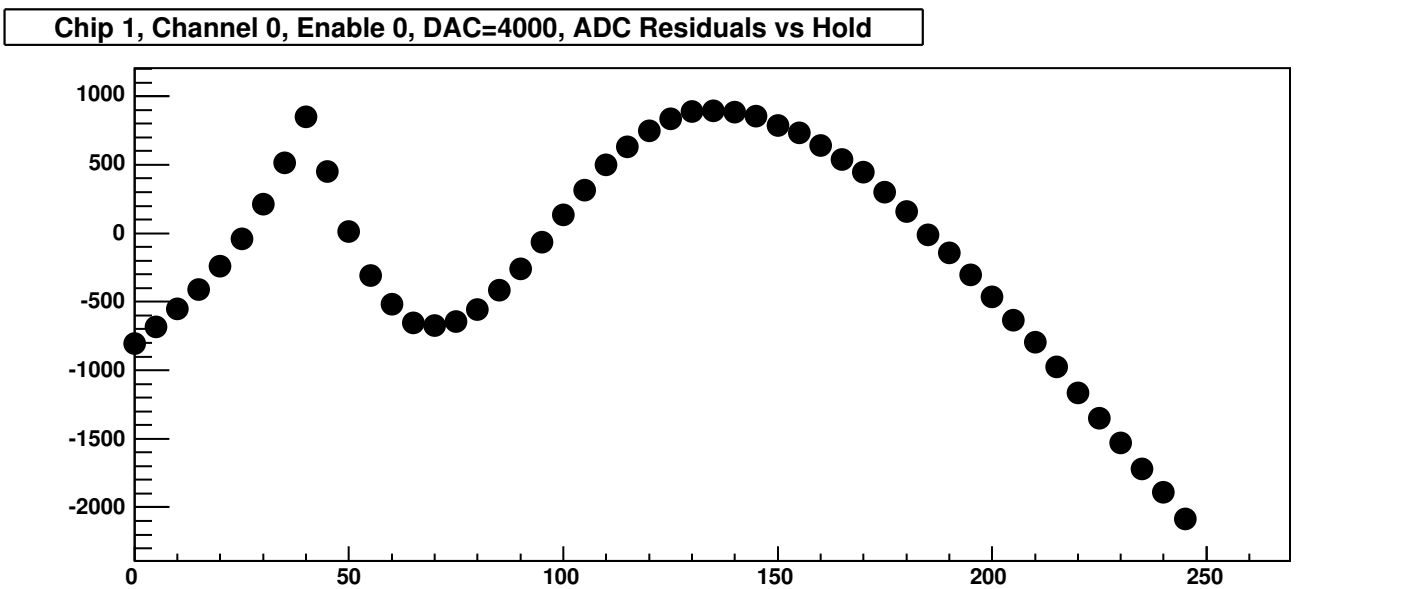


Chip 0, Channel 17, Enable 5, DAC=4000, ADC Residuals vs Hold

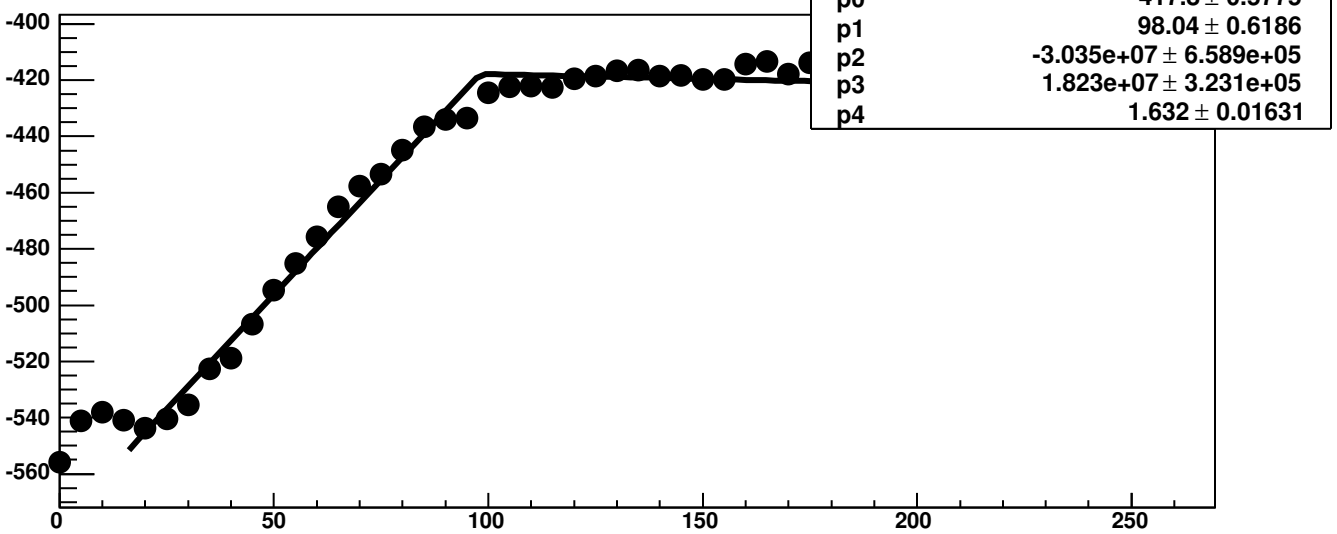



Chip 1, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.878e+05 / 41
p0 -765.2 ± 1.457
p1 40.85 ± 0.01751
p2 9918 ± 15.15
p3 49.82 ± 0.08106
p4 -24.07 ± 0.07659
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

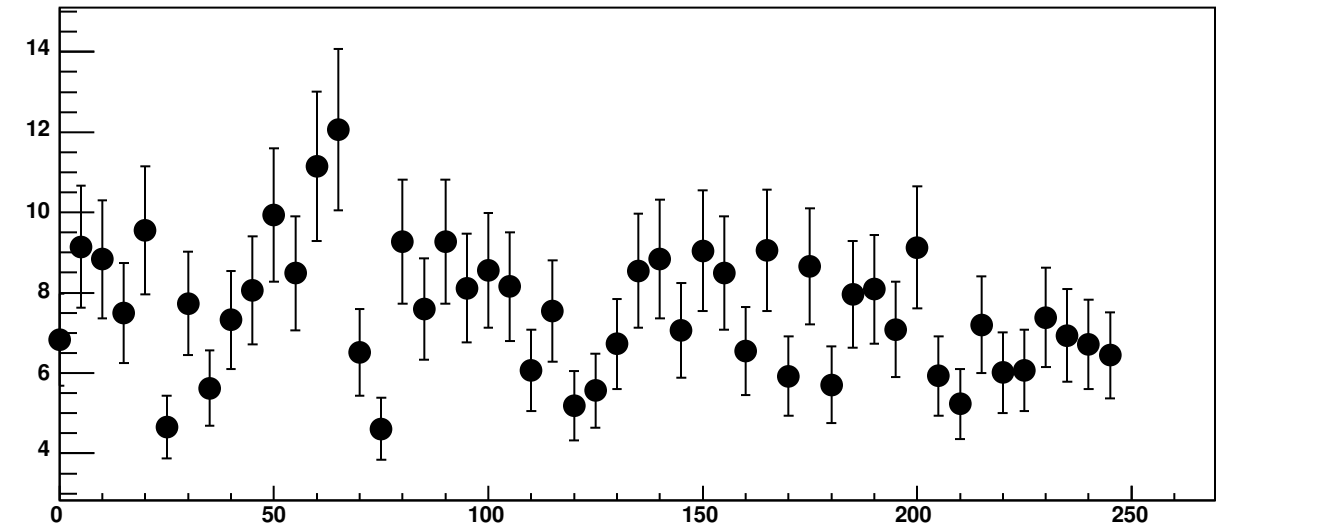

Chip 1, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 1, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

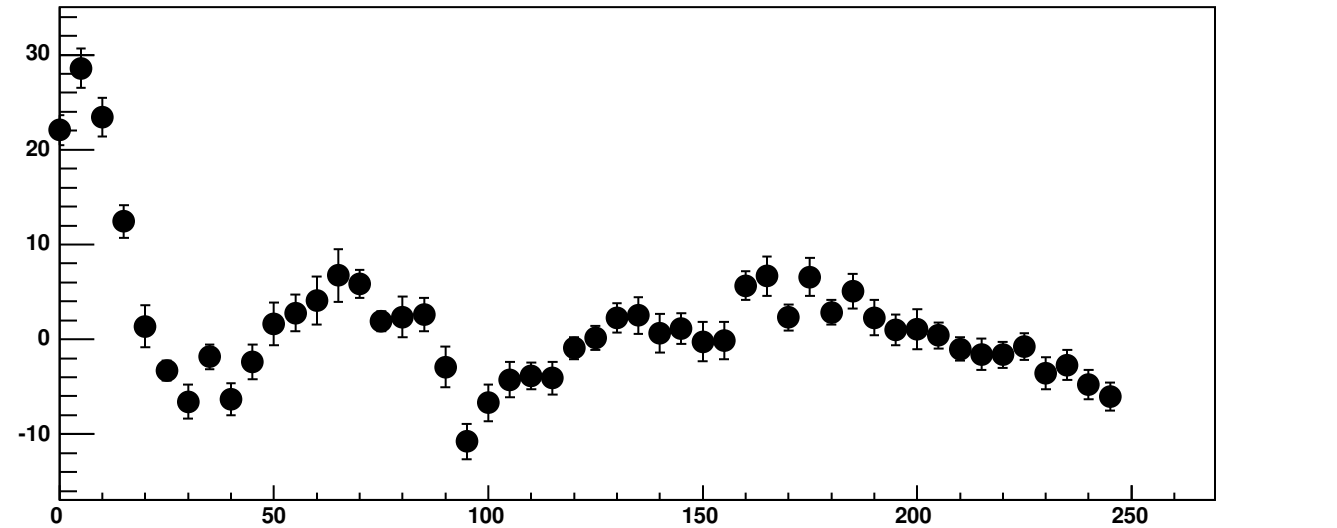
Chip 1, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold



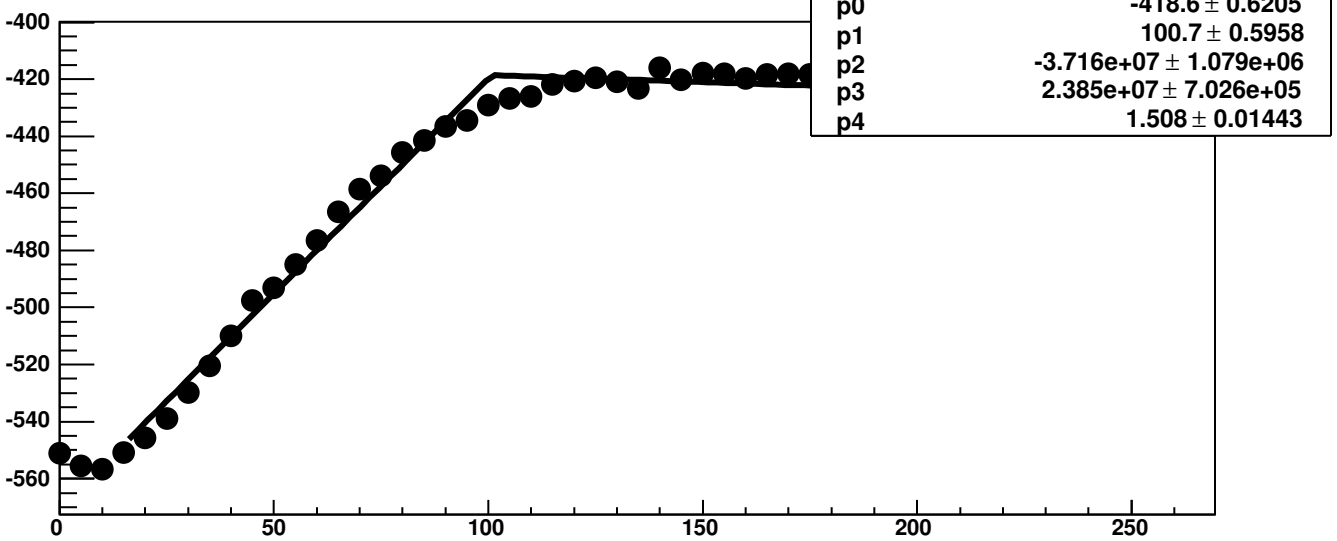
Chip 1, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



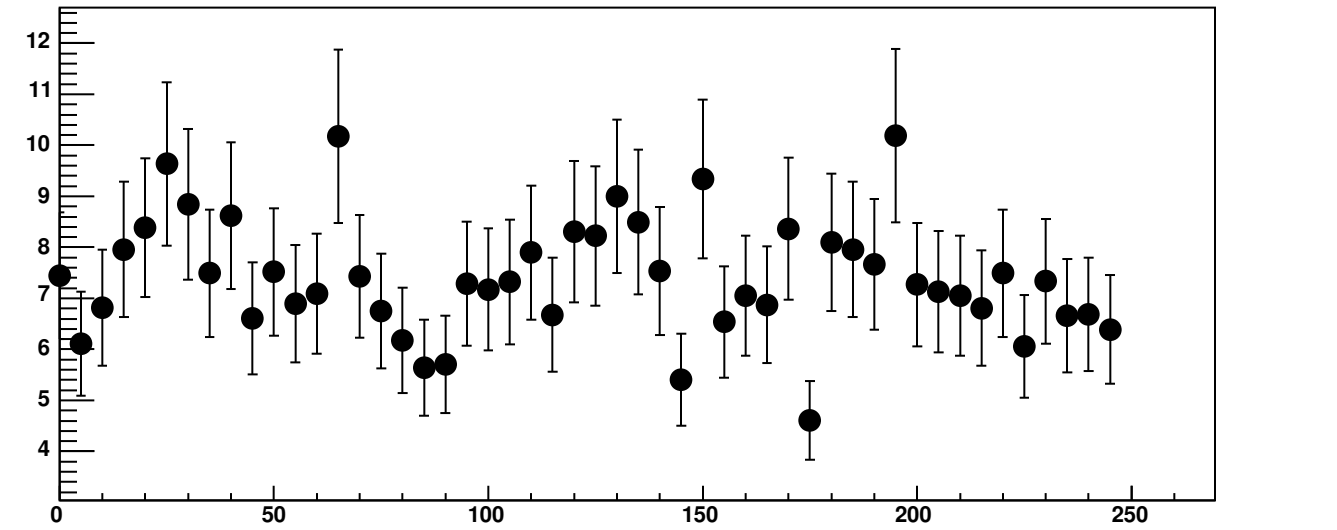
Chip 1, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold



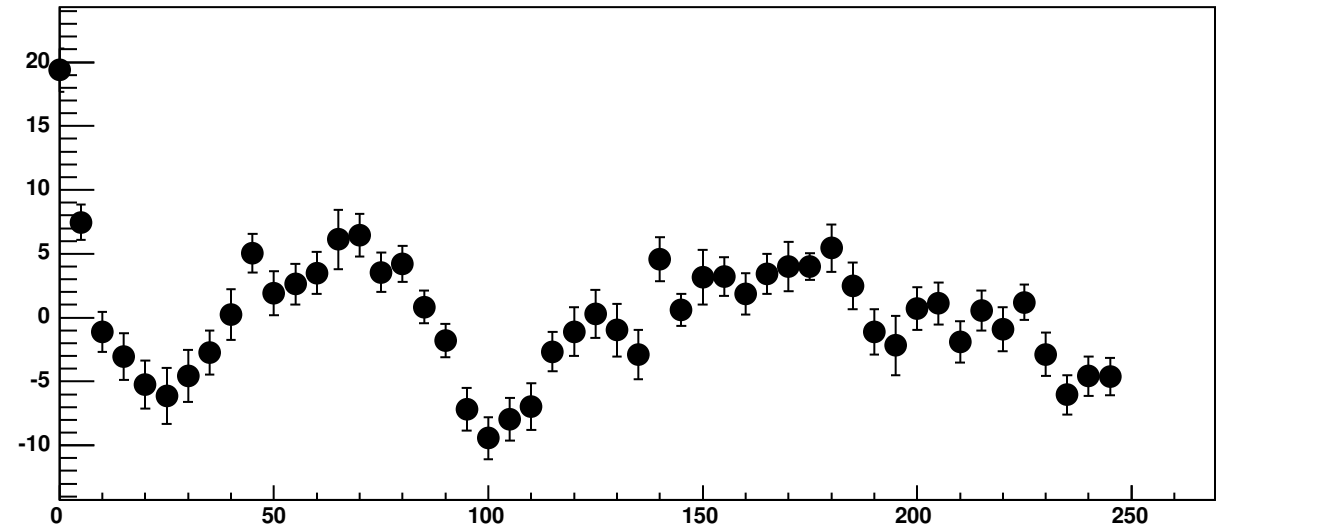
Chip 1, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold



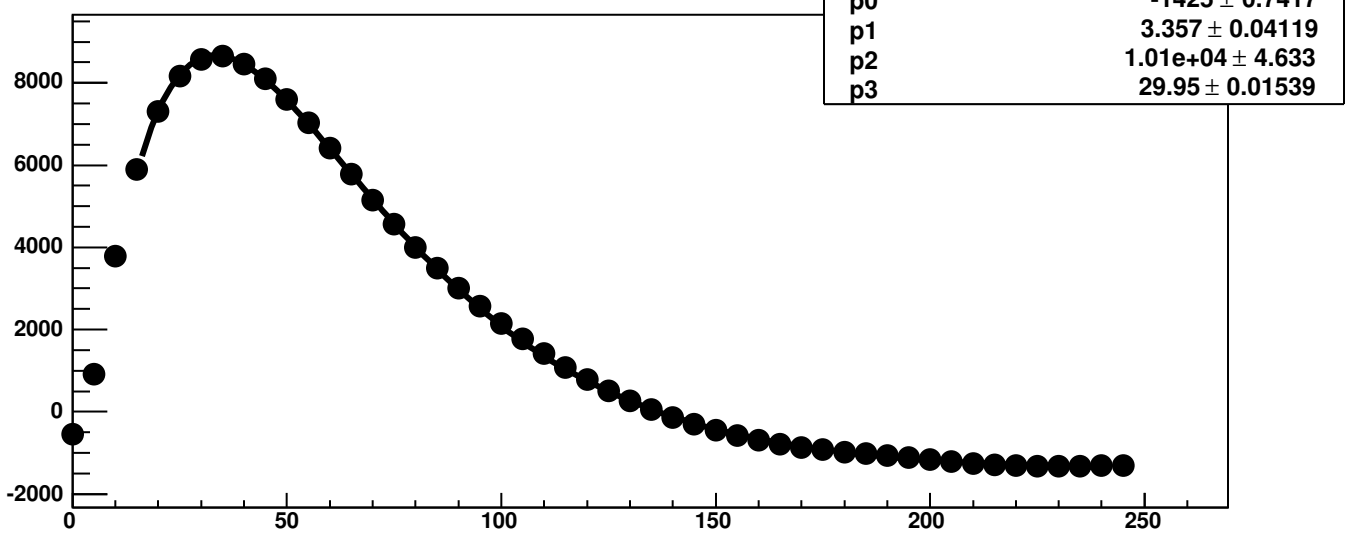
Chip 1, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



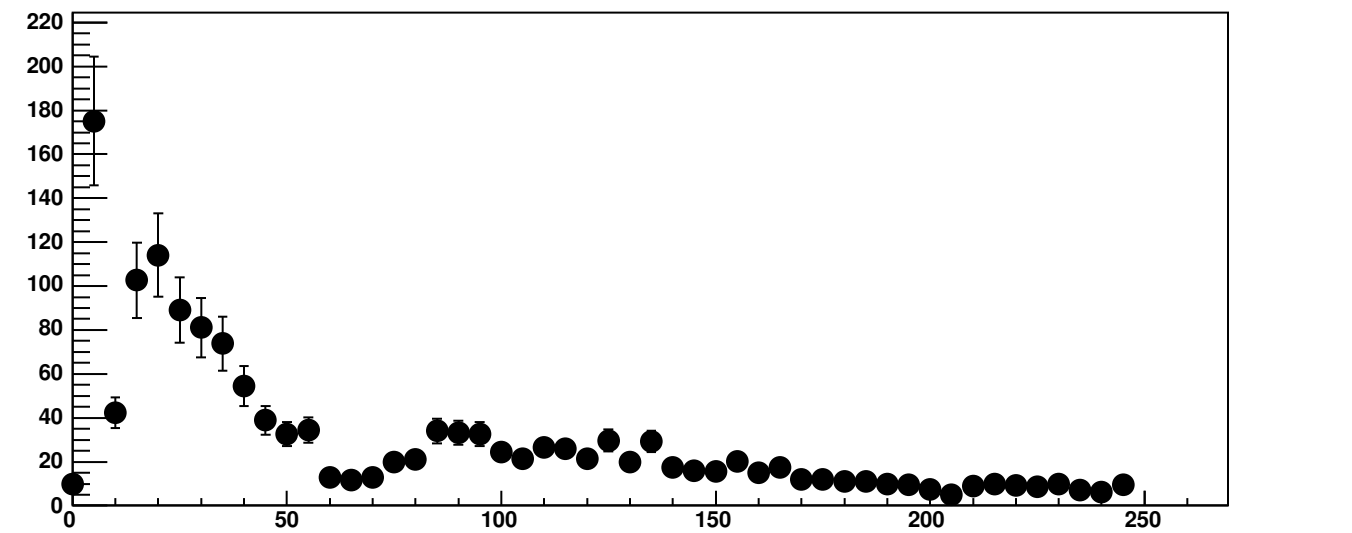
Chip 1, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold



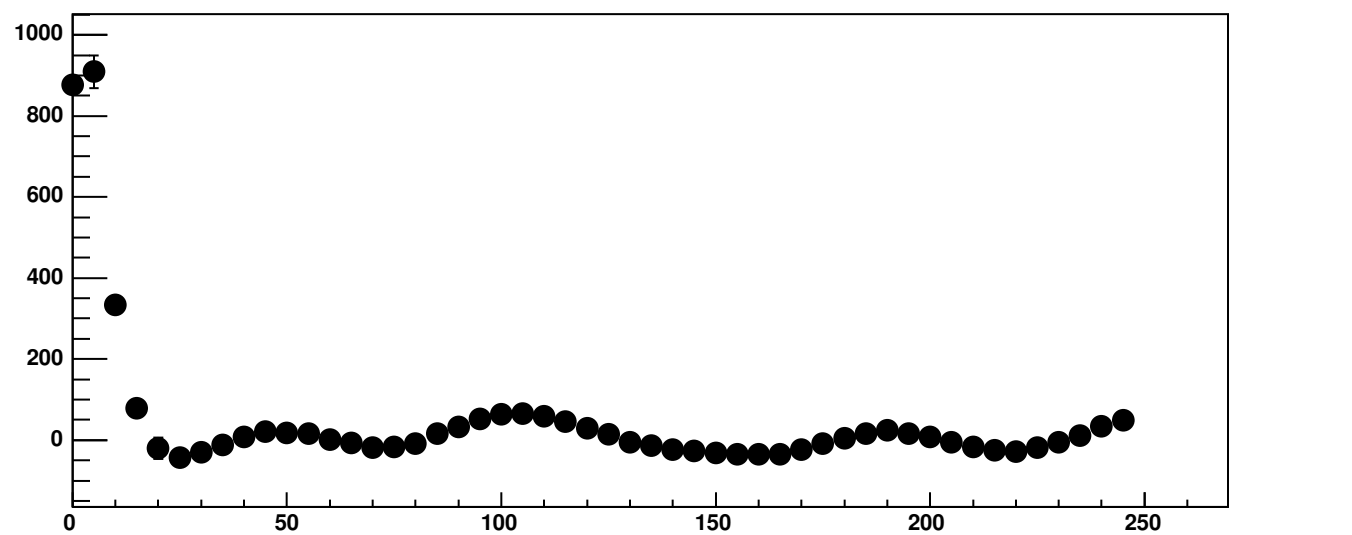
Chip 1, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold



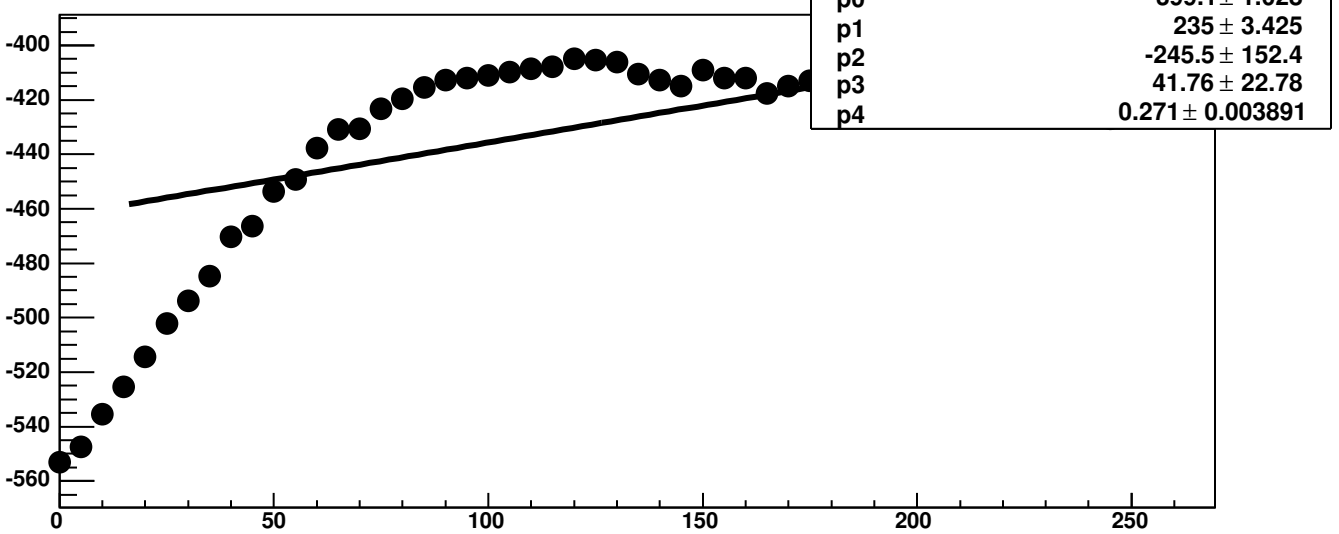
Chip 1, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold



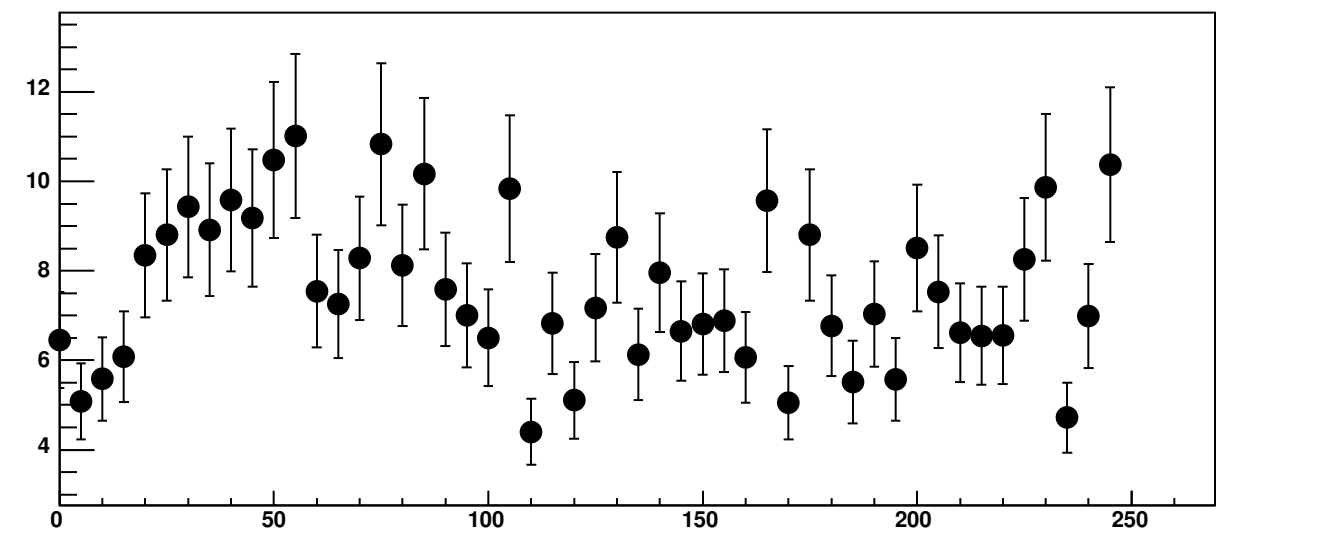
Chip 1, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold



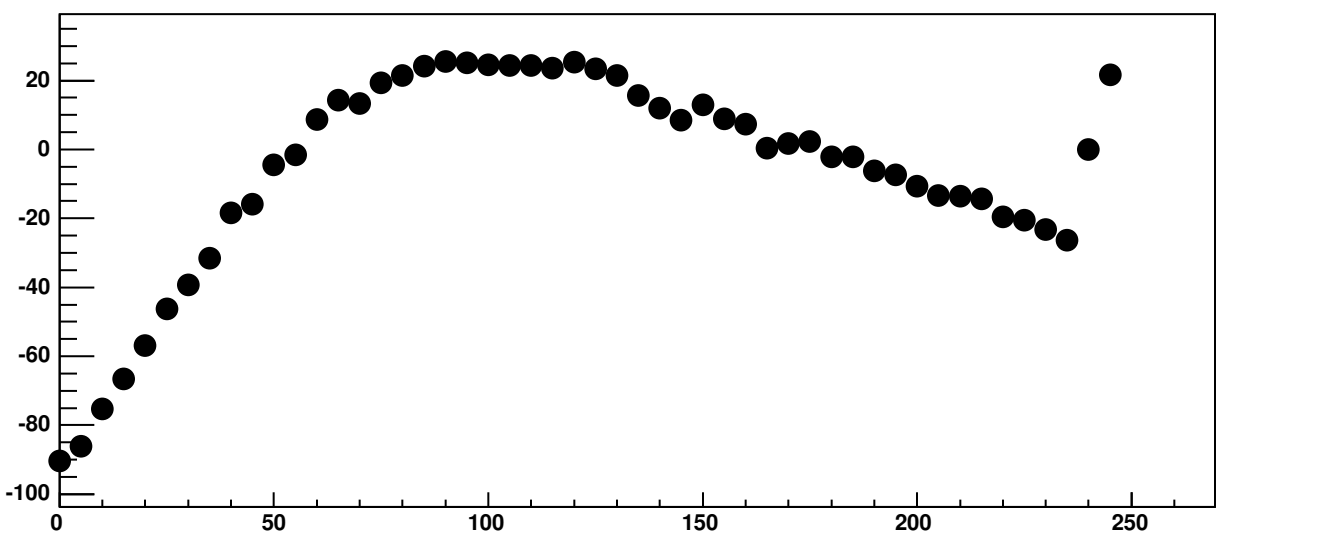
Chip 1, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

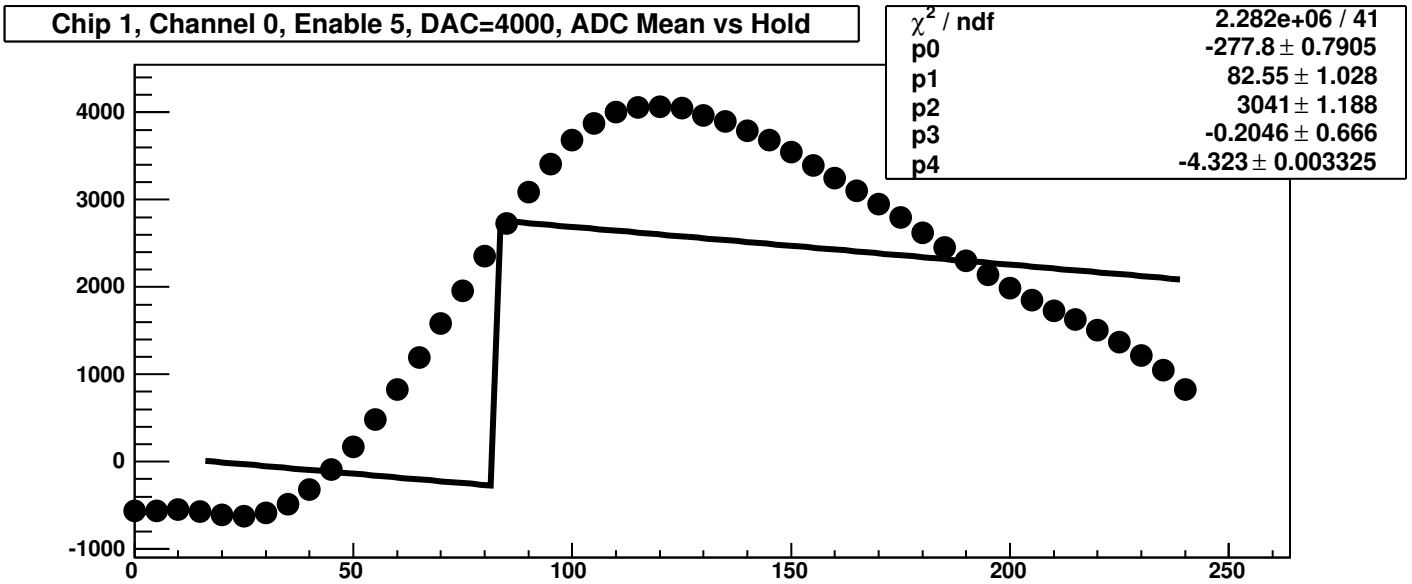


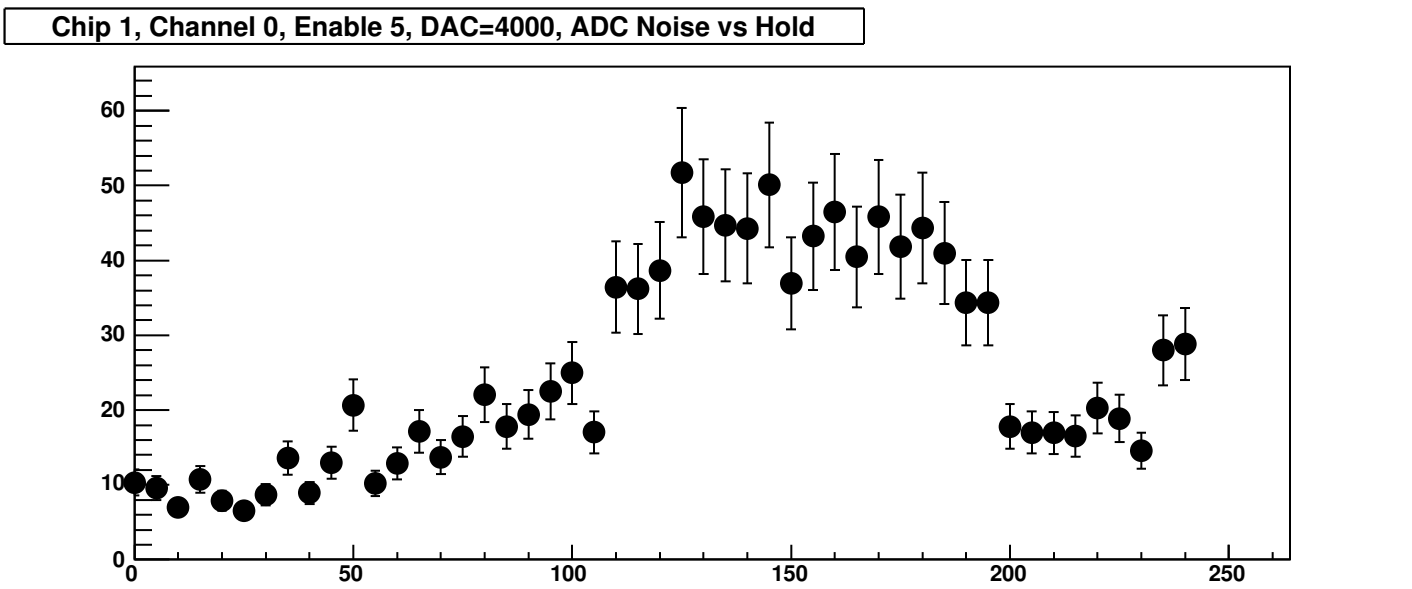
Chip 1, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold

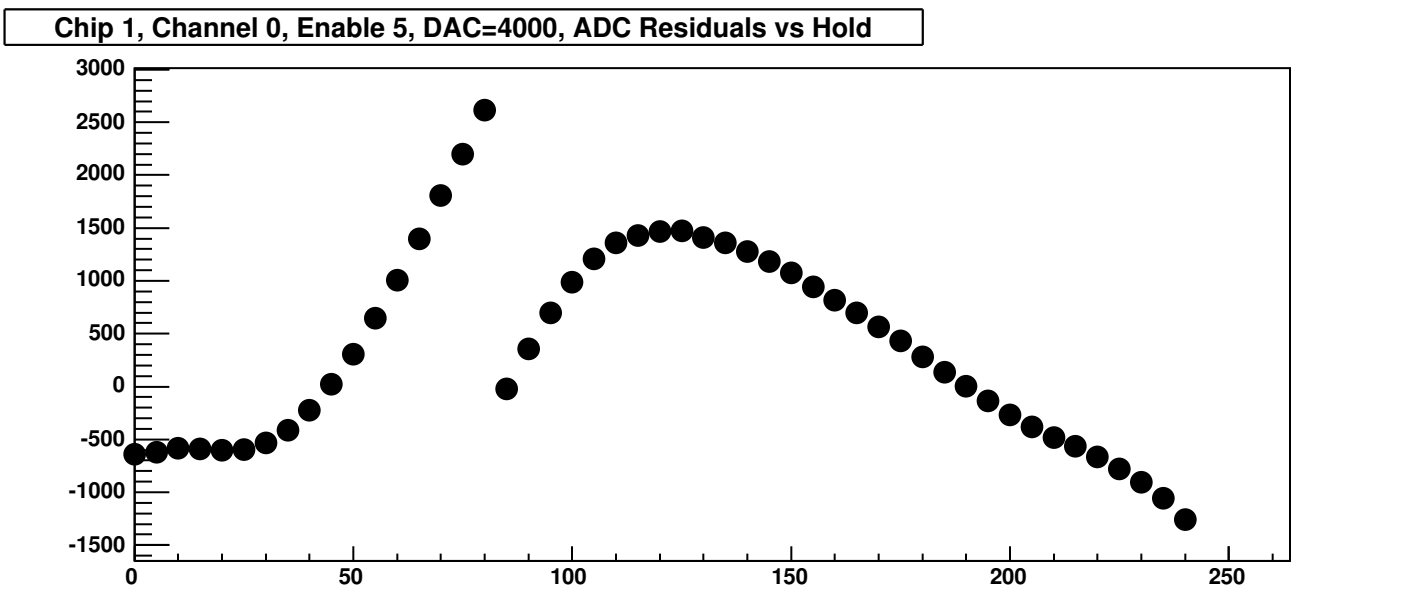


Chip 1, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold



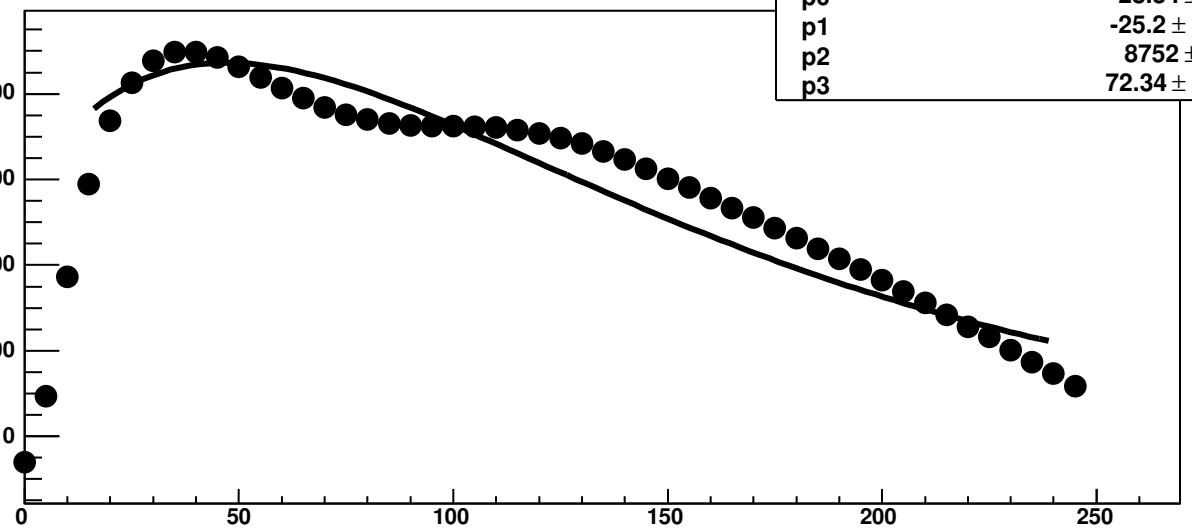

Chip 1, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.282e+06 / 41
p0 -277.8 ± 0.7905
p1 82.55 ± 1.028
p2 3041 ± 1.188
p3 -0.2046 ± 0.666
p4 -4.323 ± 0.003325
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 1, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold
60
50
40
30
20
10
0
0
50
100
150
200
250

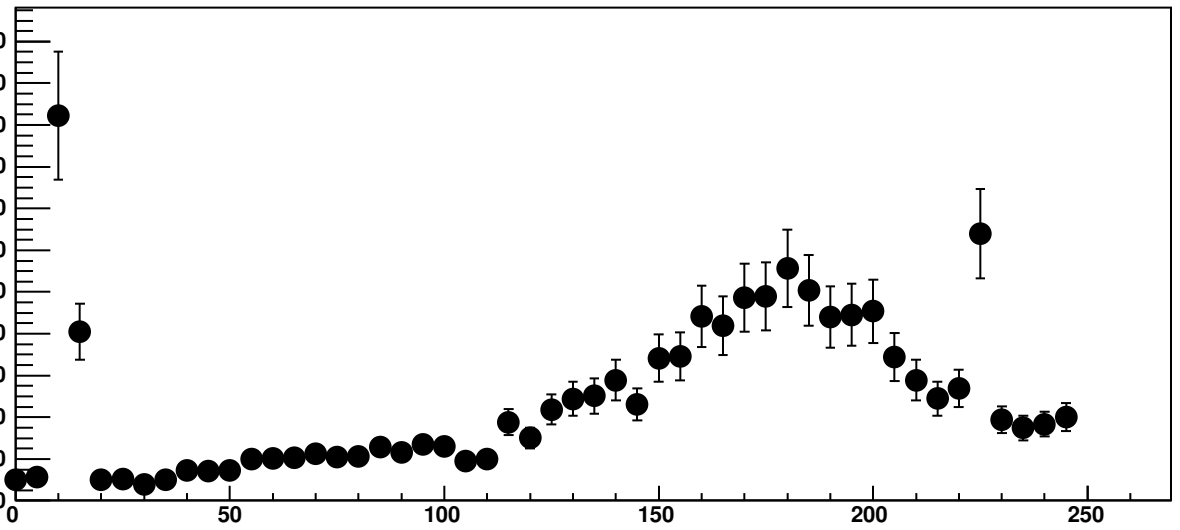

Chip 1, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold
3000
2500
2000
1500
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

Chip 1, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold

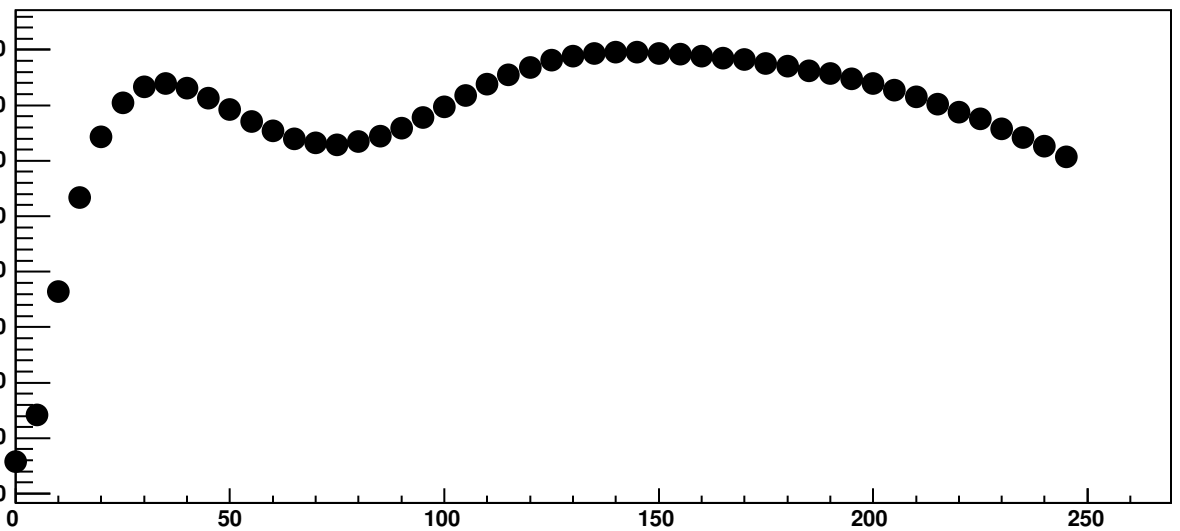
χ^2 / ndf	3.368e+05 / 42
p0	-28.34 ± 16.19
p1	-25.2 ± 0.1442
p2	8752 ± 15.45
p3	72.34 ± 0.1443

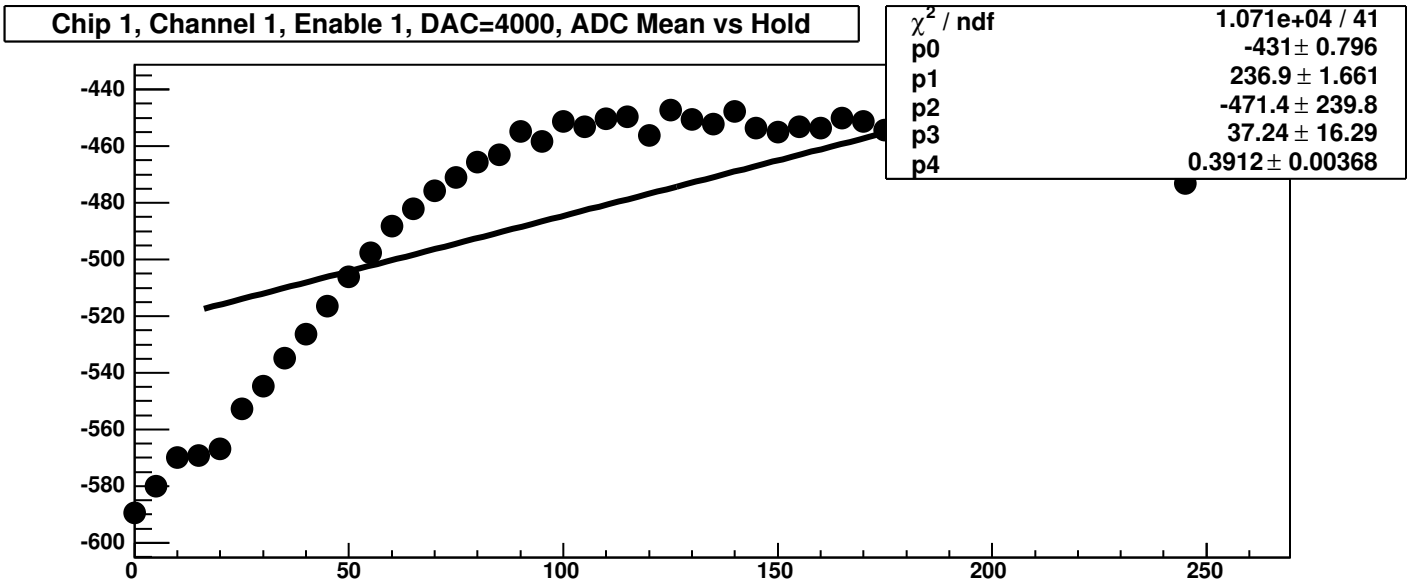


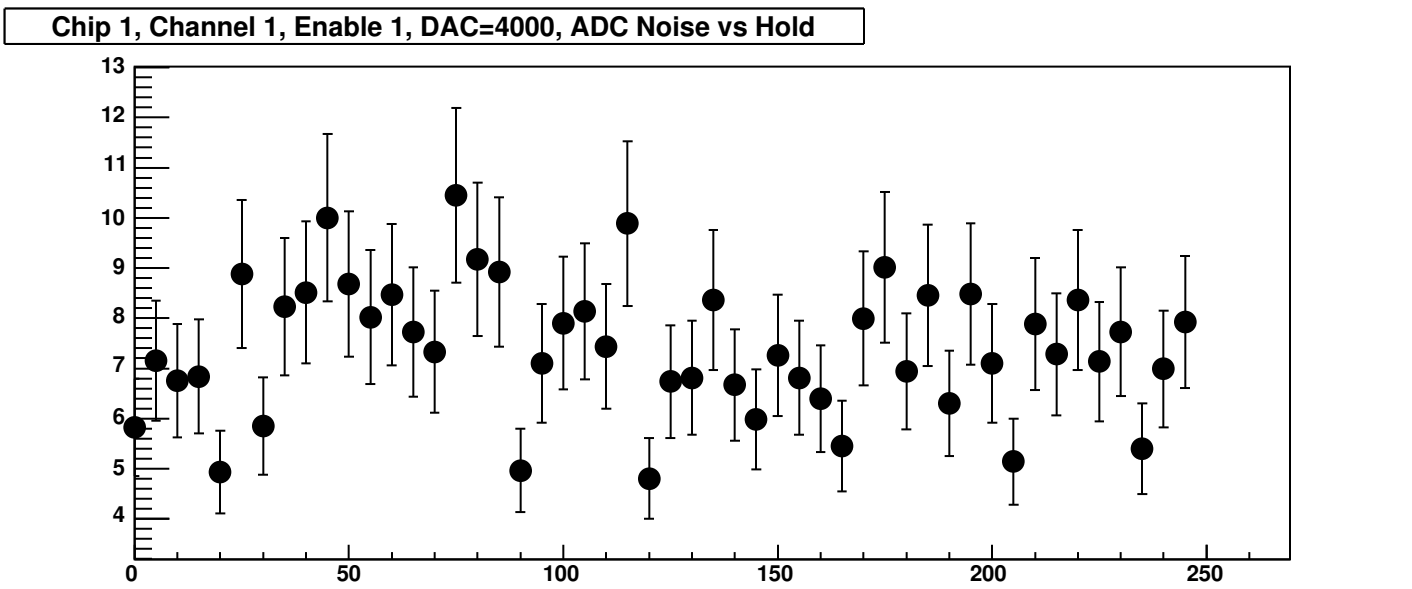
Chip 1, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold

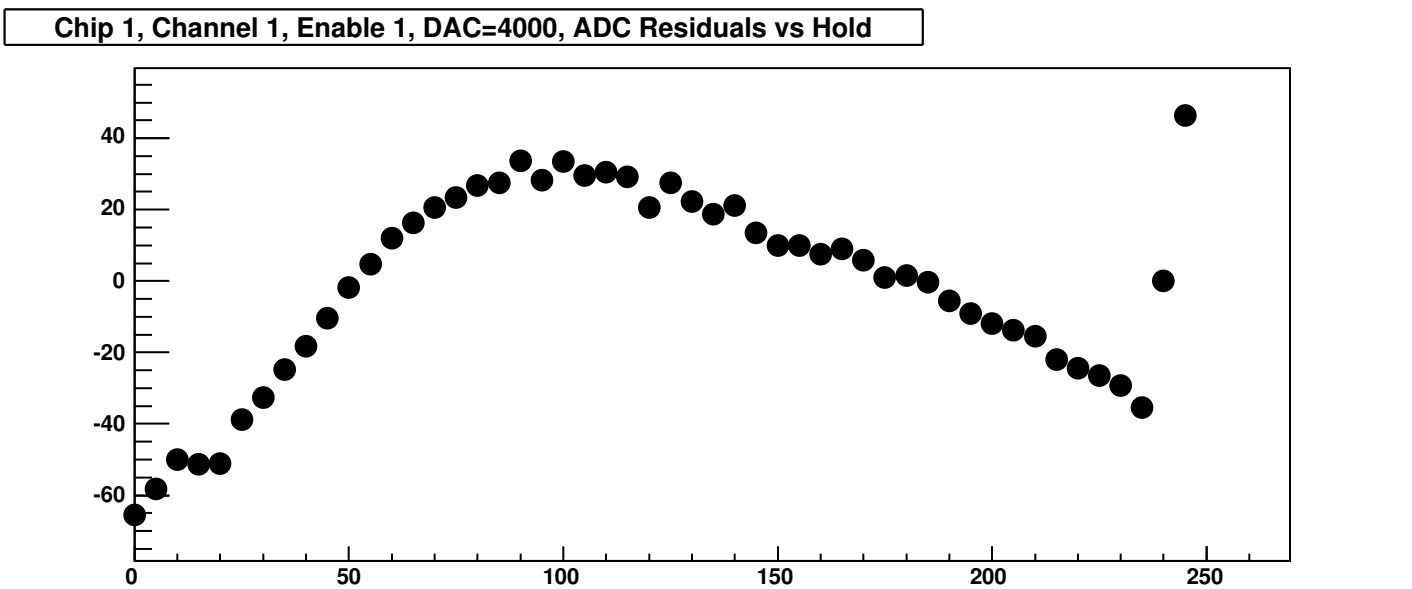


Chip 1, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold

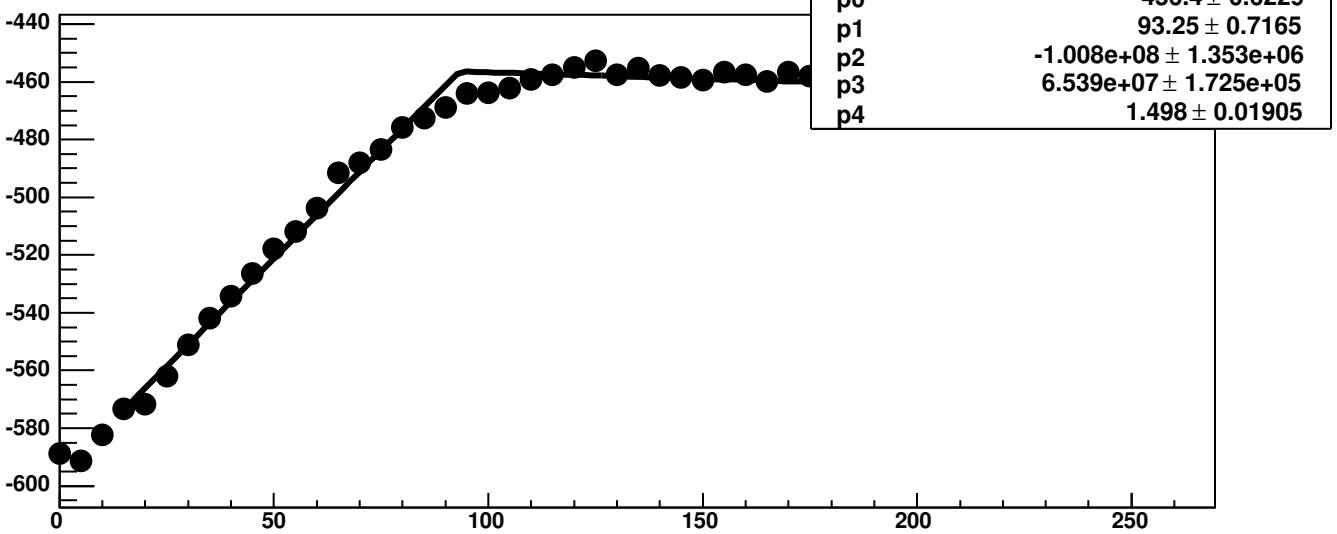



Chip 1, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.071e+04 / 41
p0 -431 ± 0.796
p1 236.9 ± 1.661
p2 -471.4 ± 239.8
p3 37.24 ± 16.29
p4 0.3912 ± 0.00368
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
50
100
150
200
250

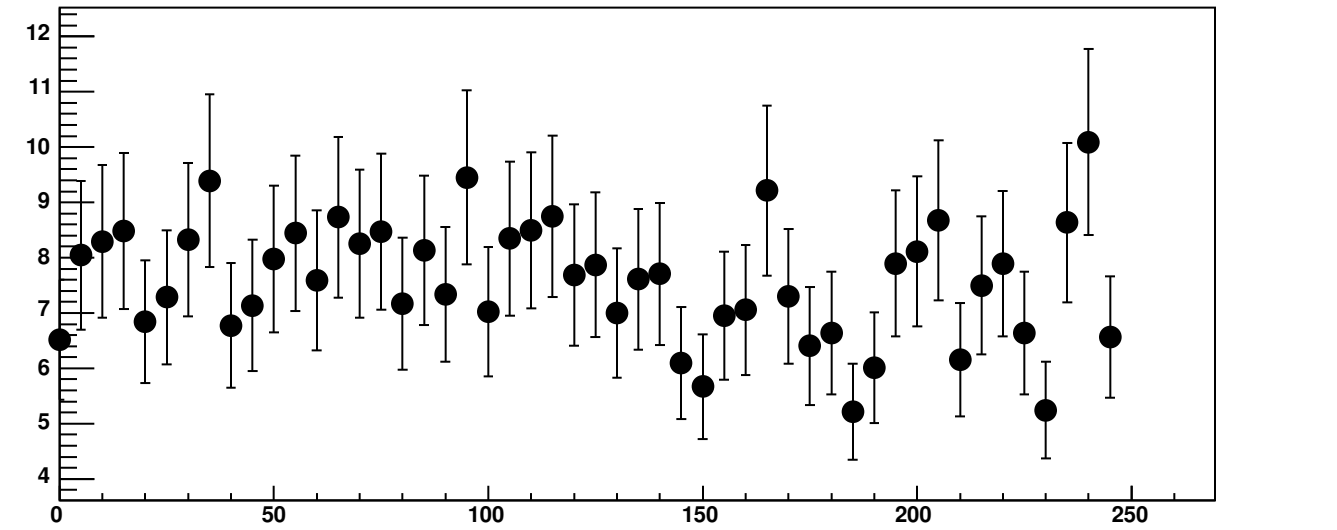

Chip 1, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 1, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
0
50
100
150
200
250

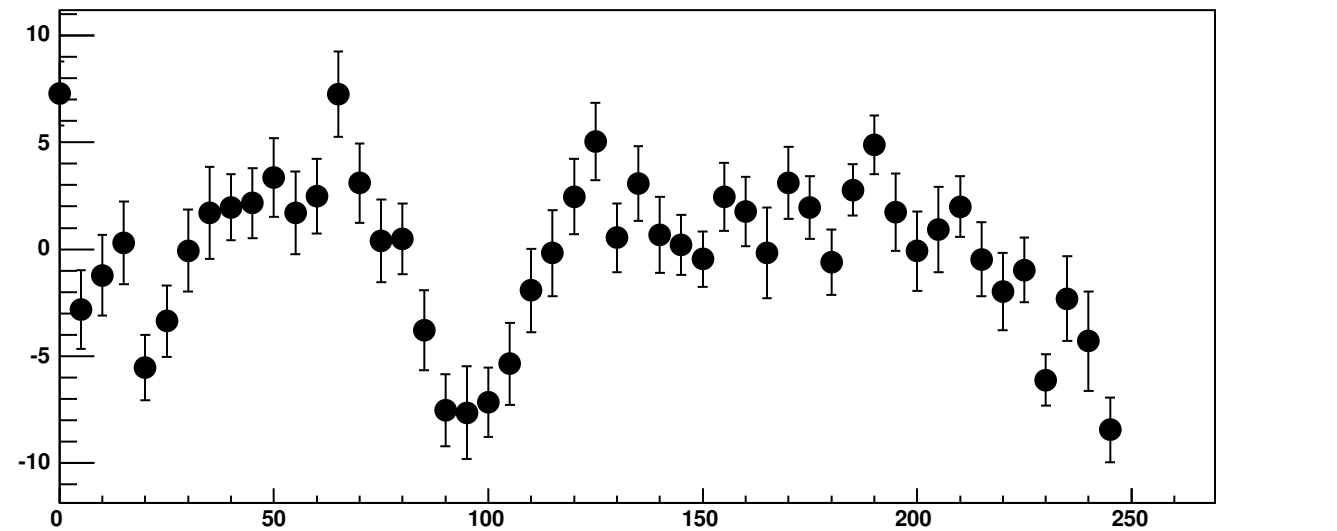
Chip 1, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

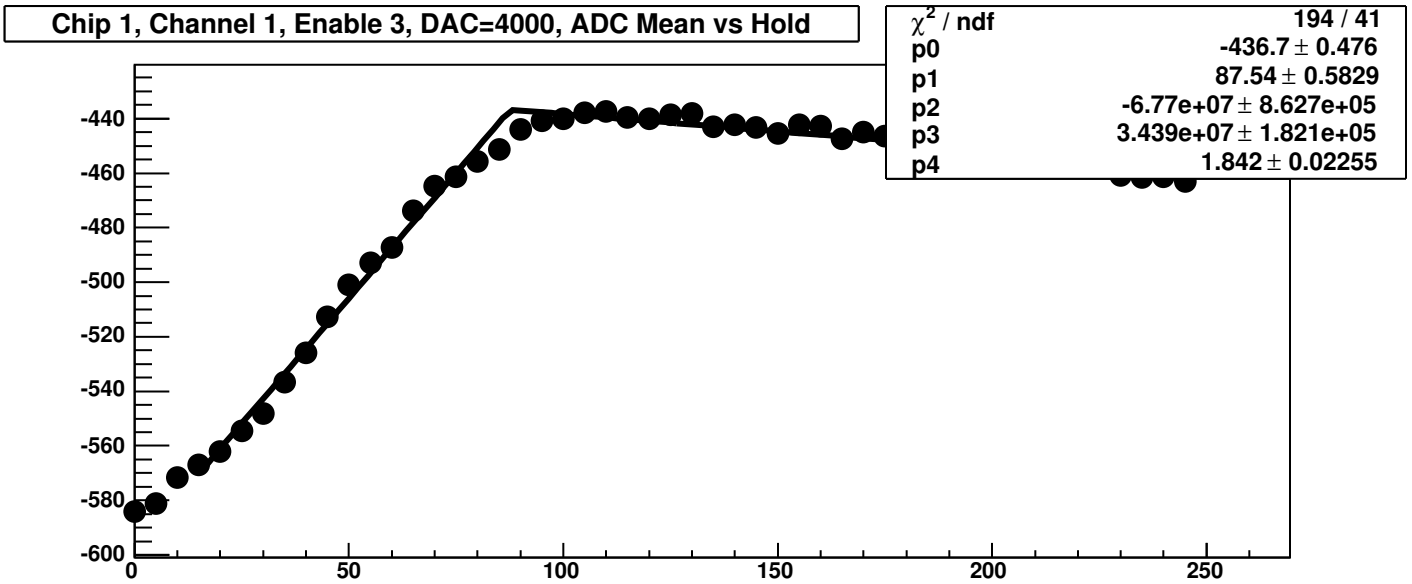


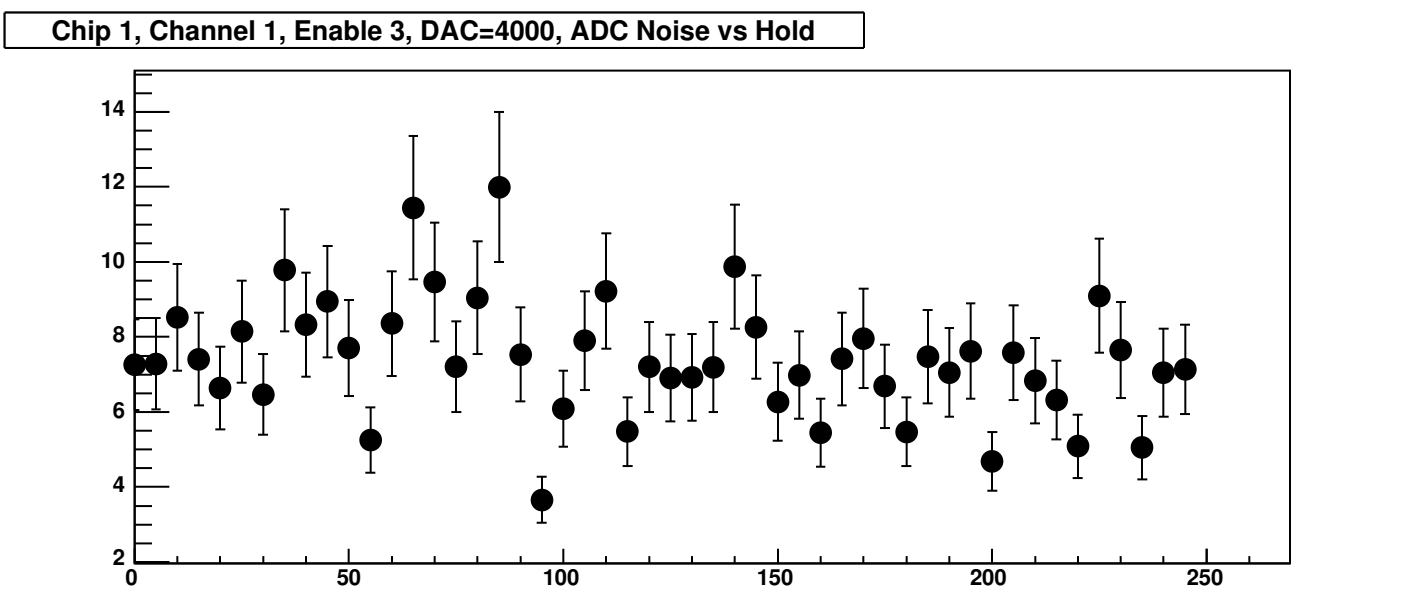
Chip 1, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold

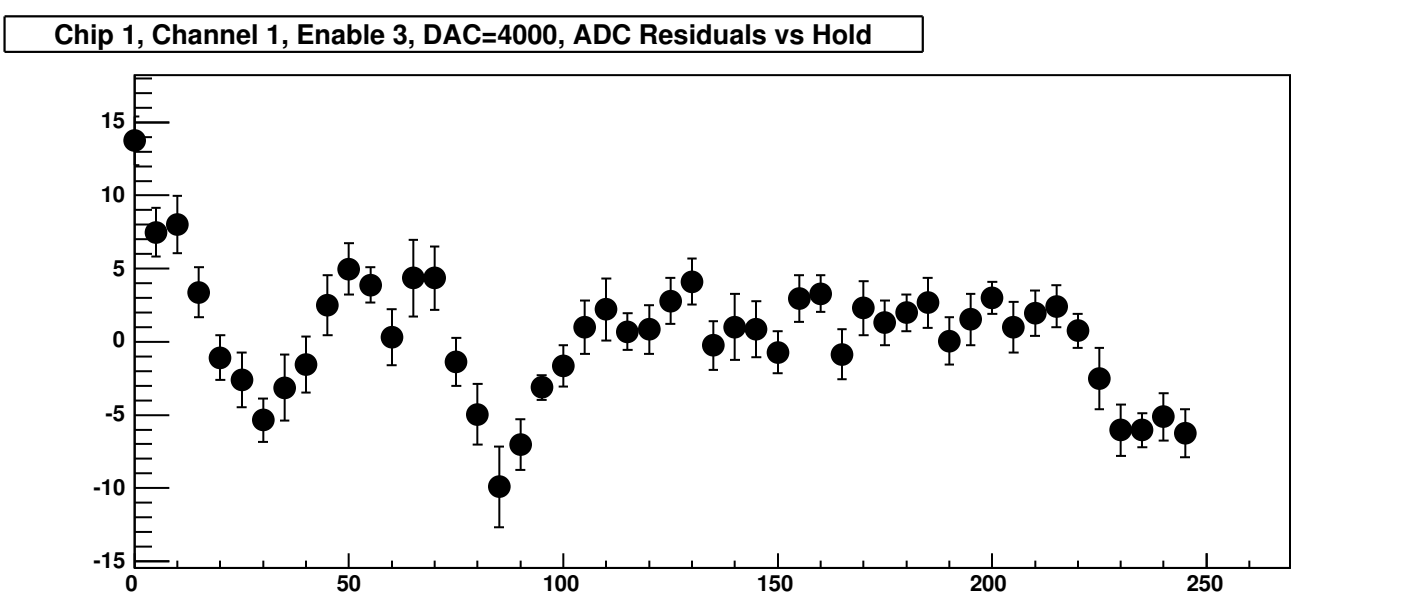


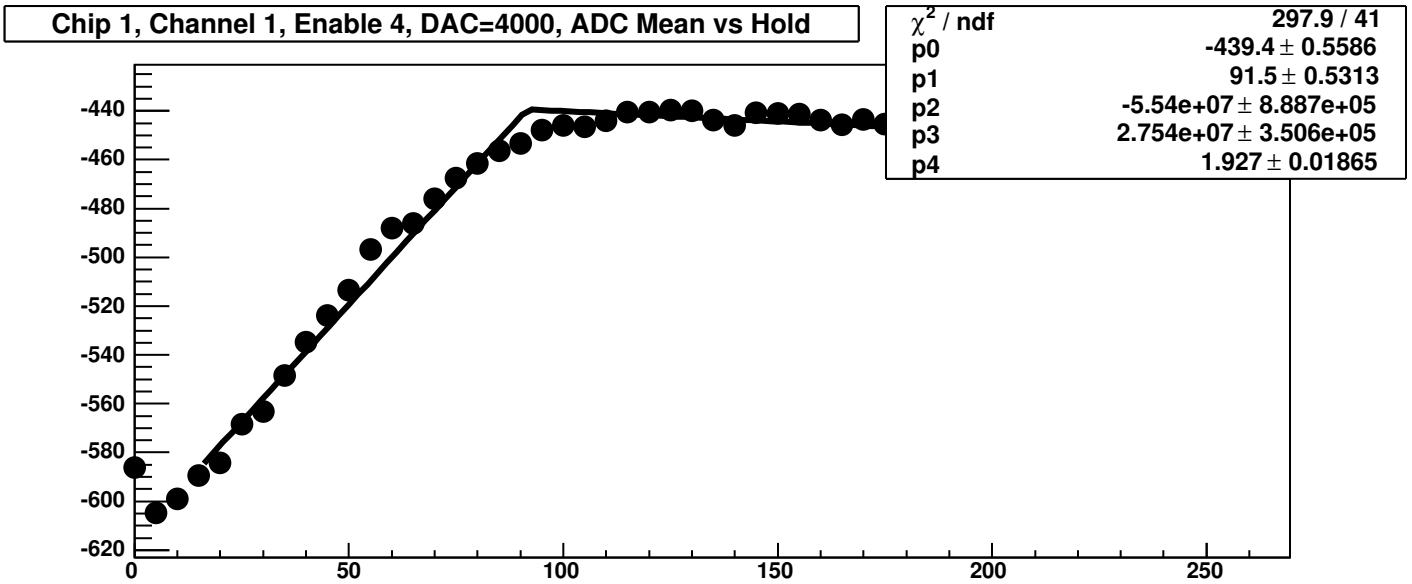
Chip 1, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

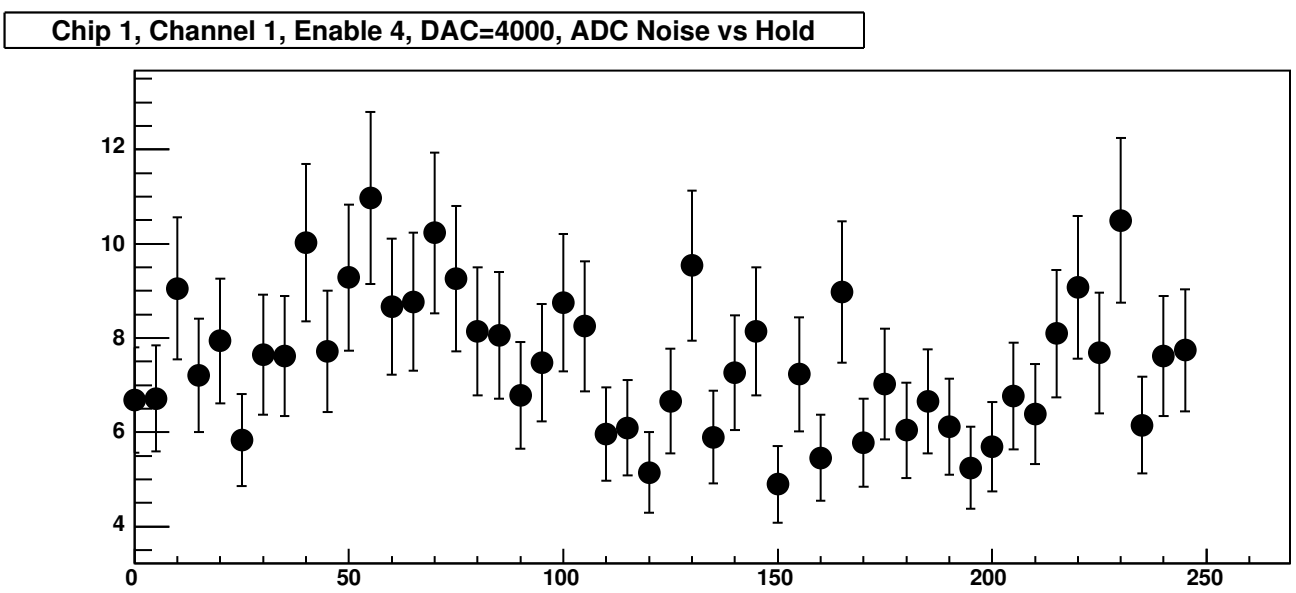


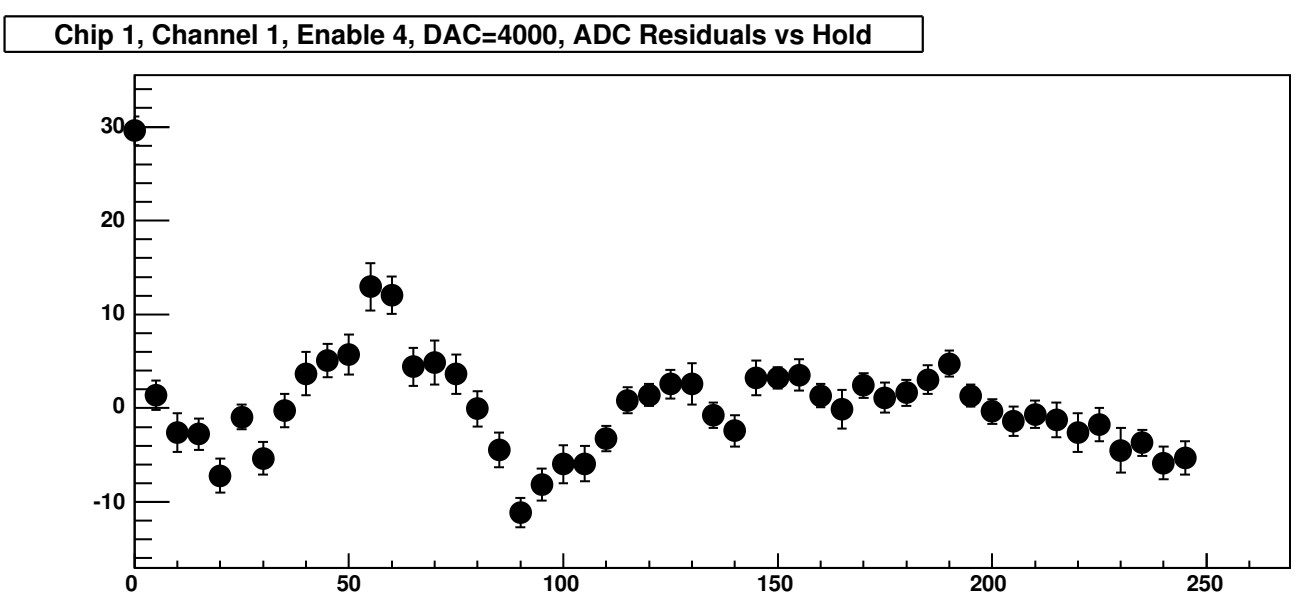

Chip 1, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 194 / 41
p0 -436.7 ± 0.476
p1 87.54 ± 0.5829
p2 -6.77e+07 ± 8.627e+05
p3 3.439e+07 ± 1.821e+05
p4 1.842 ± 0.02255
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
50
100
150
200
250

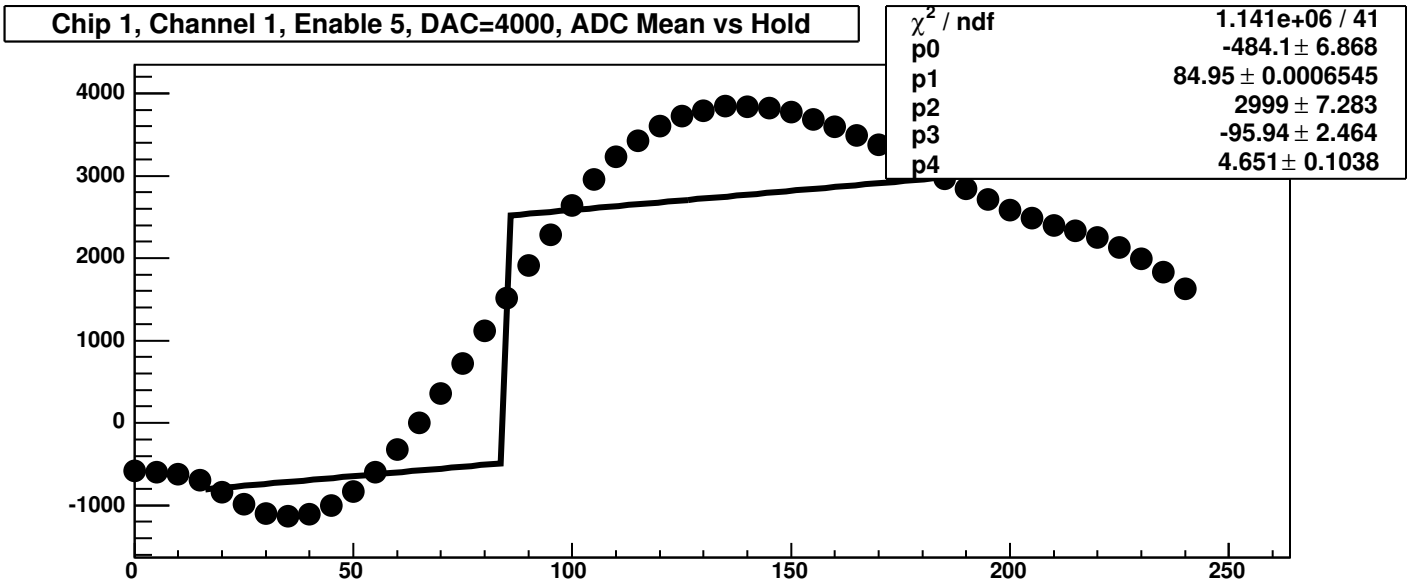

Chip 1, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
2
0
50
100
150
200
250

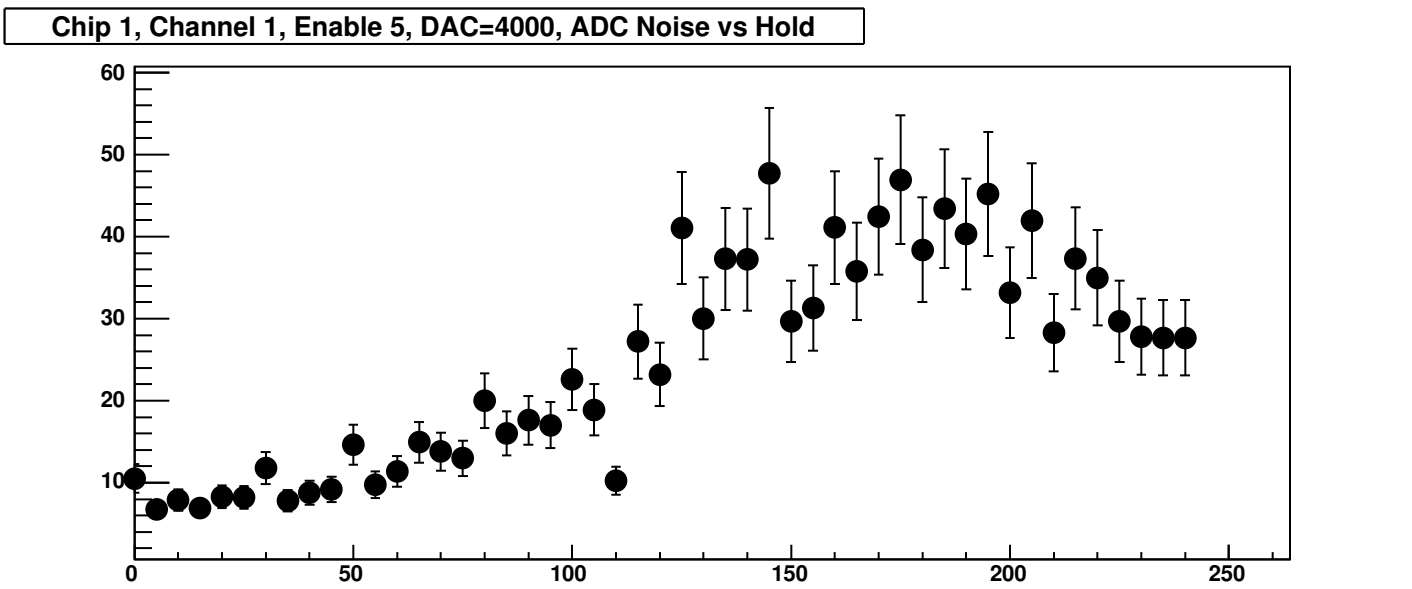

Chip 1, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

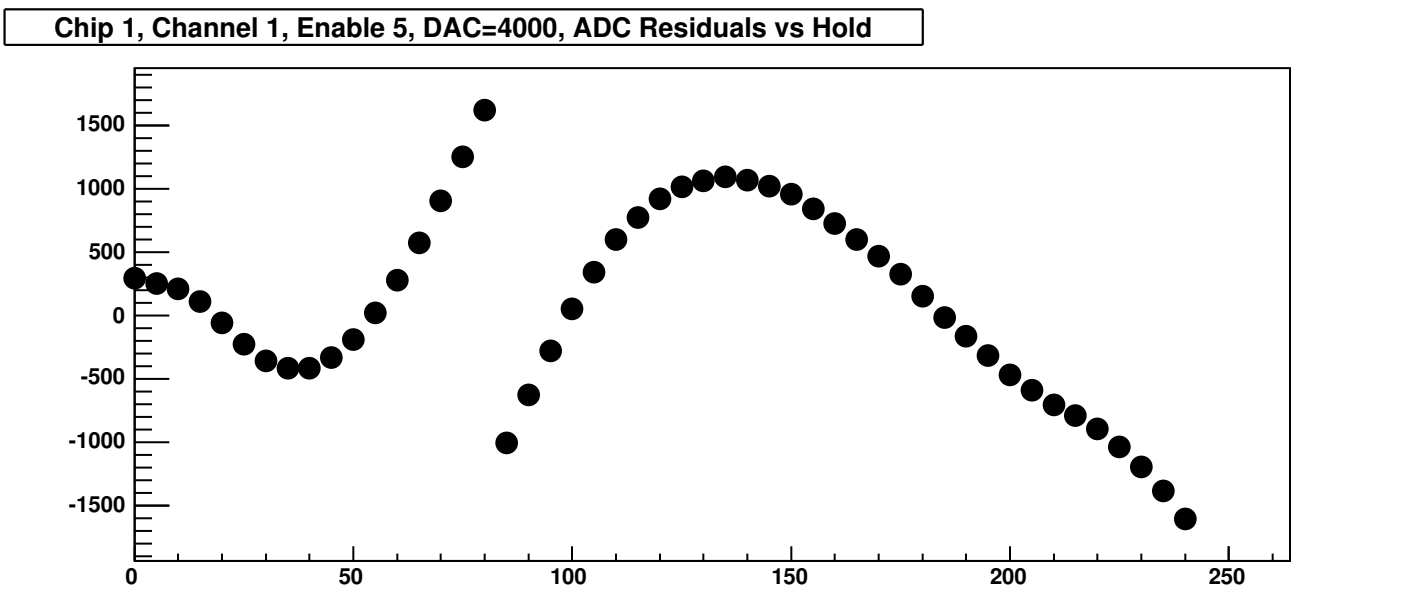

Chip 1, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 297.9 / 41
p0 -439.4 ± 0.5586
p1 91.5 ± 0.5313
p2 -5.54e+07 ± 8.887e+05
p3 2.754e+07 ± 3.506e+05
p4 1.927 ± 0.01865
-440
-460
-480
-500
-520
-540
-560
-580
-600
-620
0
50
100
150
200
250


Chip 1, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

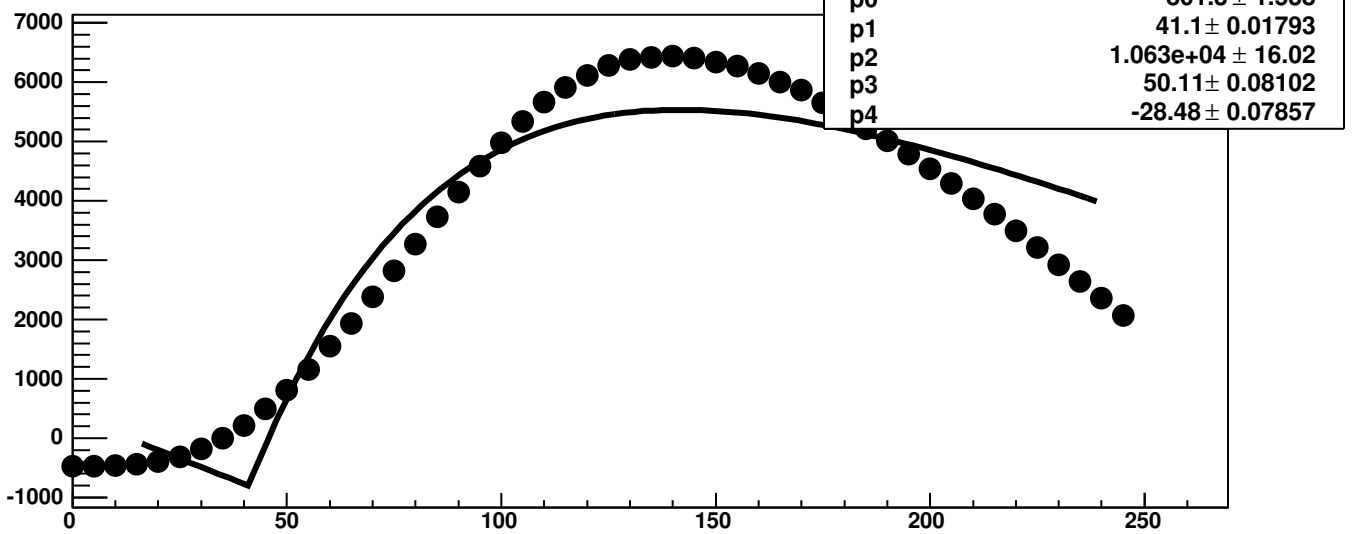

Chip 1, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
0
50
100
150
200
250


Chip 1, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.141e+06 / 41
p0 -484.1 ± 6.868
p1 84.95 ± 0.0006545
p2 2999 ± 7.283
p3 -95.94 ± 2.464
p4 4.651 ± 0.1038
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

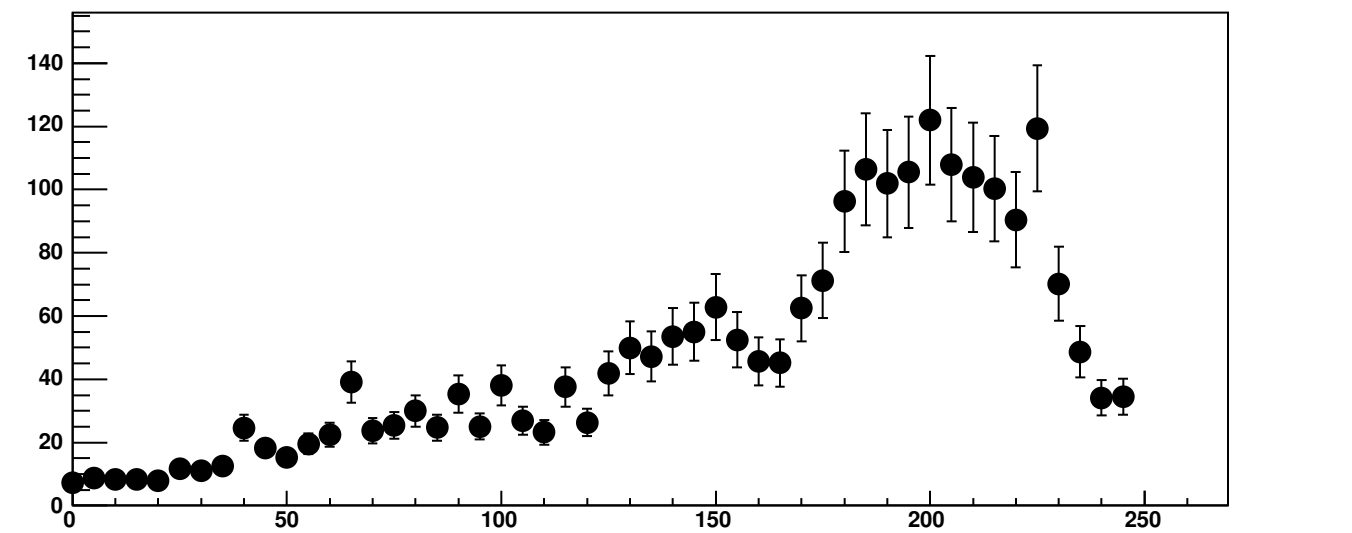

Chip 1, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold
60
50
40
30
20
10
0
50
100
150
200
250


Chip 1, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold
1500
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

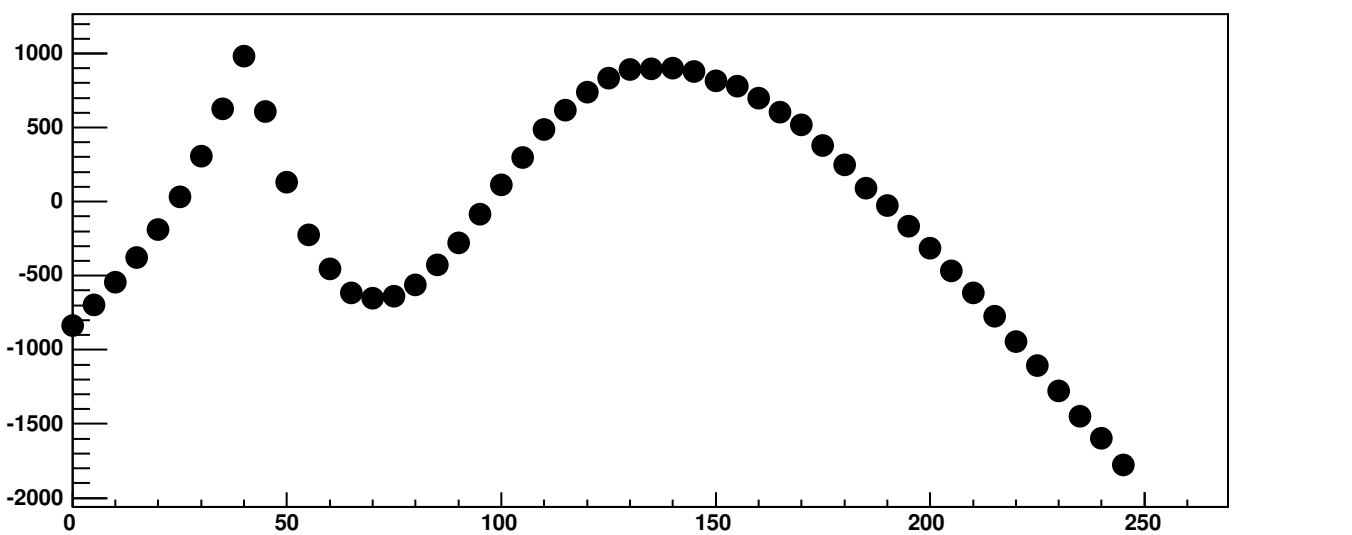
Chip 1, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold

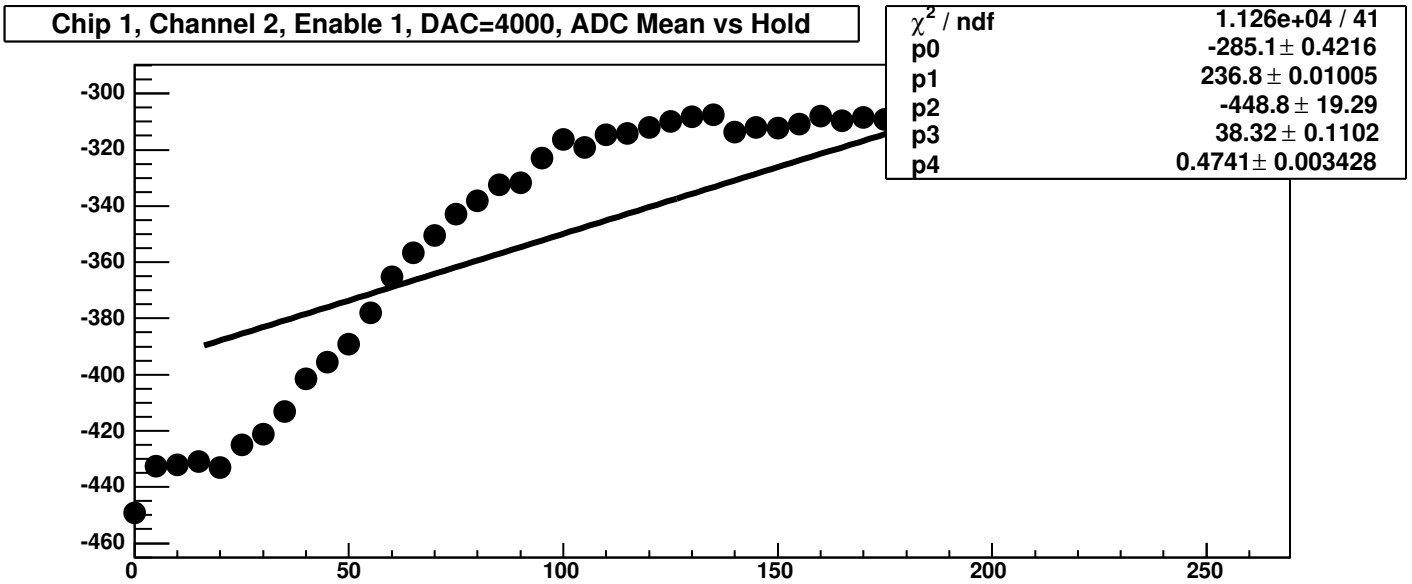


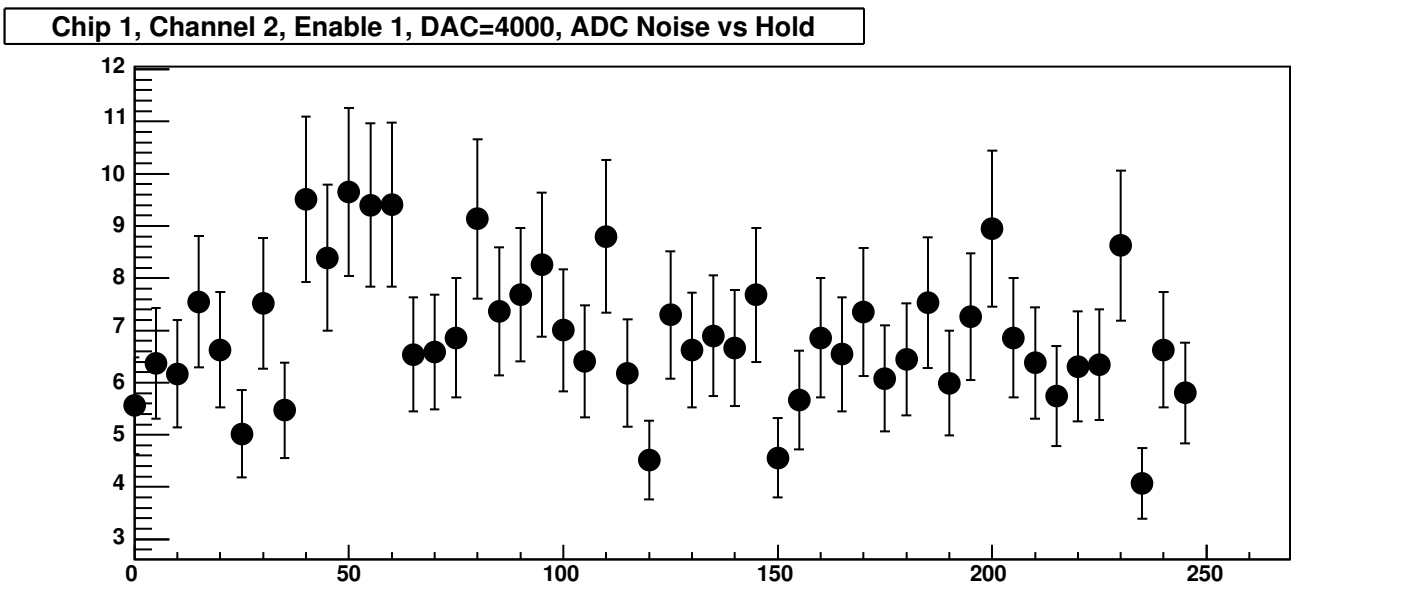
Chip 1, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold

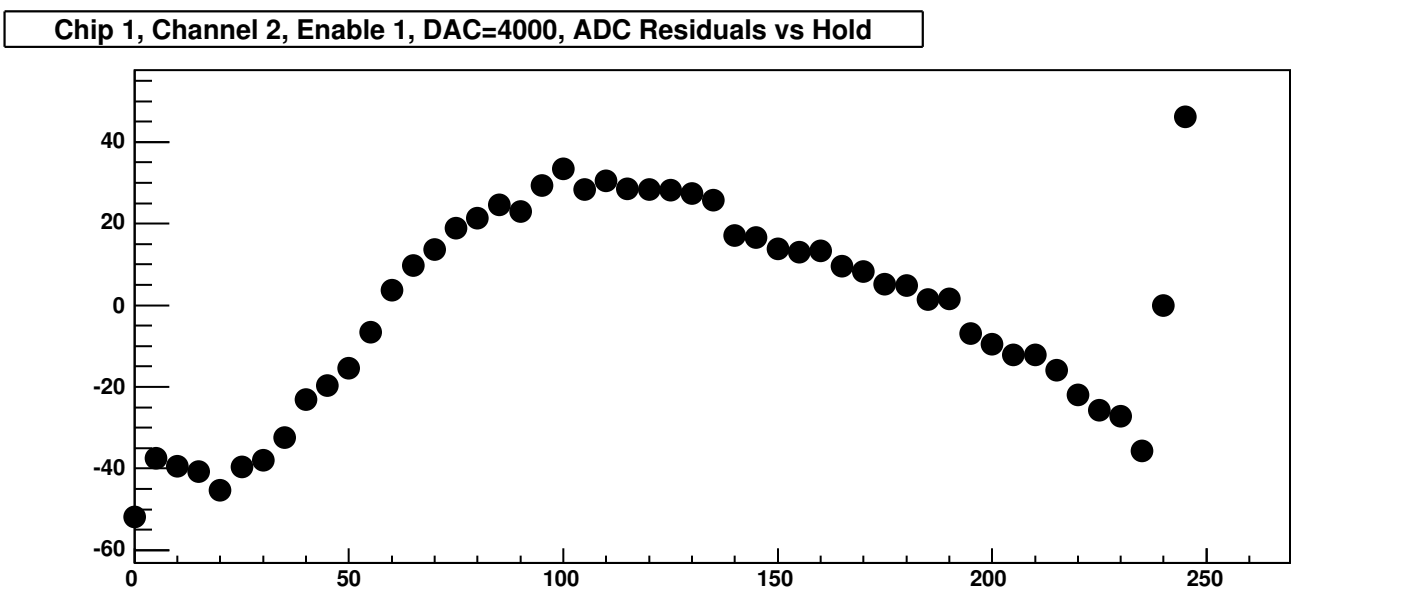


Chip 1, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold

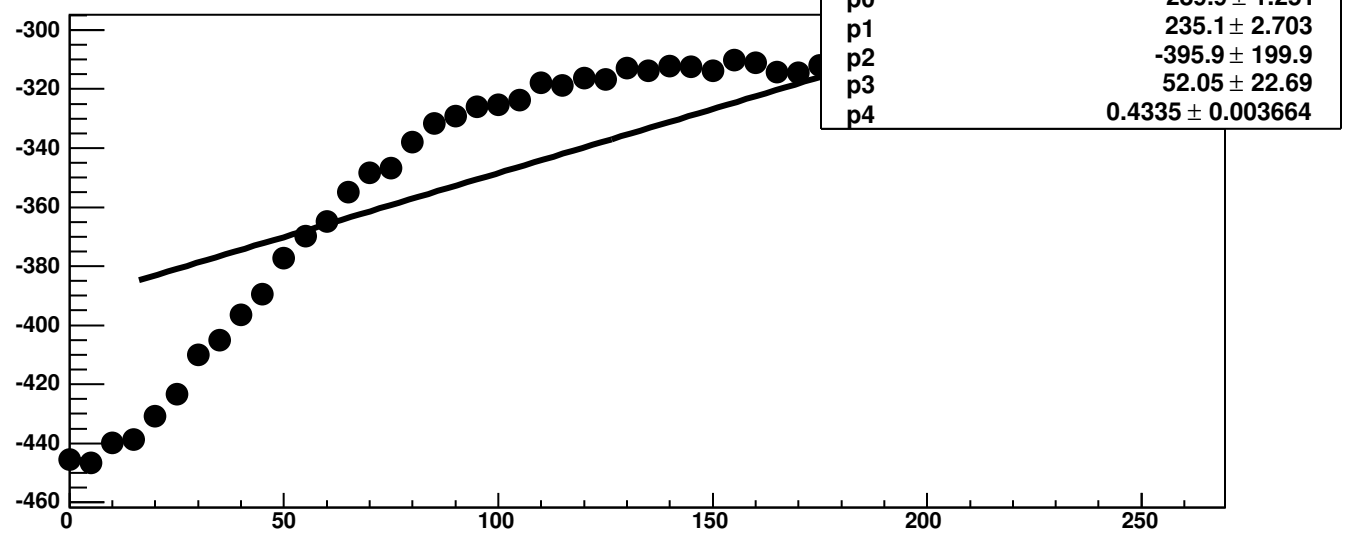



Chip 1, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.126e+04 / 41
p0 -285.1 ± 0.4216
p1 236.8 ± 0.01005
p2 -448.8 ± 19.29
p3 38.32 ± 0.1102
p4 0.4741 ± 0.003428
-300
-320
-340
-360
-380
-400
-420
-440
-460
0
50
100
150
200
250

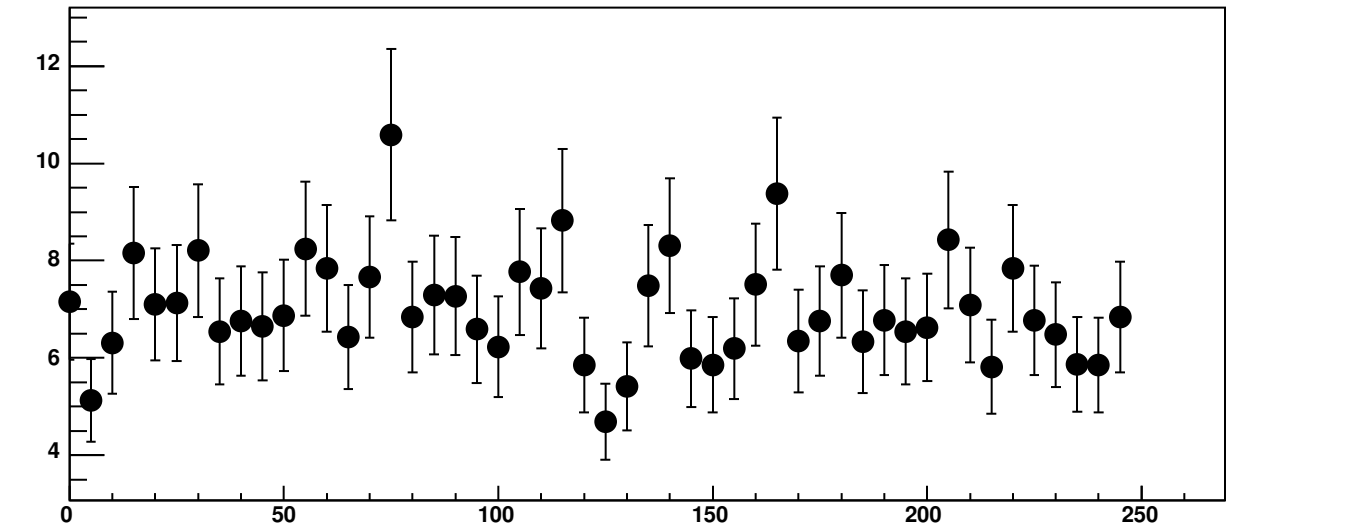

Chip 1, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 1, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
0
50
100
150
200
250

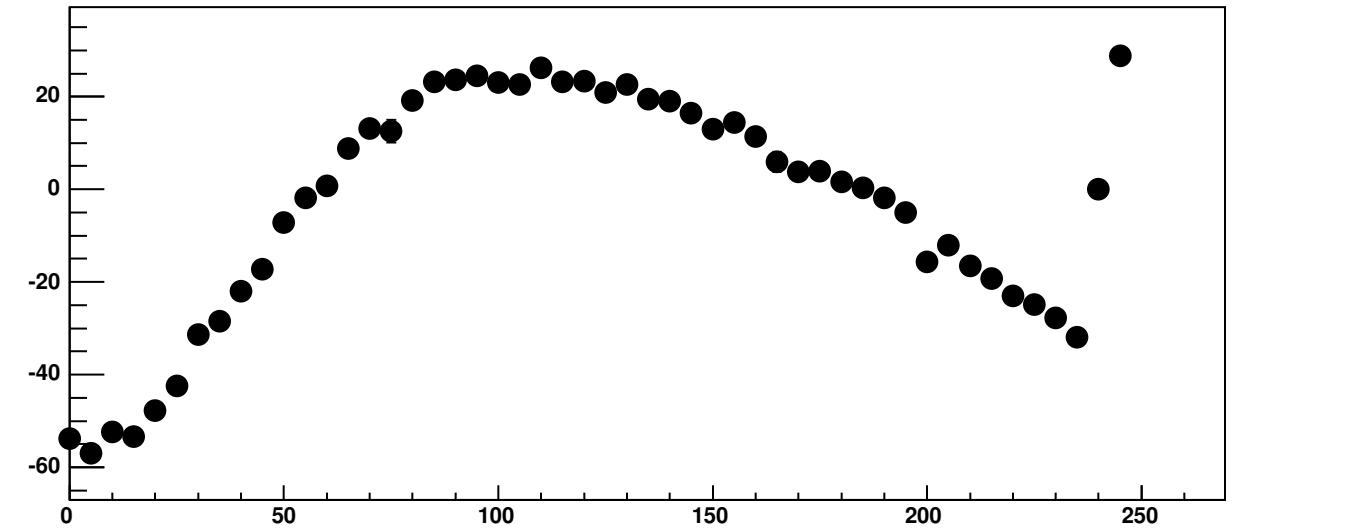
Chip 1, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold



Chip 1, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold

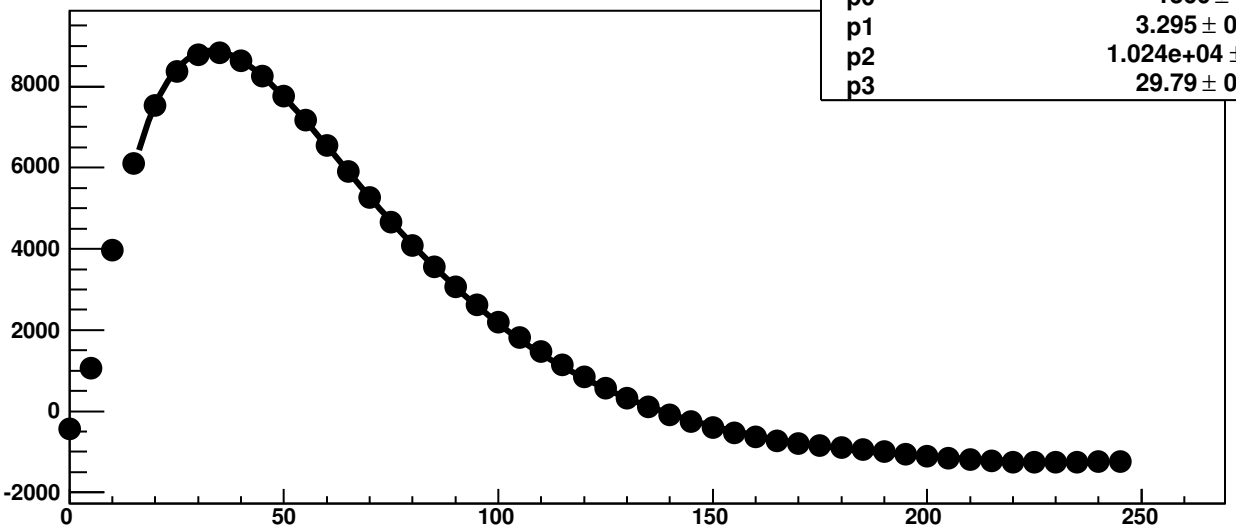


Chip 1, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

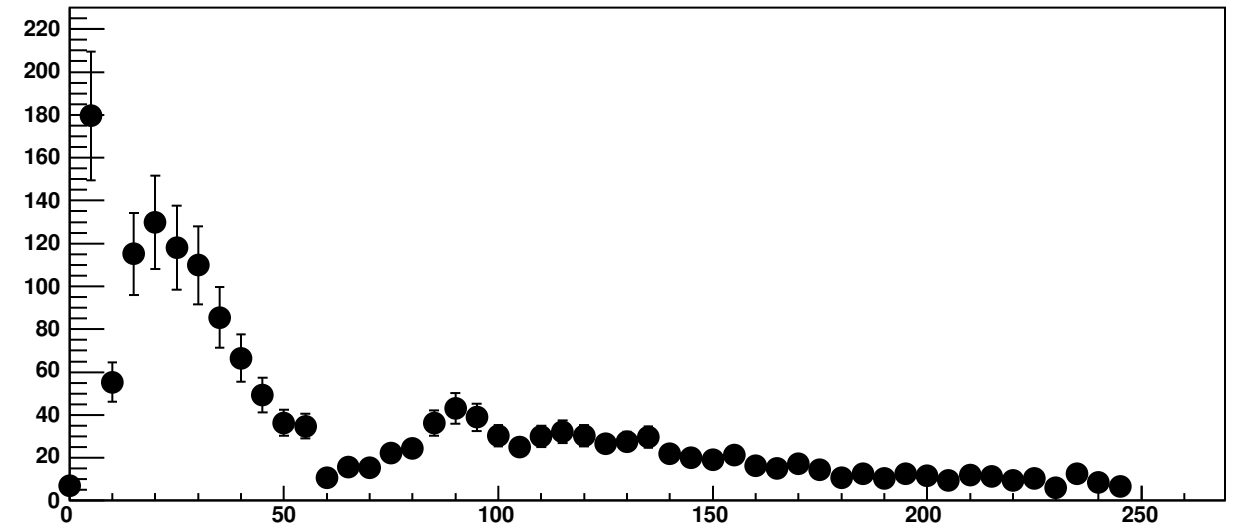


Chip 1, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold

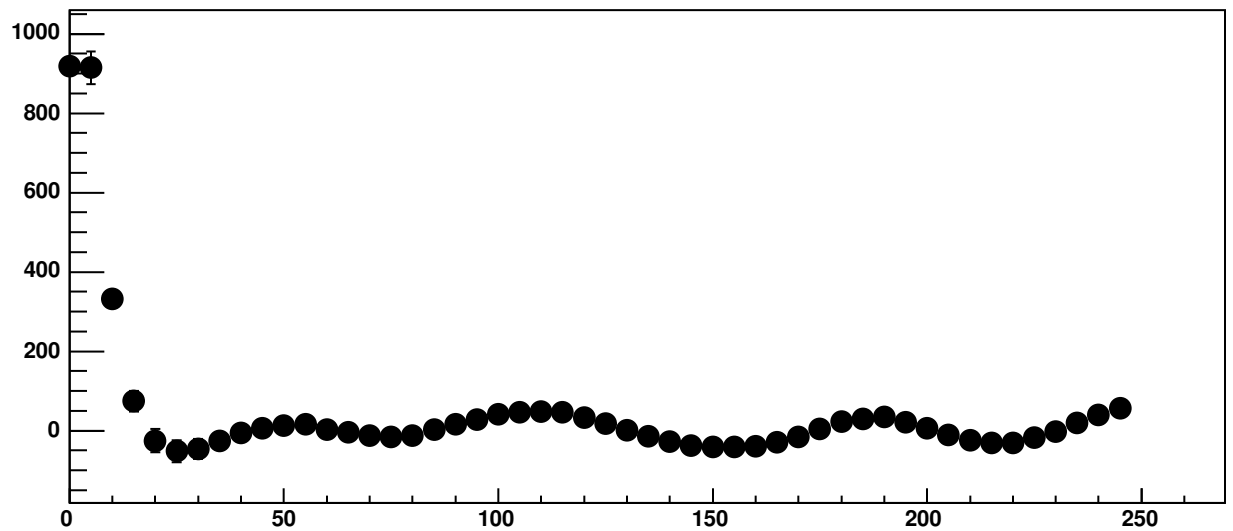
χ^2 / ndf	2166 / 42
p0	-1360 ± 0.8855
p1	3.295 ± 0.04712
p2	1.024e+04 ± 5.487
p3	29.79 ± 0.01748



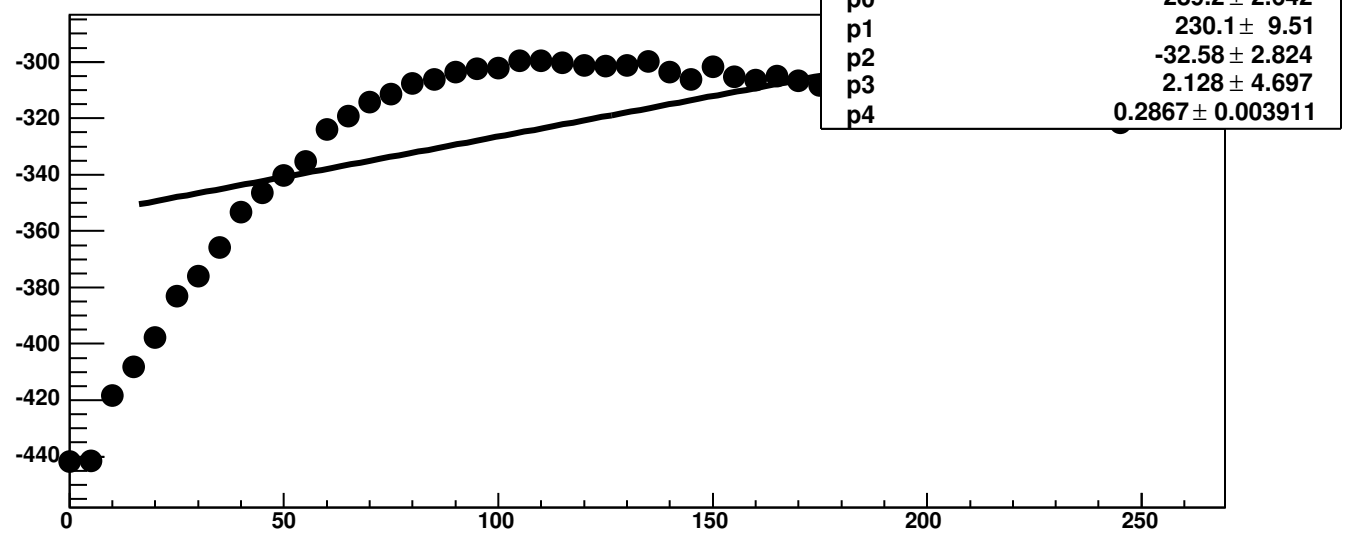
Chip 1, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold



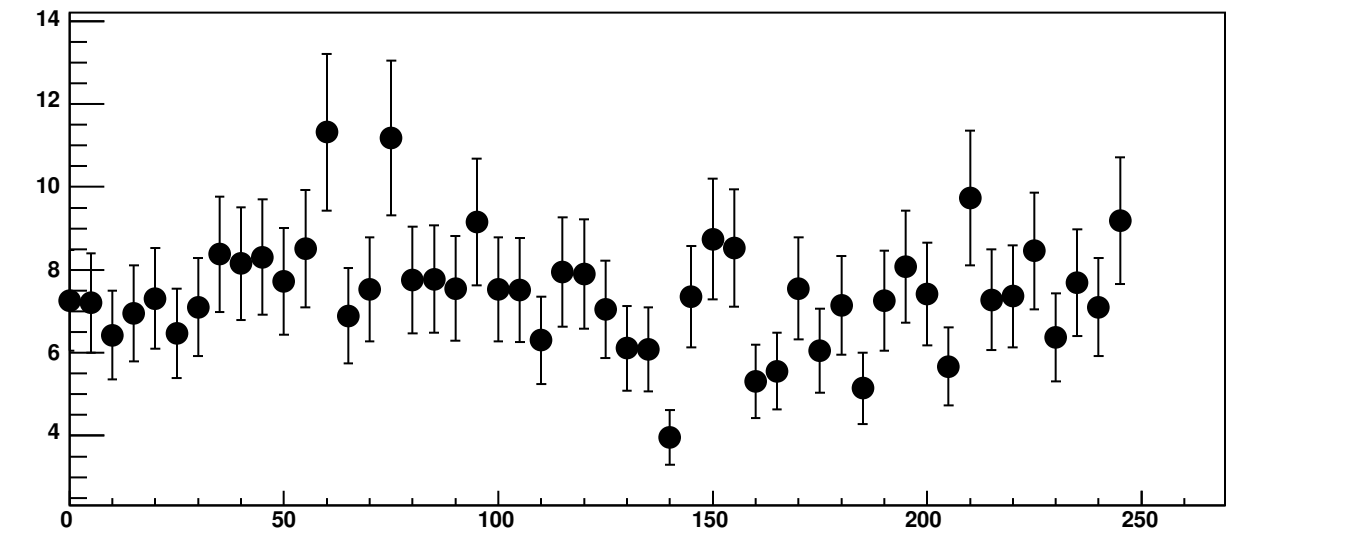
Chip 1, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold



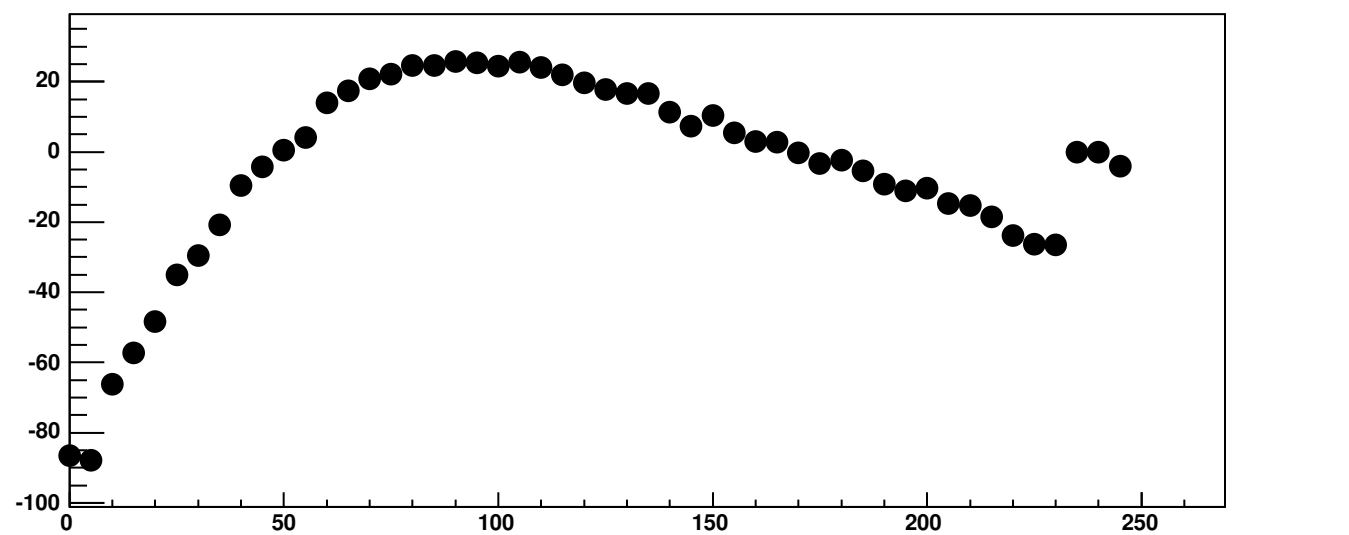
Chip 1, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold



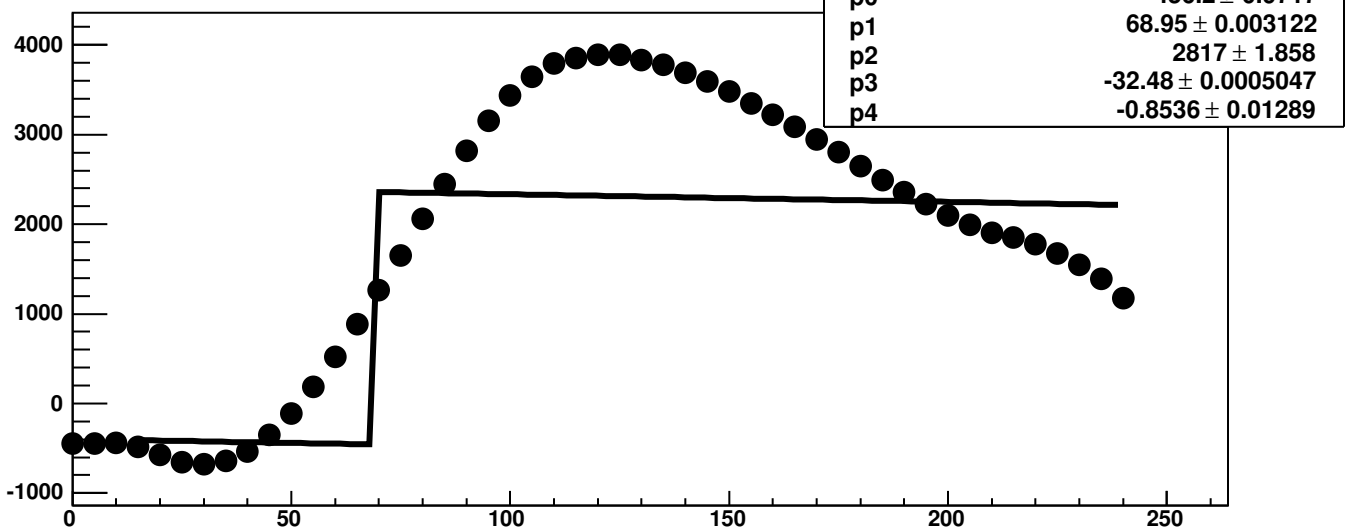
Chip 1, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



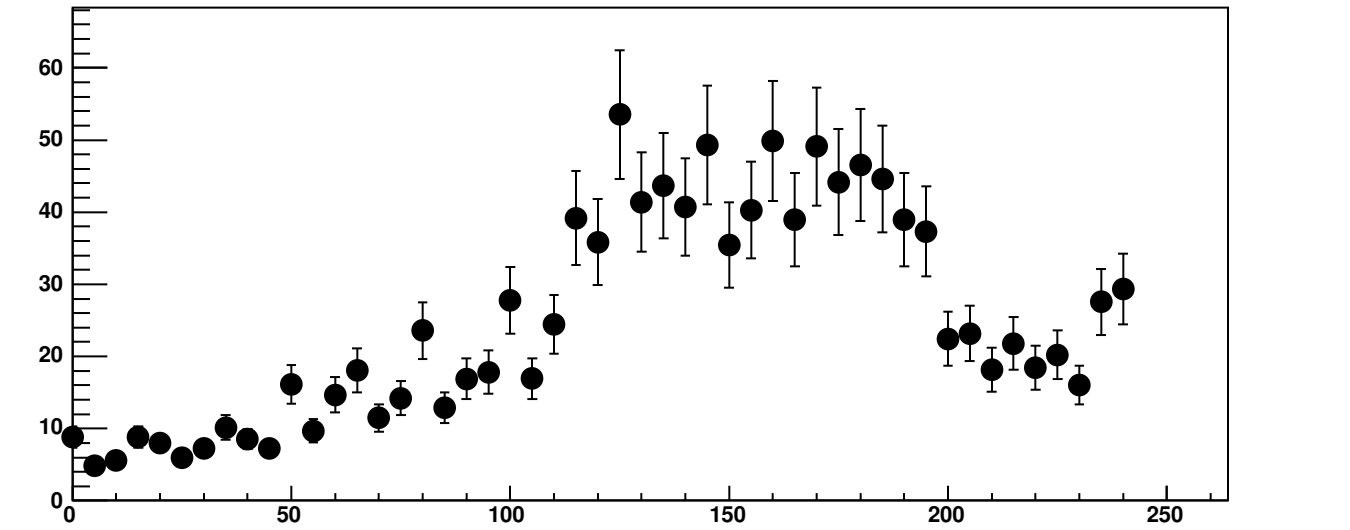
Chip 1, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold



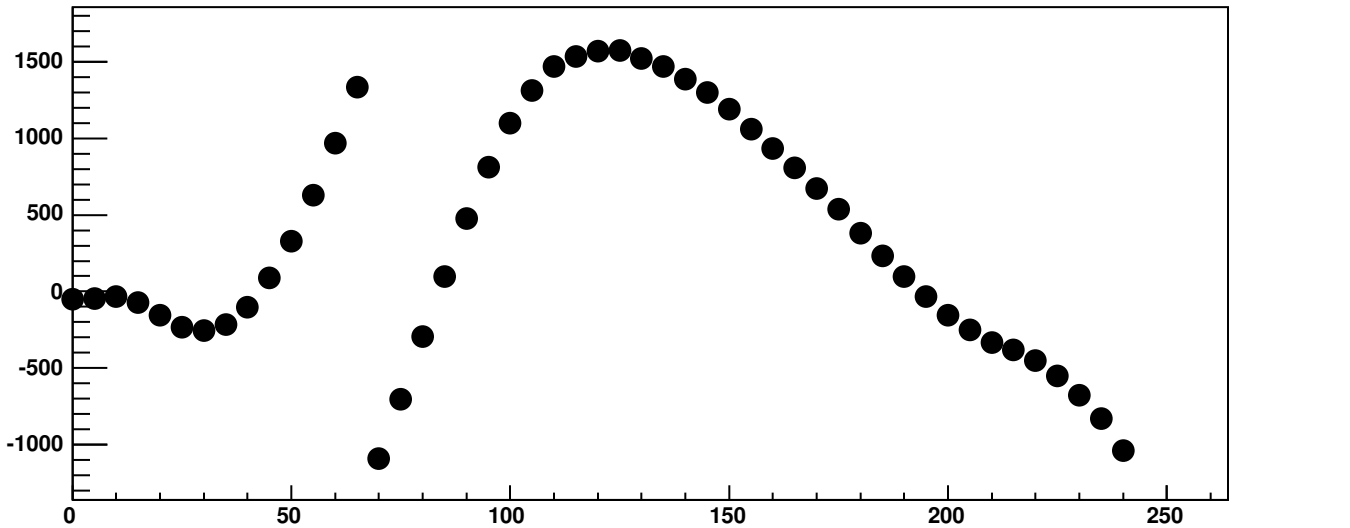
Chip 1, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

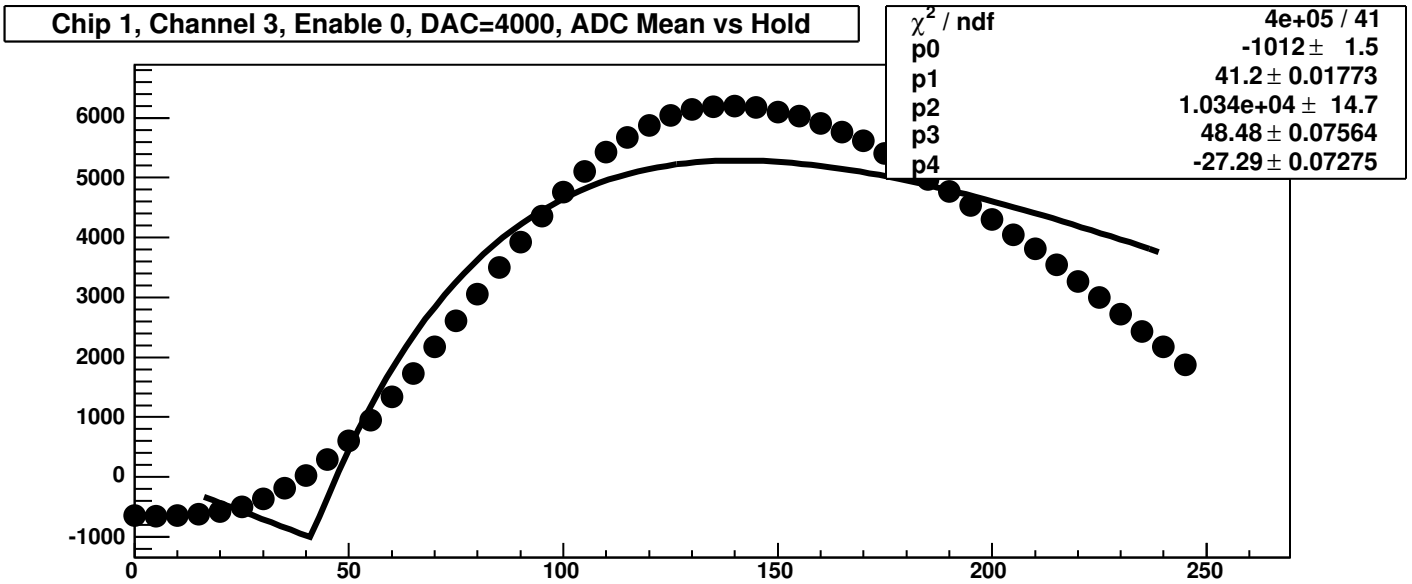


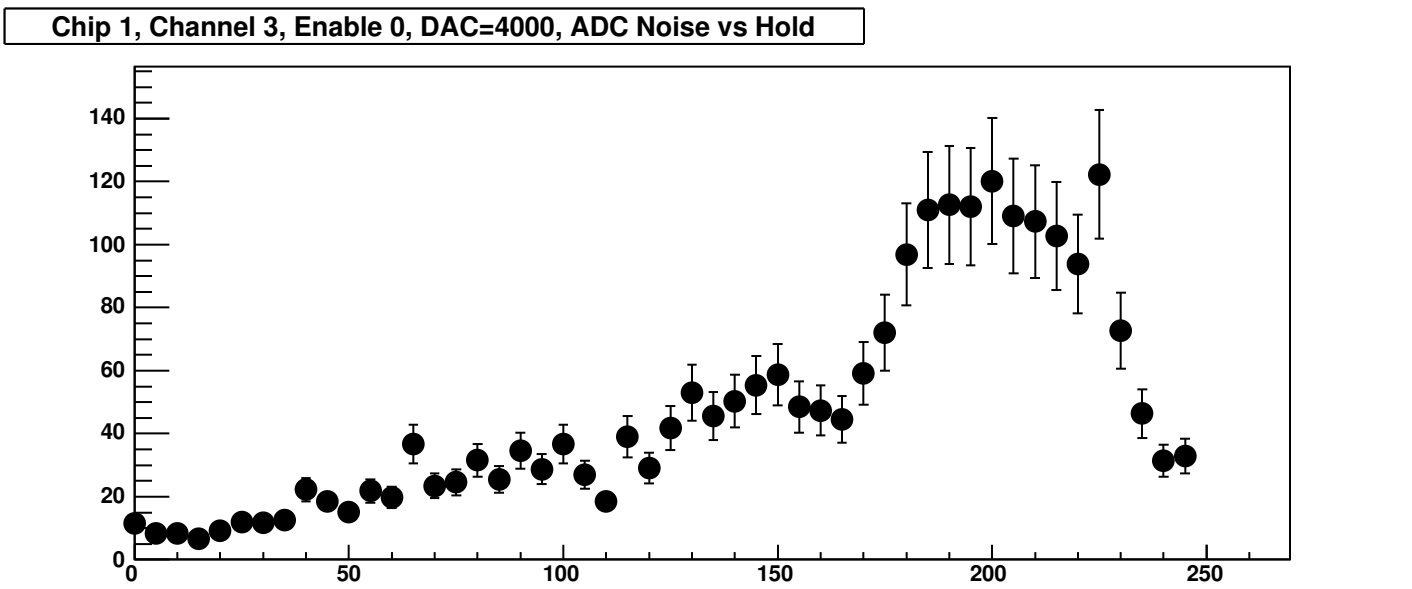
Chip 1, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold

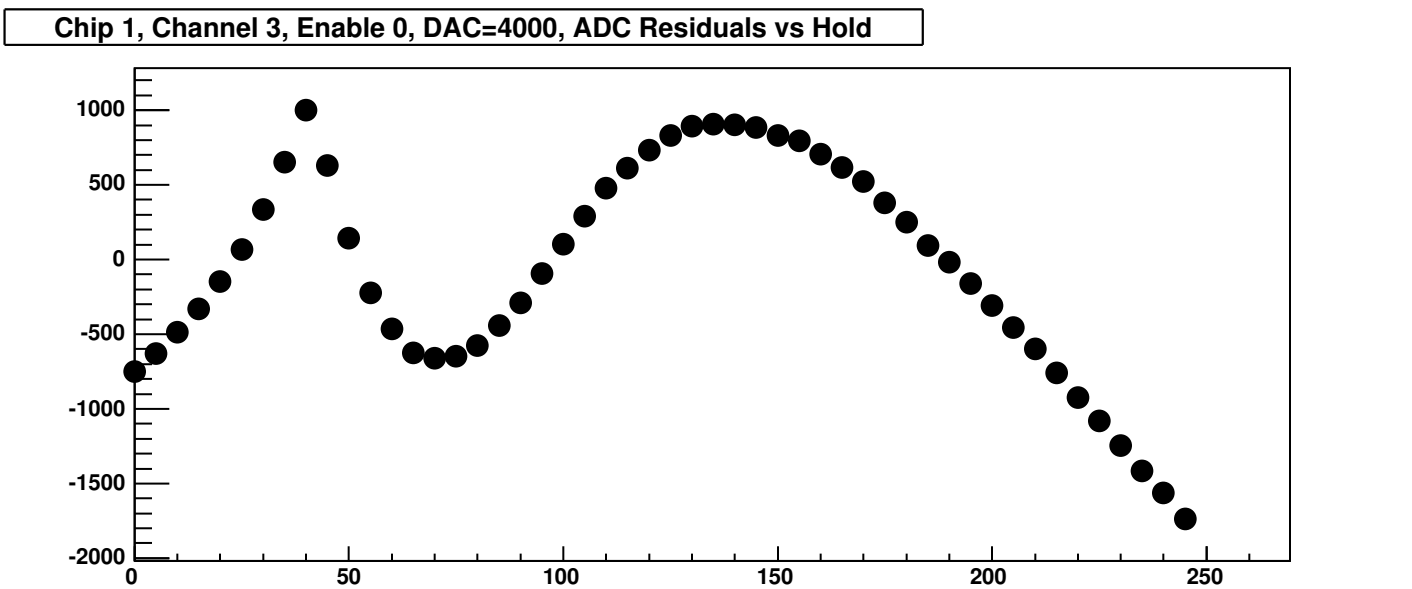


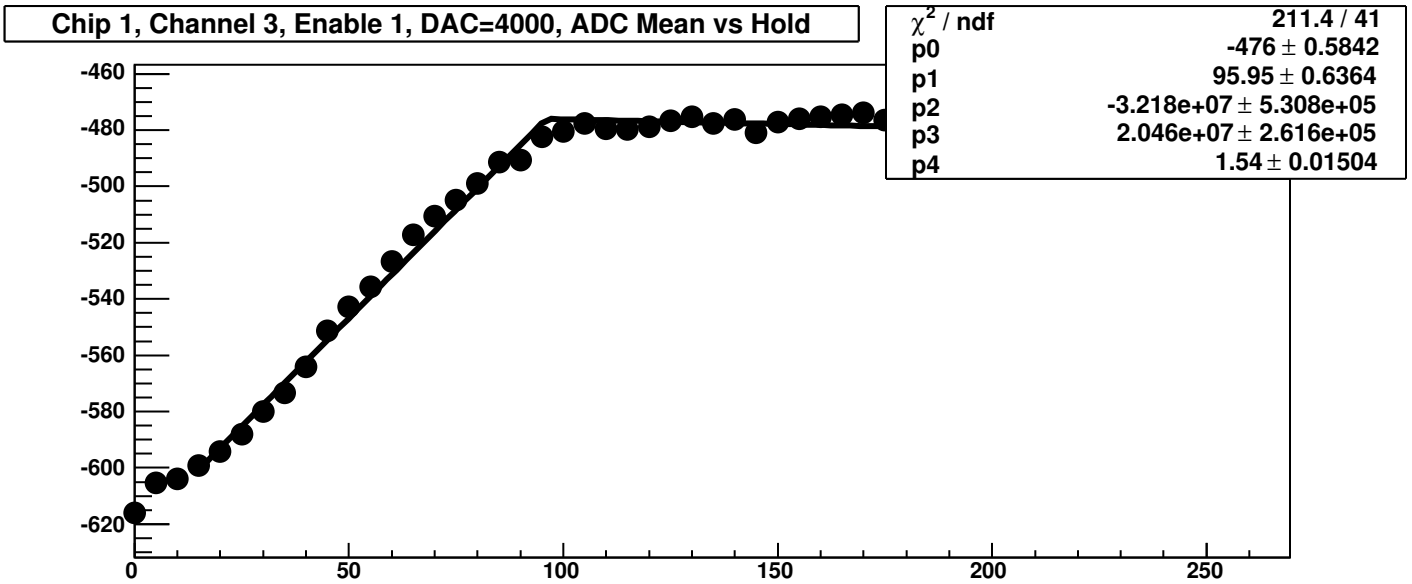
Chip 1, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold

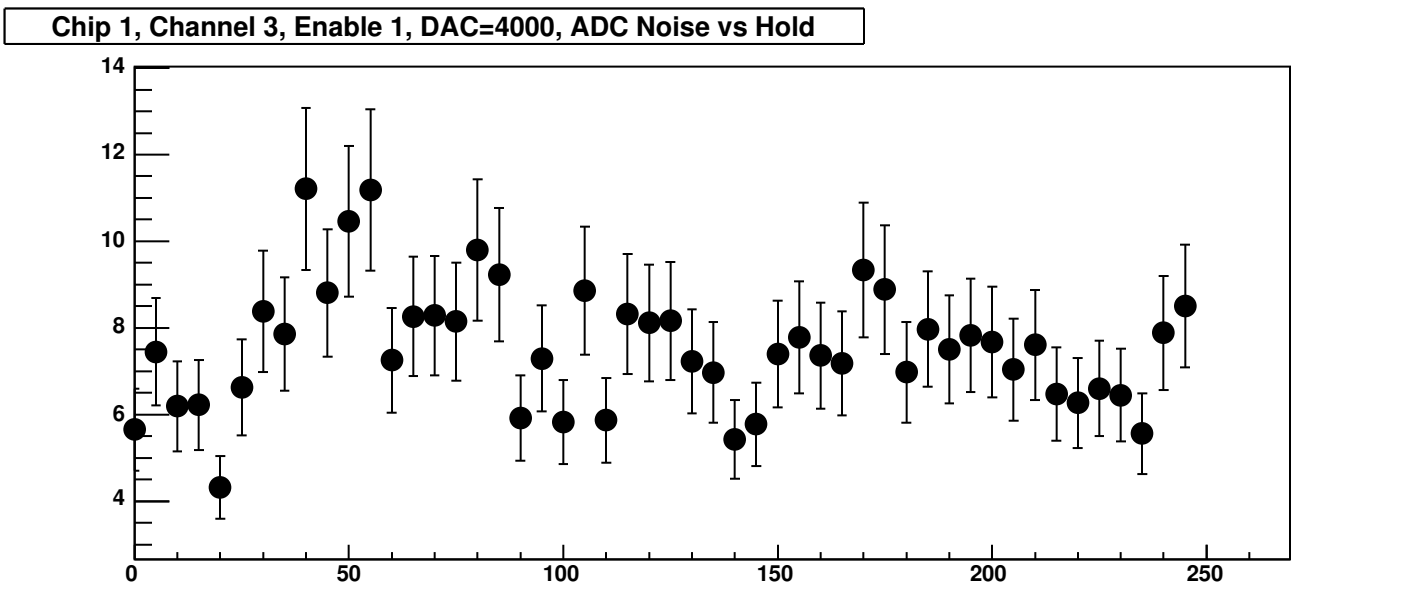


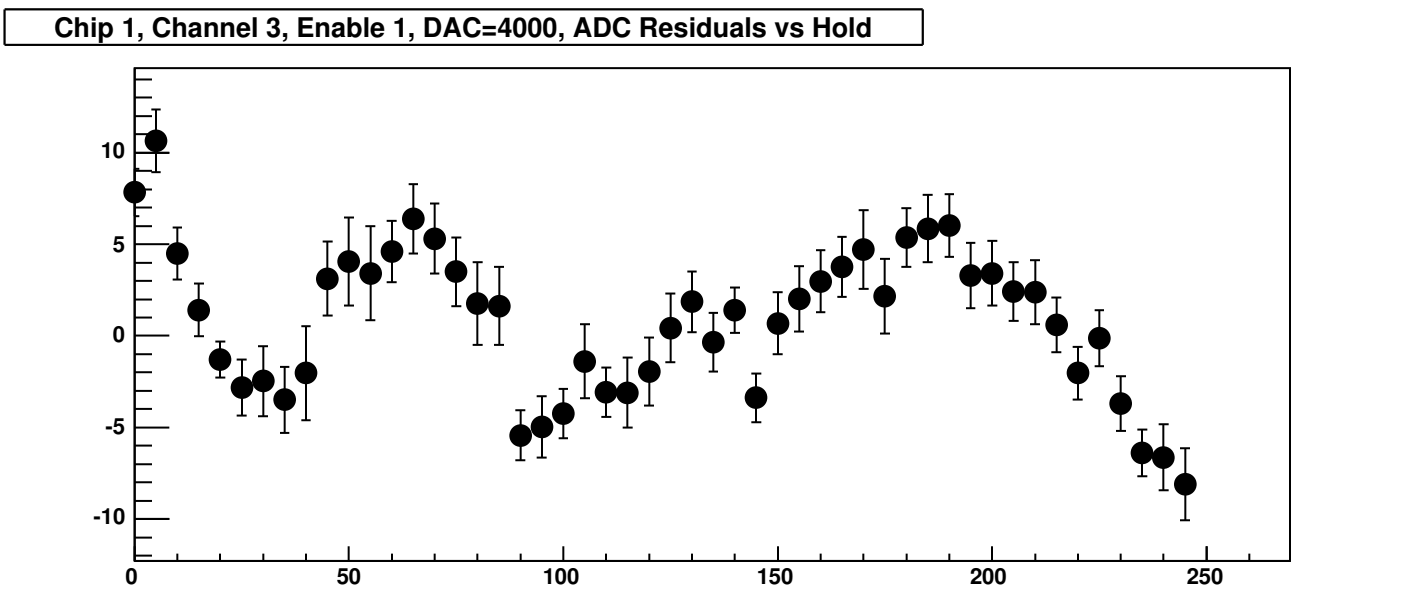

Chip 1, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4e+05 / 41
p0 -1012 ± 1.5
p1 41.2 ± 0.01773
p2 1.034e+04 ± 14.7
p3 48.48 ± 0.07564
p4 -27.29 ± 0.07275
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

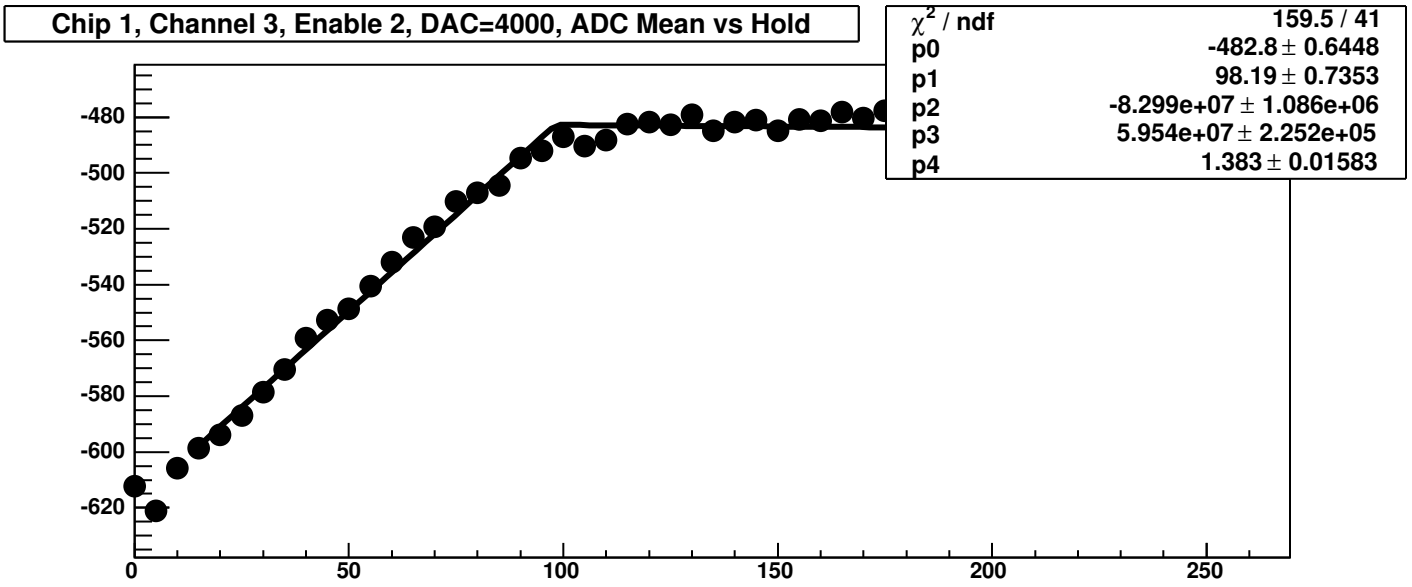

Chip 1, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250

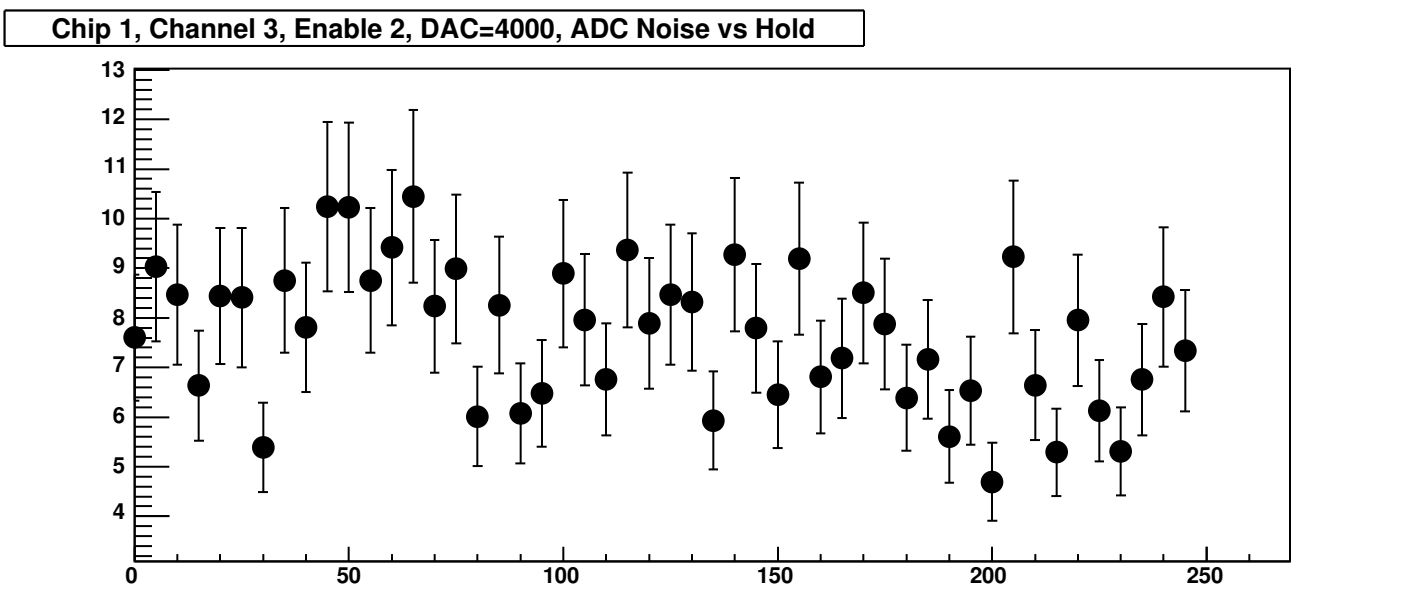

Chip 1, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

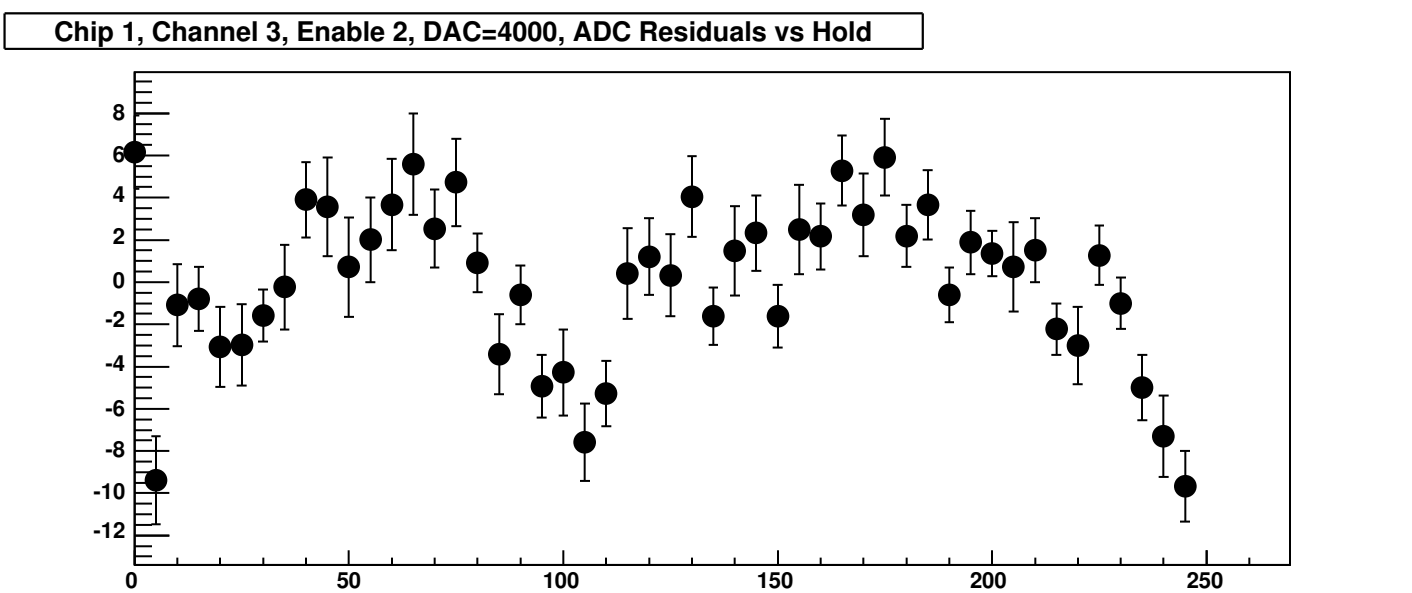

Chip 1, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 211.4 / 41
p0 -476 ± 0.5842
p1 95.95 ± 0.6364
p2 -3.218e+07 ± 5.308e+05
p3 2.046e+07 ± 2.616e+05
p4 1.54 ± 0.01504
-460
-480
-500
-520
-540
-560
-580
-600
-620
0
50
100
150
200
250

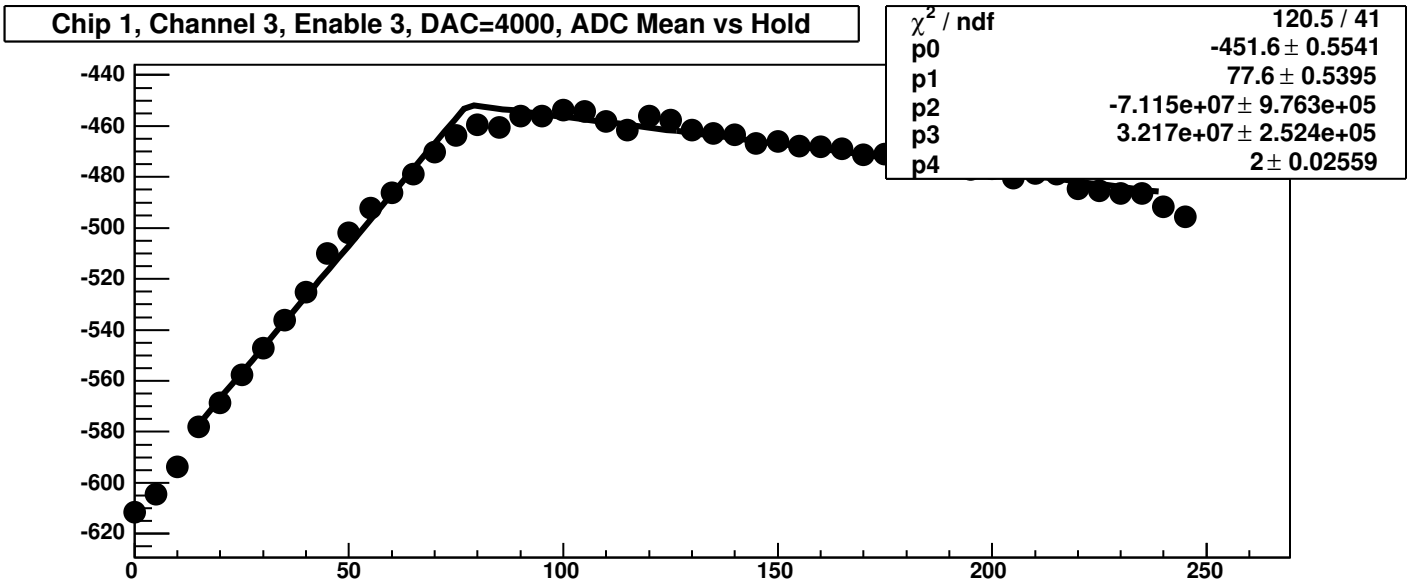

Chip 1, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

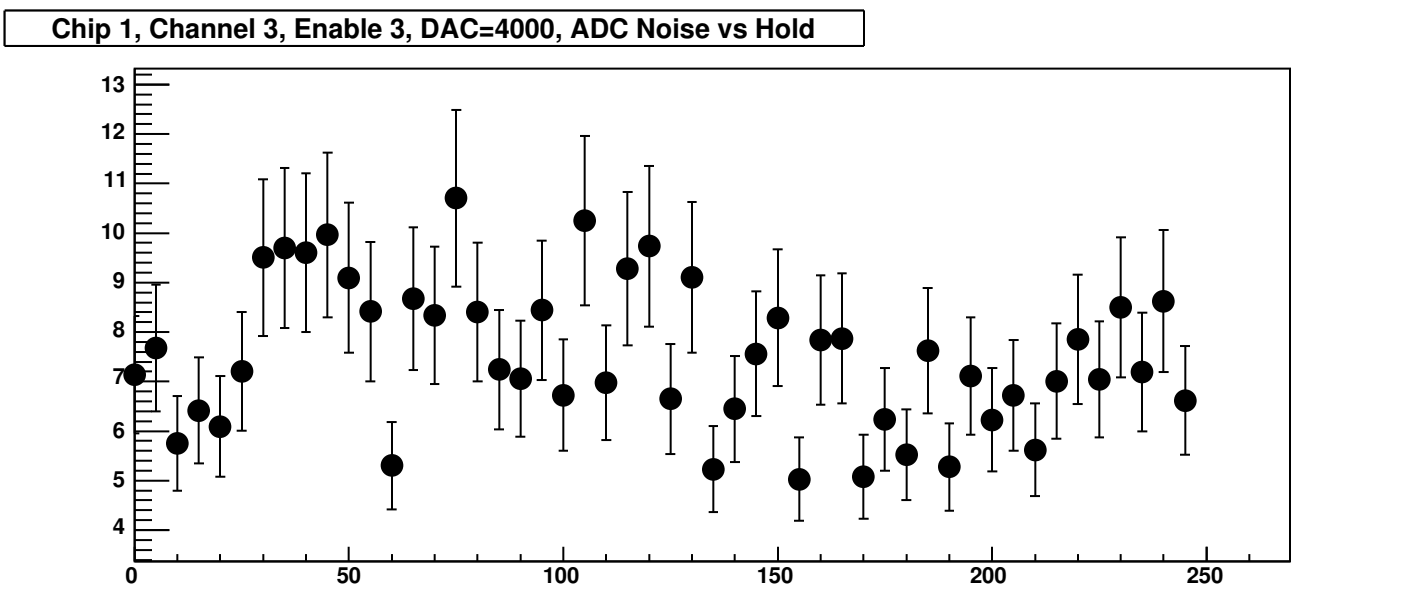

Chip 1, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250

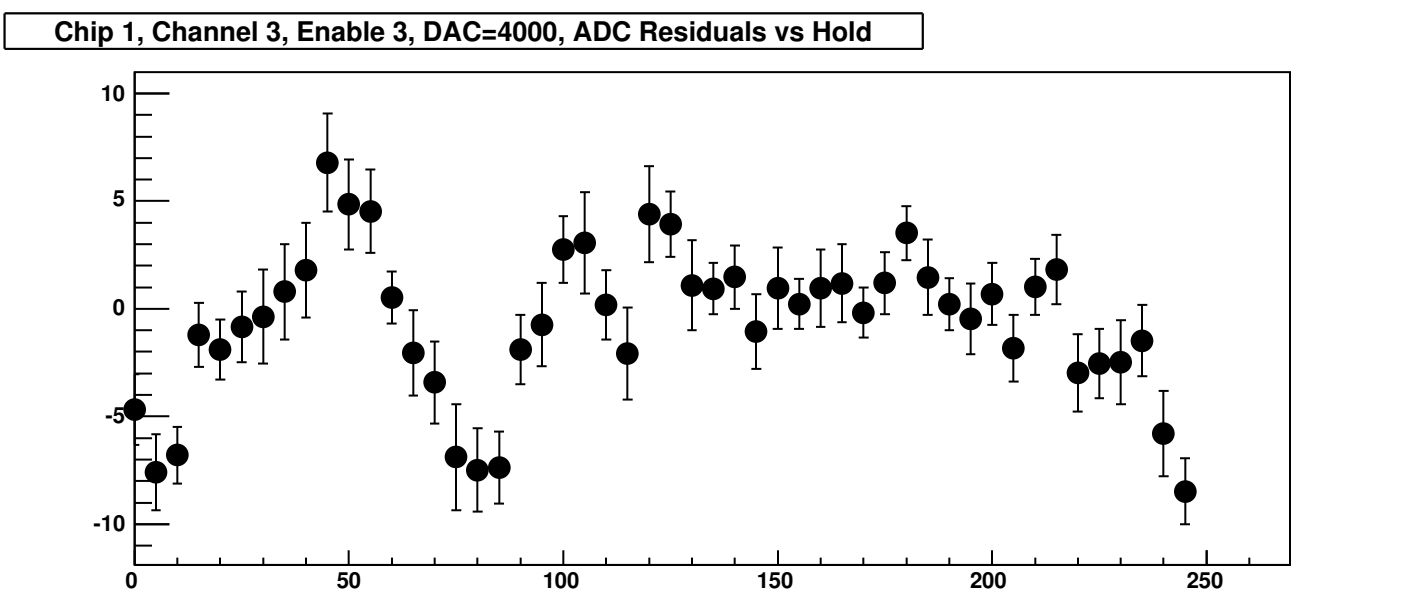

Chip 1, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 159.5 / 41
p0 -482.8 ± 0.6448
p1 98.19 ± 0.7353
p2 -8.299e+07 ± 1.086e+06
p3 5.954e+07 ± 2.252e+05
p4 1.383 ± 0.01583
-480
-500
-520
-540
-560
-580
-600
-620
0
50
100
150
200
250


Chip 1, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

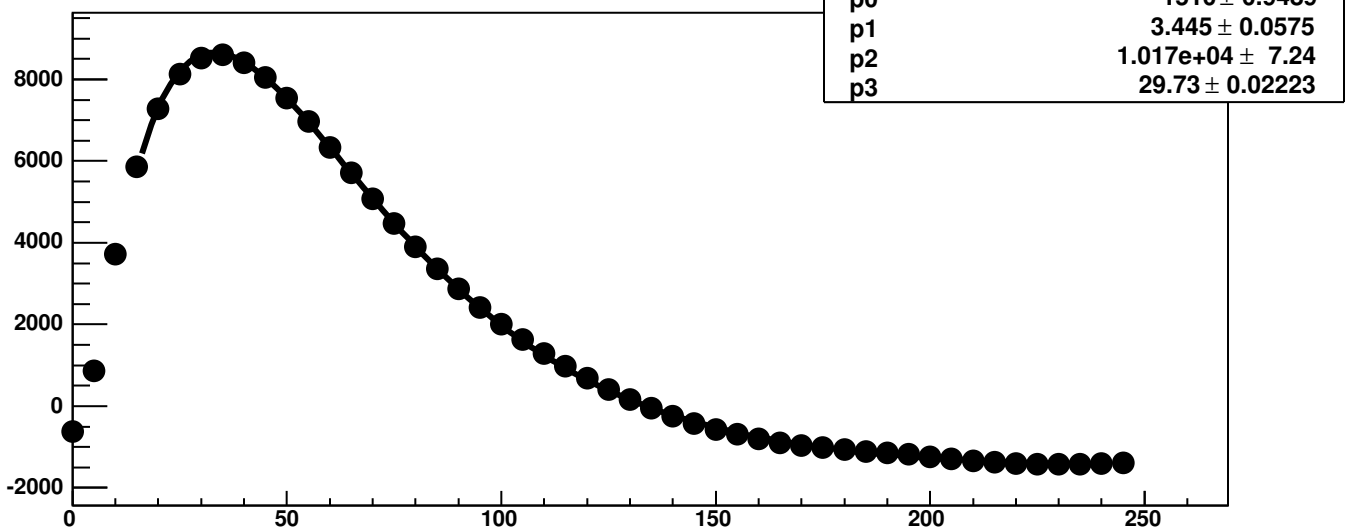

Chip 1, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
-12
0
50
100
150
200
250


Chip 1, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 120.5 / 41
p0 -451.6 ± 0.5541
p1 77.6 ± 0.5395
p2 -7.115e+07 ± 9.763e+05
p3 3.217e+07 ± 2.524e+05
p4 2 ± 0.02559
-440
-460
-480
-500
-520
-540
-560
-580
-600
-620
0
50
100
150
200
250

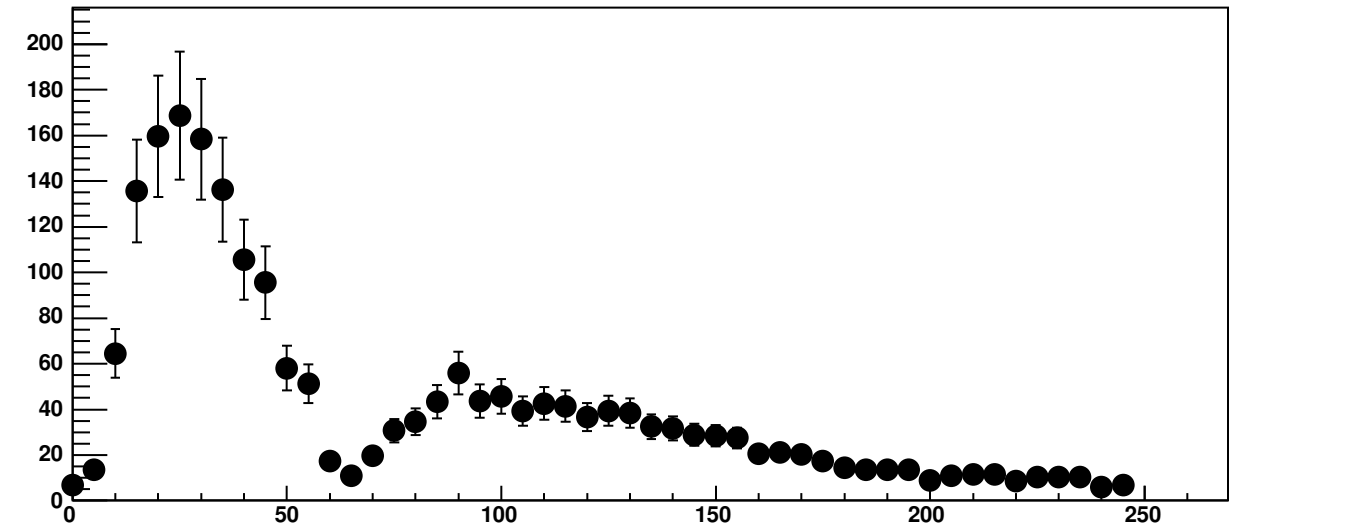

Chip 1, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 1, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250

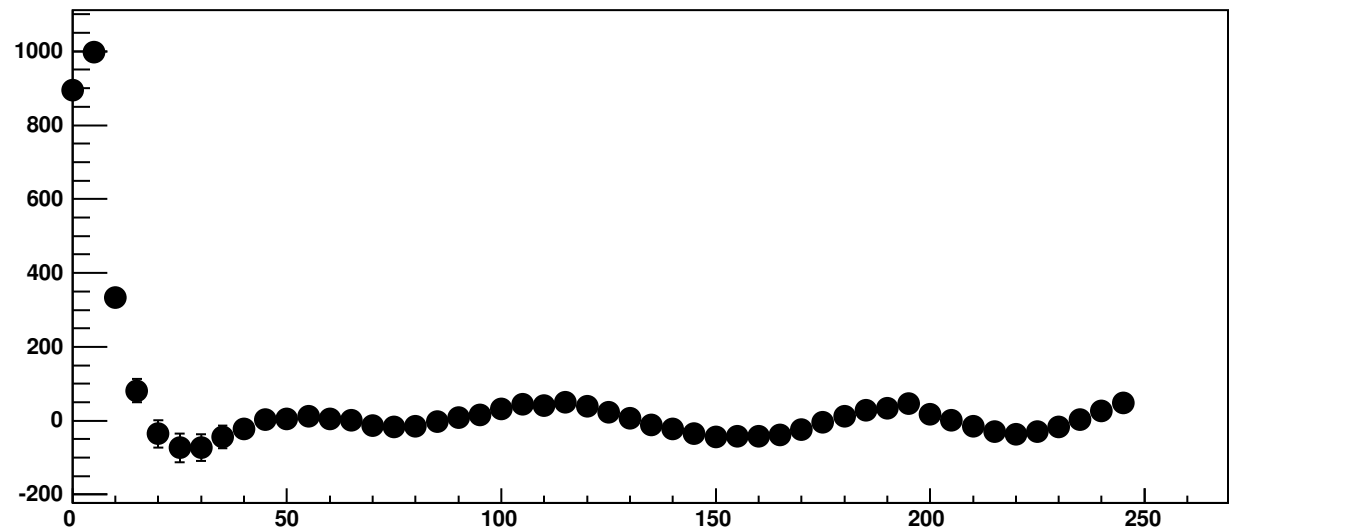
Chip 1, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold



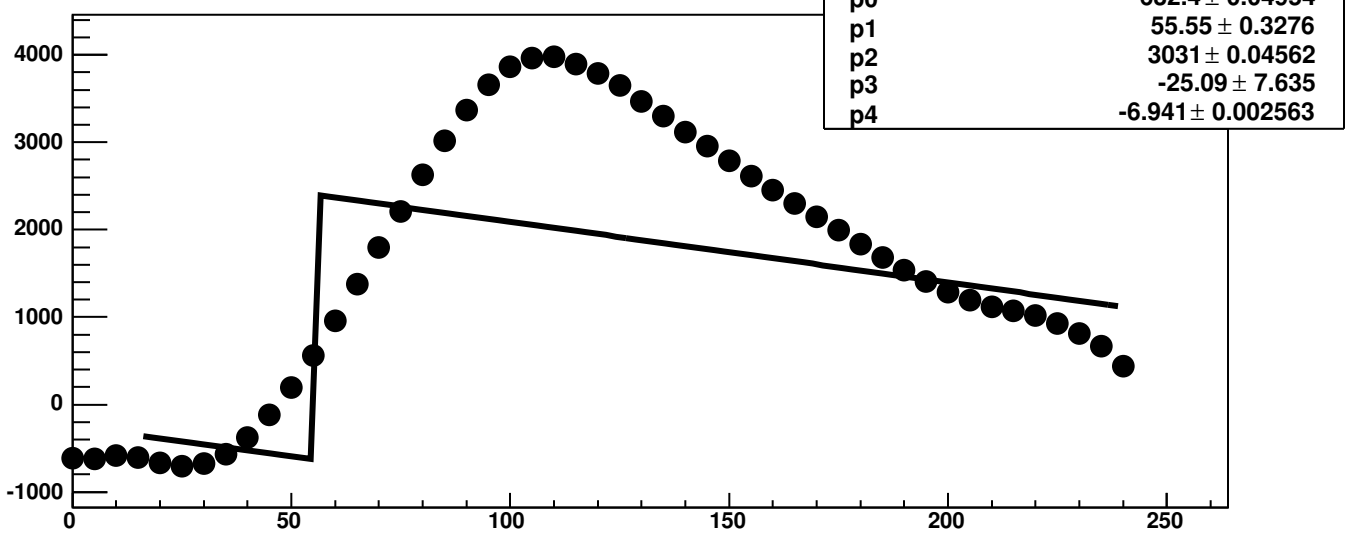
Chip 1, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold



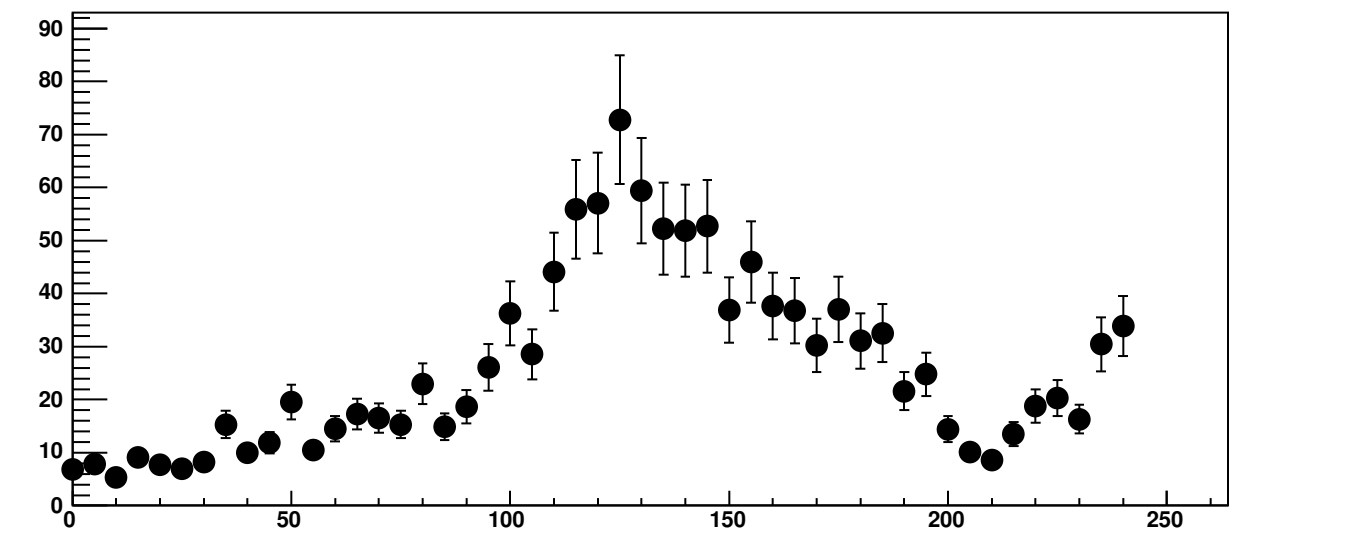
Chip 1, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold



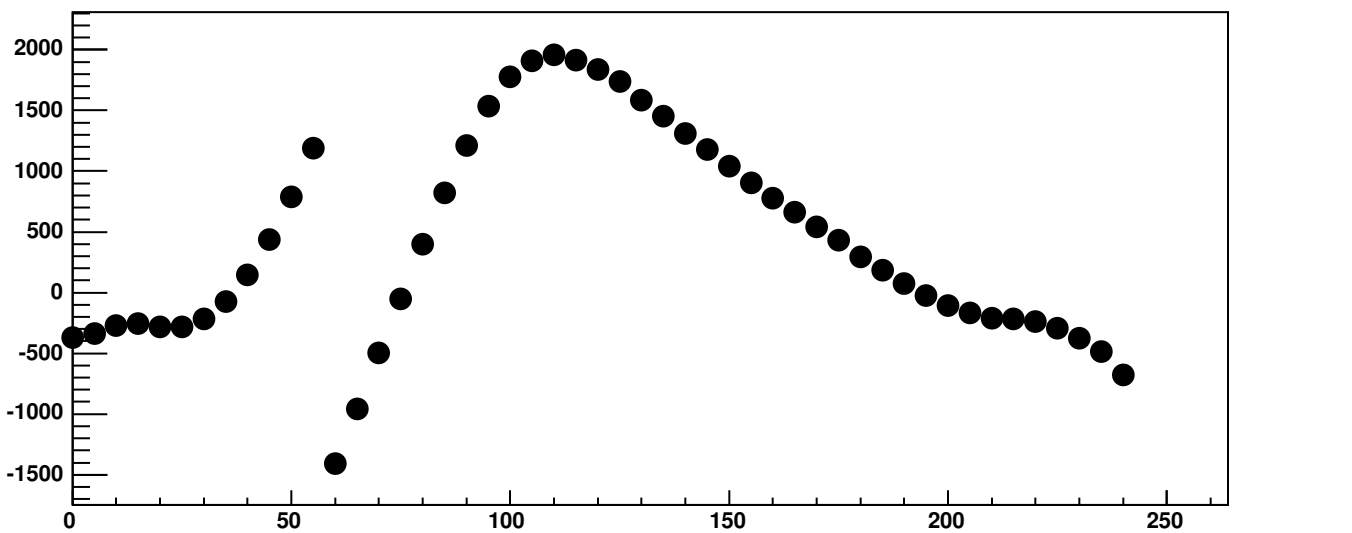
Chip 1, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold



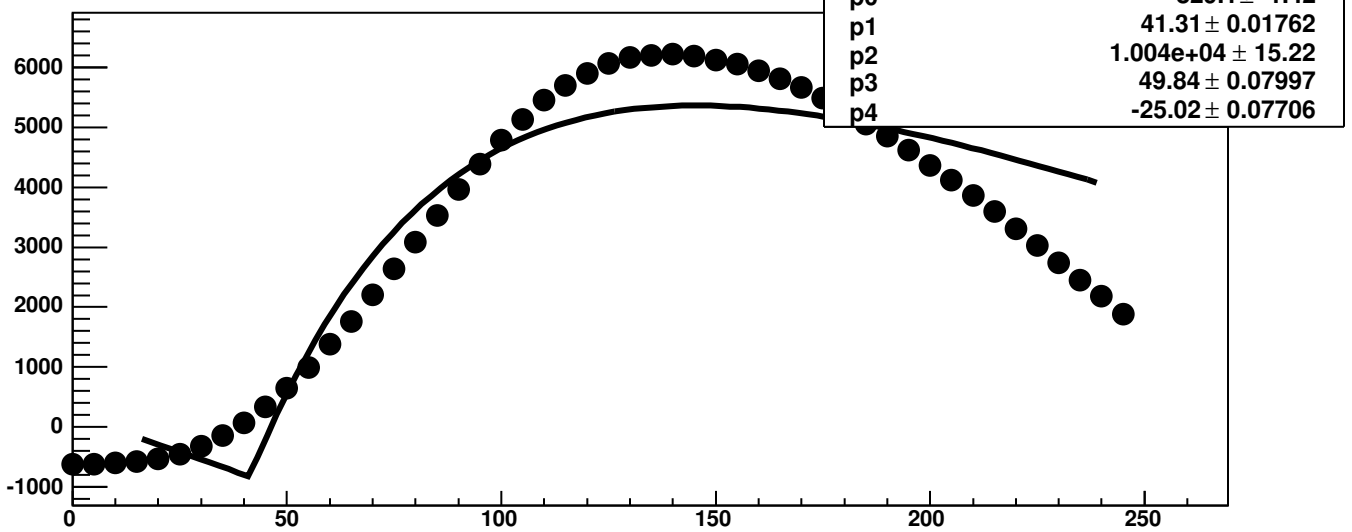
Chip 1, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



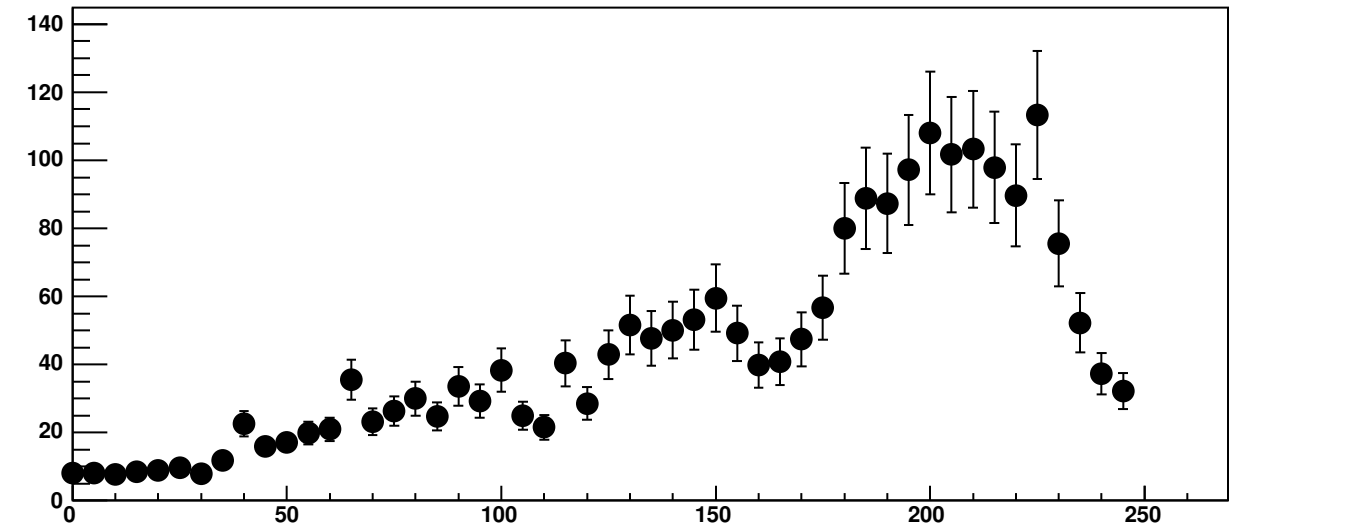
Chip 1, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold



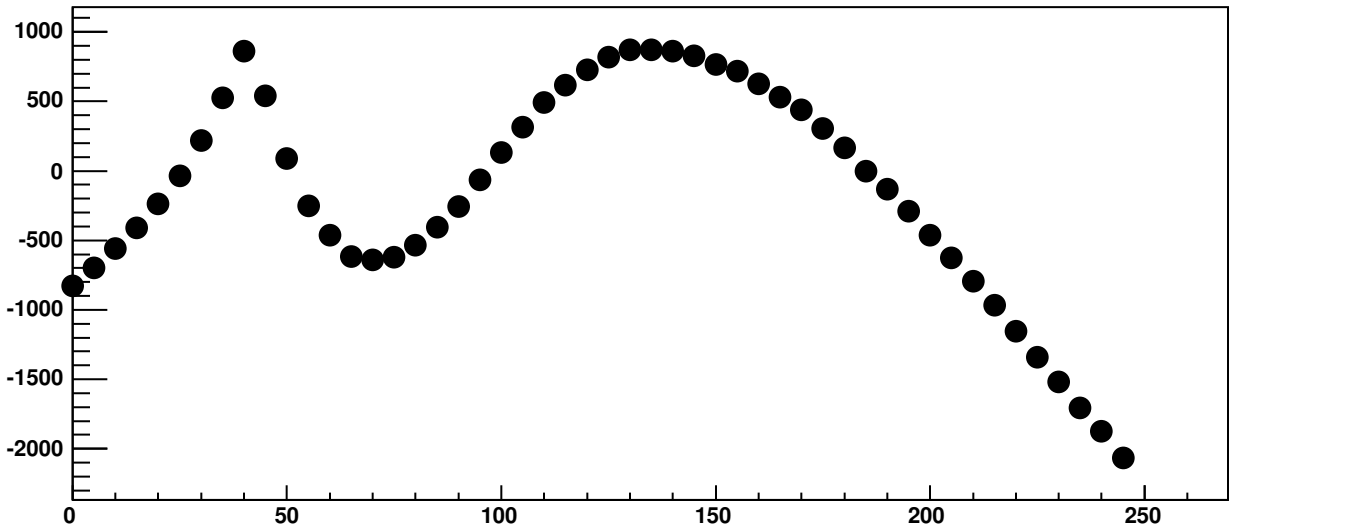
Chip 1, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

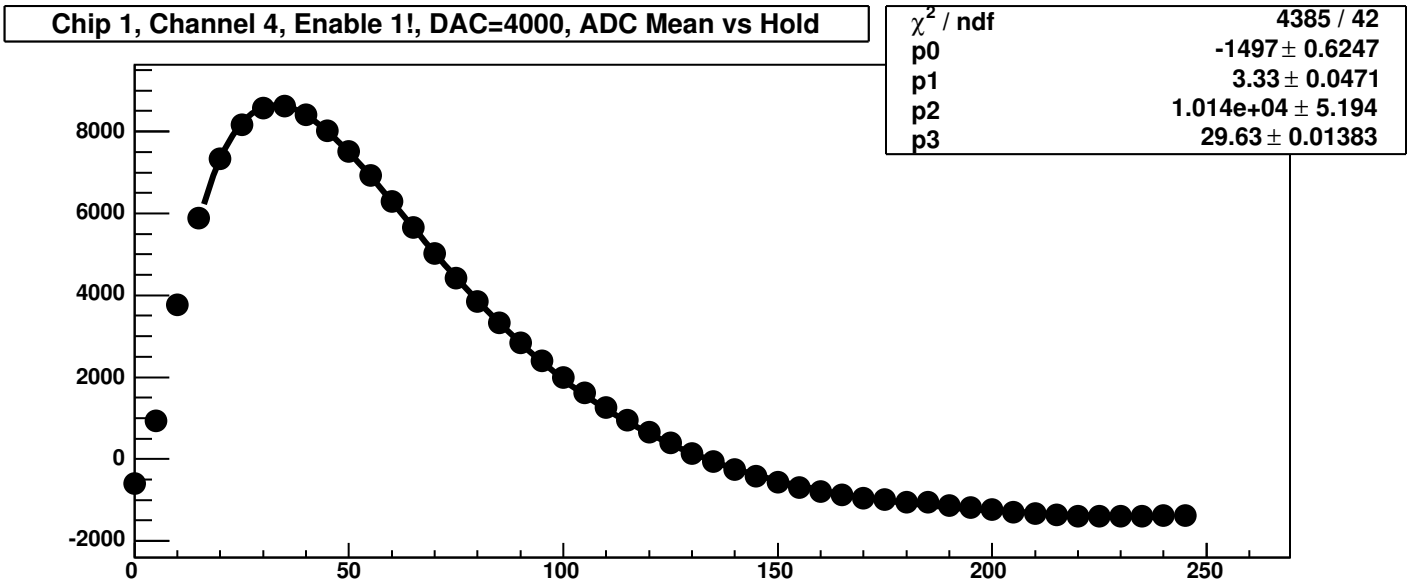


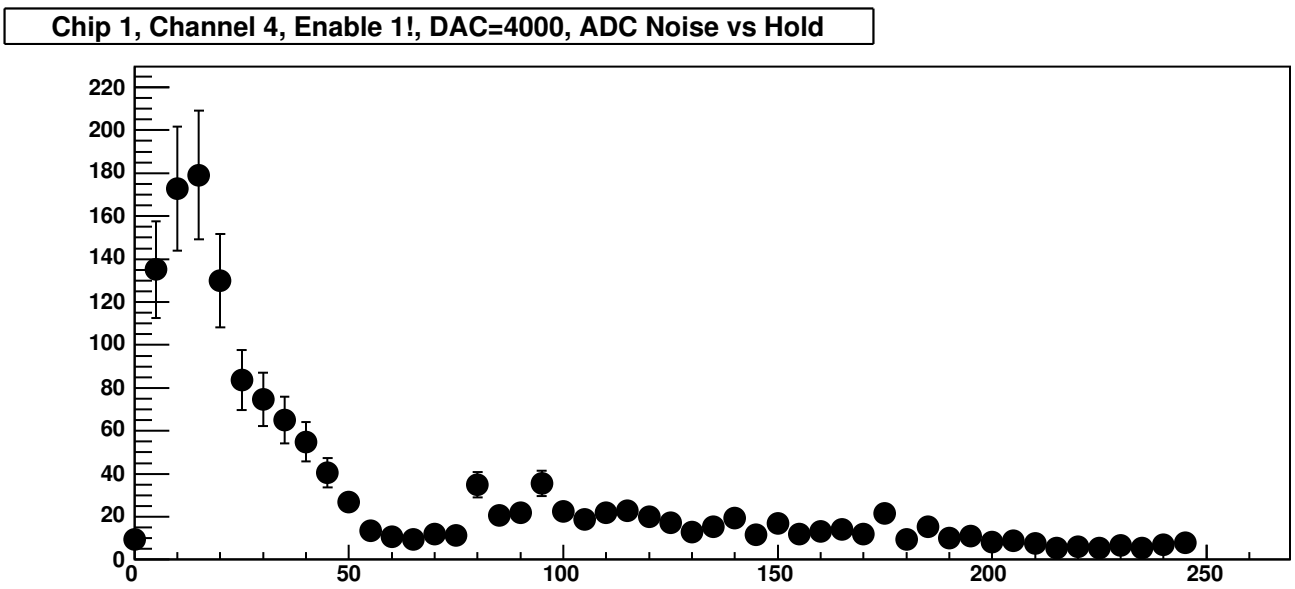
Chip 1, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold

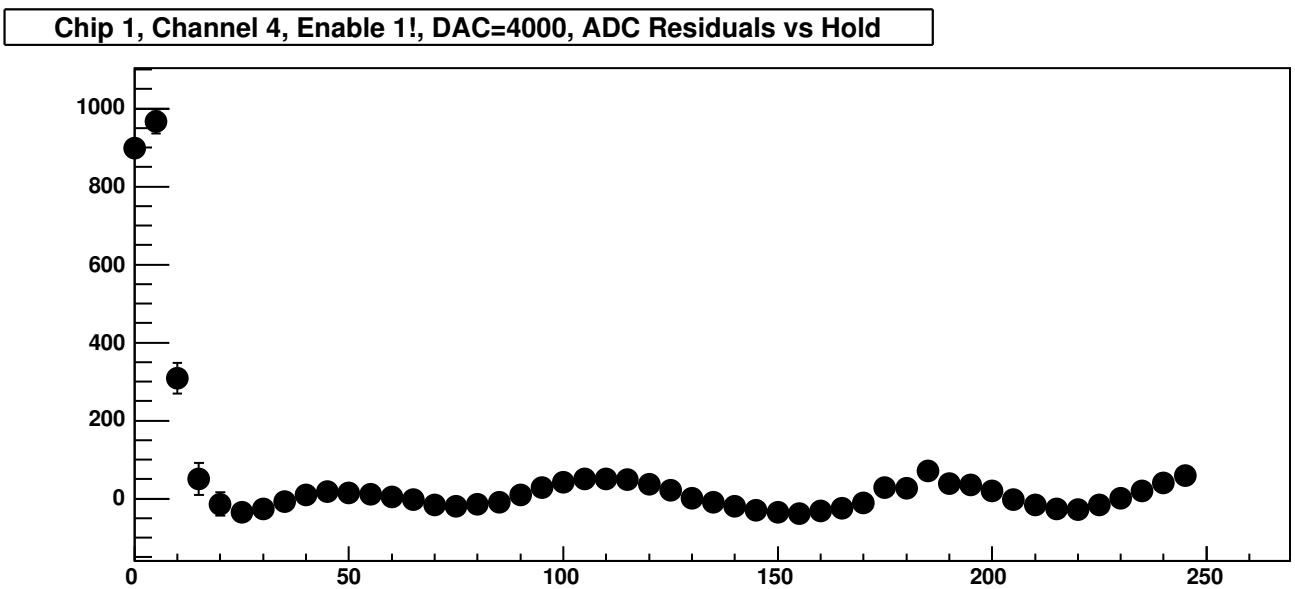


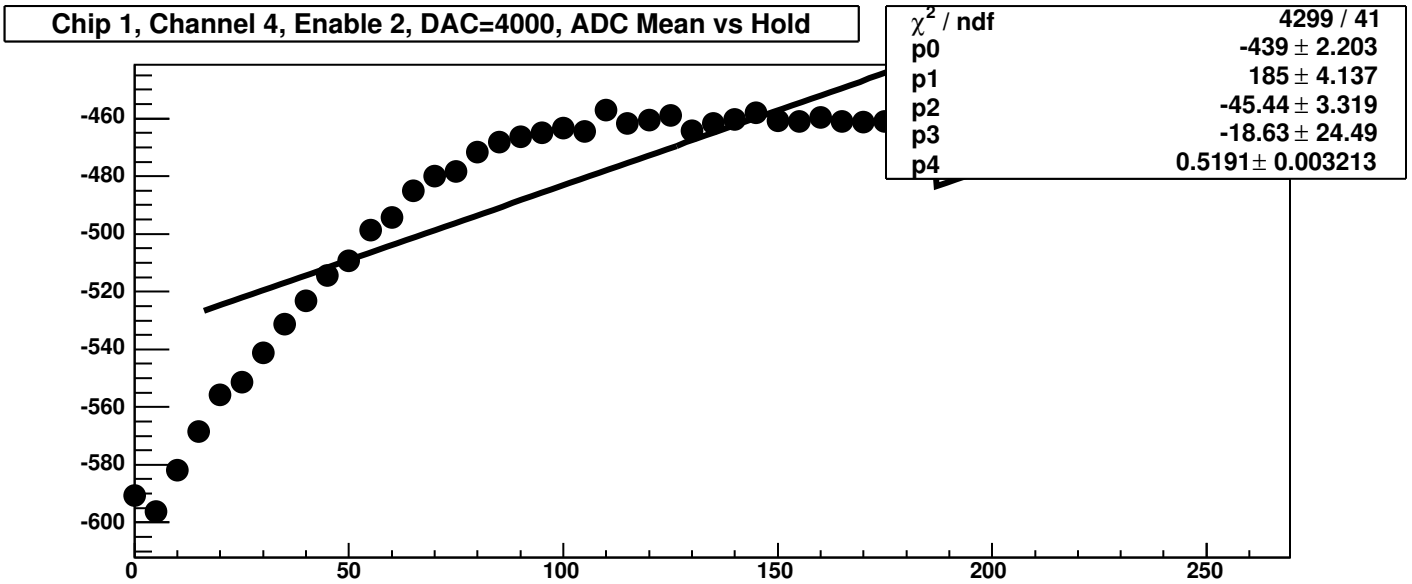
Chip 1, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

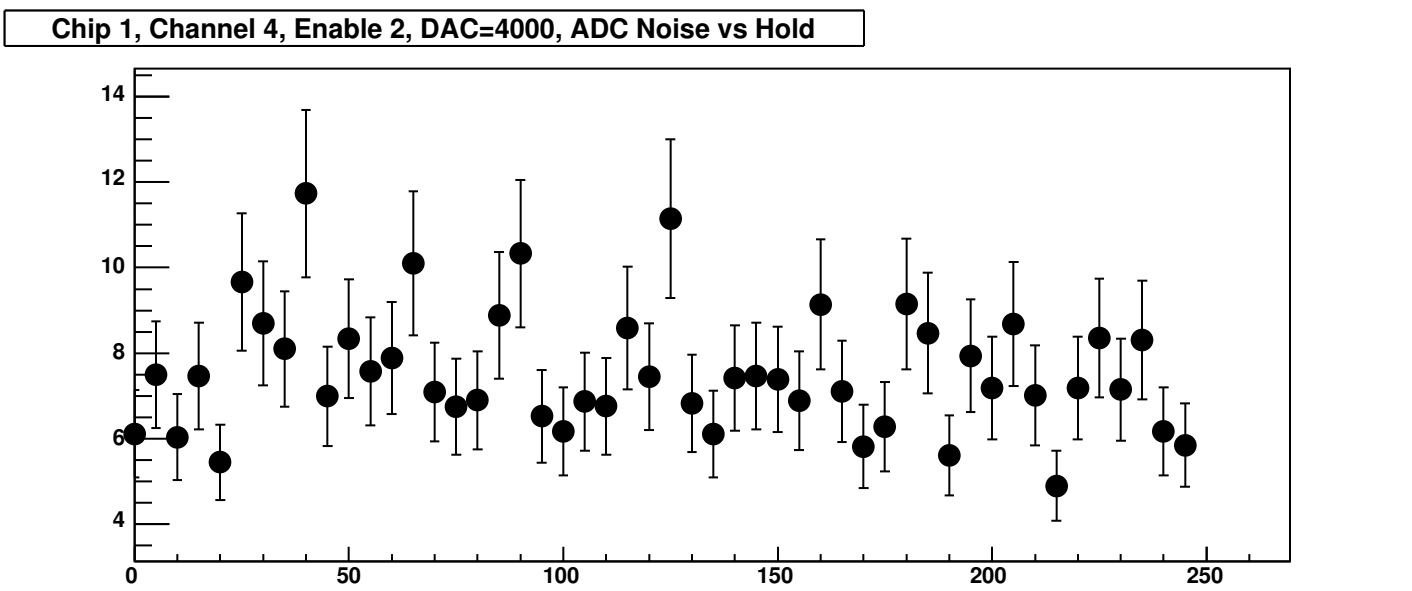


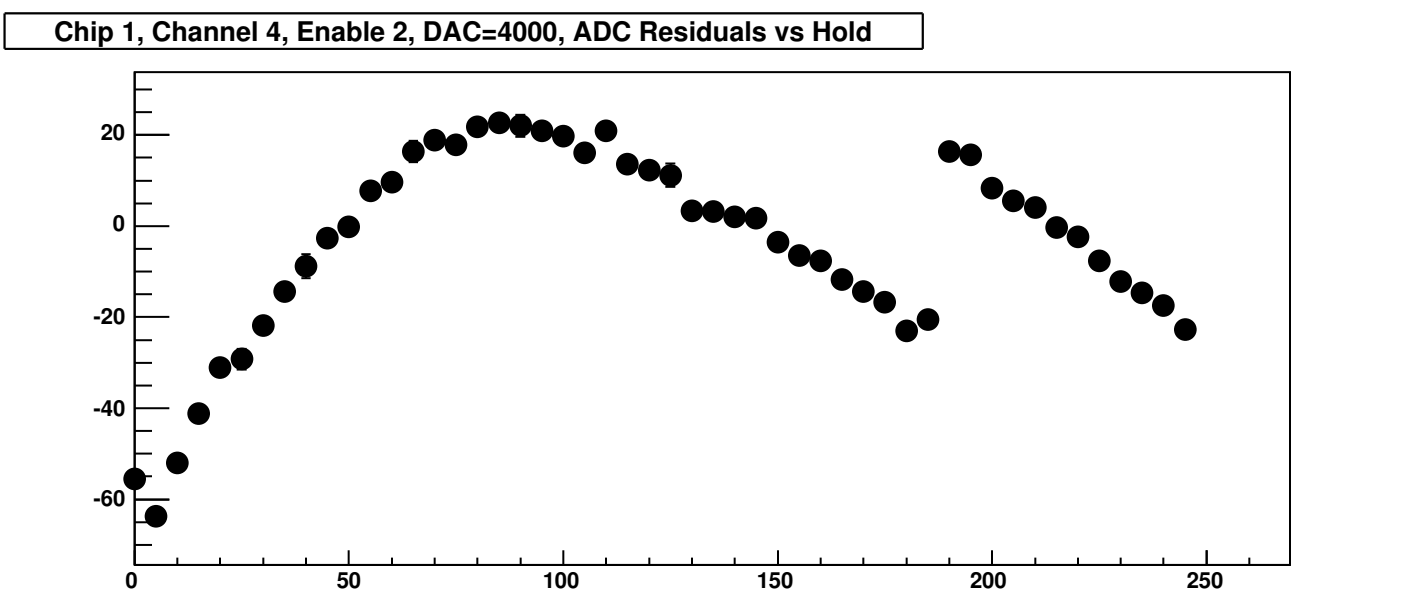

Chip 1, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 4385 / 42
p0 -1497 ± 0.6247
p1 3.33 ± 0.0471
p2 1.014e+04 ± 5.194
p3 29.63 ± 0.01383
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250


Chip 1, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

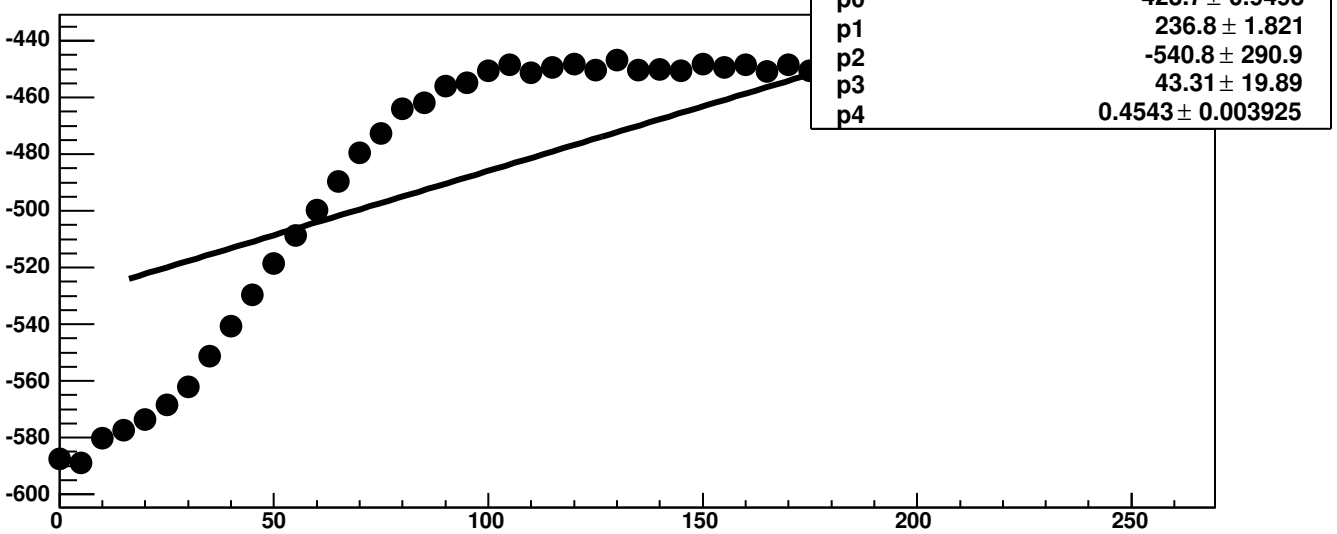

Chip 1, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 1, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 4299 / 41
p0 -439 ± 2.203
p1 185 ± 4.137
p2 -45.44 ± 3.319
p3 -18.63 ± 24.49
p4 0.5191 ± 0.003213
-460
-480
-500
-520
-540
-560
-580
-600
0
50
100
150
200
250

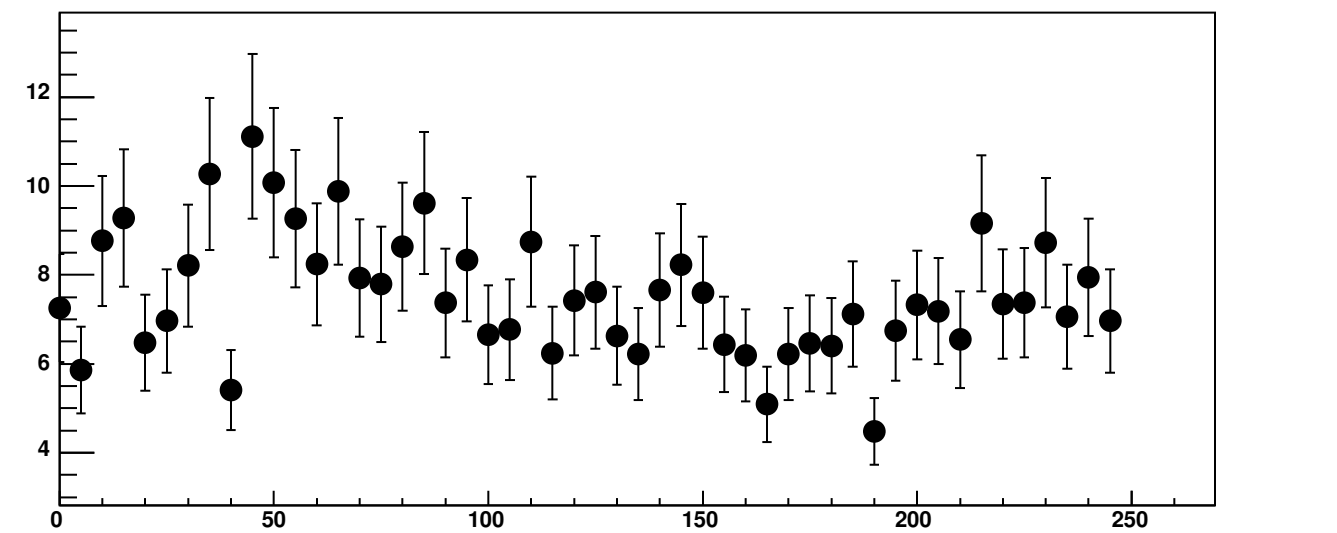

Chip 1, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 1, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
0
50
100
150
200
250

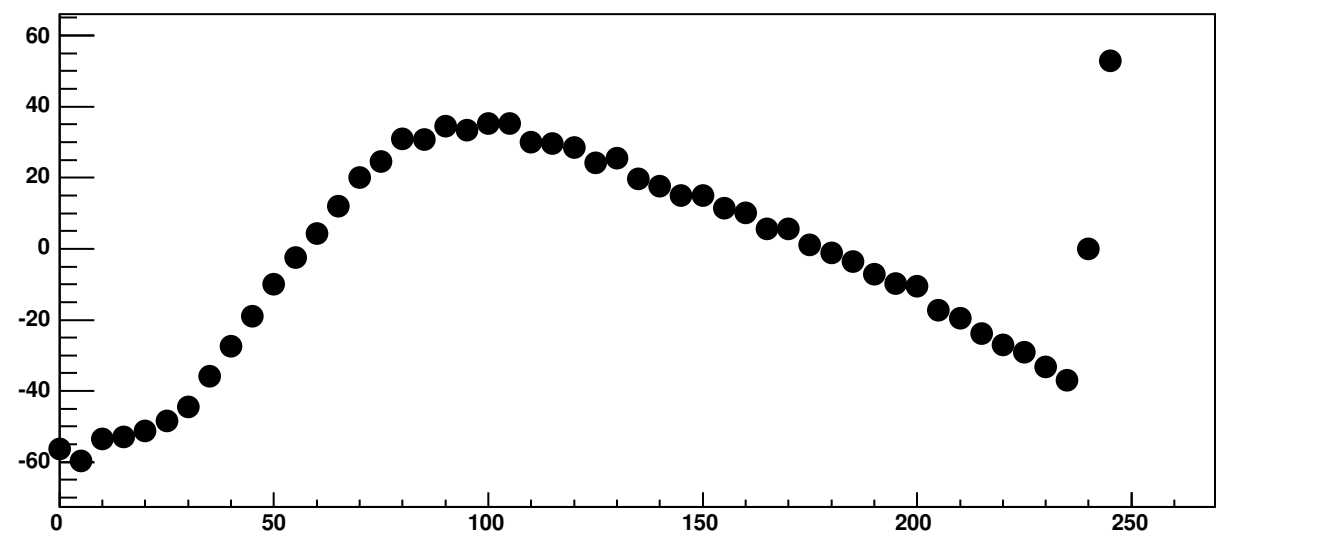
Chip 1, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold



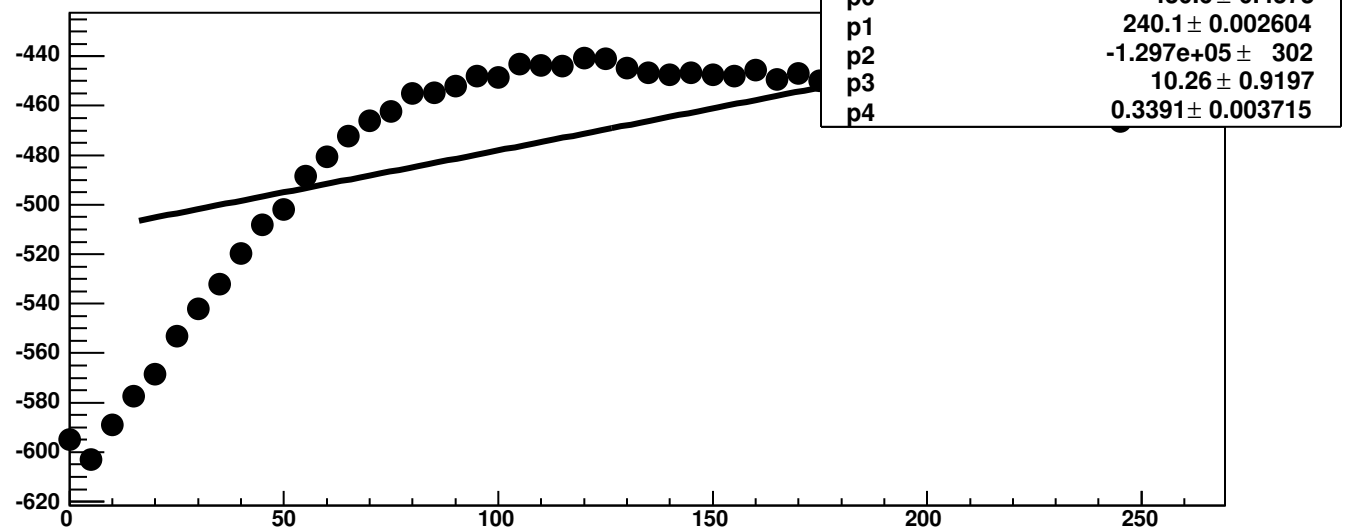
Chip 1, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



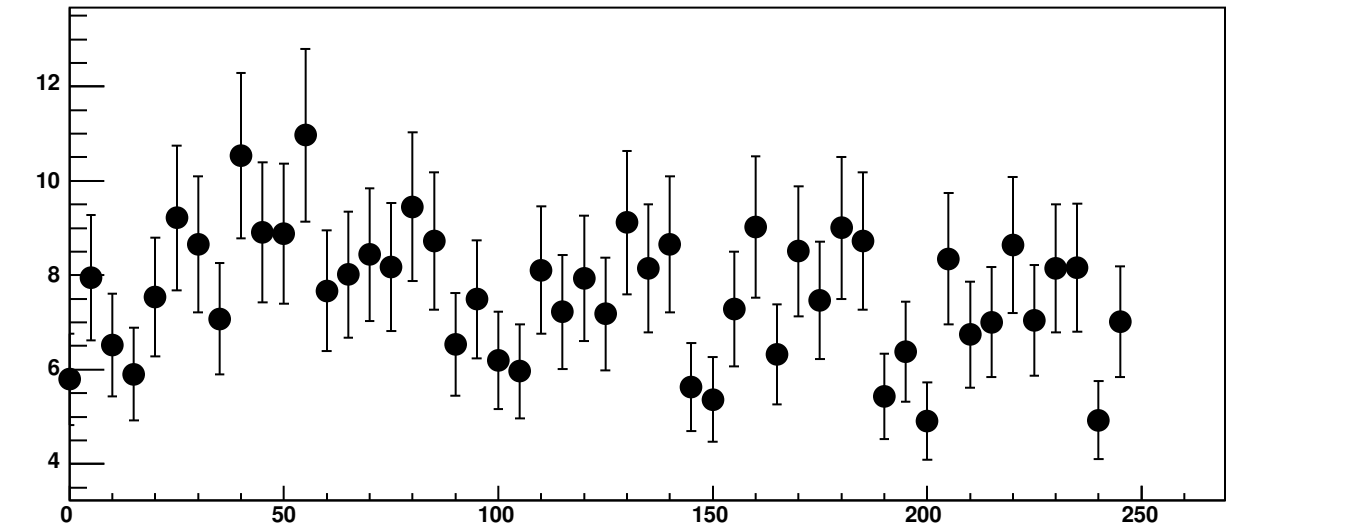
Chip 1, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold



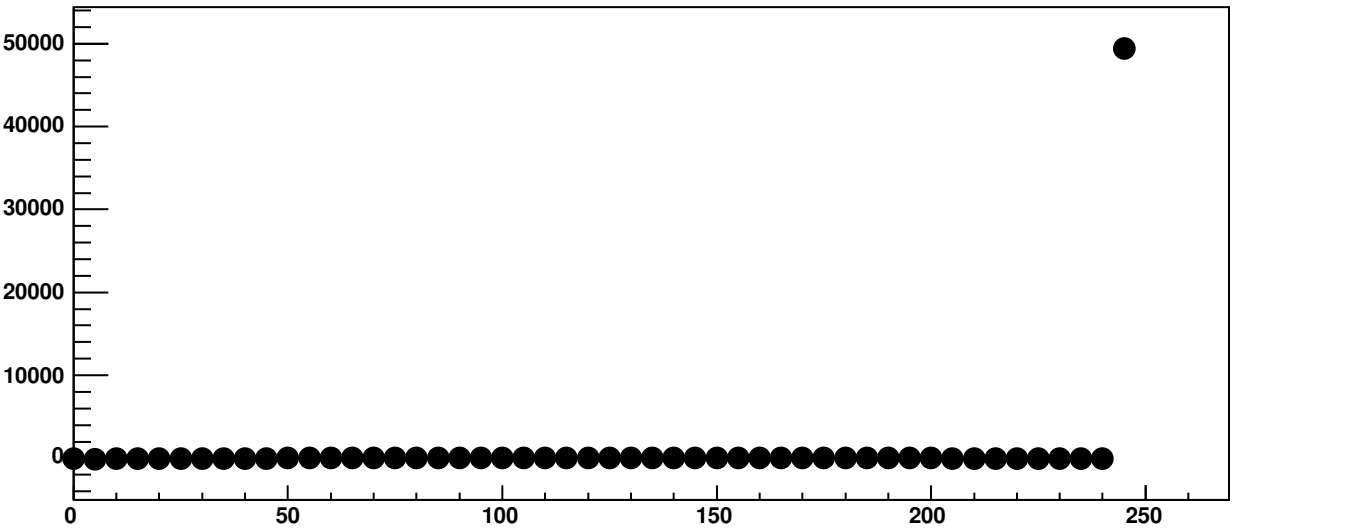
Chip 1, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold

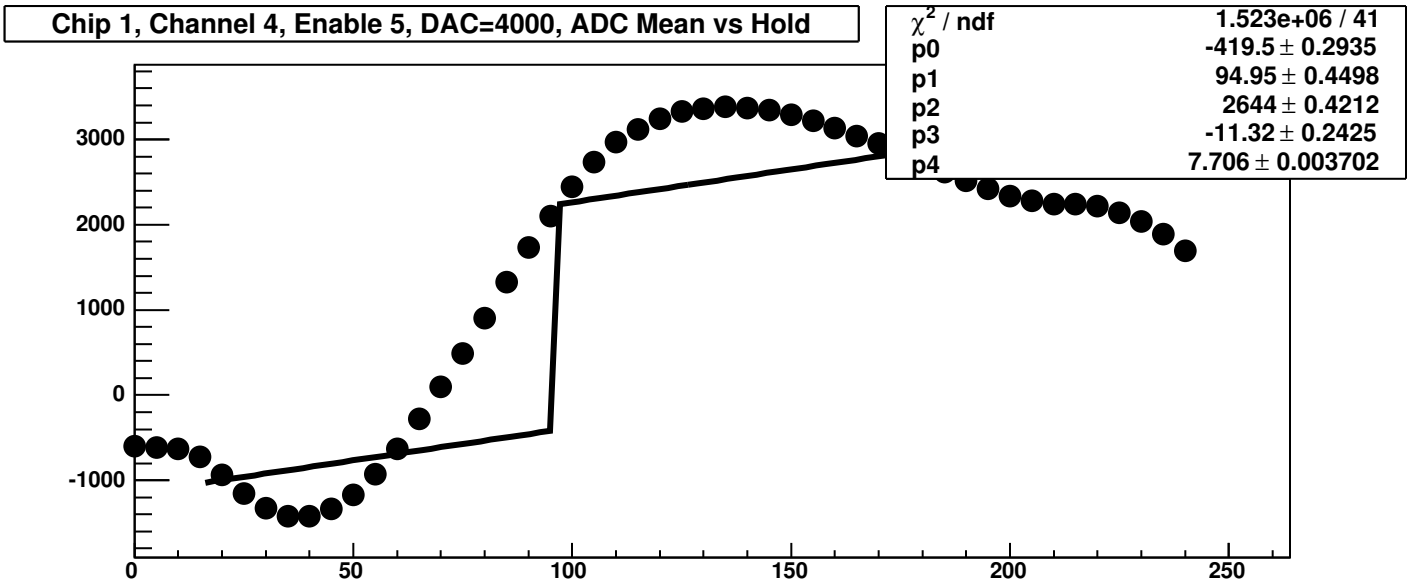


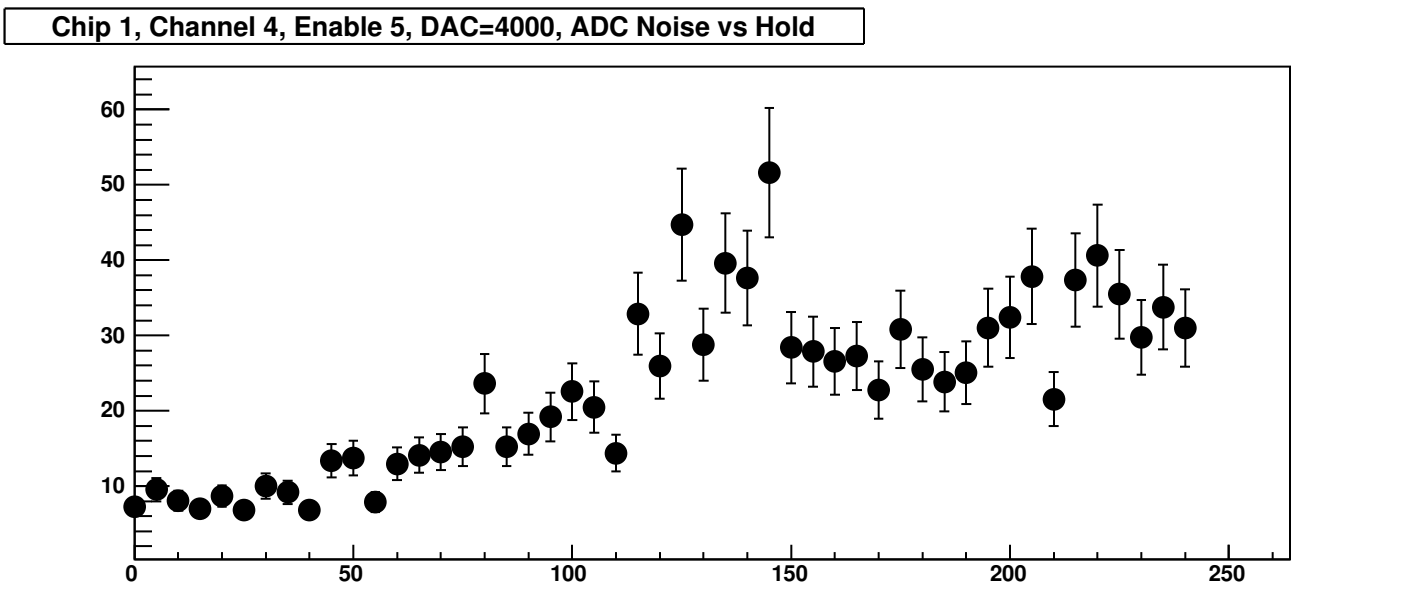
Chip 1, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold

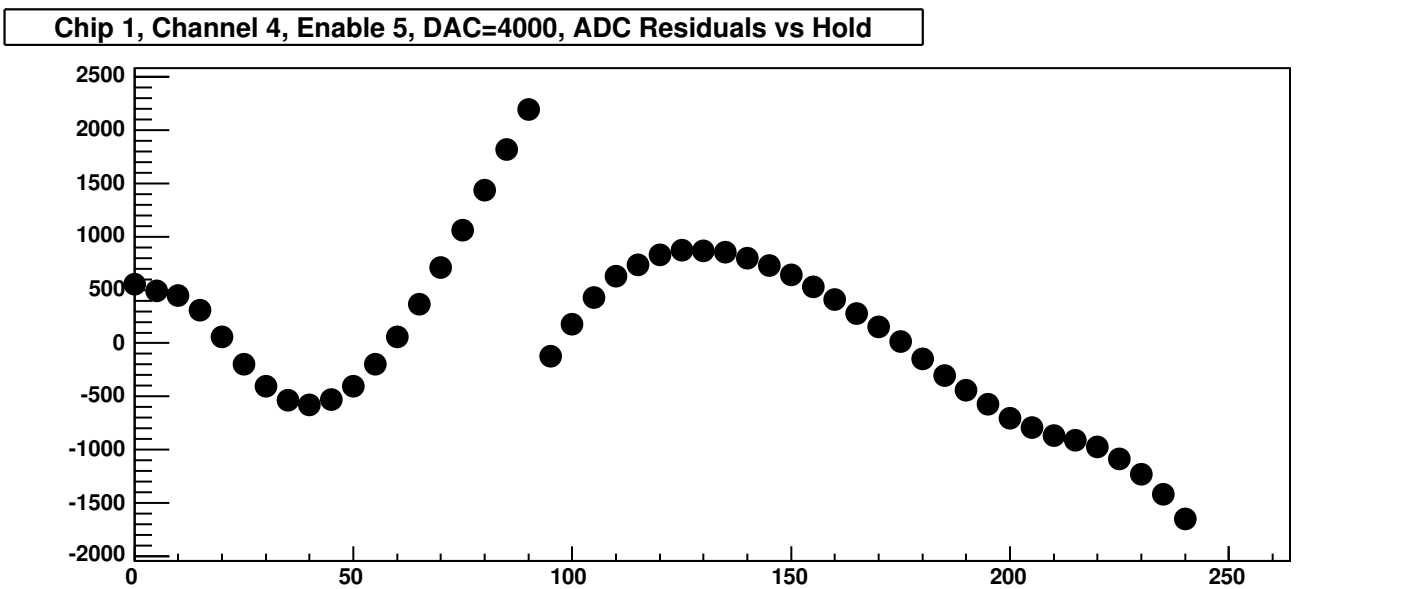


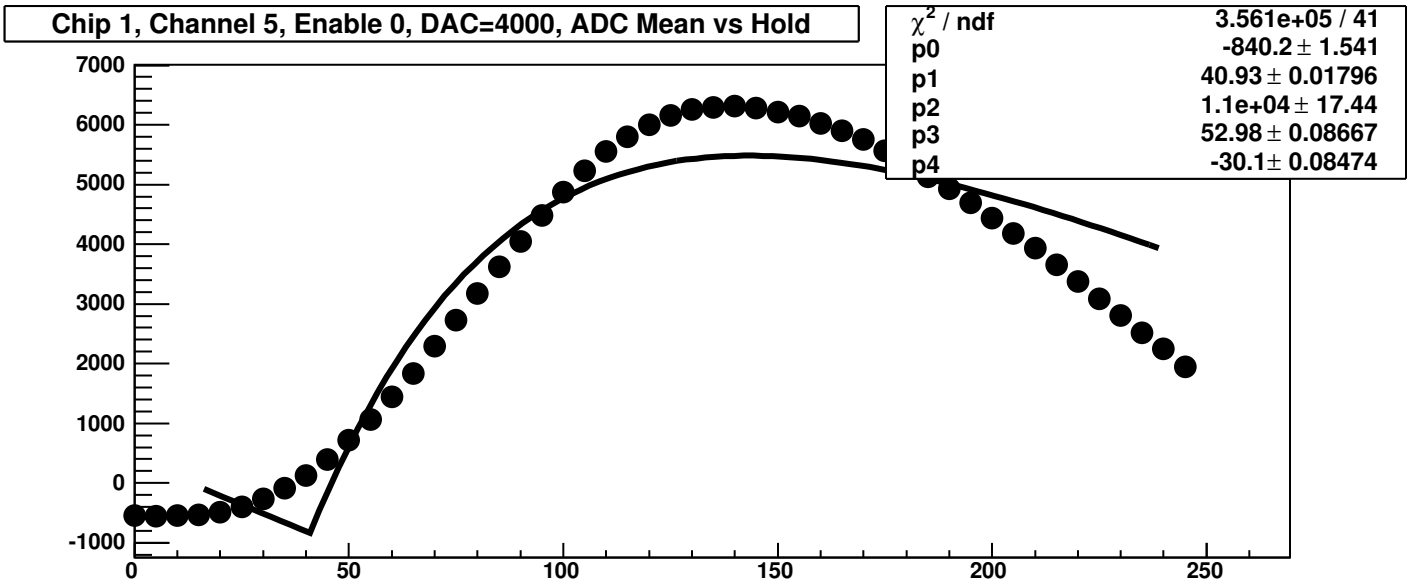
Chip 1, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold

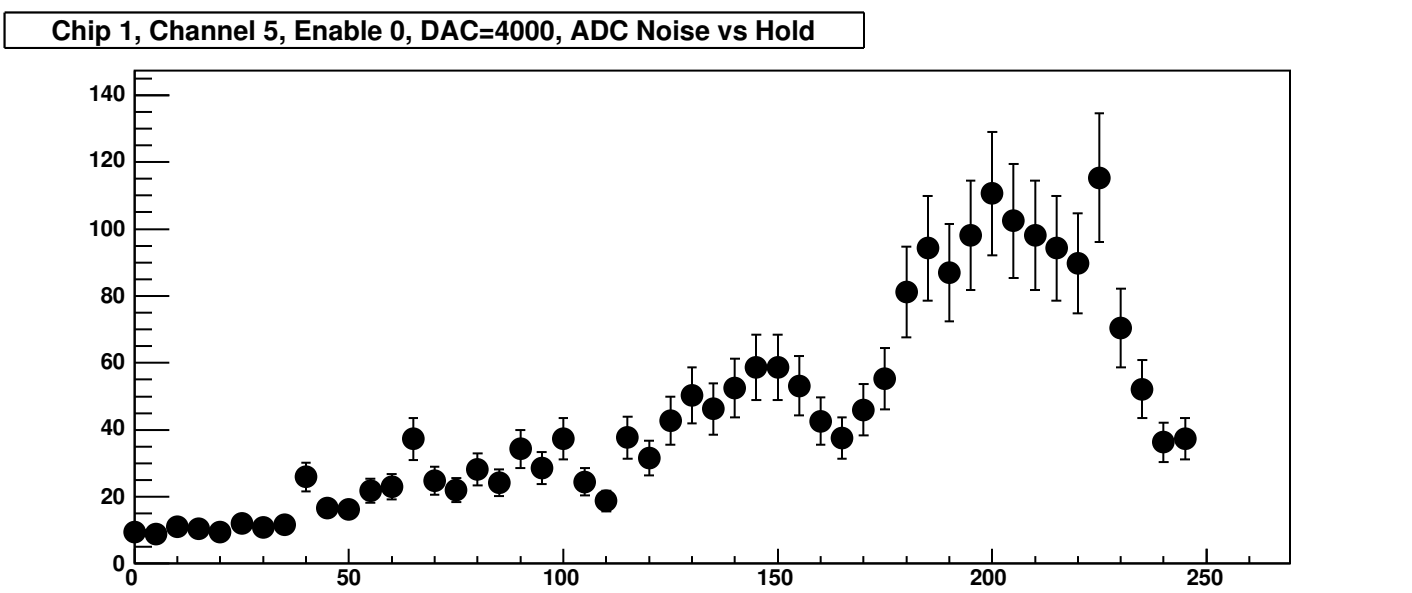


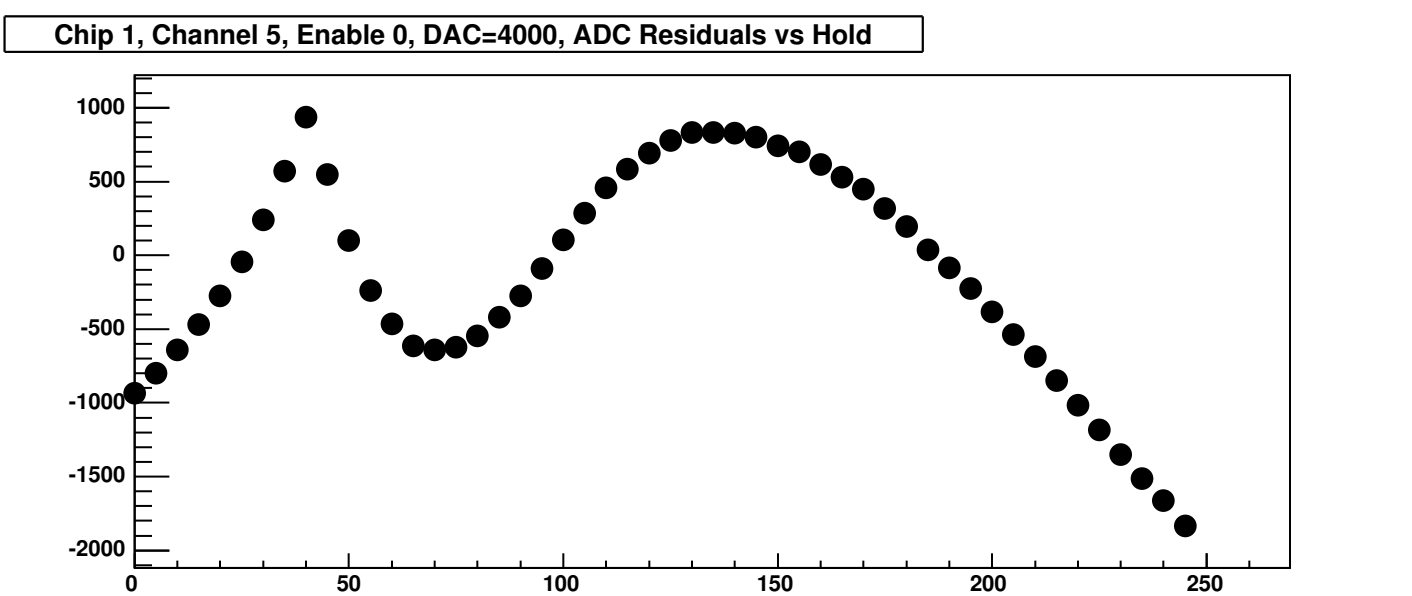

Chip 1, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.523e+06 / 41
p0 -419.5 ± 0.2935
p1 94.95 ± 0.4498
p2 2644 ± 0.4212
p3 -11.32 ± 0.2425
p4 7.706 ± 0.003702
3000
2000
1000
0
-1000
0
50
100
150
200
250

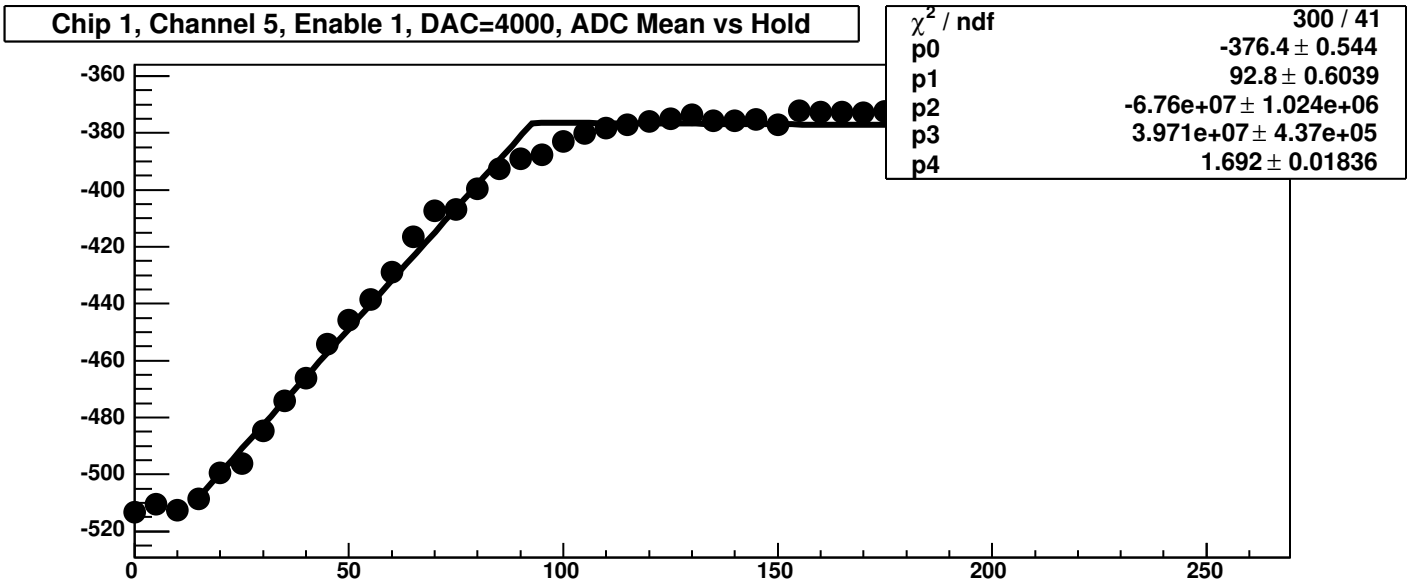

Chip 1, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold
60
50
40
30
20
10
0
50
100
150
200
250

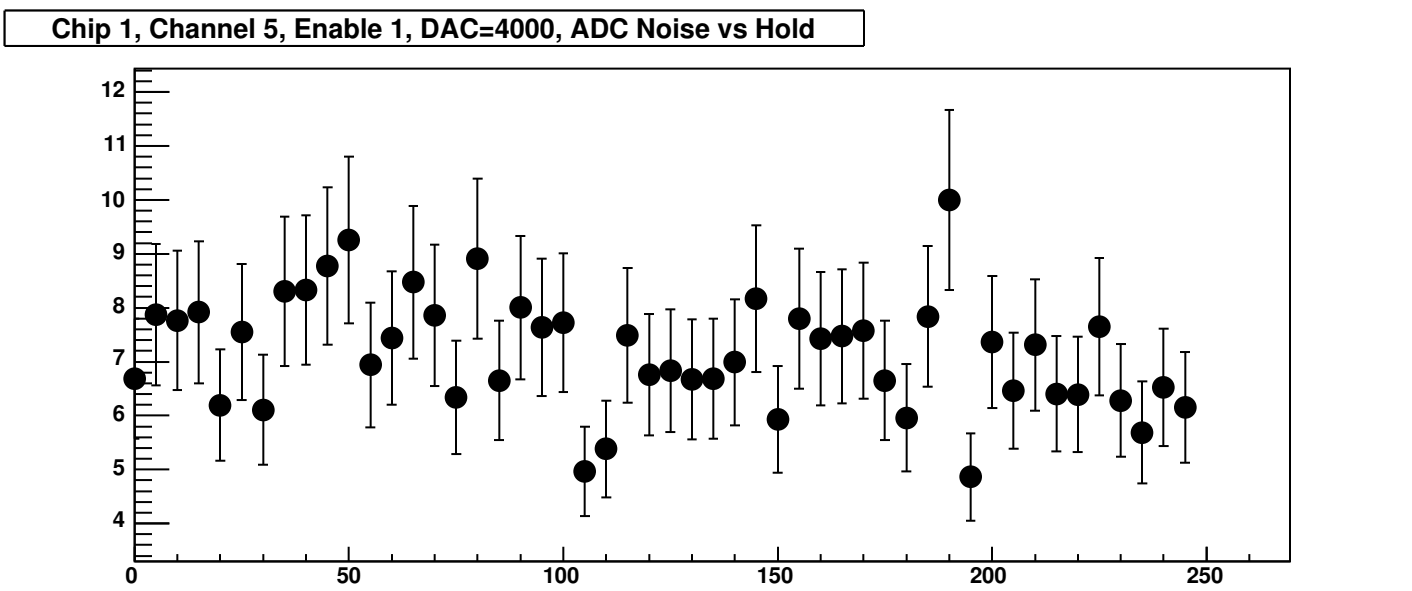

Chip 1, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold
2500
2000
1500
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

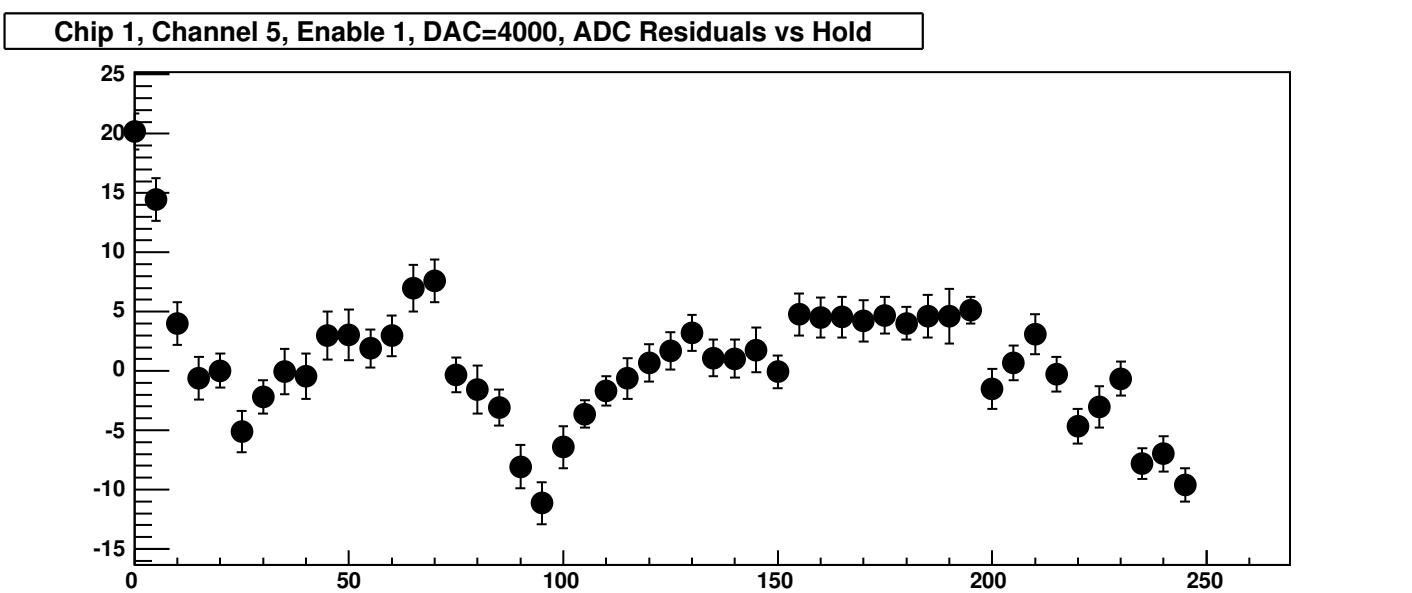

Chip 1, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.561e+05 / 41
p0 -840.2 ± 1.541
p1 40.93 ± 0.01796
p2 1.1e+04 ± 17.44
p3 52.98 ± 0.08667
p4 -30.1 ± 0.08474
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

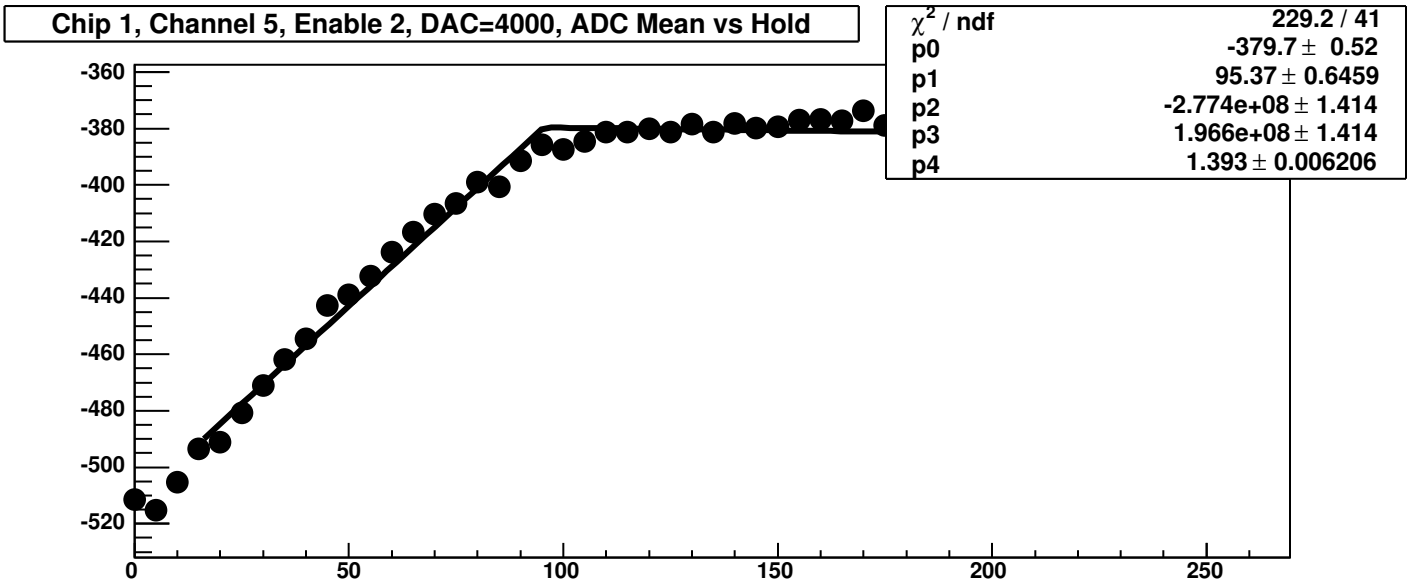

Chip 1, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250

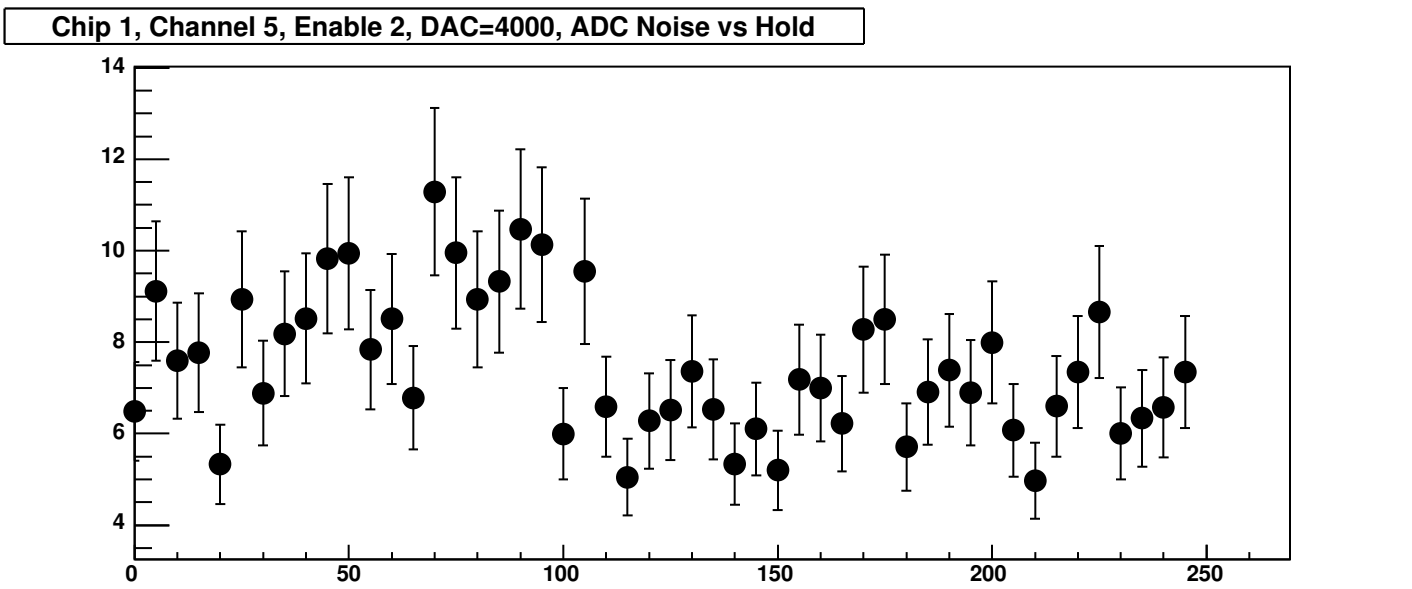

Chip 1, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

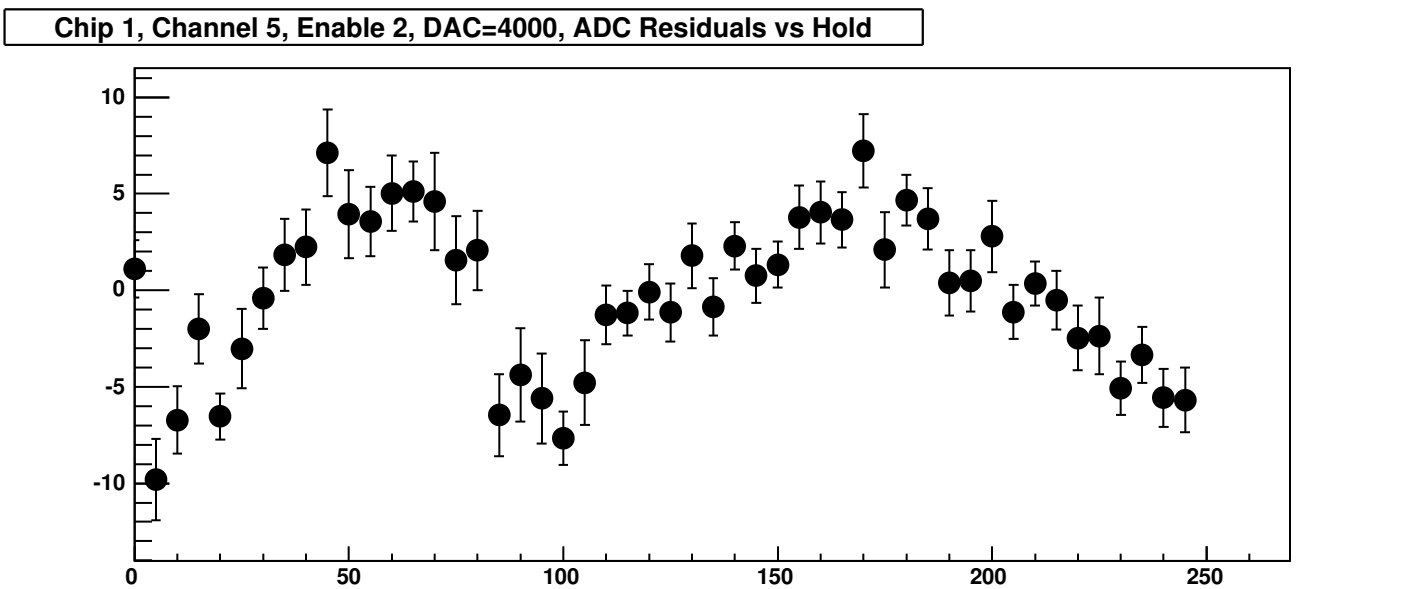

Chip 1, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 300 / 41
p0 -376.4 ± 0.544
p1 92.8 ± 0.6039
p2 -6.76e+07 ± 1.024e+06
p3 3.971e+07 ± 4.37e+05
p4 1.692 ± 0.01836
-360
-380
-400
-420
-440
-460
-480
-500
-520
0
50
100
150
200
250


Chip 1, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

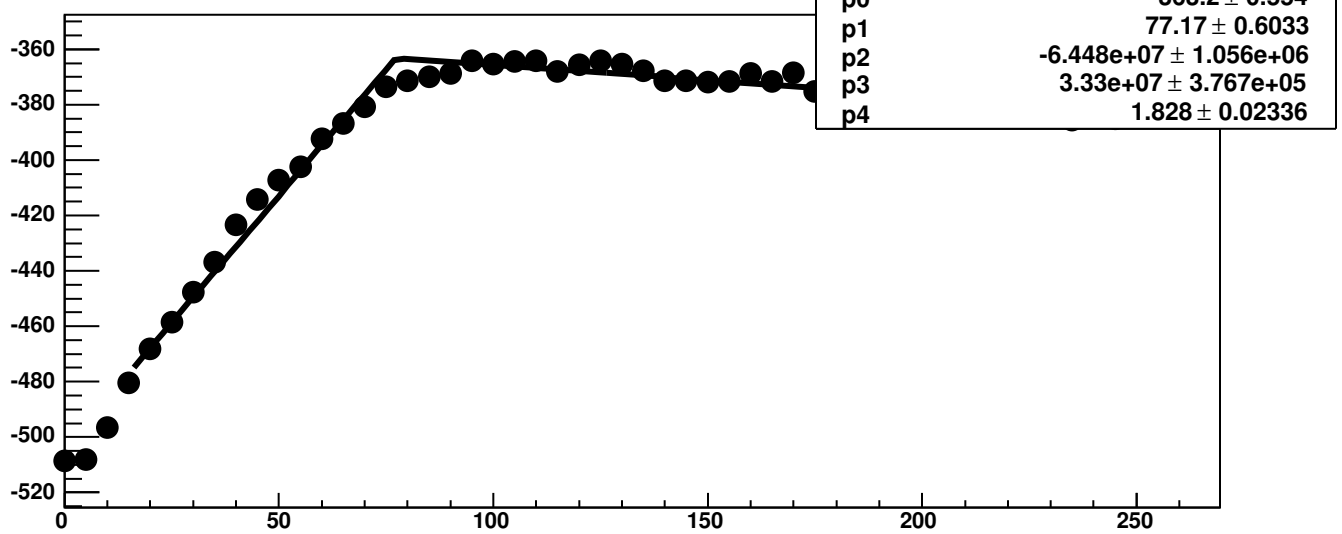

Chip 1, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250


Chip 1, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 229.2 / 41
p0 -379.7 ± 0.52
p1 95.37 ± 0.6459
p2 -2.774e+08 ± 1.414
p3 1.966e+08 ± 1.414
p4 1.393 ± 0.006206
-360
-380
-400
-420
-440
-460
-480
-500
-520
0
50
100
150
200
250

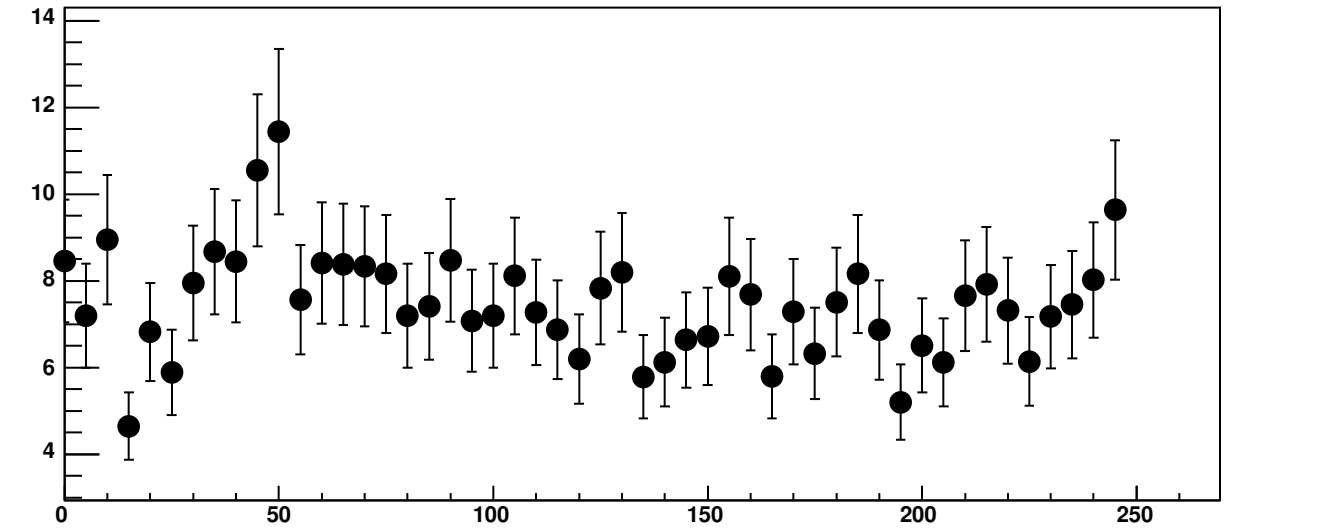

Chip 1, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 1, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250

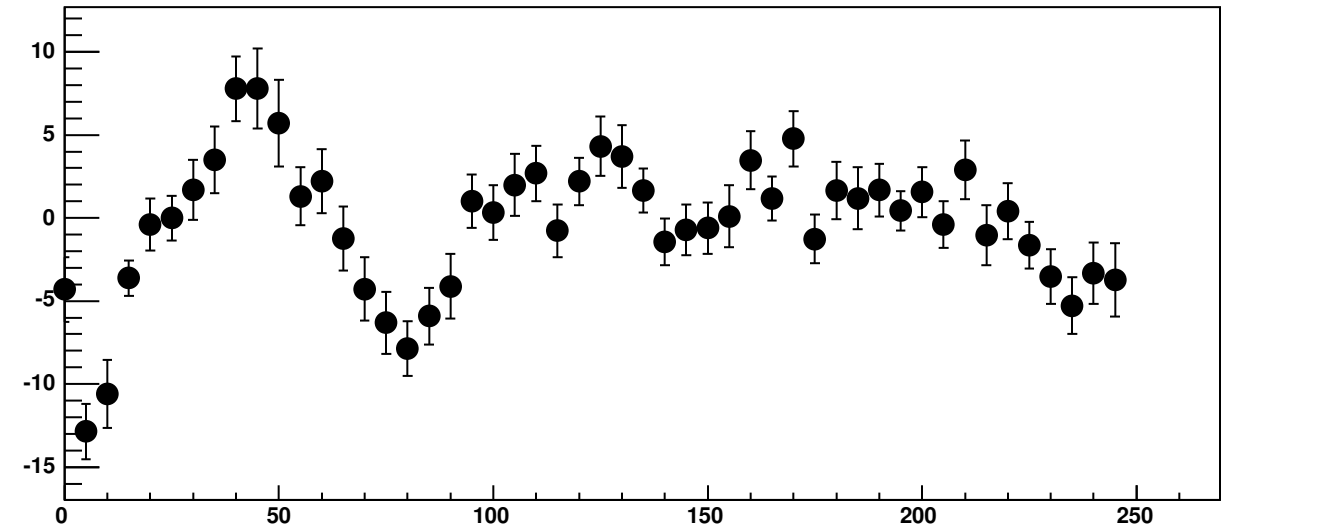
Chip 1, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold



Chip 1, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold

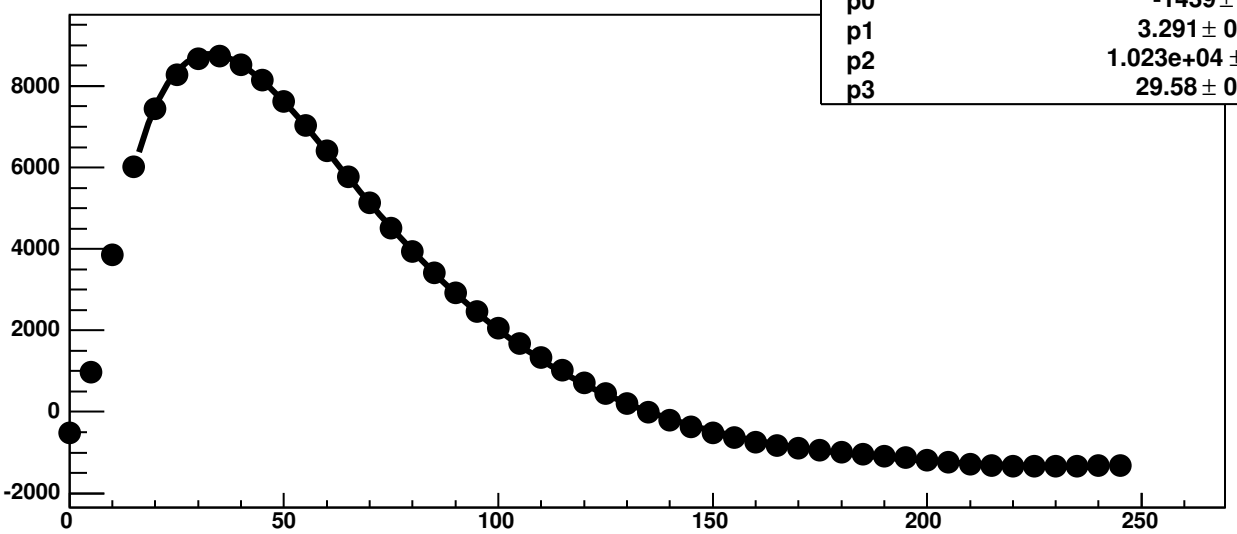


Chip 1, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

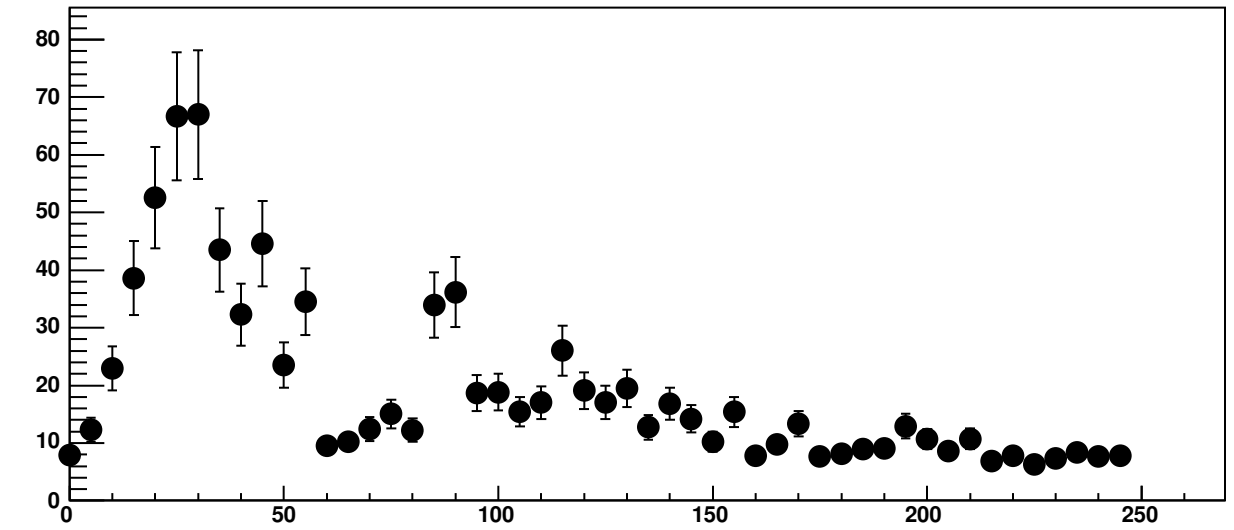


Chip 1, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold

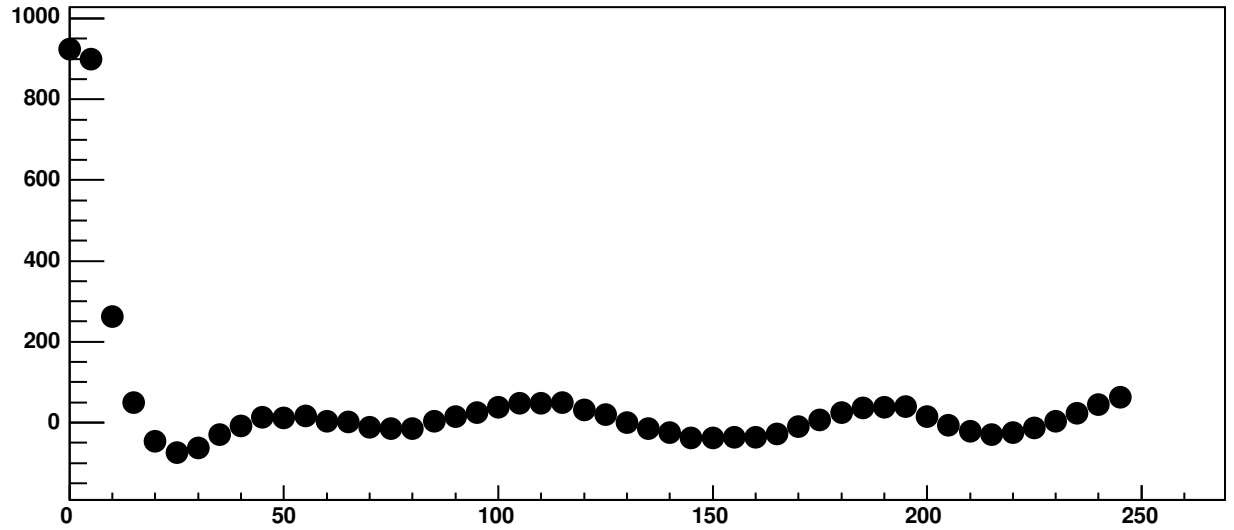
χ^2 / ndf	4431 / 42
p0	-1439 ± 0.6741
p1	3.291 ± 0.02032
p2	1.023e+04 ± 2.742
p3	29.58 ± 0.01066



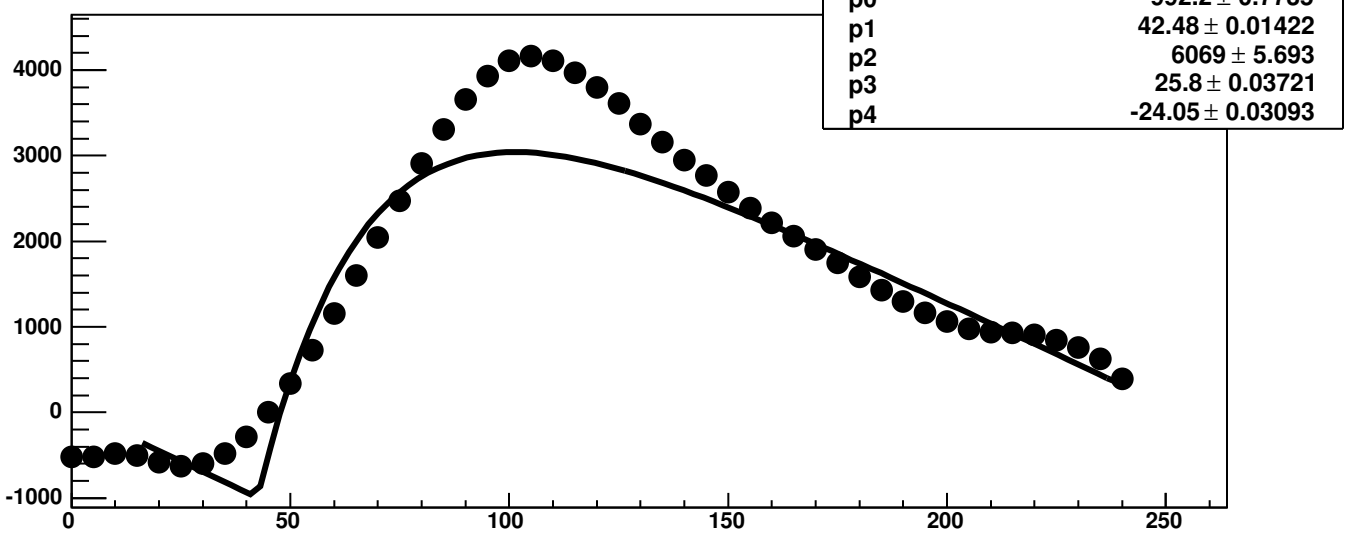
Chip 1, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold



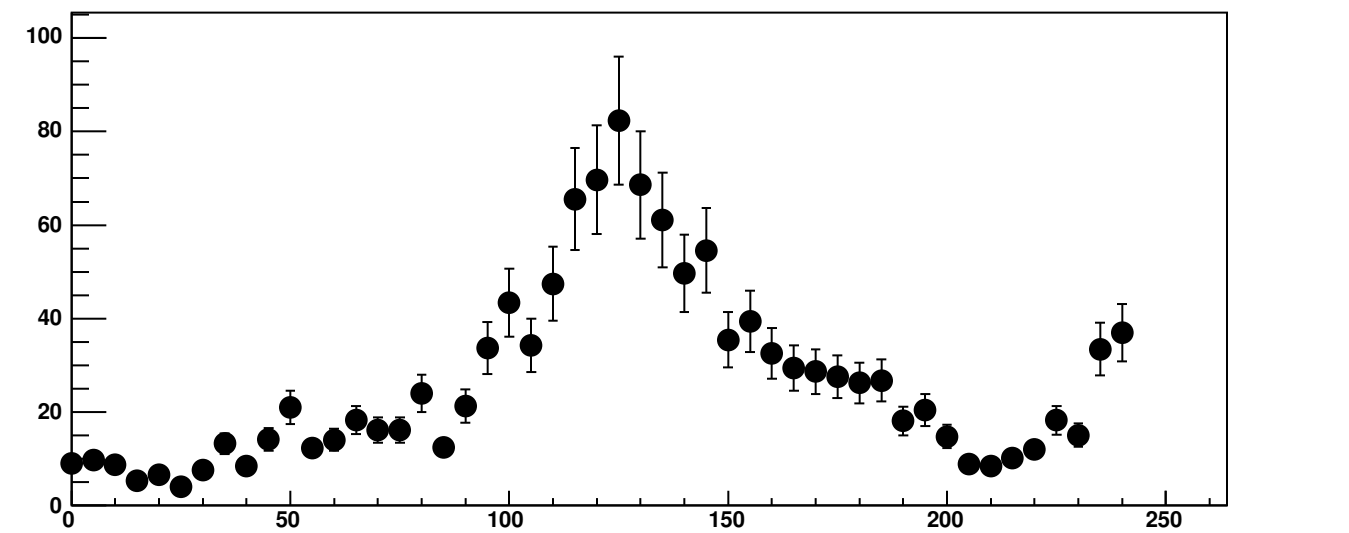
Chip 1, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold



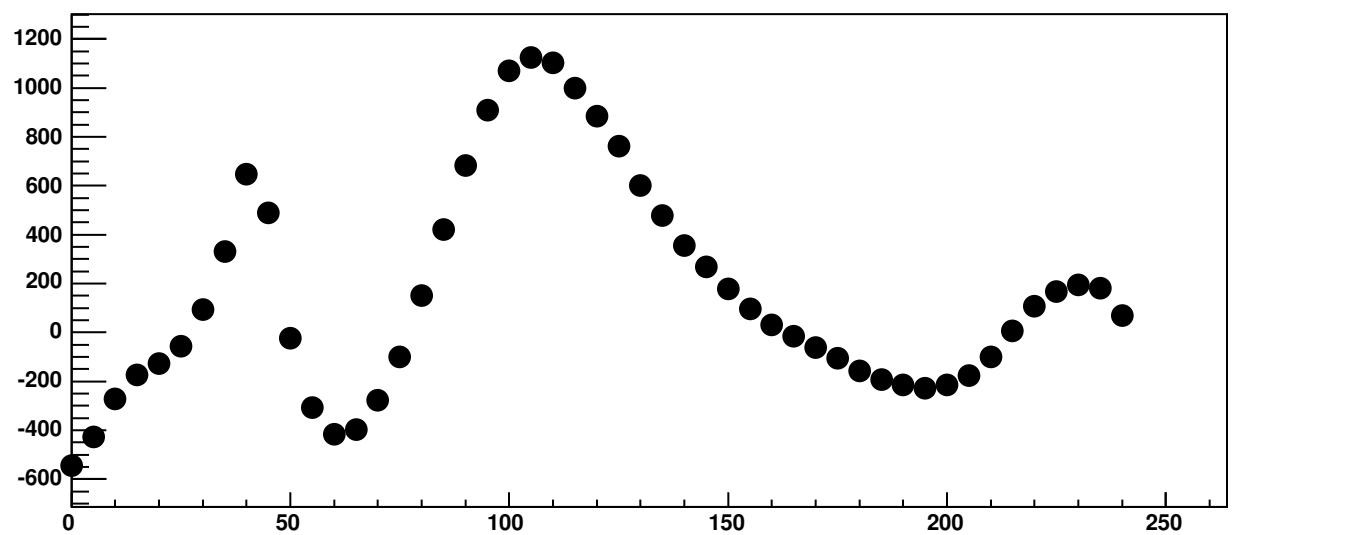
Chip 1, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold



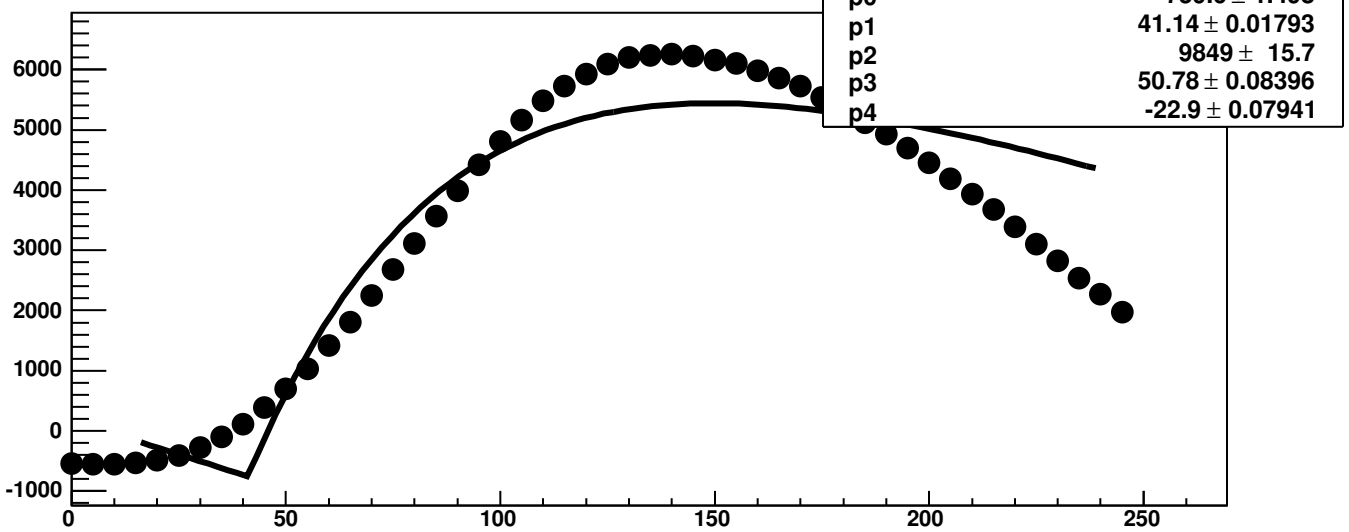
Chip 1, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



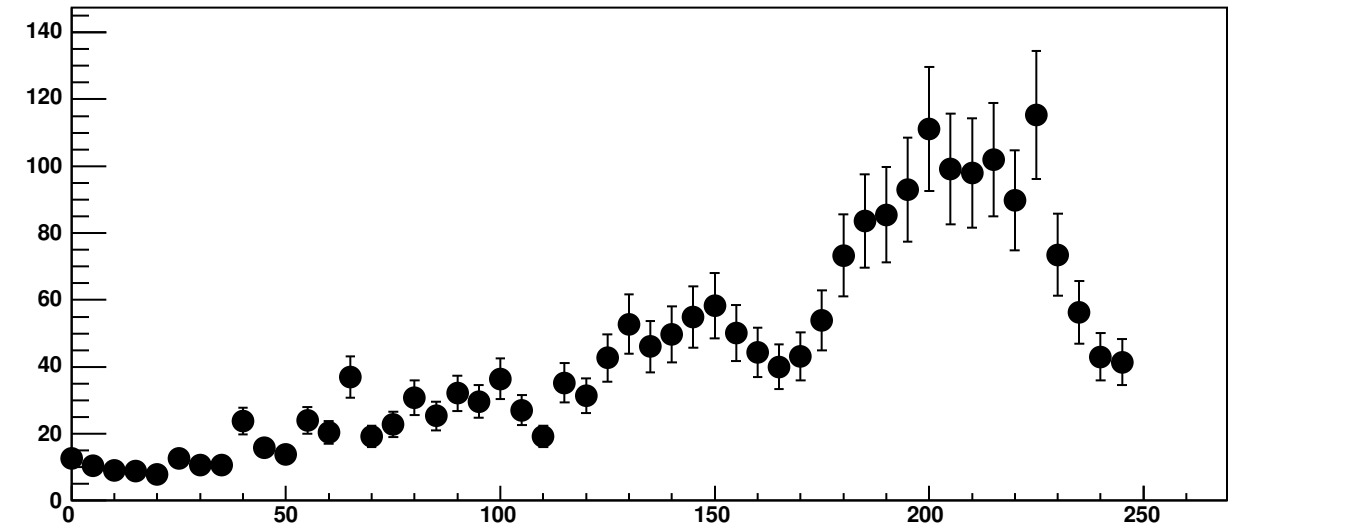
Chip 1, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold



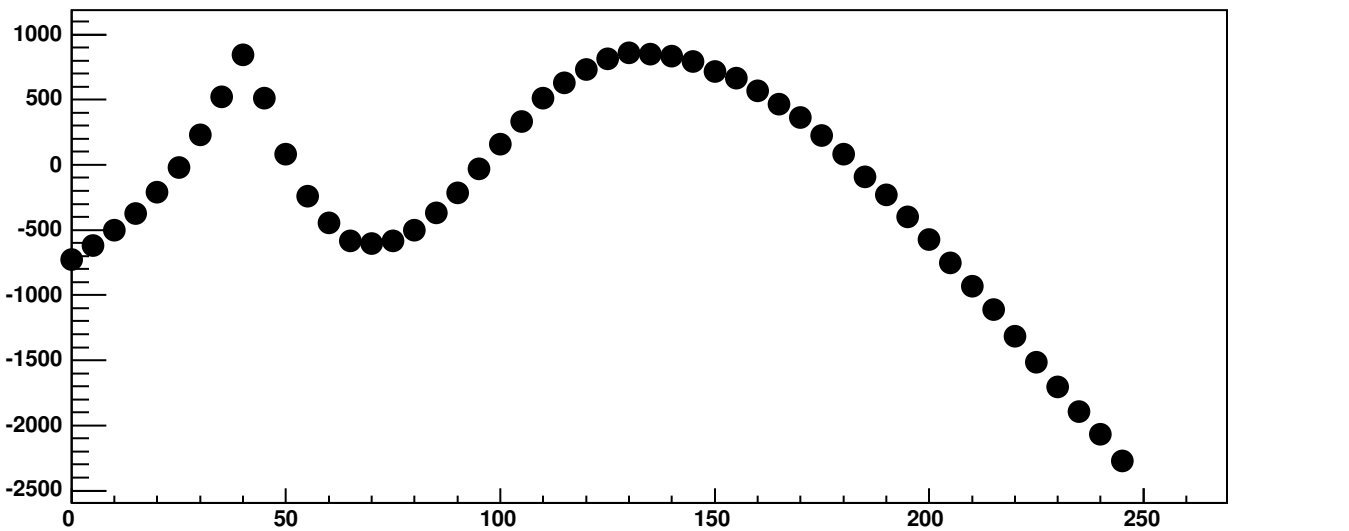
Chip 1, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold



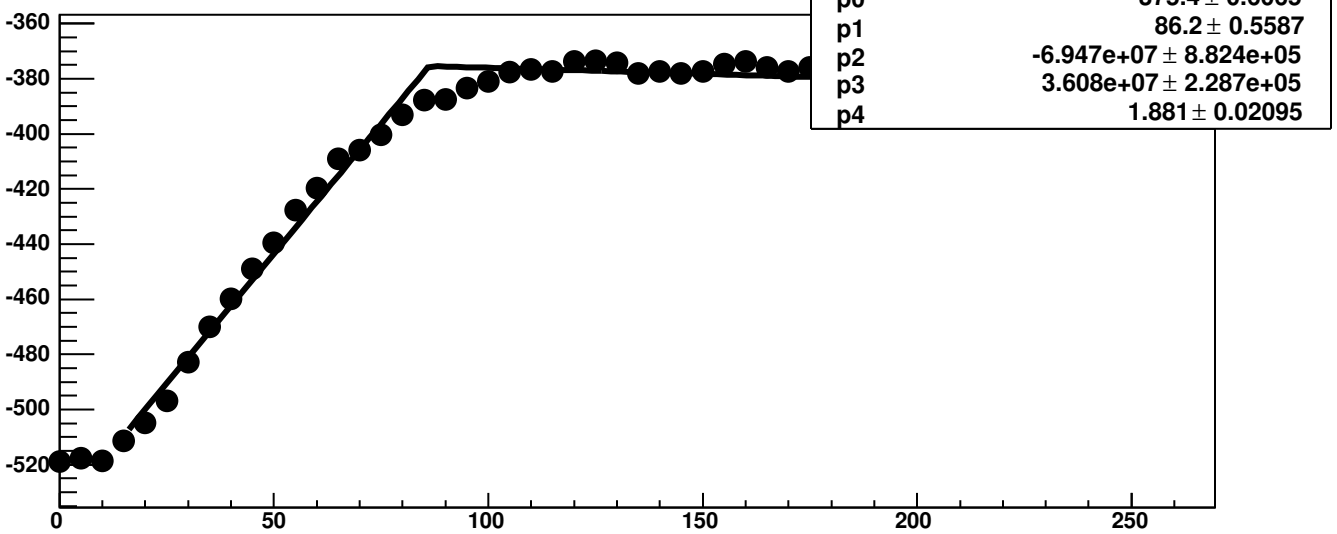
Chip 1, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



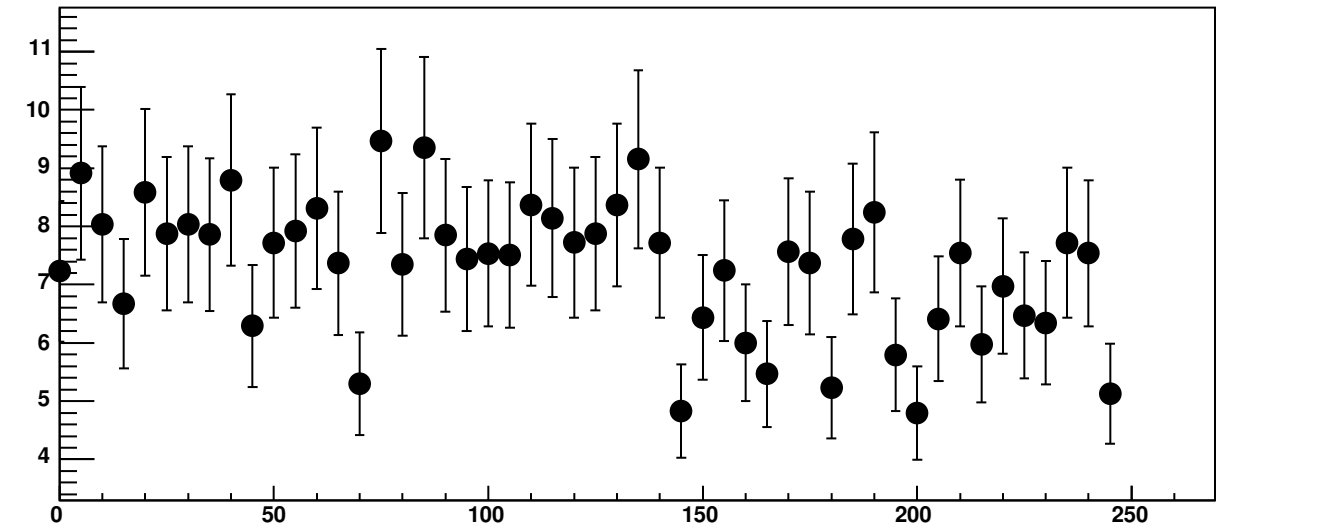
Chip 1, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold



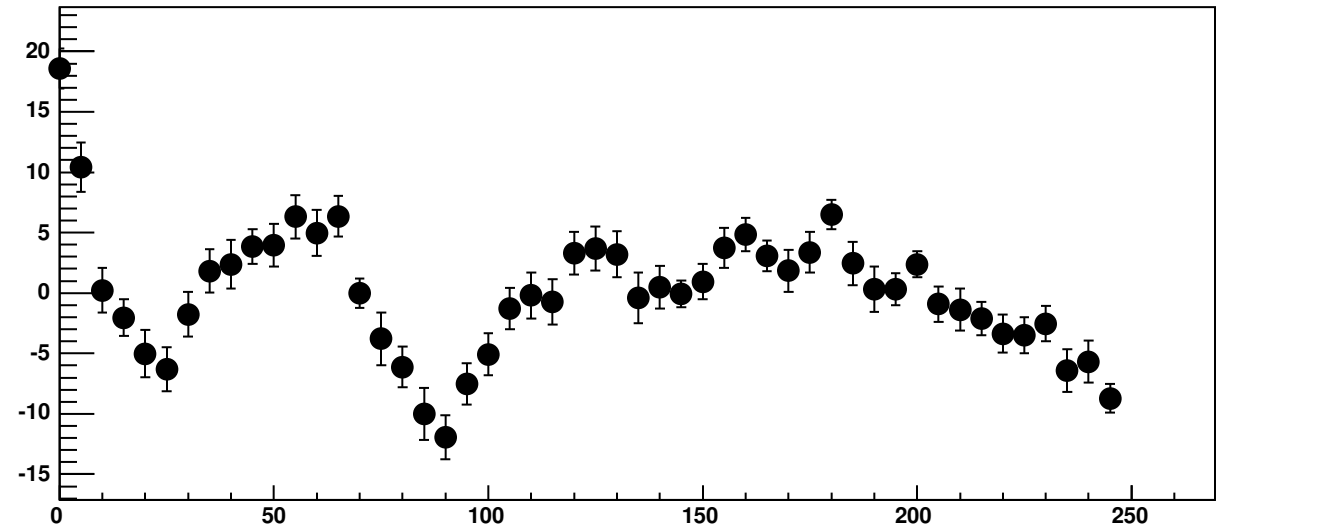
Chip 1, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold



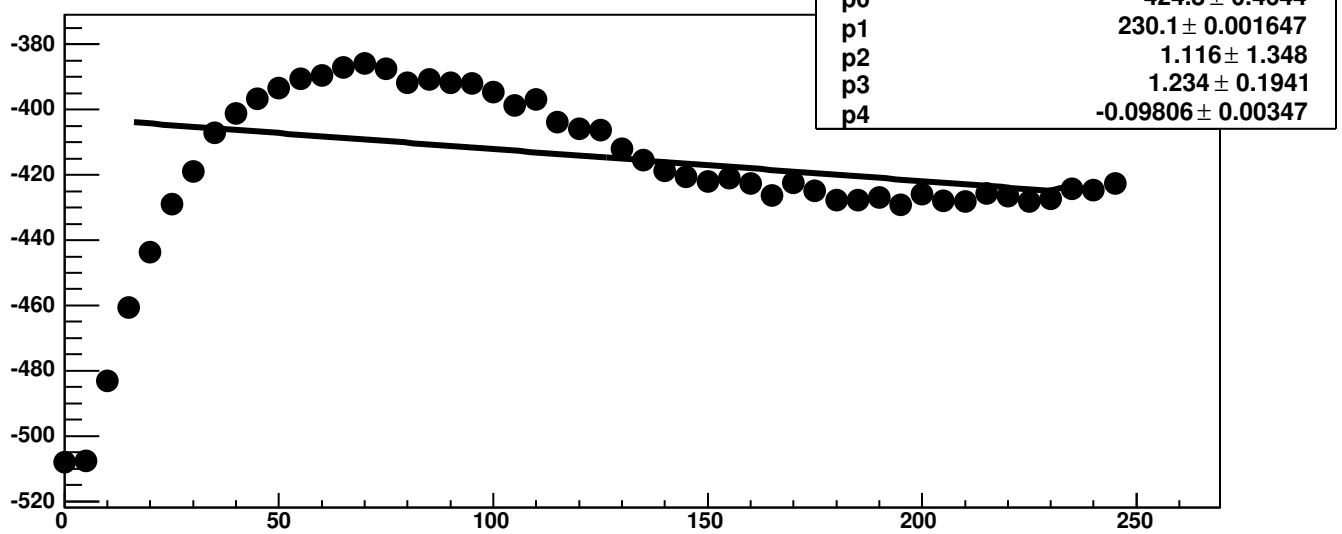
Chip 1, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



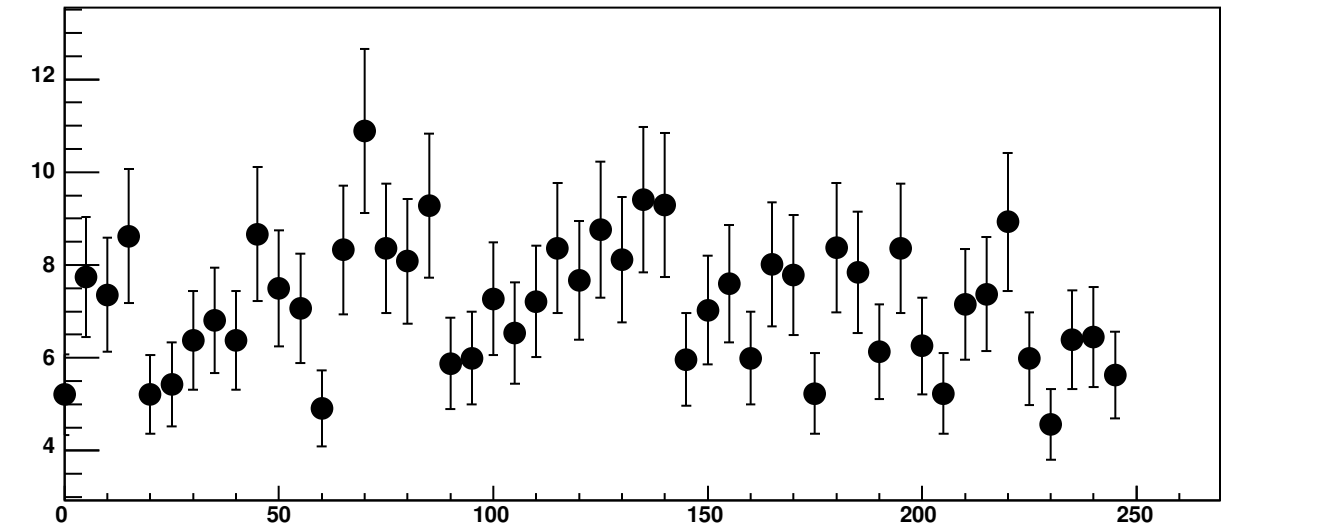
Chip 1, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold



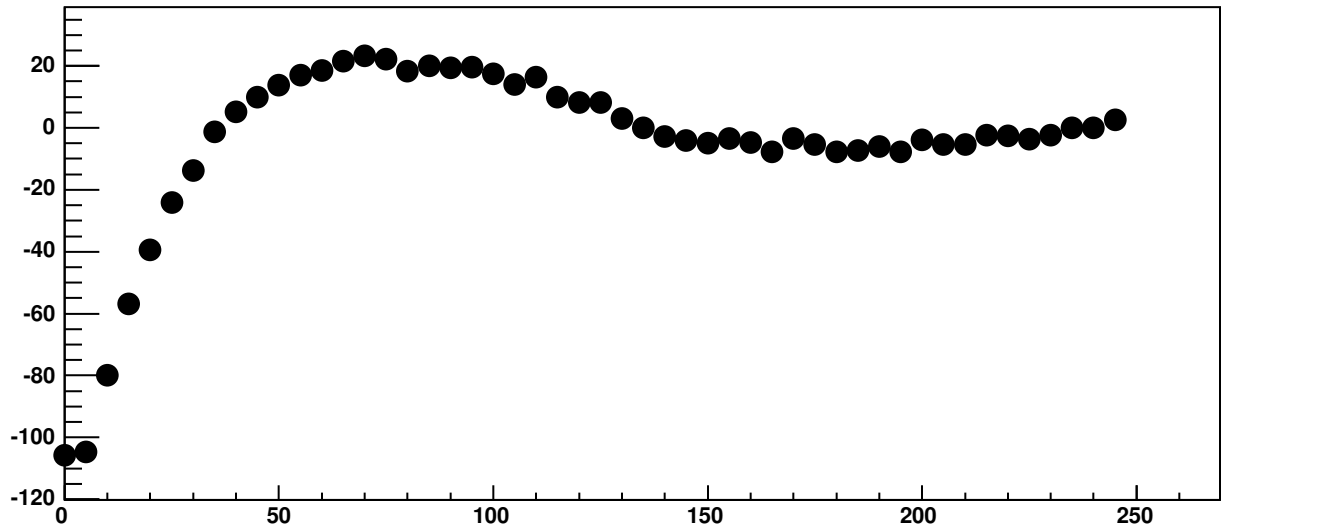
Chip 1, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold



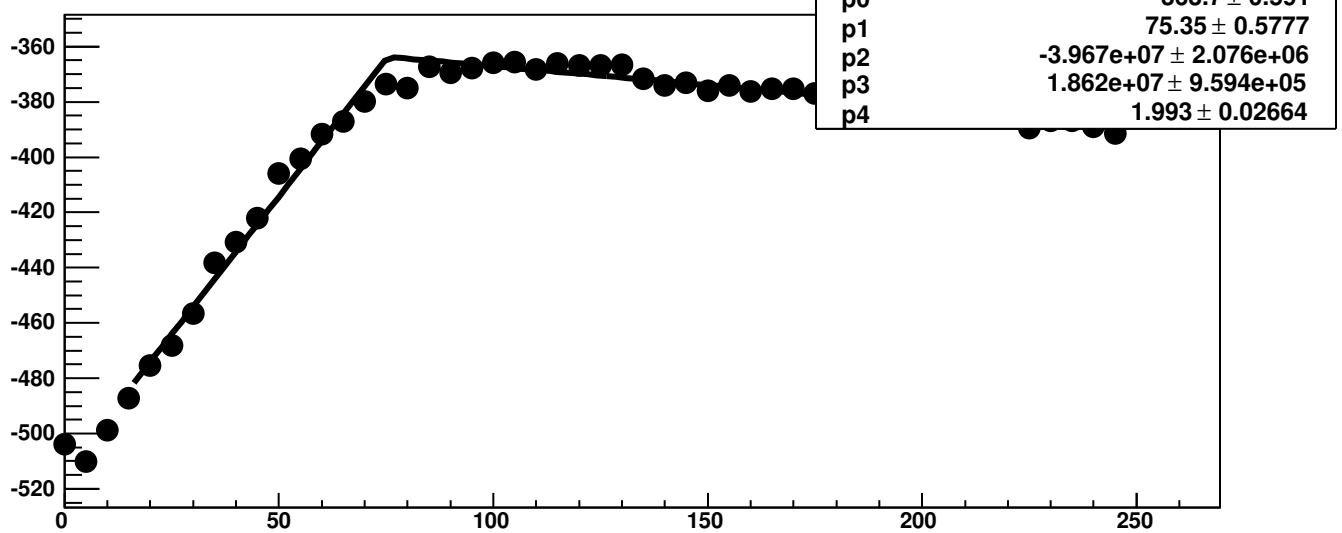
Chip 1, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold



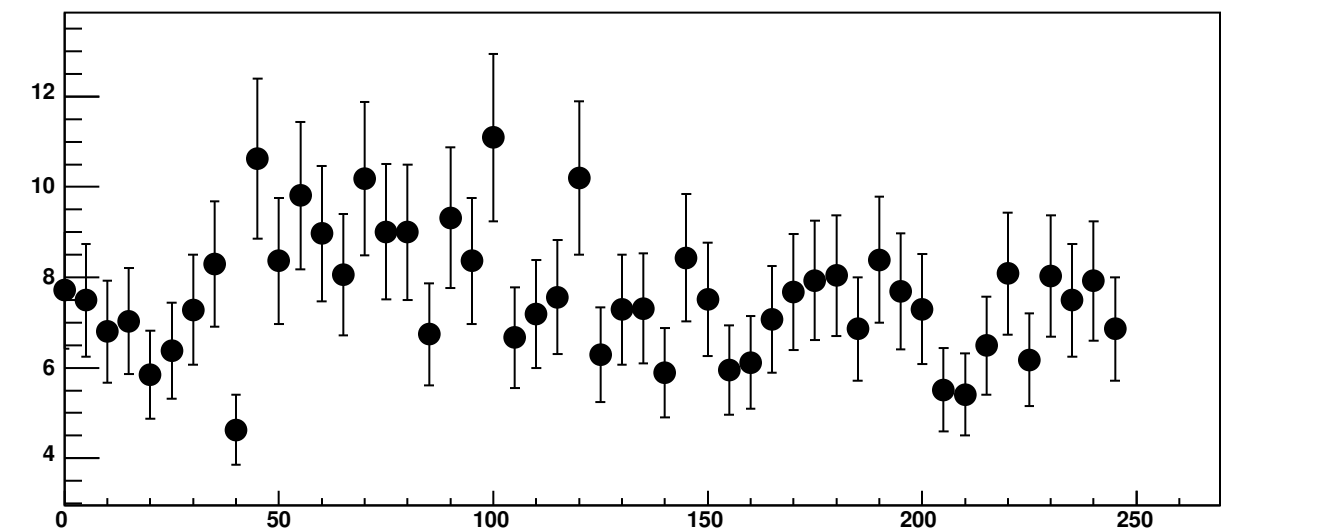
Chip 1, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold



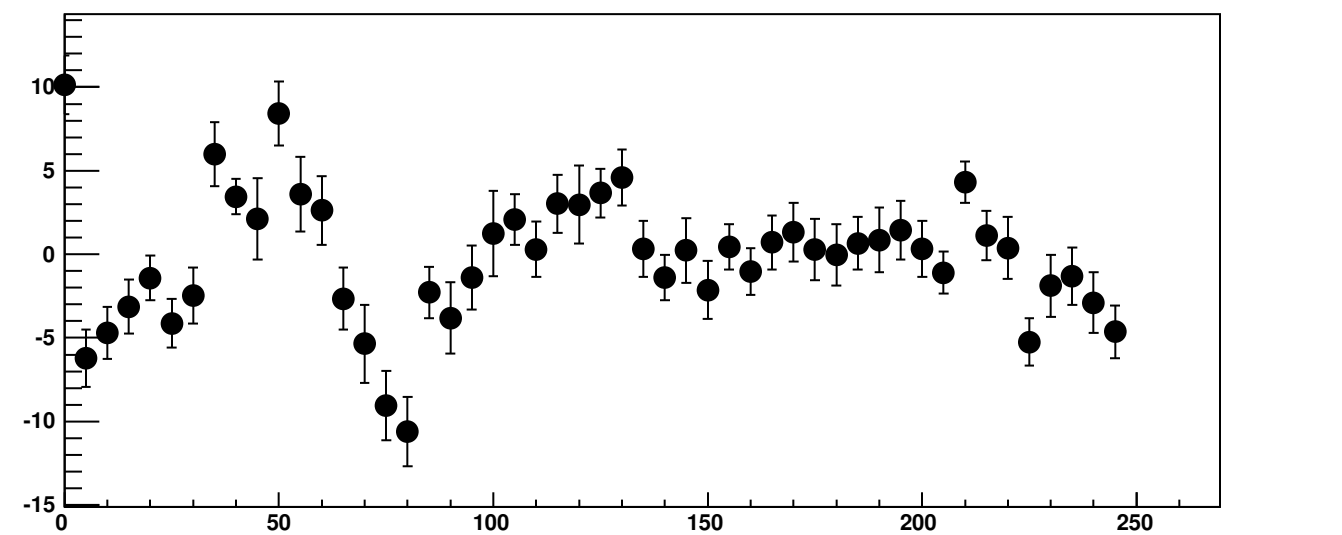
Chip 1, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

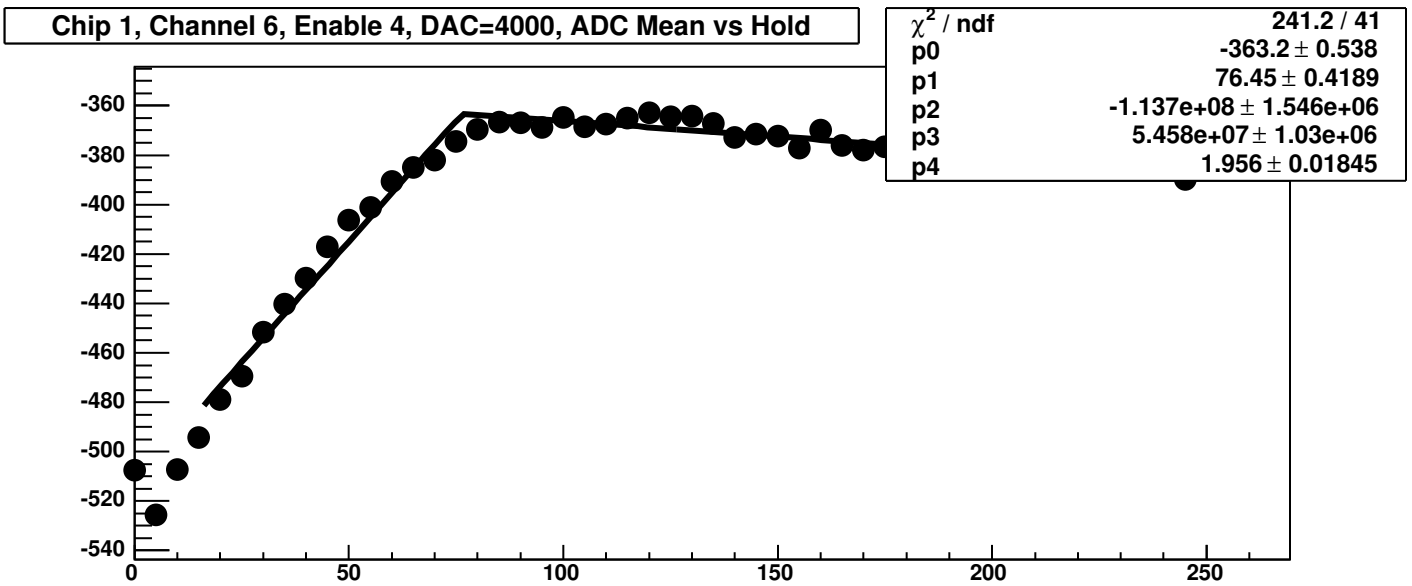


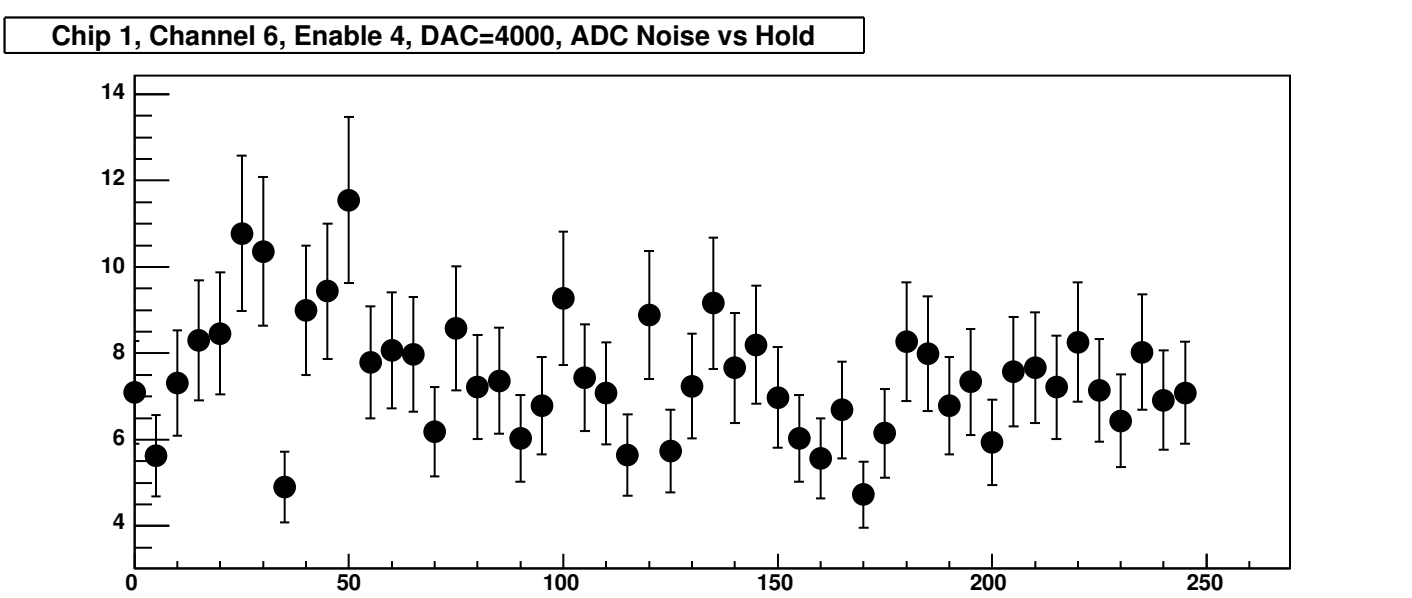
Chip 1, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold

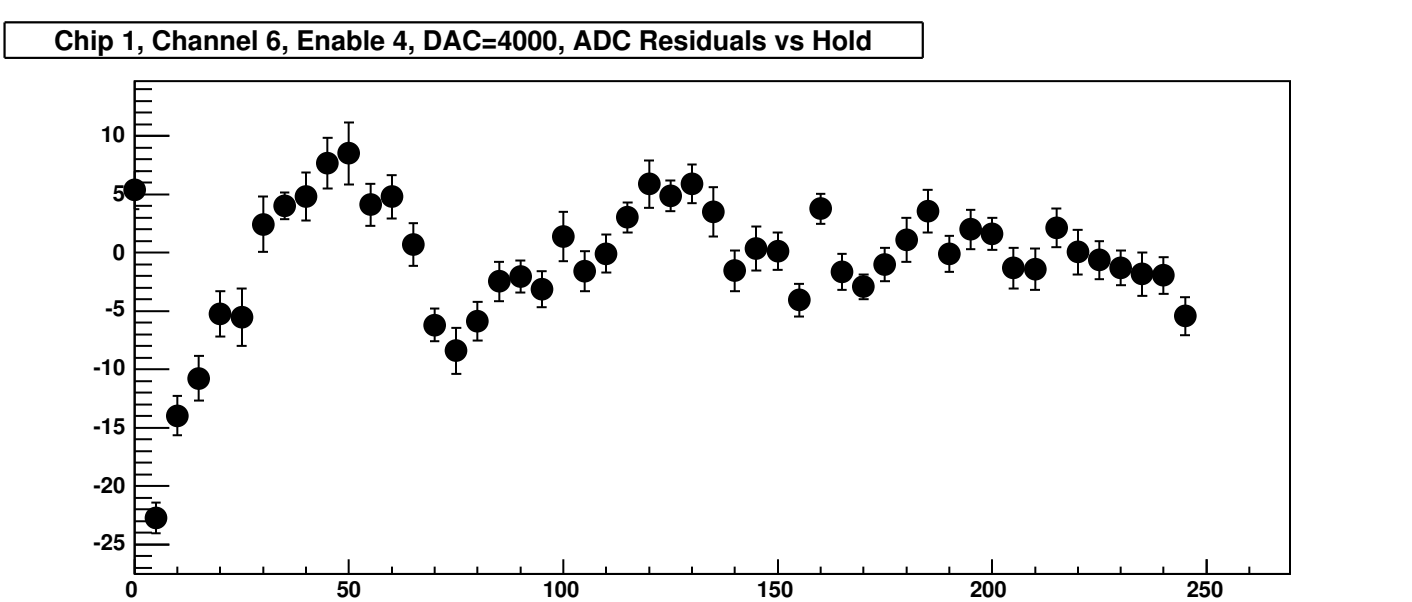


Chip 1, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold



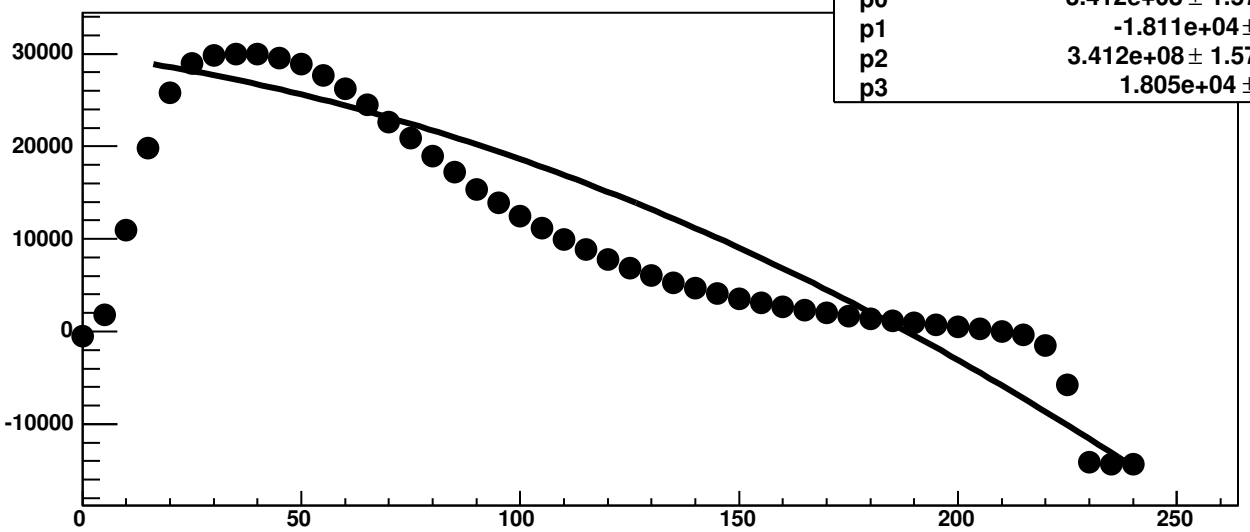

Chip 1, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 241.2 / 41
p0 -363.2 ± 0.538
p1 76.45 ± 0.4189
p2 -1.137e+08 ± 1.546e+06
p3 5.458e+07 ± 1.03e+06
p4 1.956 ± 0.01845
-360
-380
-400
-420
-440
-460
-480
-500
-520
-540
0
50
100
150
200
250


Chip 1, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

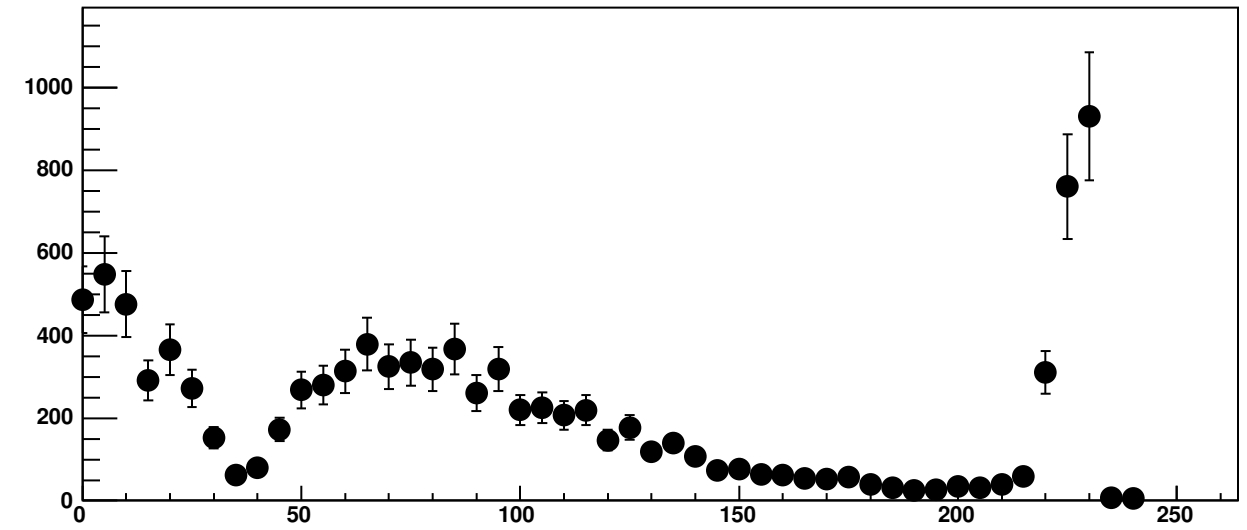

Chip 1, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
-20
-25
0
50
100
150
200
250

Chip 1, Channel 6, Enable 5!, DAC=4000, ADC Mean vs Hold

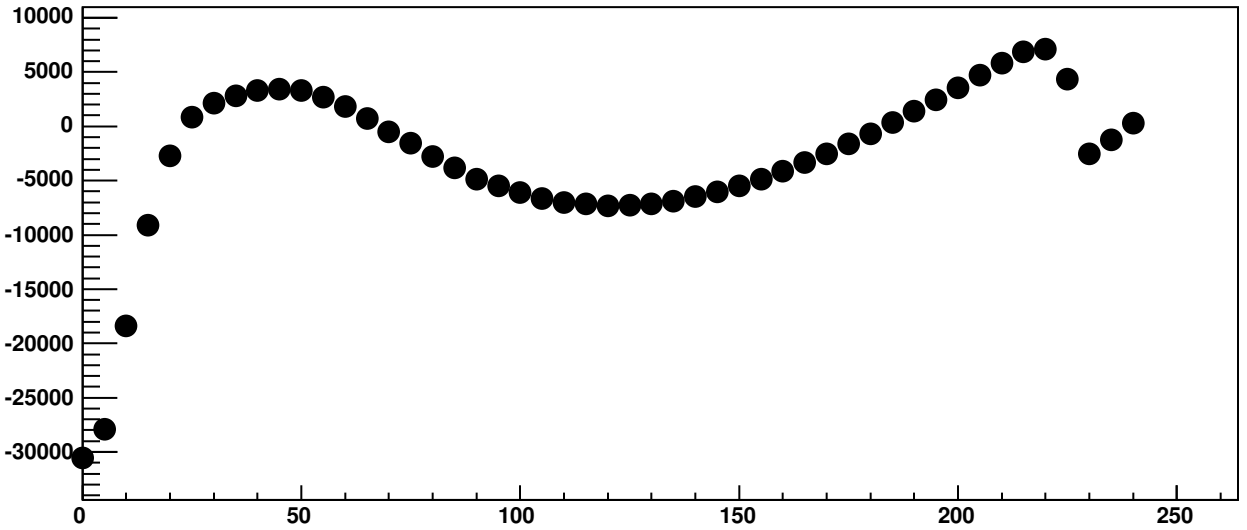
χ^2 / ndf	3.077e+06 / 42
p0	-3.412e+08 ± 1.578e+06
p1	-1.811e+04 ± 44.36
p2	3.412e+08 ± 1.578e+06
p3	1.805e+04 ± 44.28



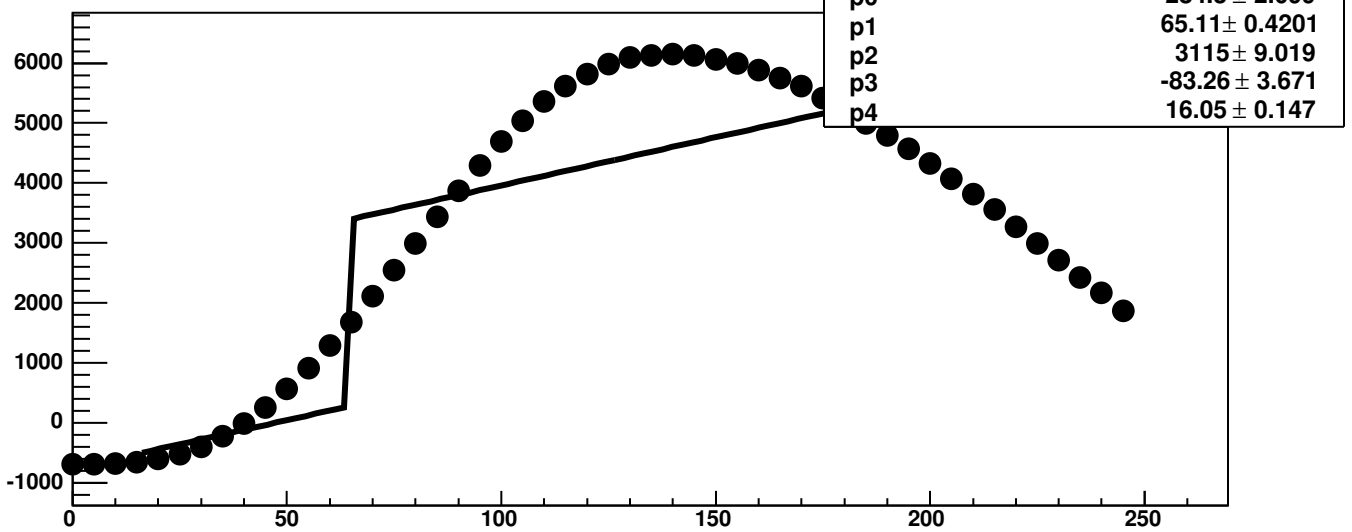
Chip 1, Channel 6, Enable 5!, DAC=4000, ADC Noise vs Hold



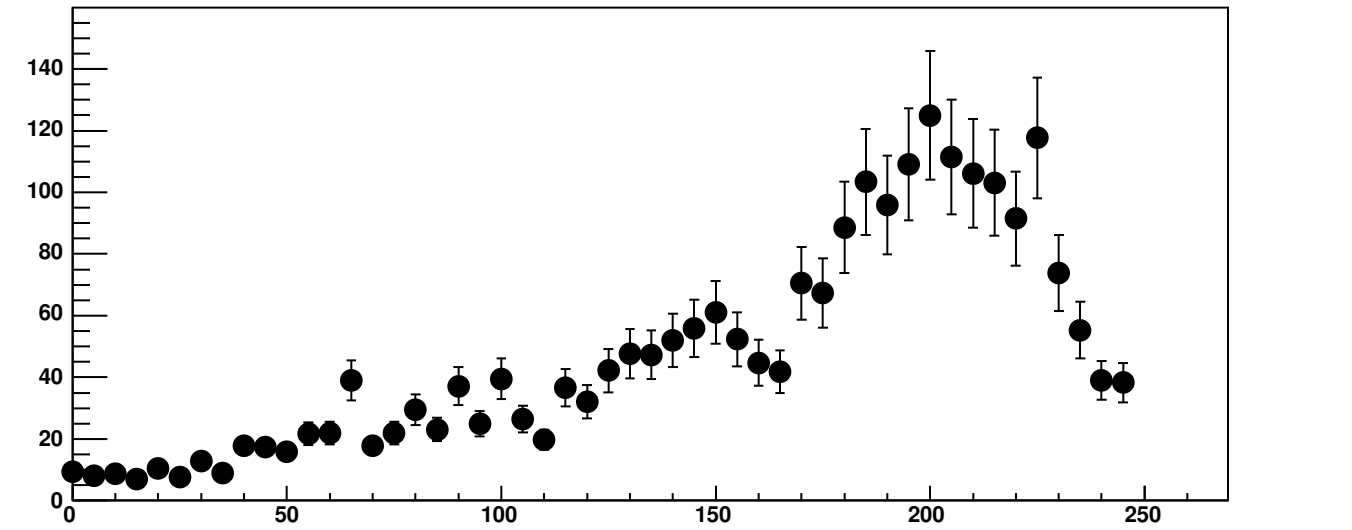
Chip 1, Channel 6, Enable 5!, DAC=4000, ADC Residuals vs Hold



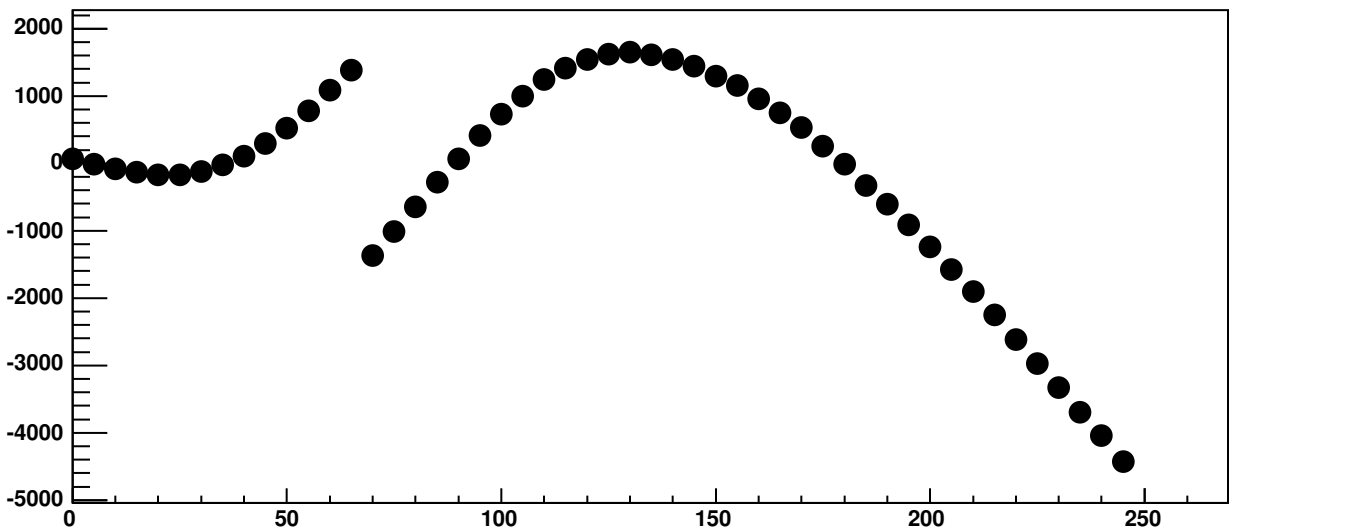
Chip 1, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

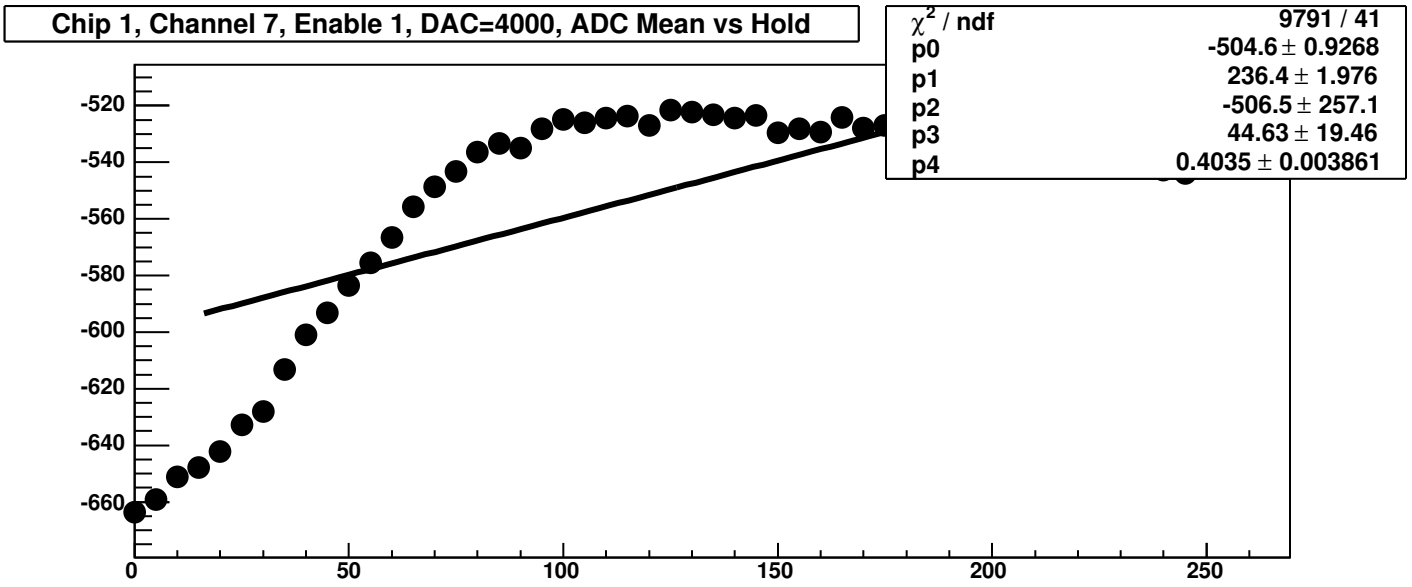


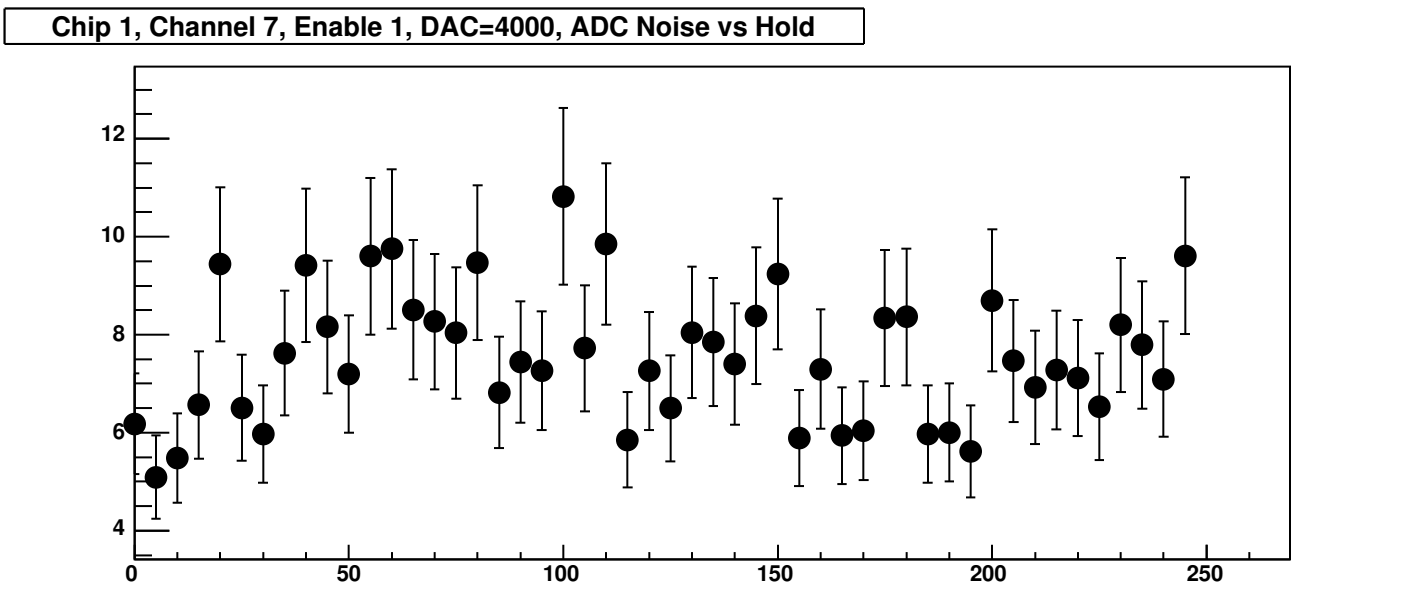
Chip 1, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold

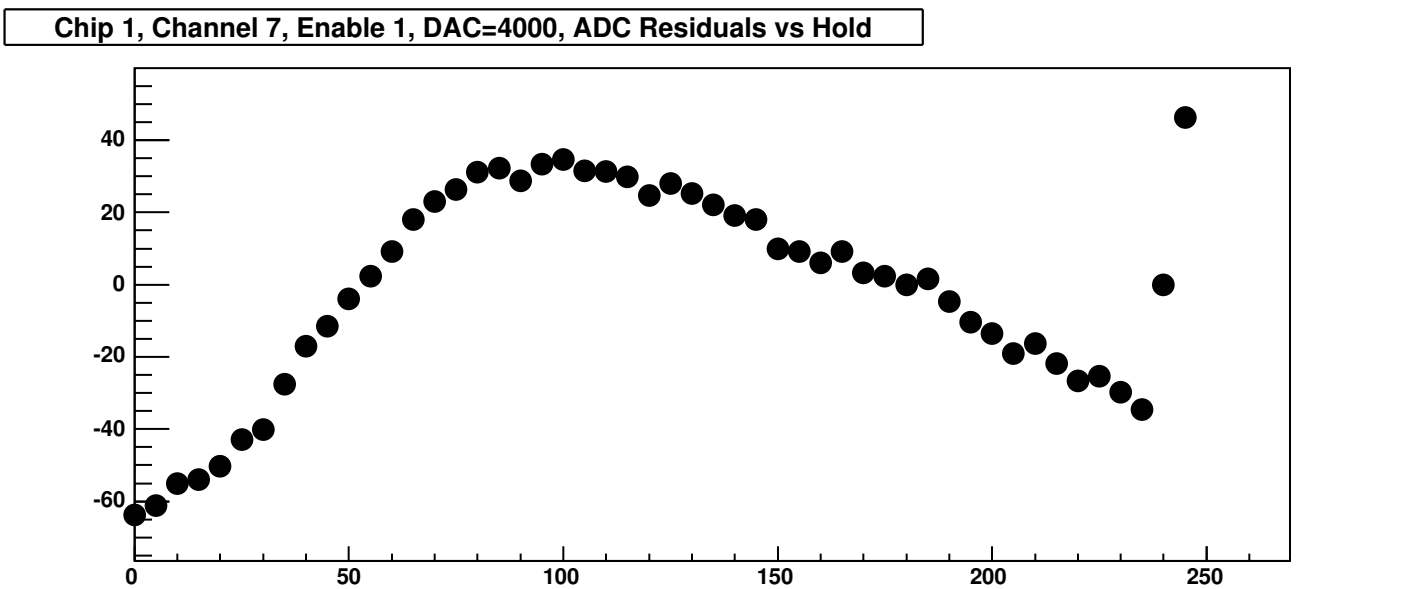


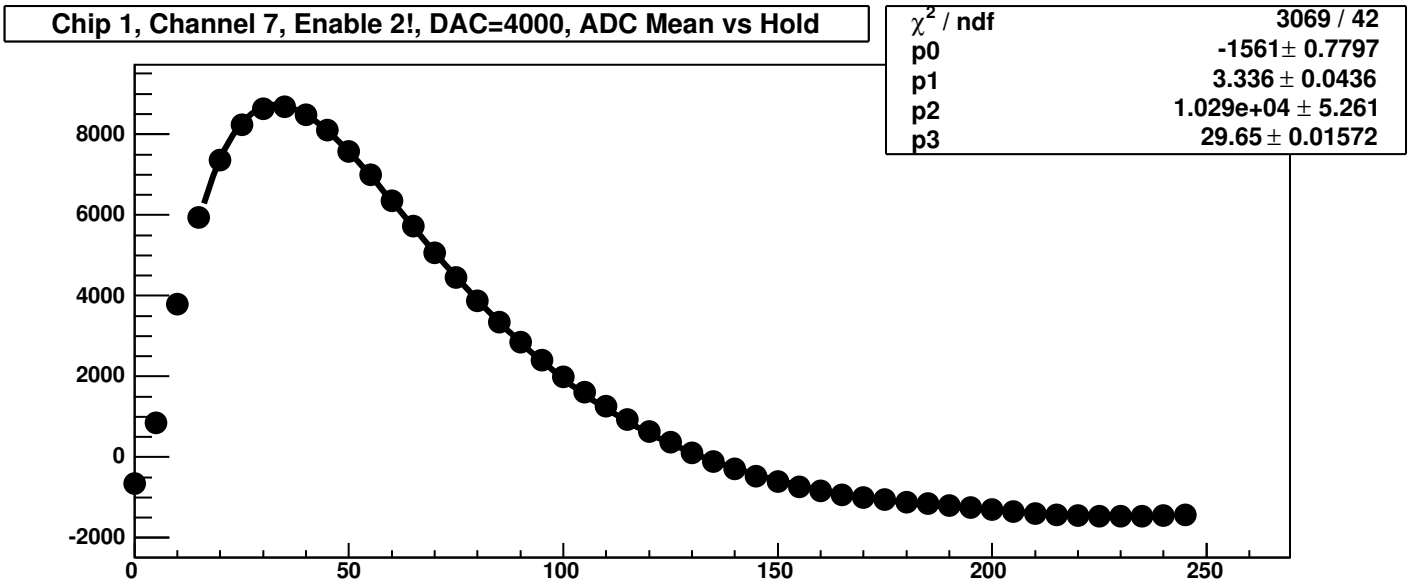
Chip 1, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

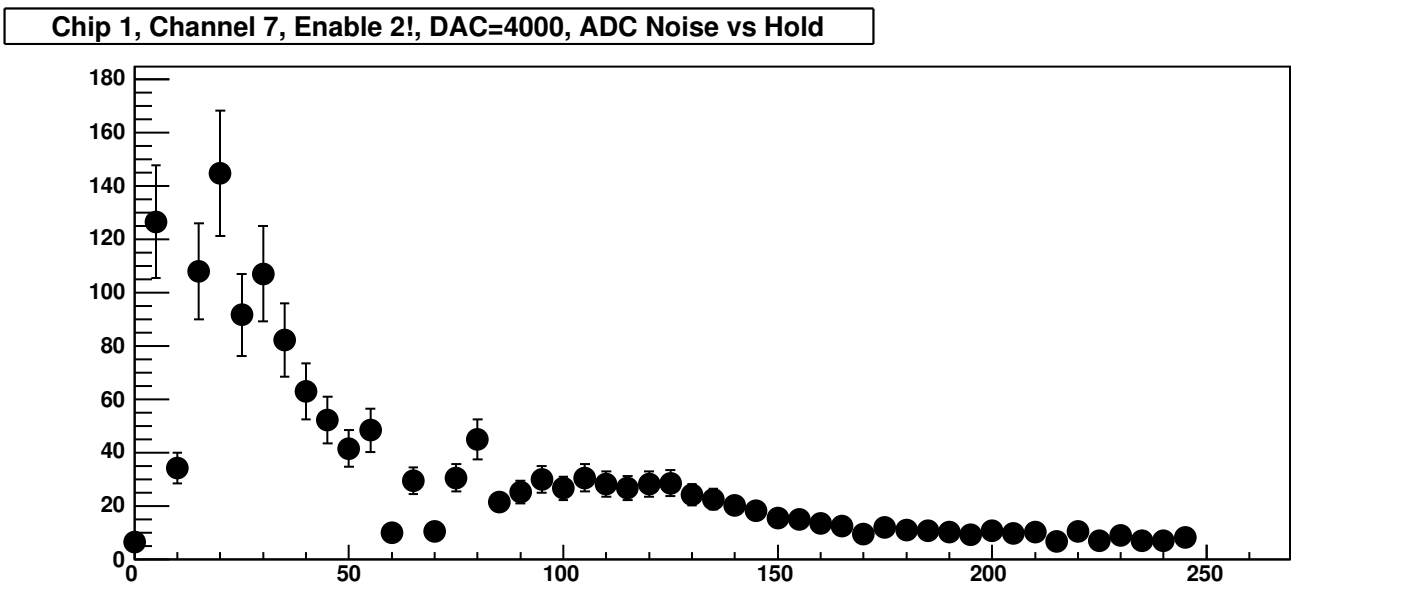


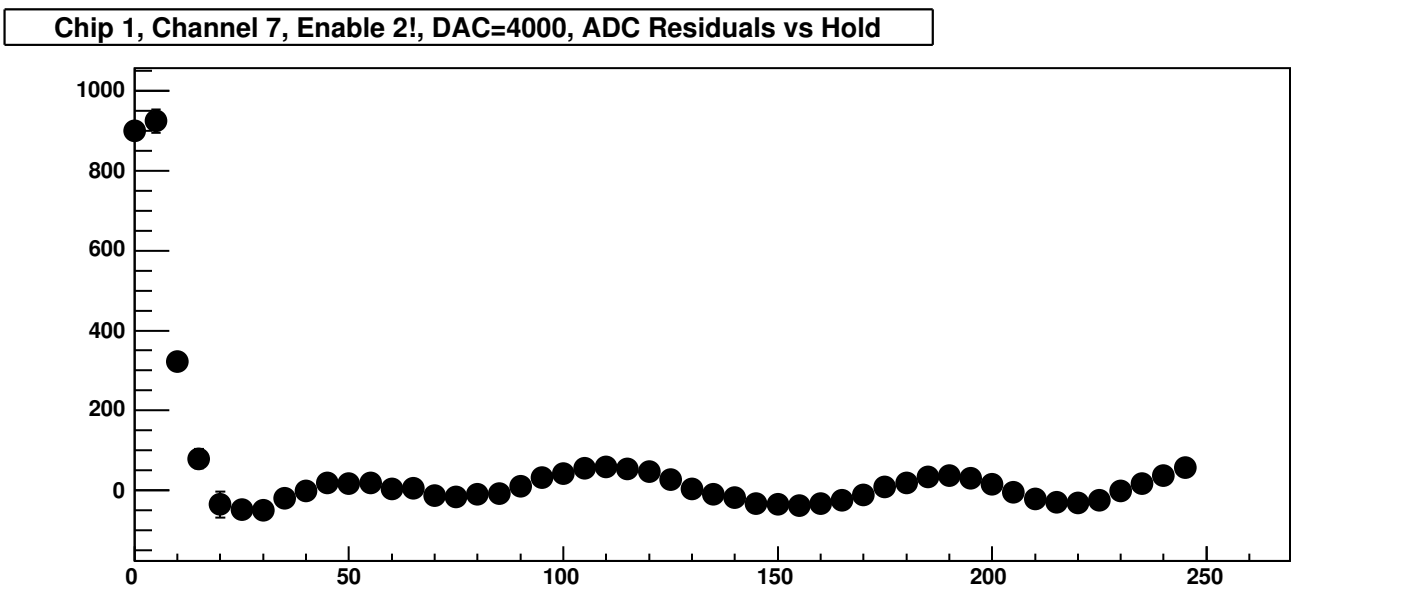

Chip 1, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 9791 / 41
p0 -504.6 ± 0.9268
p1 236.4 ± 1.976
p2 -506.5 ± 257.1
p3 44.63 ± 19.46
p4 0.4035 ± 0.003861
-520
-540
-560
-580
-600
-620
-640
-660
0
50
100
150
200
250


Chip 1, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

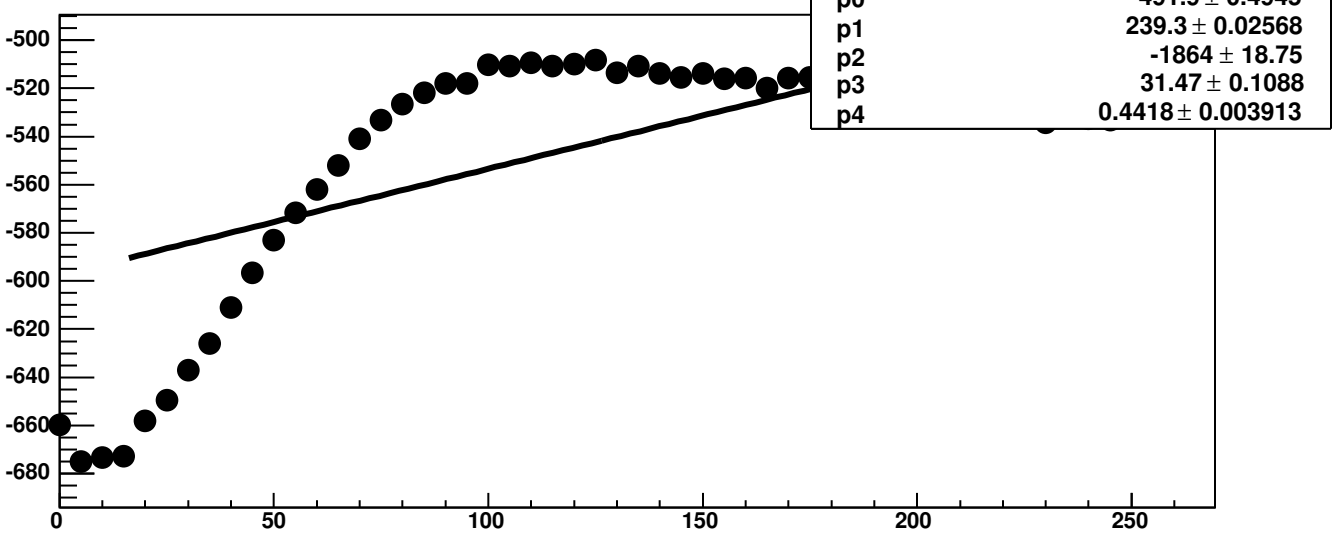

Chip 1, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
0
50
100
150
200
250


Chip 1, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 3069 / 42
p0 -1561 ± 0.7797
p1 3.336 ± 0.0436
p2 1.029e+04 ± 5.261
p3 29.65 ± 0.01572
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

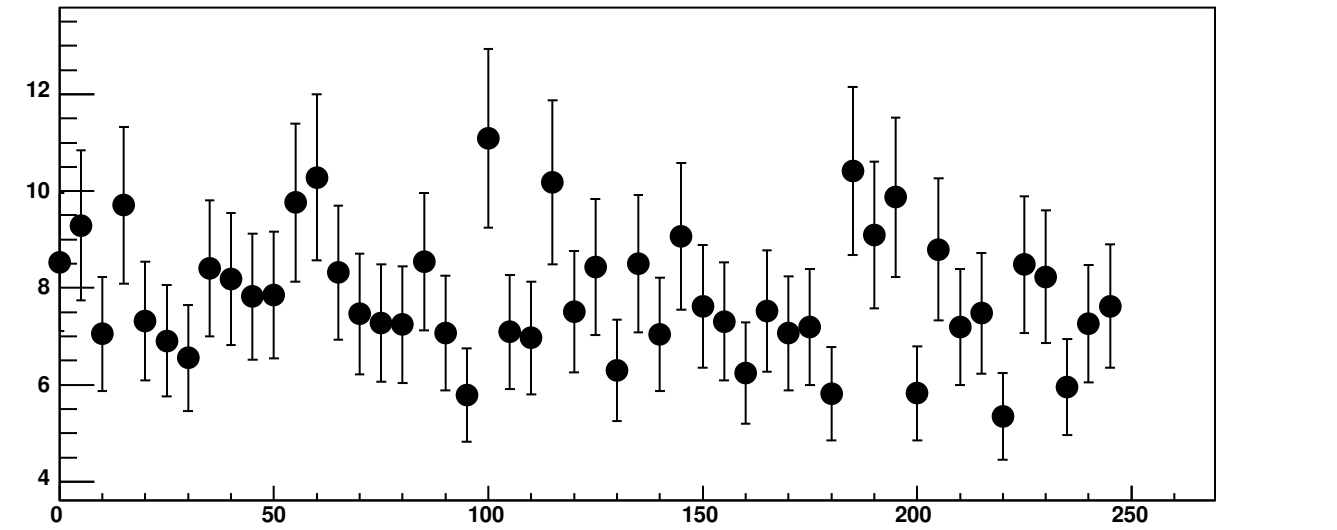

Chip 1, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 1, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold
1000
800
600
400
200
0
0
50
100
150
200
250

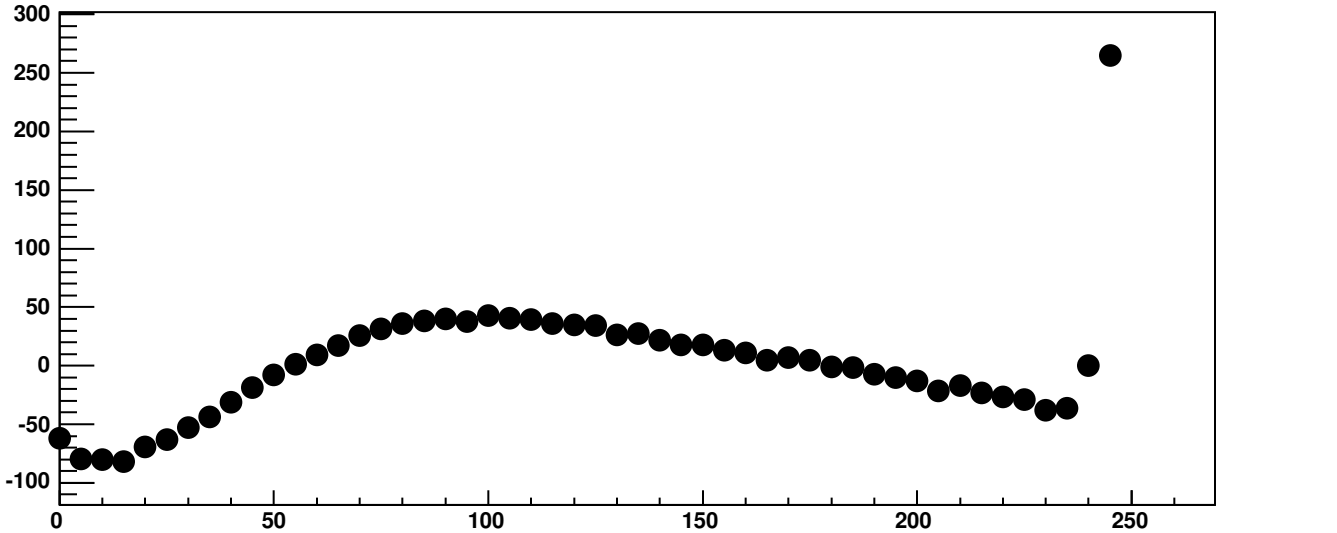
Chip 1, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

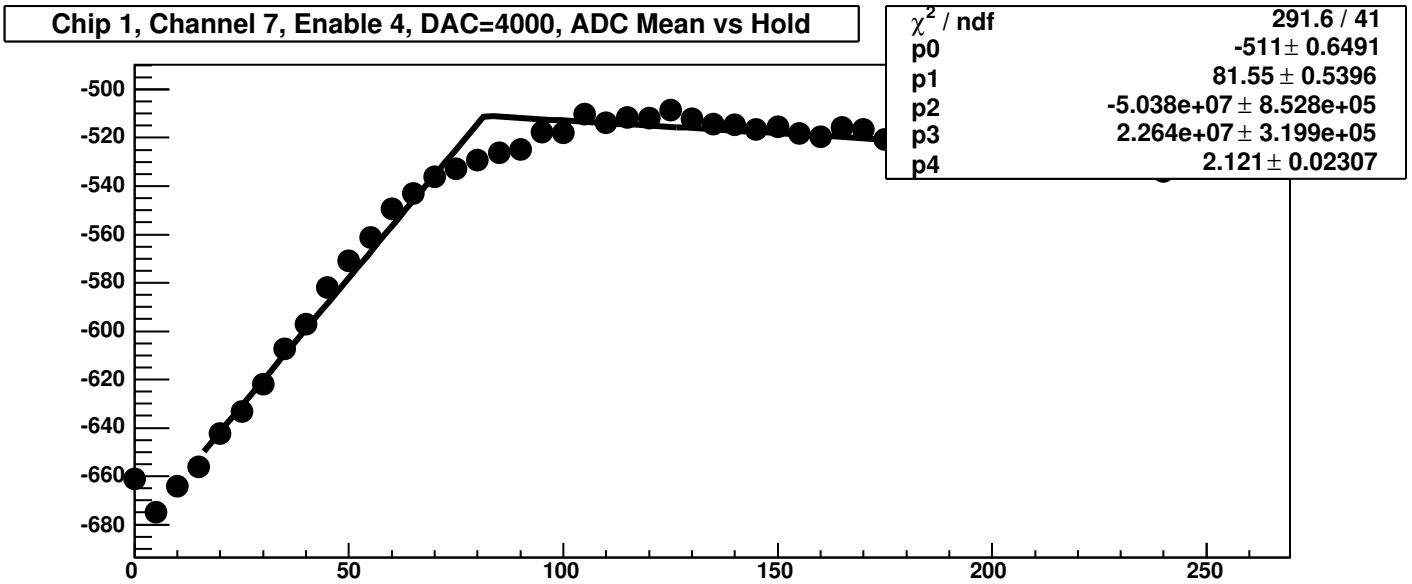


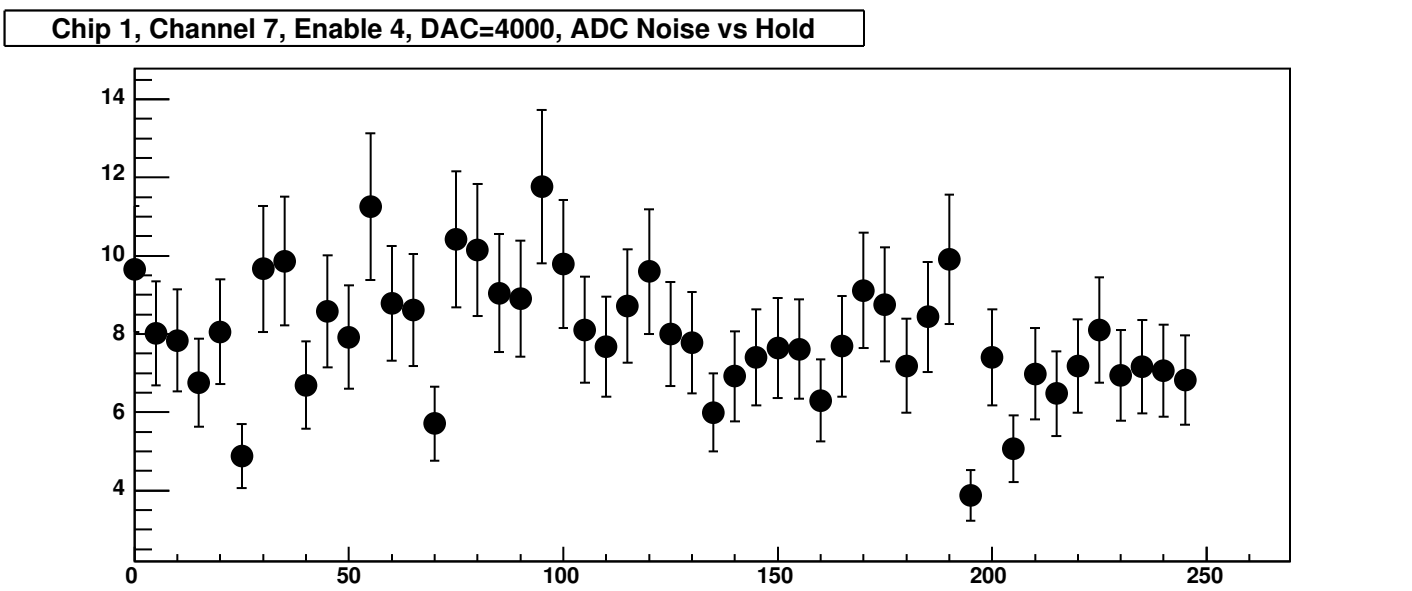
Chip 1, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold

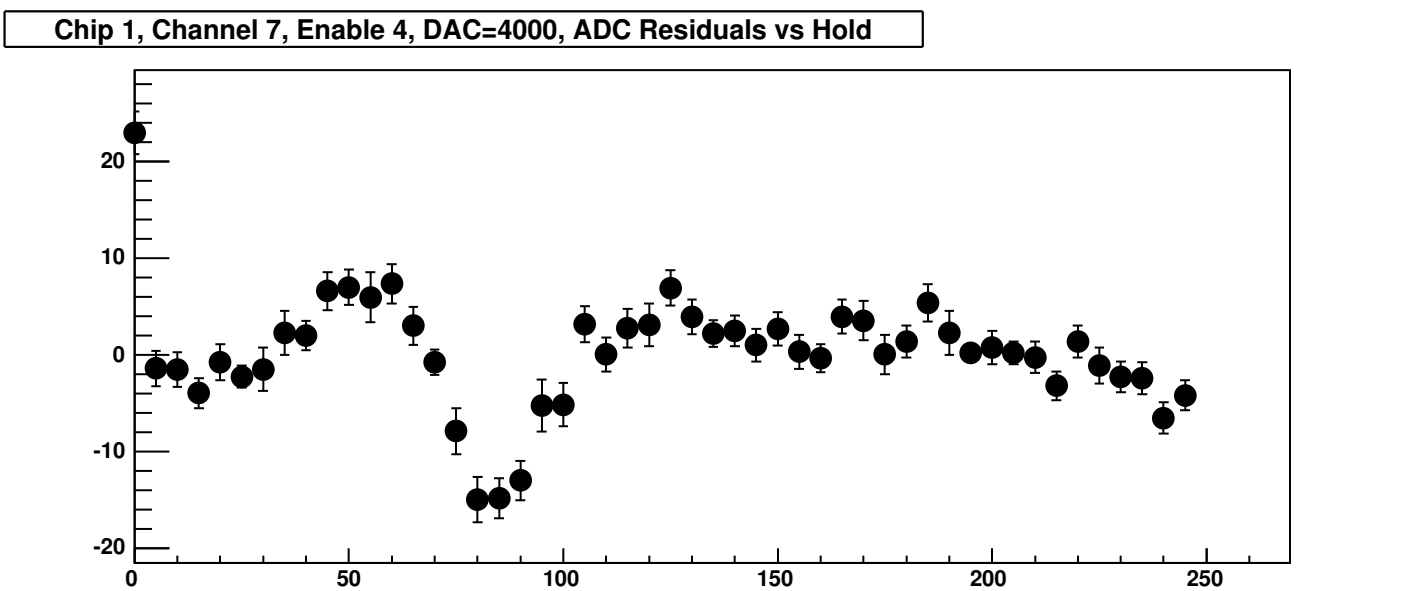


Chip 1, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

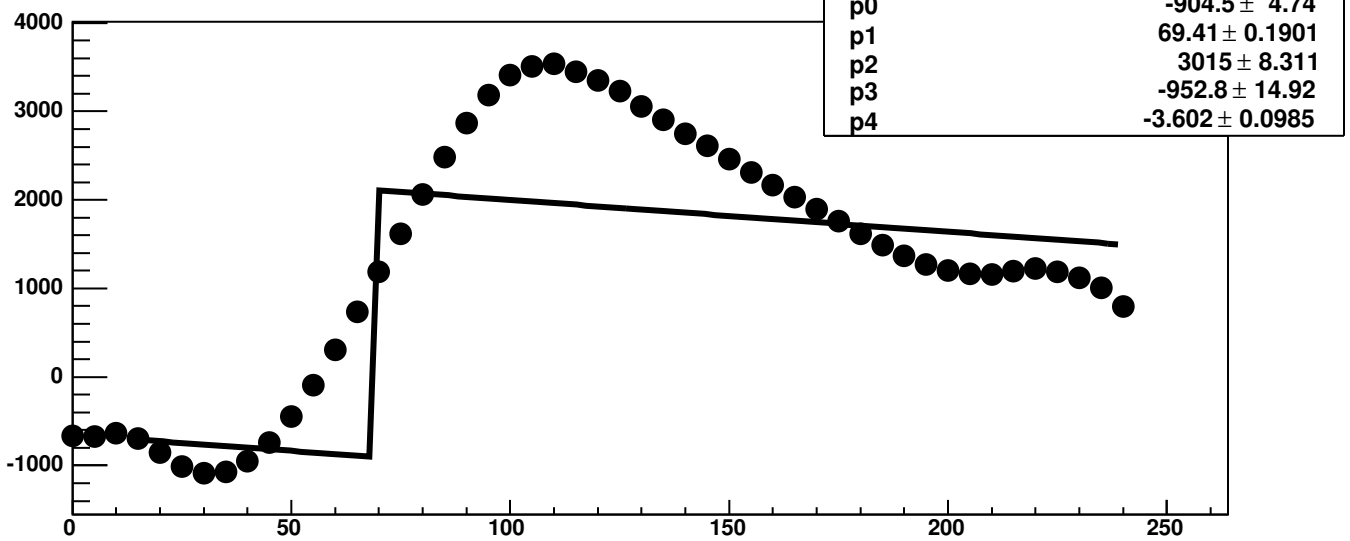



Chip 1, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 291.6 / 41
p0 -511 ± 0.6491
p1 81.55 ± 0.5396
p2 -5.038e+07 ± 8.528e+05
p3 2.264e+07 ± 3.199e+05
p4 2.121 ± 0.02307
-500
-520
-540
-560
-580
-600
-620
-640
-660
-680
0
50
100
150
200
250

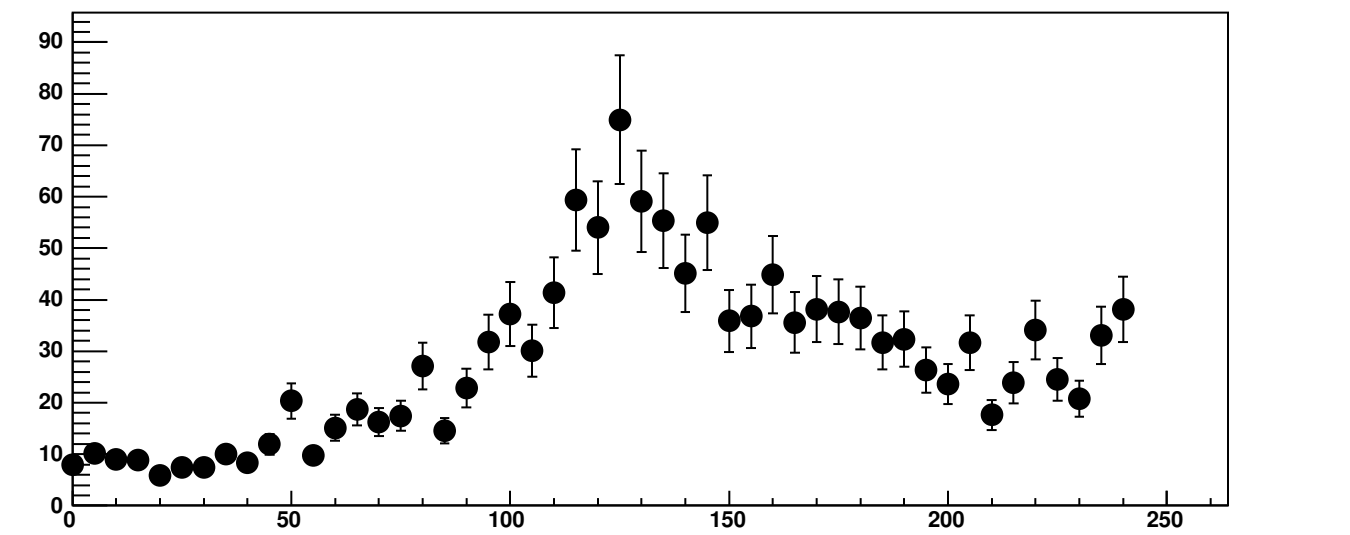

Chip 1, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 1, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
0
50
100
150
200
250

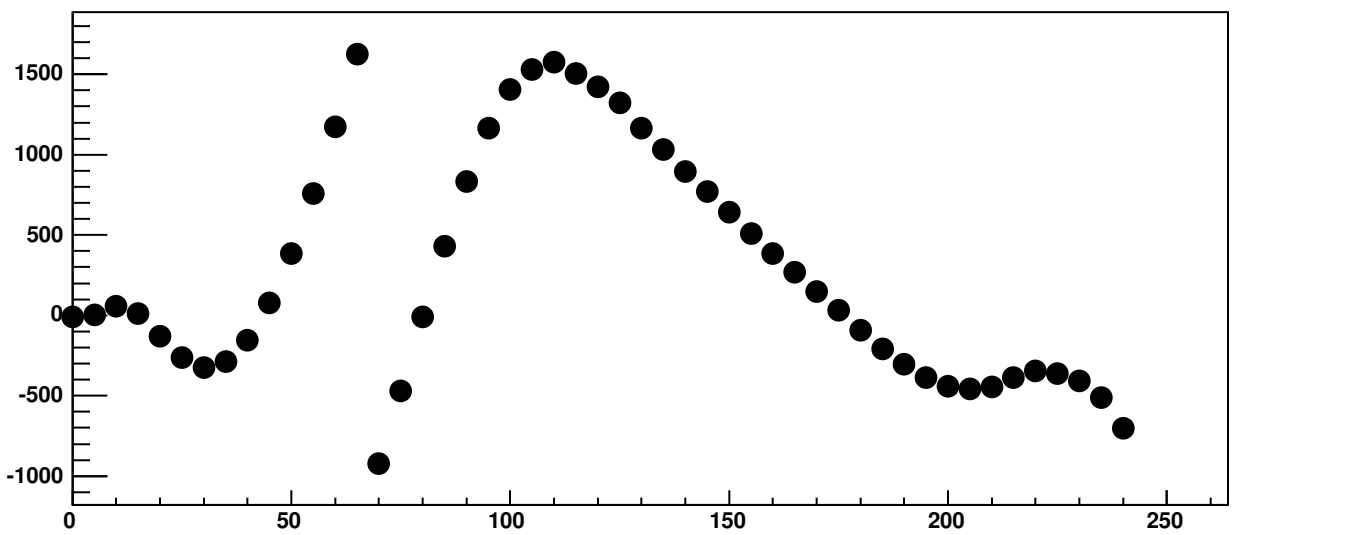
Chip 1, Channel 7, Enable 5, DAC=4000, ADC Mean vs Hold



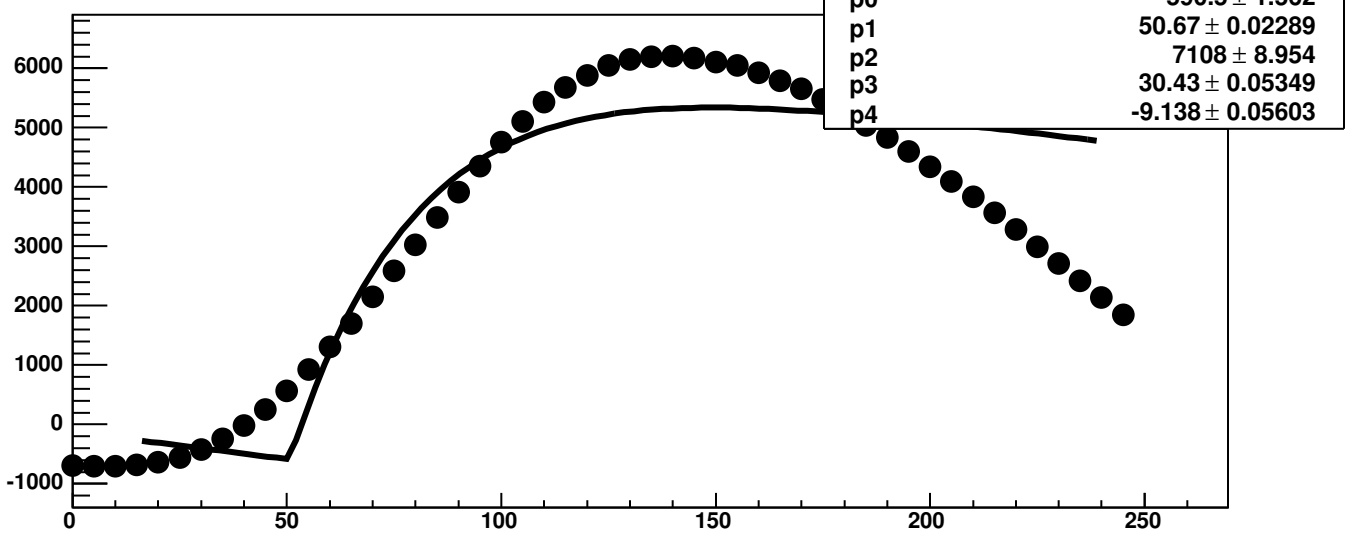
Chip 1, Channel 7, Enable 5, DAC=4000, ADC Noise vs Hold



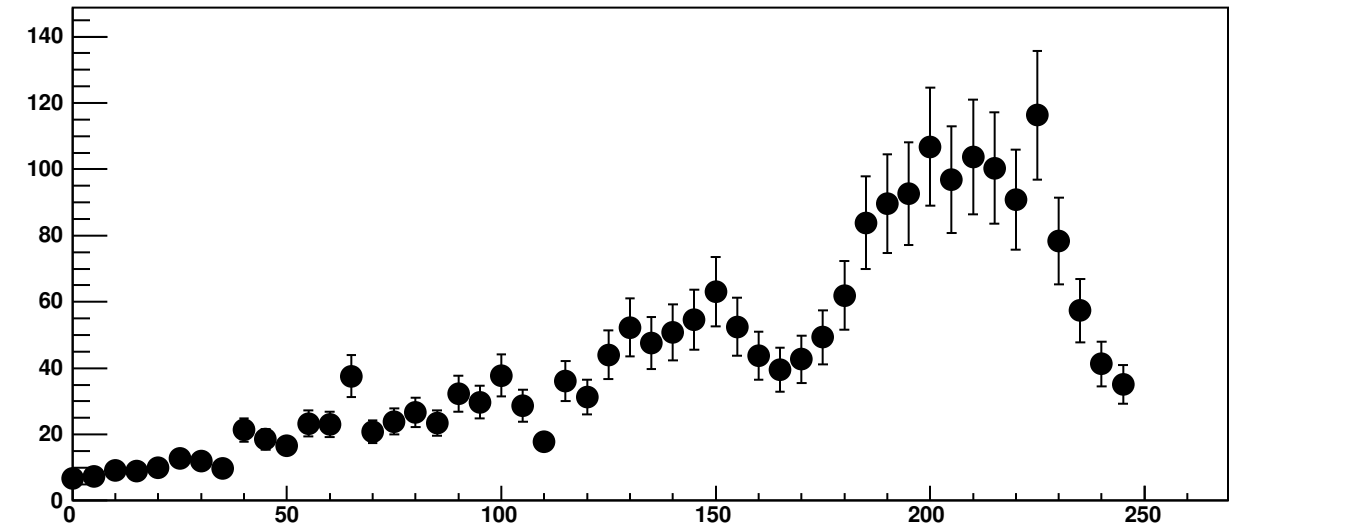
Chip 1, Channel 7, Enable 5, DAC=4000, ADC Residuals vs Hold



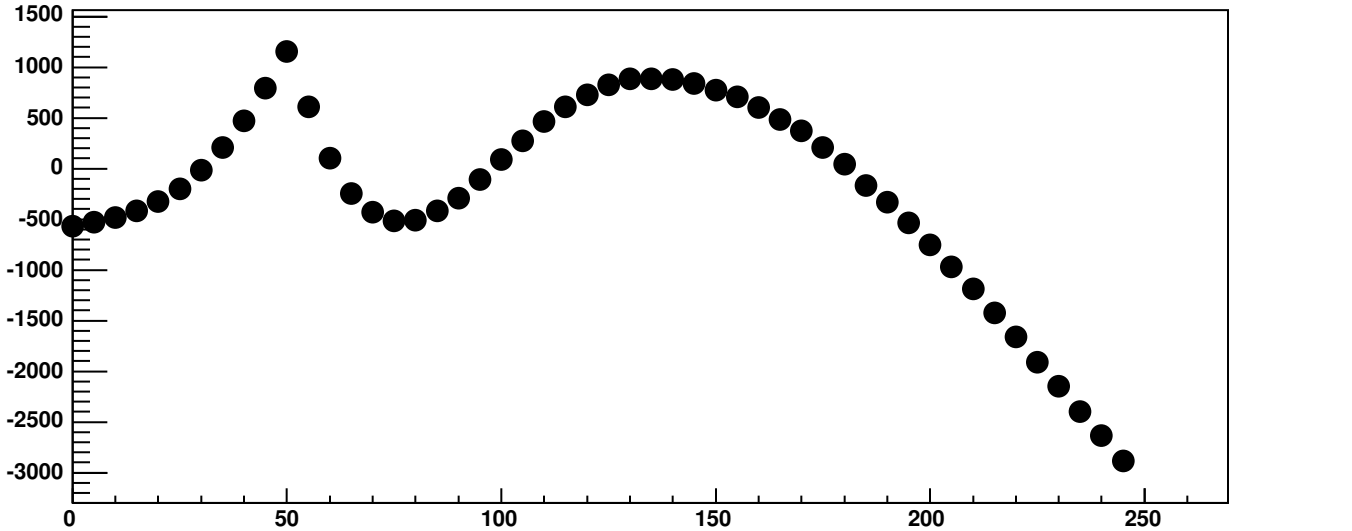
Chip 1, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold



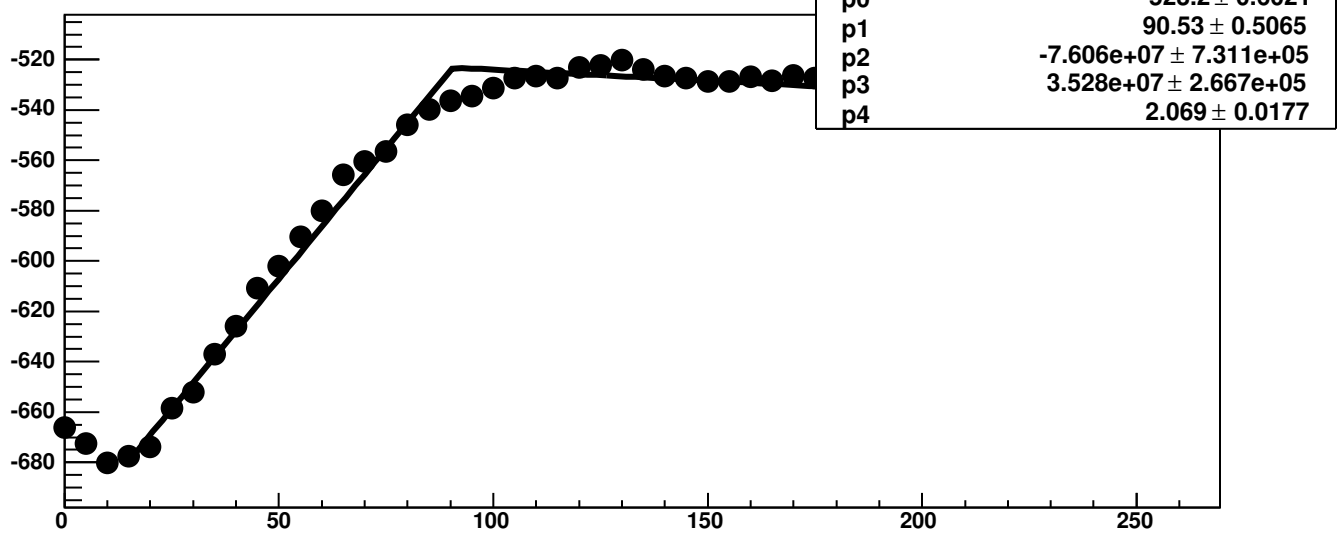
Chip 1, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



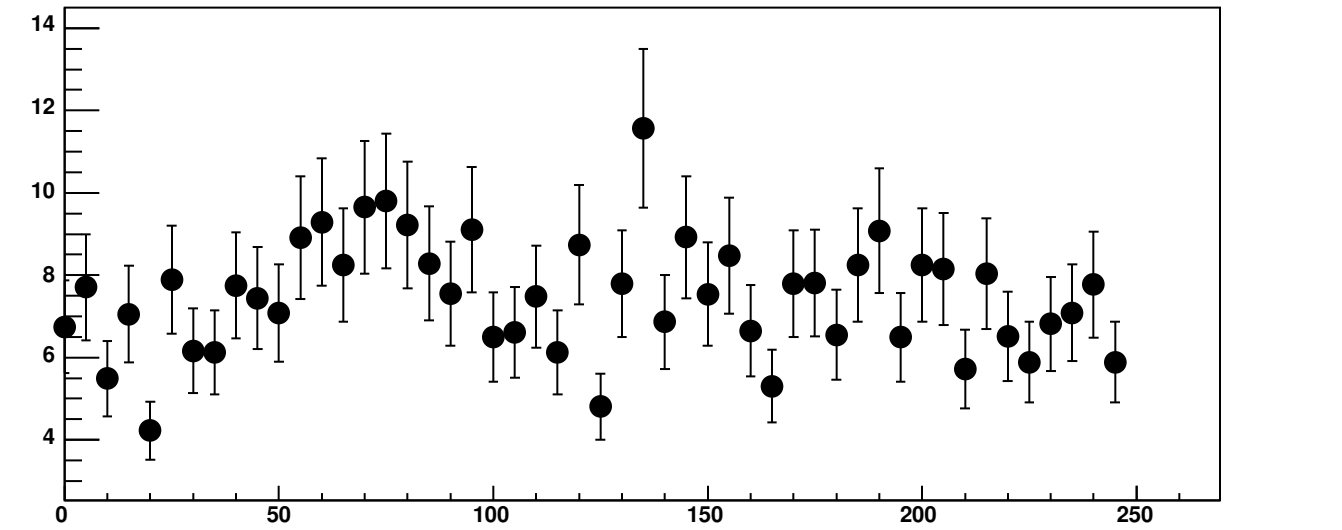
Chip 1, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold



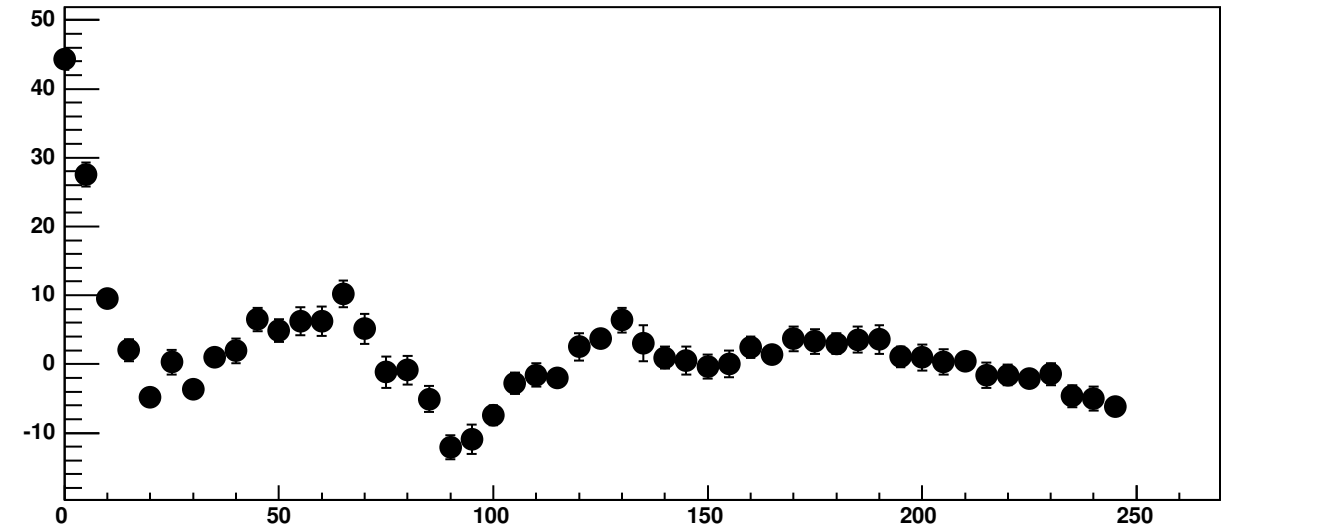
Chip 1, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

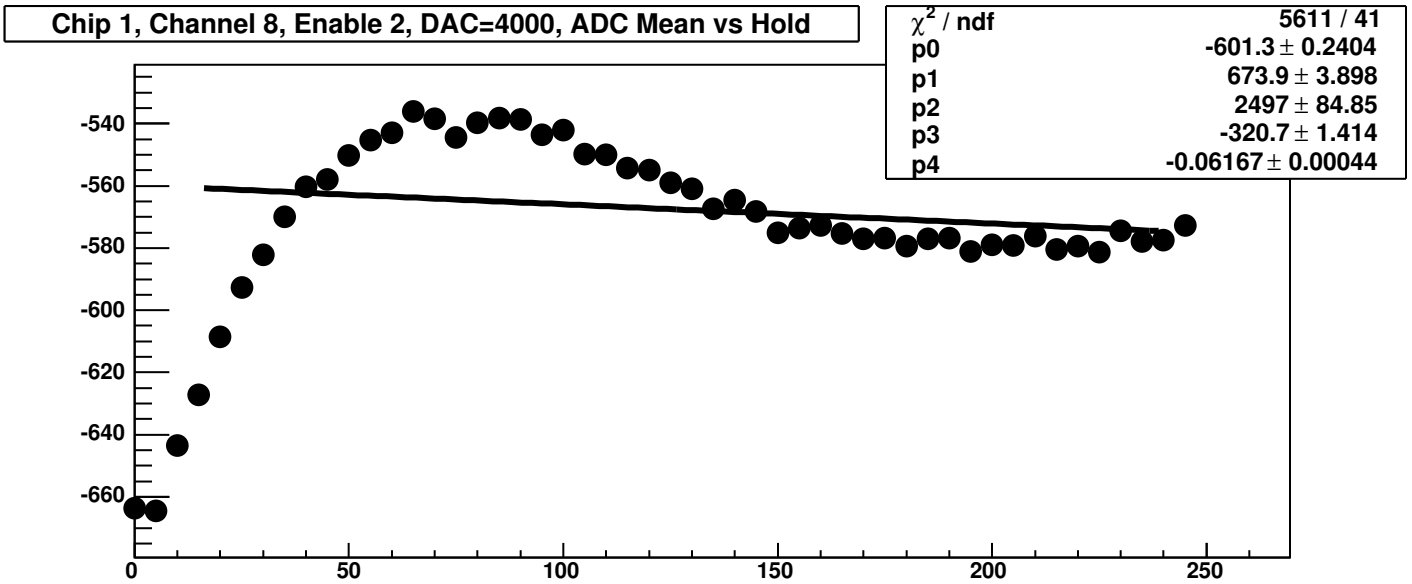


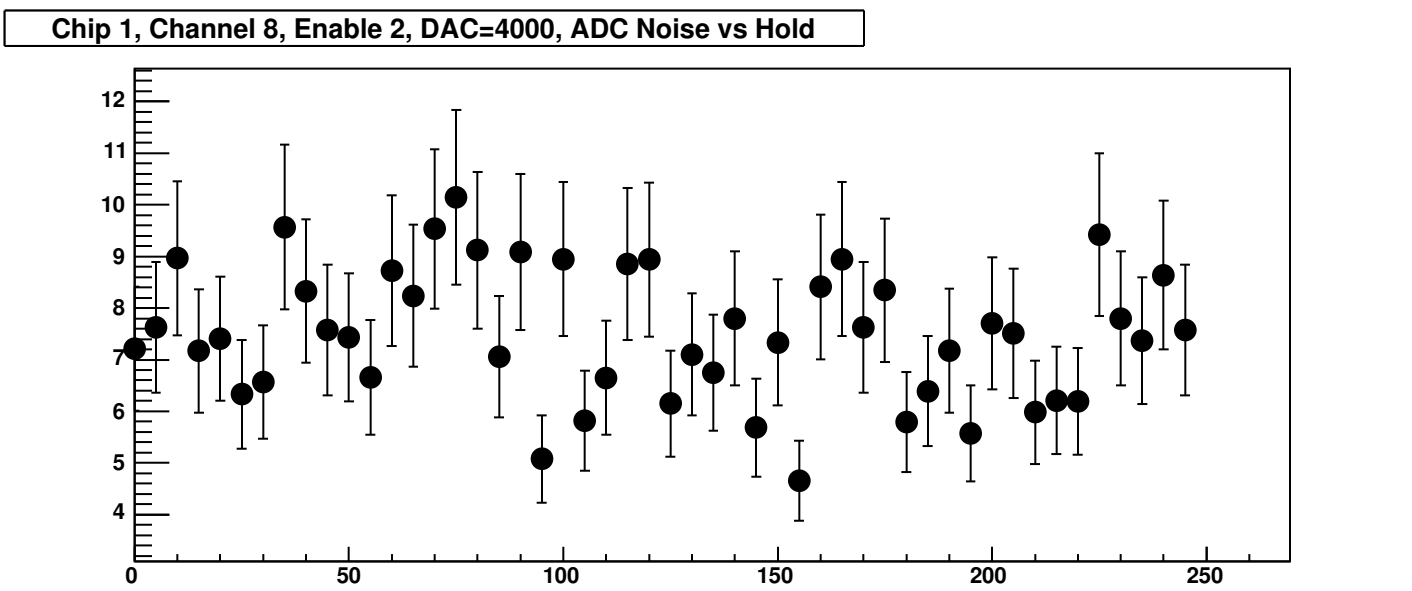
Chip 1, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold

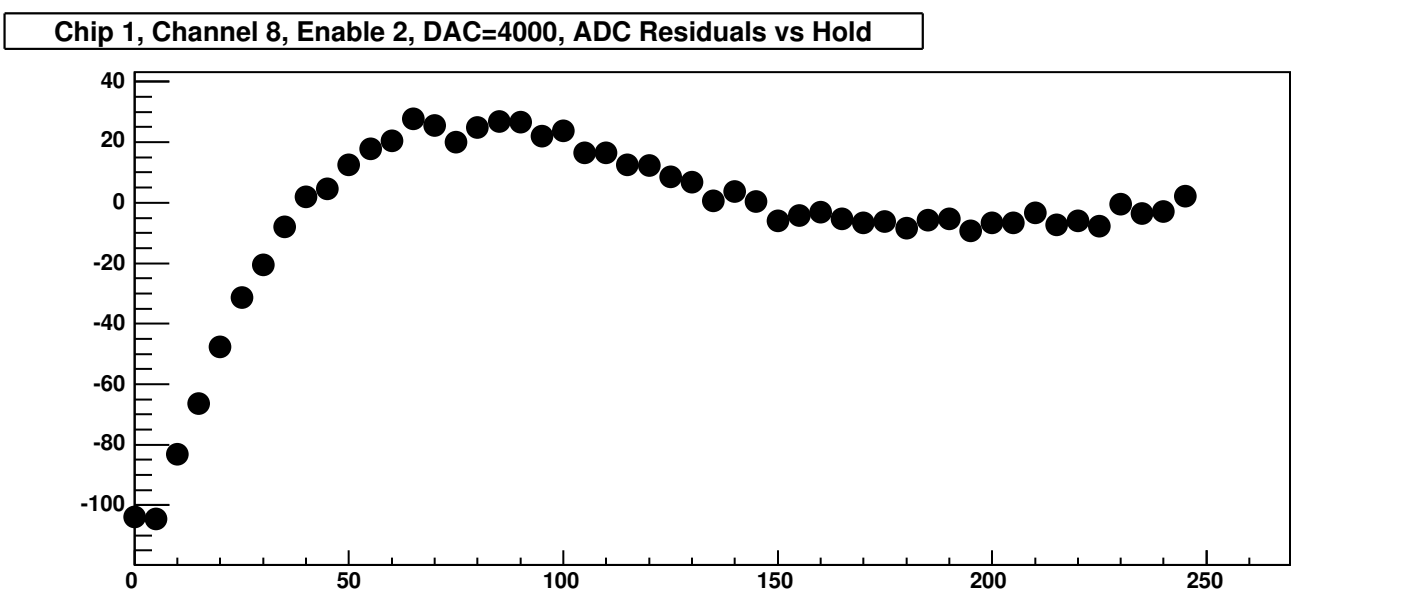


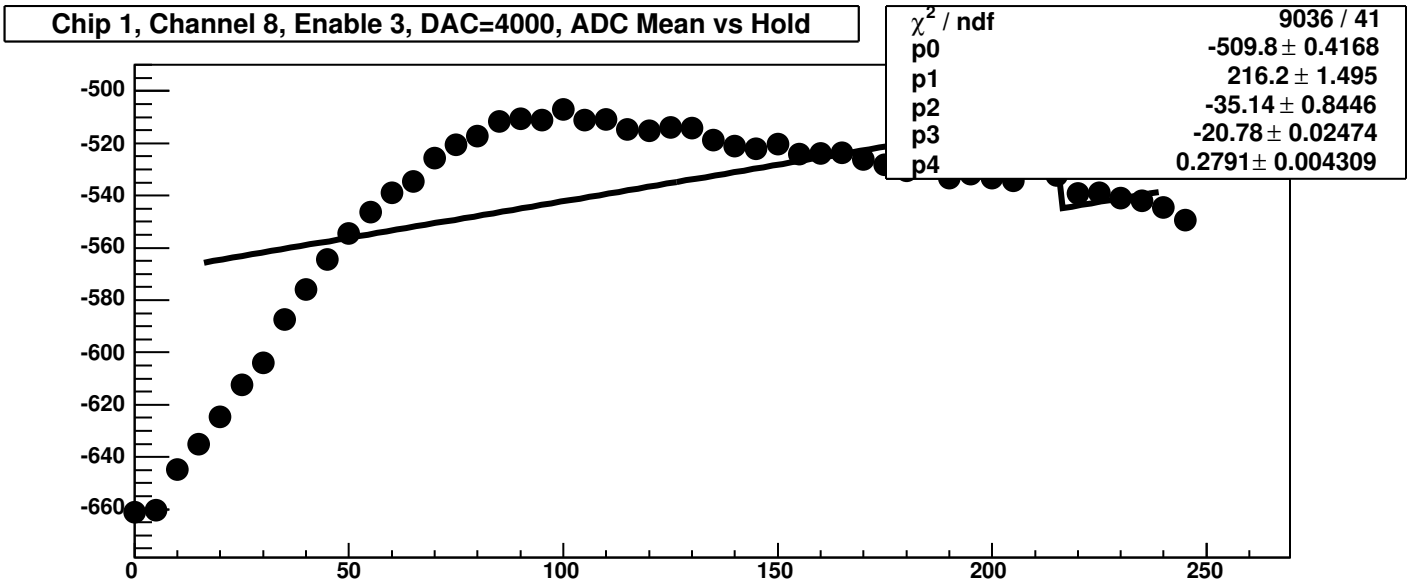
Chip 1, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

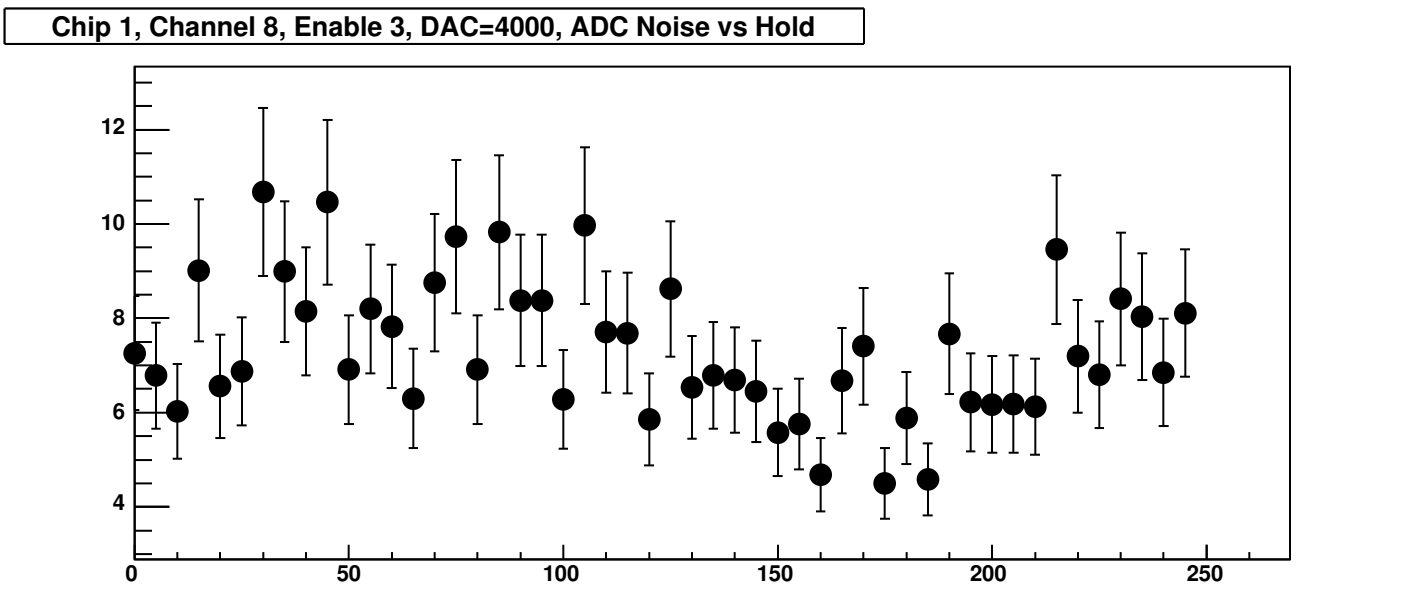


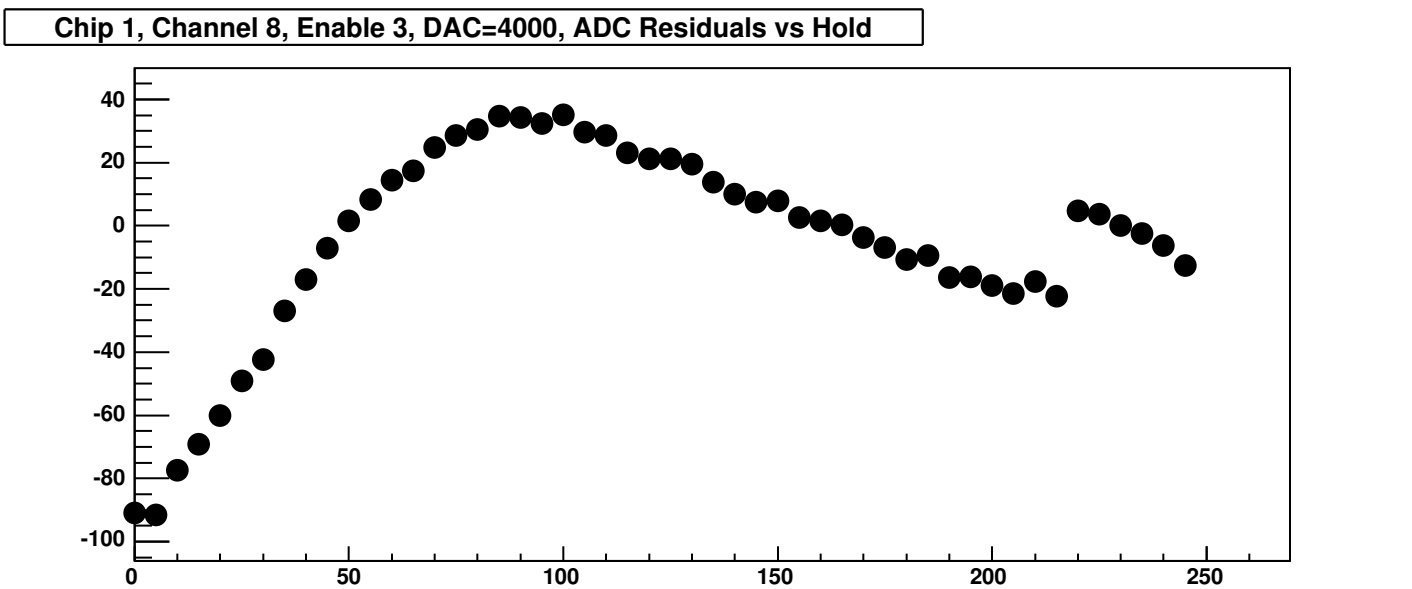

Chip 1, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 5611 / 41
p0 -601.3 ± 0.2404
p1 673.9 ± 3.898
p2 2497 ± 84.85
p3 -320.7 ± 1.414
p4 -0.06167 ± 0.00044
-540
-560
-580
-600
-620
-640
-660
0
50
100
150
200
250

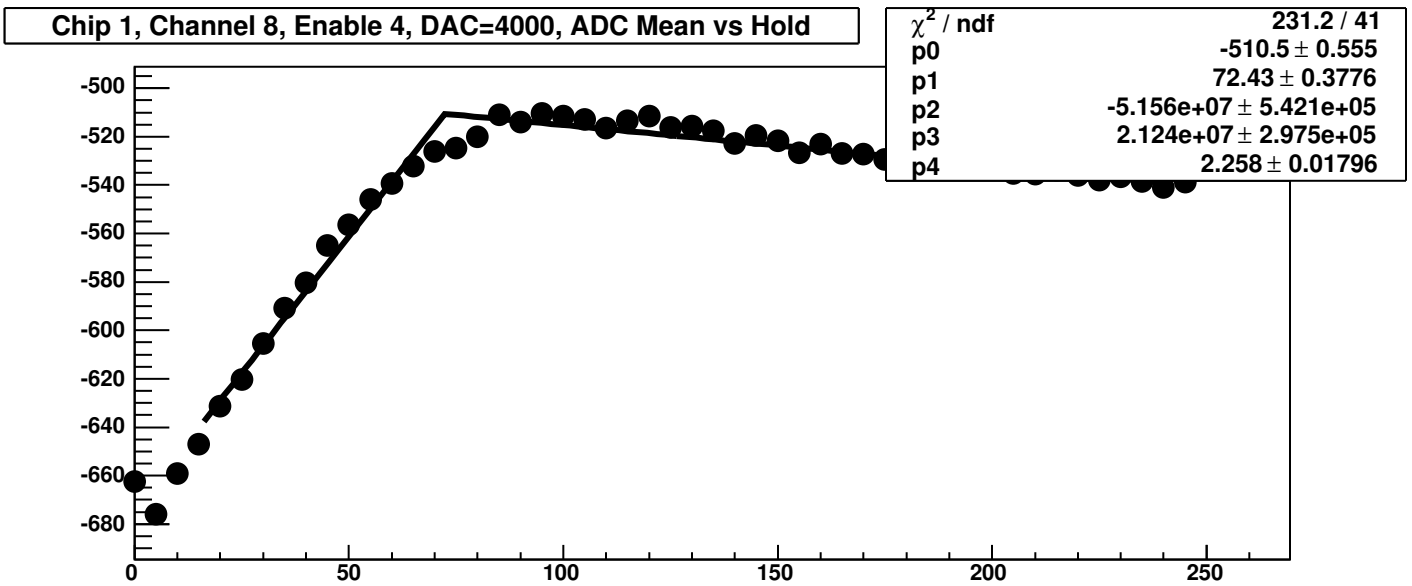

Chip 1, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

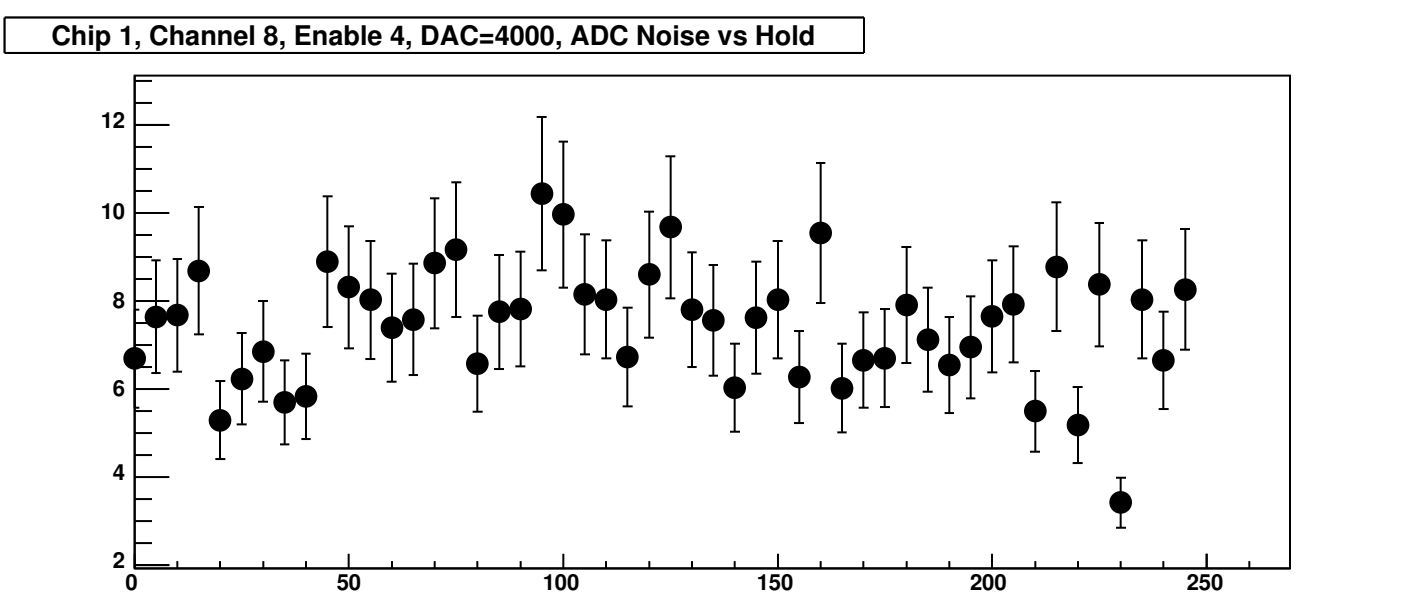

Chip 1, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
-100
0
50
100
150
200
250

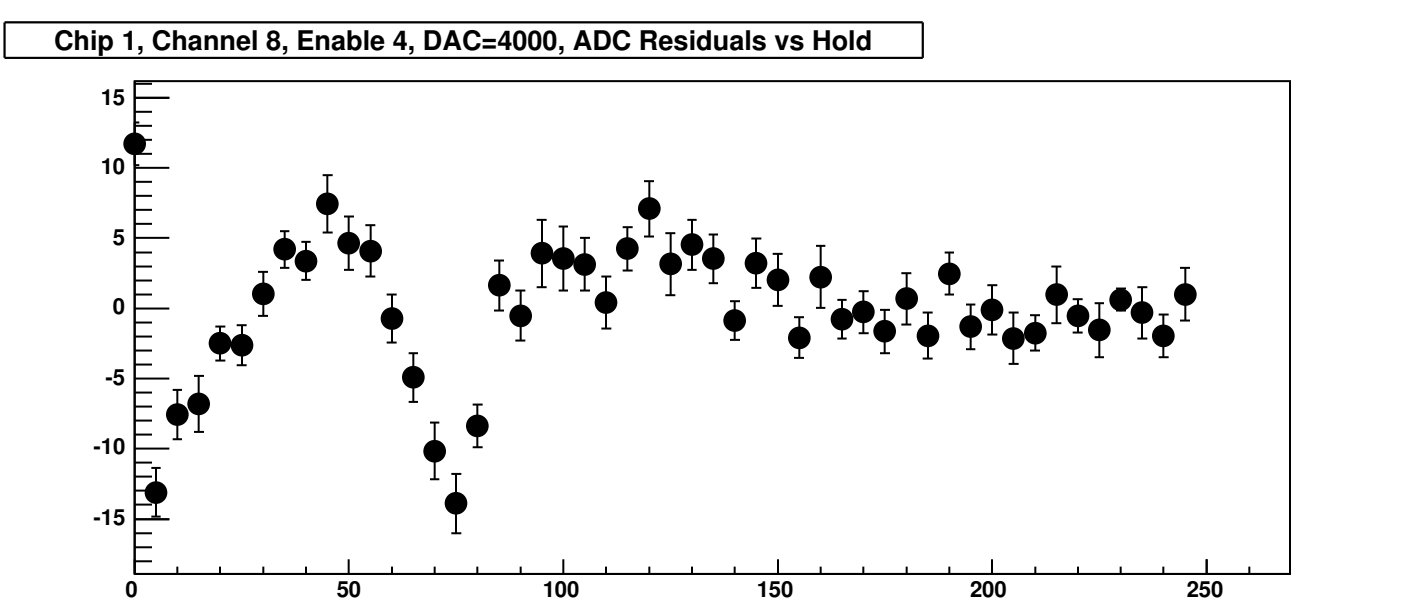

Chip 1, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 9036 / 41
p0 -509.8 ± 0.4168
p1 216.2 ± 1.495
p2 -35.14 ± 0.8446
p3 -20.78 ± 0.02474
p4 0.2791 ± 0.004309
-500
-520
-540
-560
-580
-600
-620
-640
-660
0
50
100
150
200
250


Chip 1, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 1, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
-100
0
50
100
150
200
250

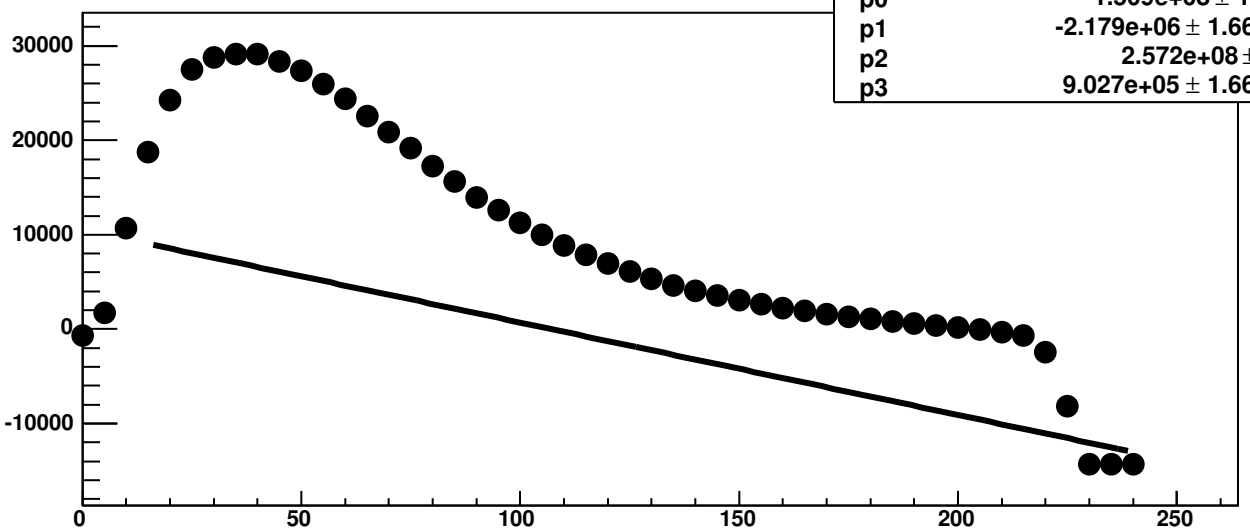

Chip 1, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 231.2 / 41
p0 -510.5 ± 0.555
p1 72.43 ± 0.3776
p2 -5.156e+07 ± 5.421e+05
p3 2.124e+07 ± 2.975e+05
p4 2.258 ± 0.01796
-500
-520
-540
-560
-580
-600
-620
-640
-660
-680
0
50
100
150
200
250


Chip 1, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
2
0
50
100
150
200
250

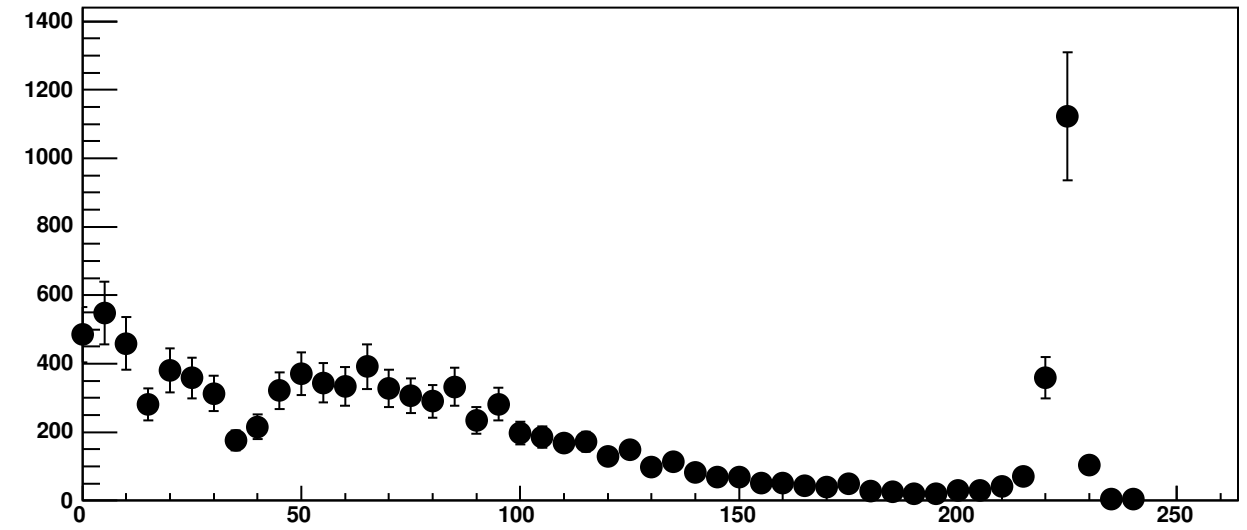

Chip 1, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

Chip 1, Channel 8, Enable 5!, DAC=4000, ADC Mean vs Hold

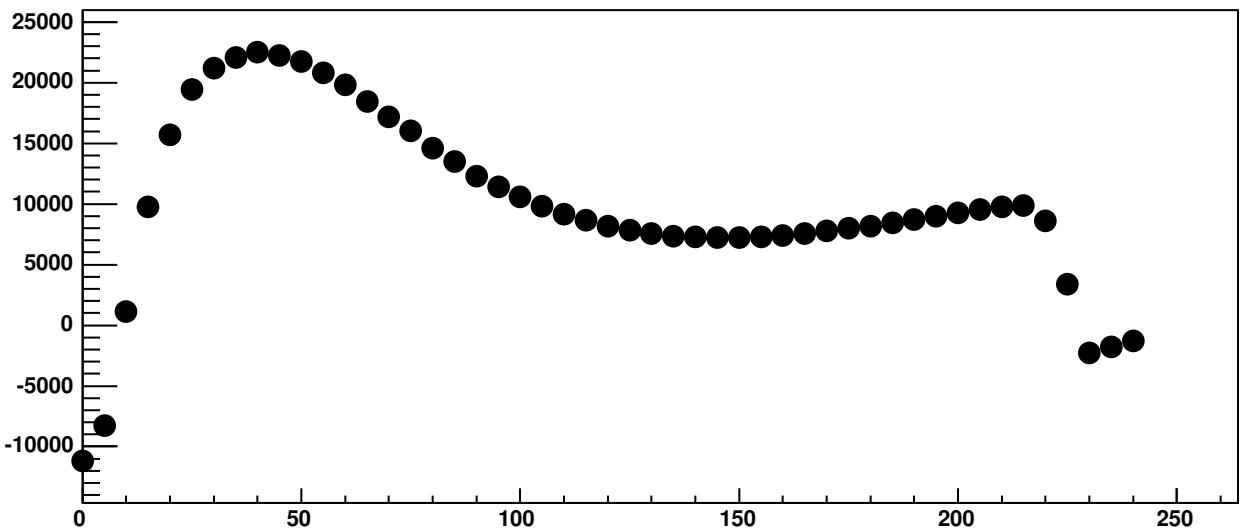
χ^2 / ndf	2.463e+07 / 42
p0	-1.509e+08 ± 1.6e+05
p1	-2.179e+06 ± 1.666e+05
p2	2.572e+08 ± 36.53
p3	9.027e+05 ± 1.666e+05

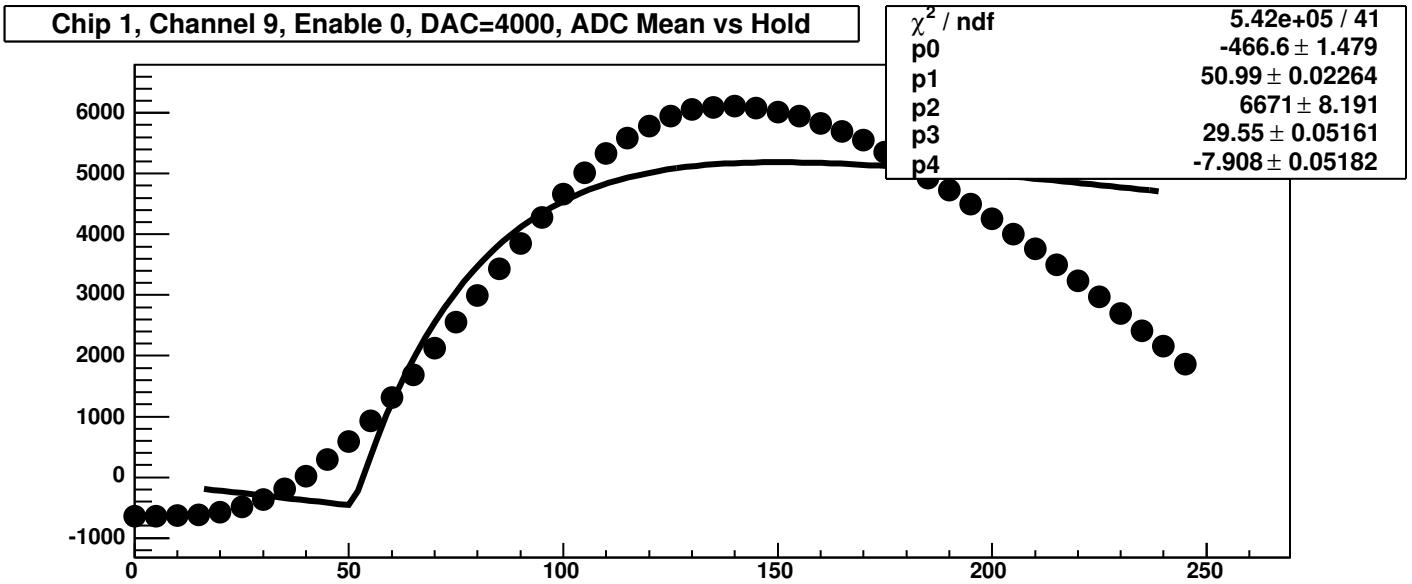


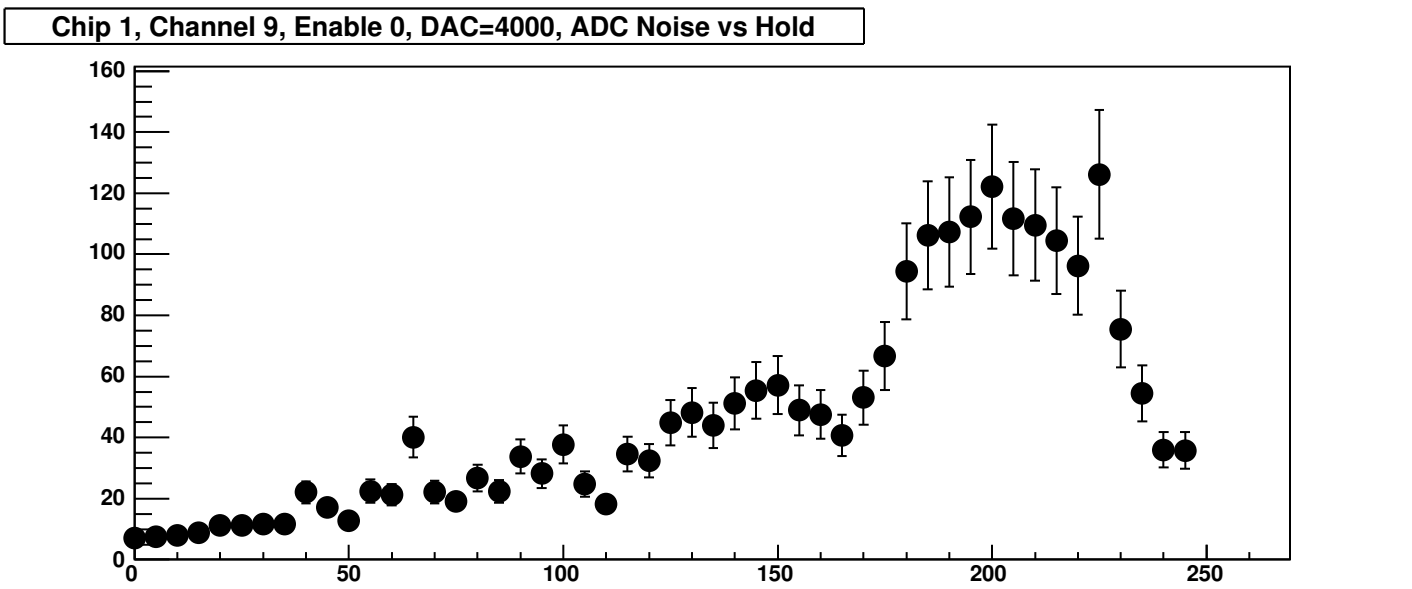
Chip 1, Channel 8, Enable 5!, DAC=4000, ADC Noise vs Hold

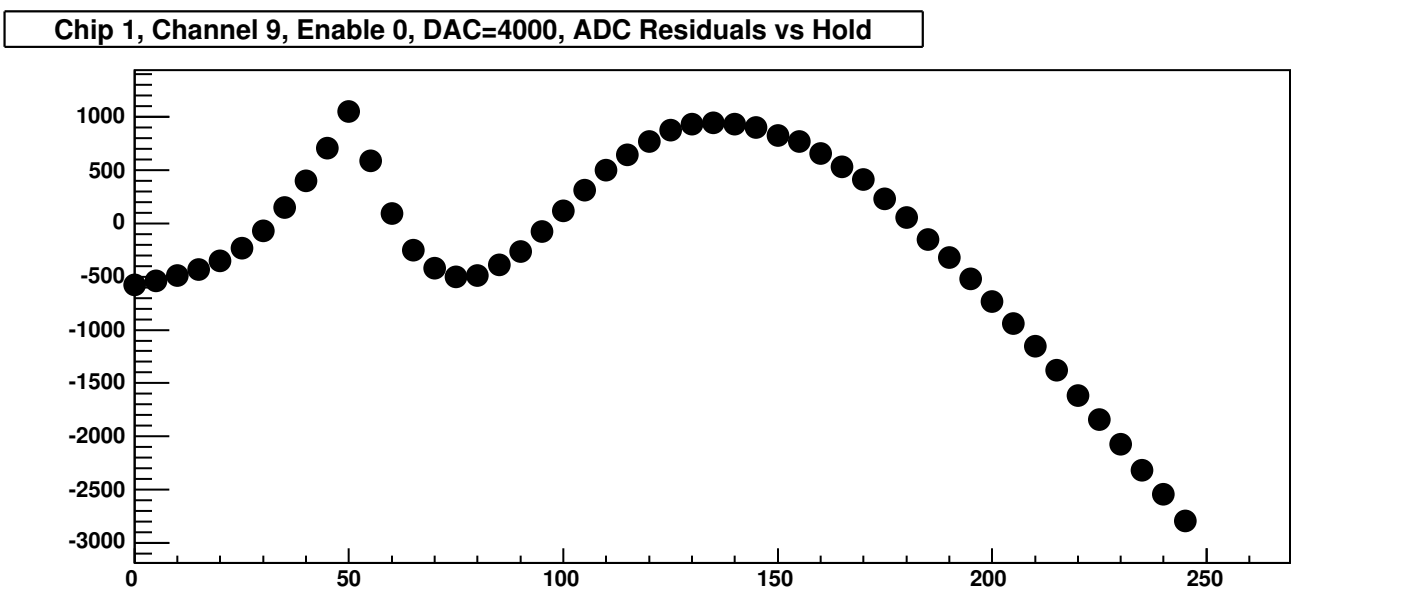


Chip 1, Channel 8, Enable 5!, DAC=4000, ADC Residuals vs Hold

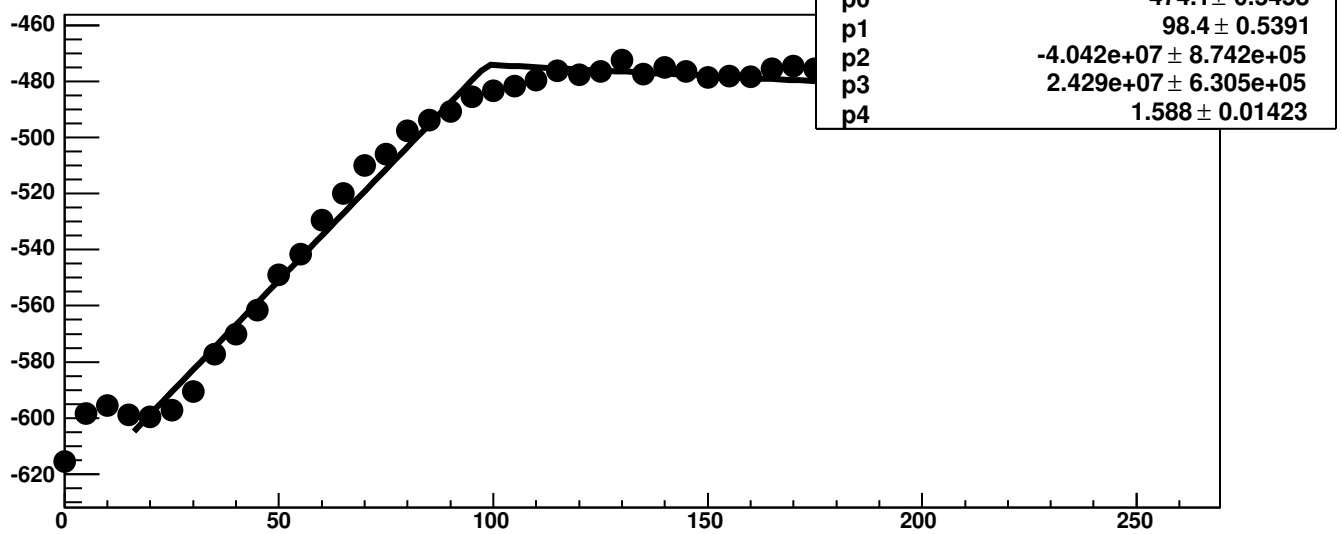



Chip 1, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.42e+05 / 41
p0 -466.6 ± 1.479
p1 50.99 ± 0.02264
p2 6671 ± 8.191
p3 29.55 ± 0.05161
p4 -7.908 ± 0.05182
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

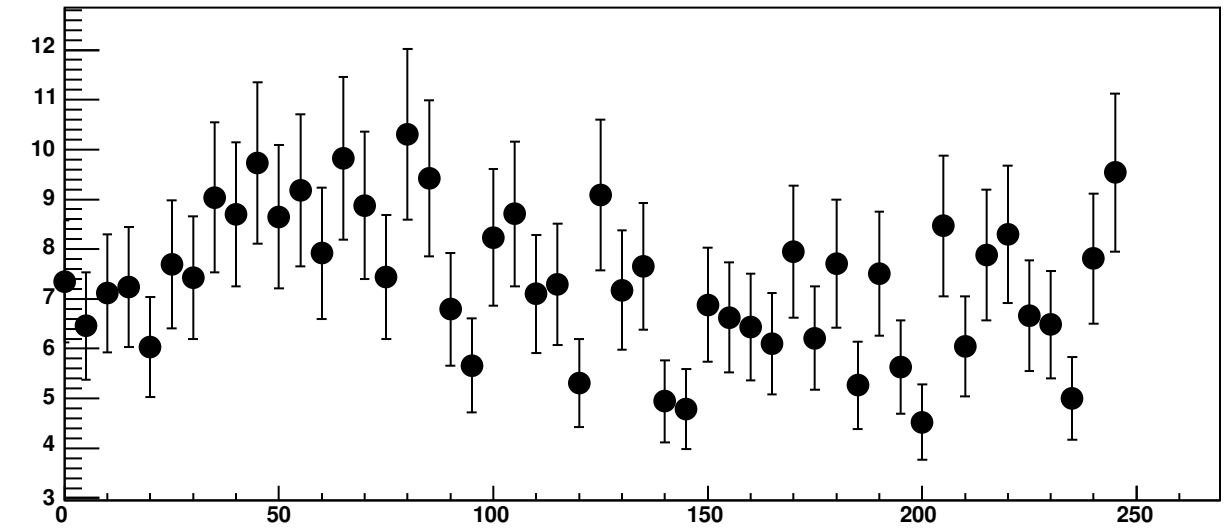

Chip 1, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 1, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250

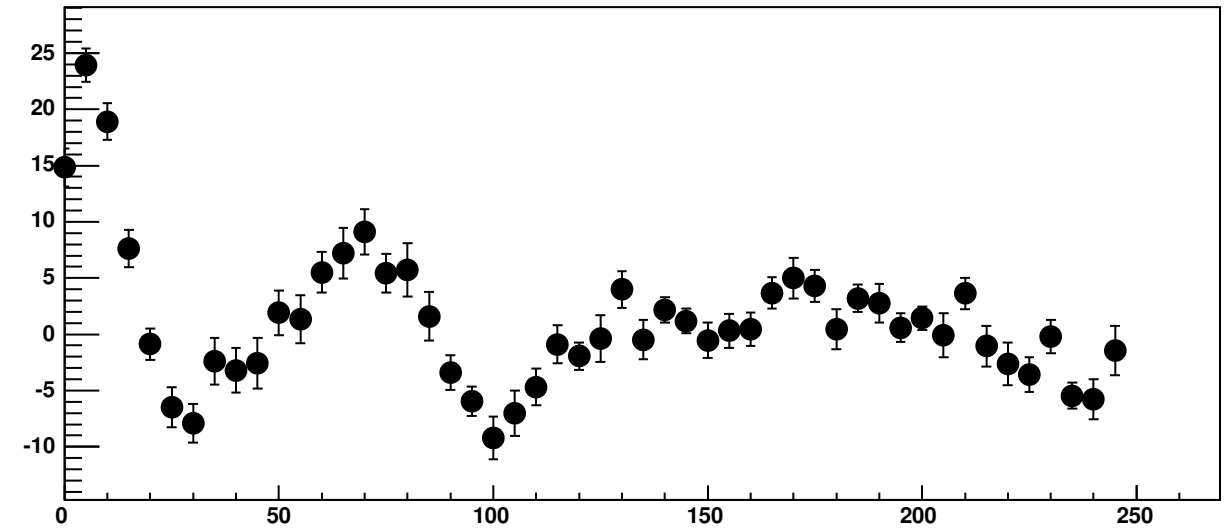
Chip 1, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

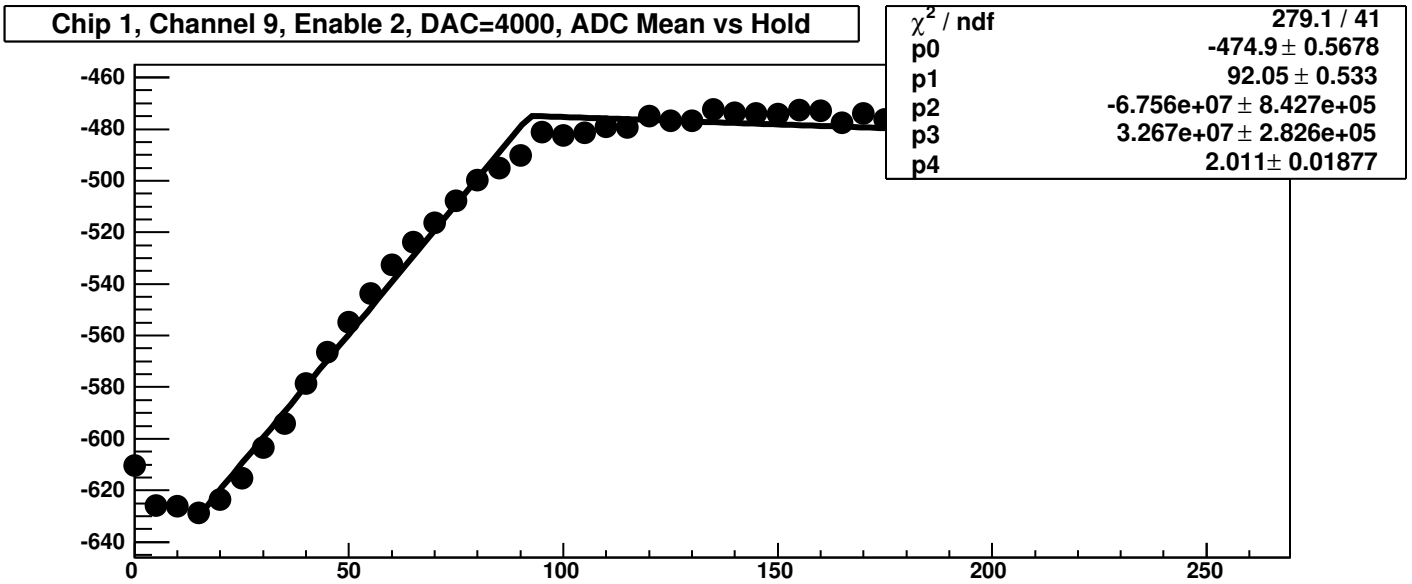


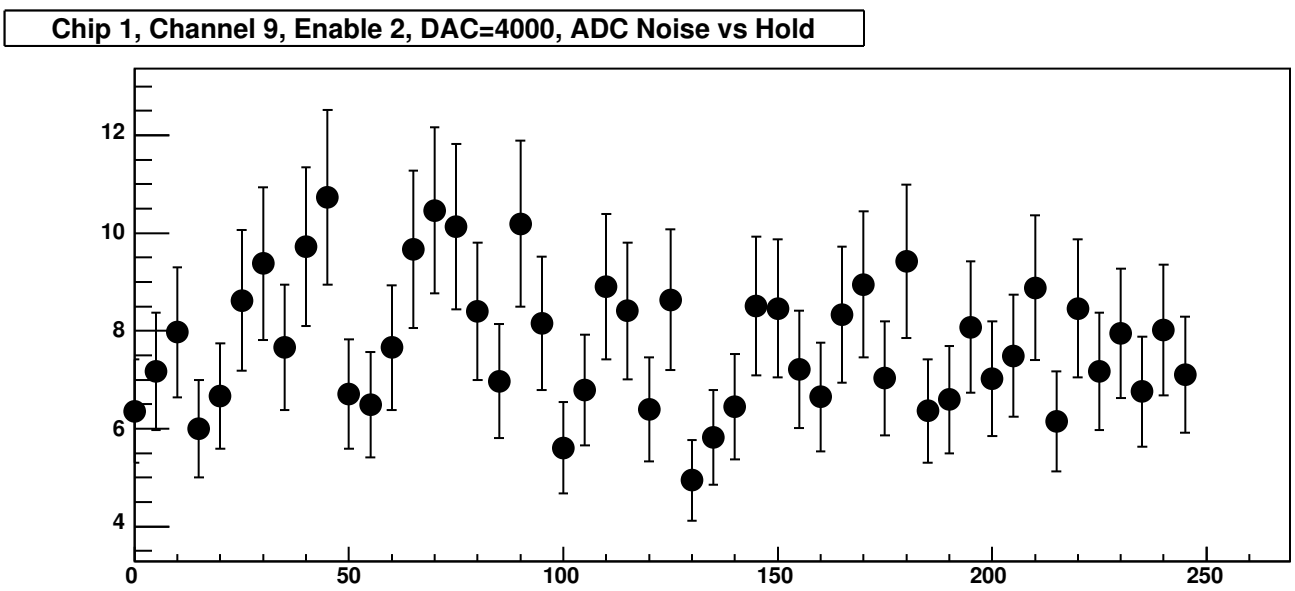
Chip 1, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold

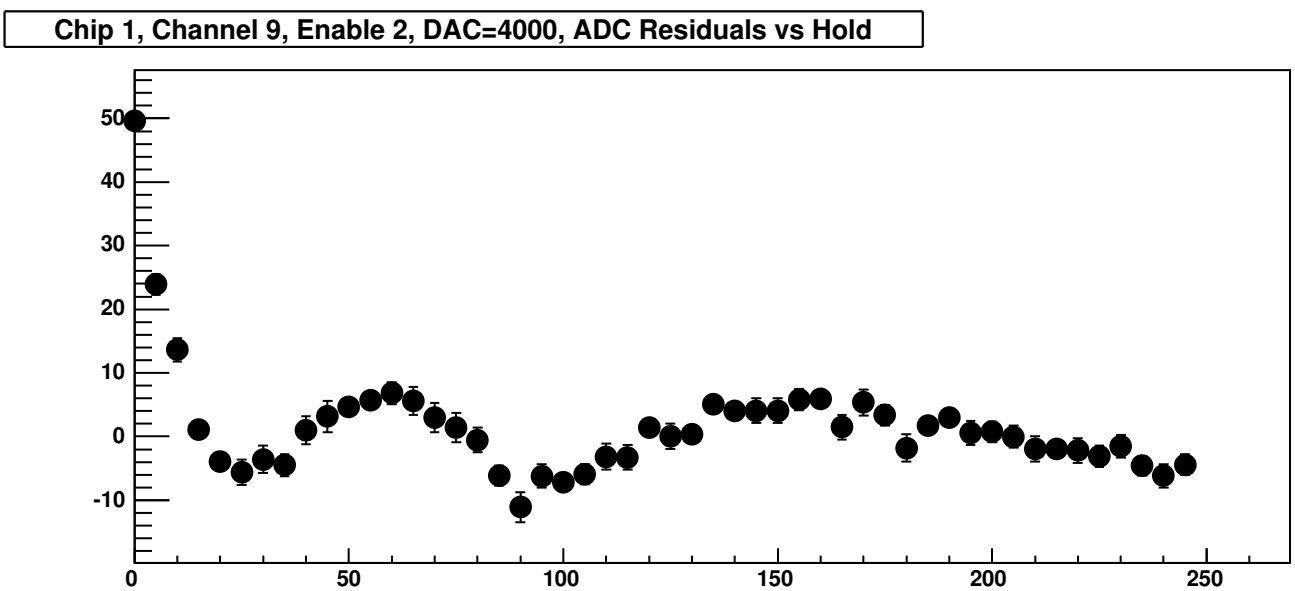


Chip 1, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

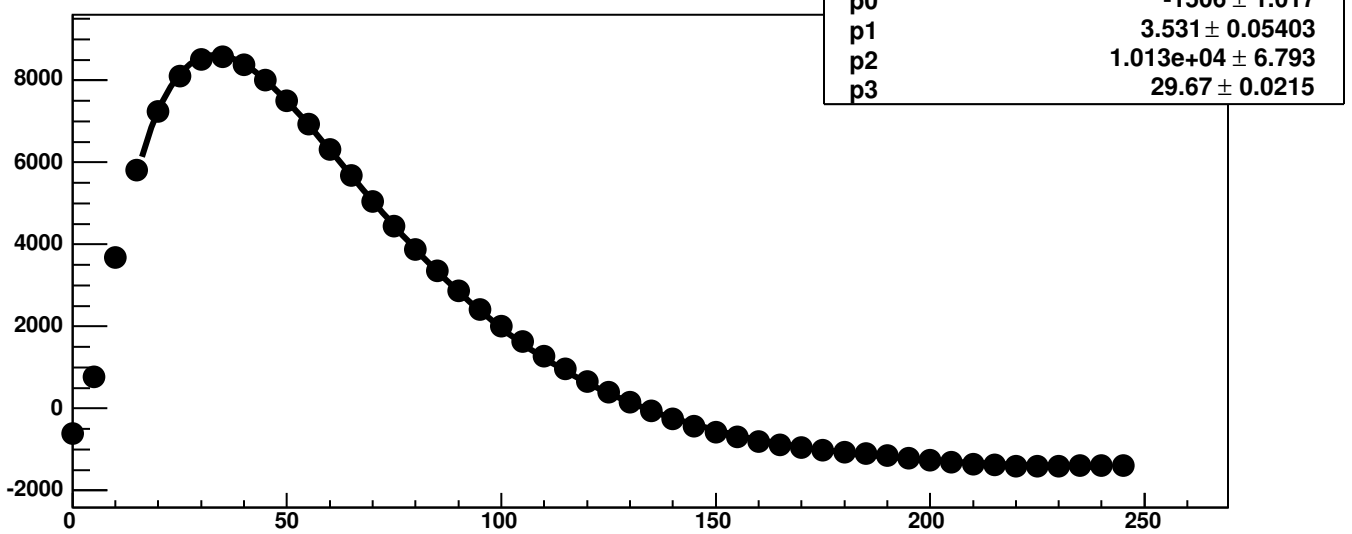



Chip 1, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 279.1 / 41
p0 -474.9 ± 0.5678
p1 92.05 ± 0.533
p2 -6.756e+07 ± 8.427e+05
p3 3.267e+07 ± 2.826e+05
p4 2.011 ± 0.01877
-460
-480
-500
-520
-540
-560
-580
-600
-620
-640
0
50
100
150
200
250

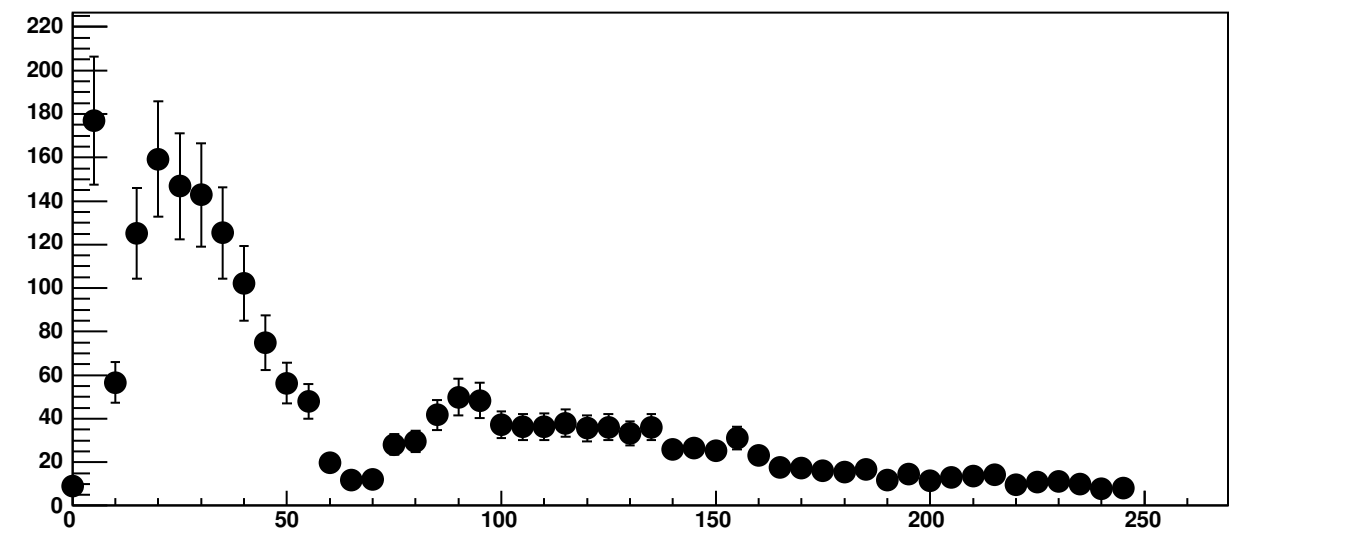

Chip 1, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 1, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250

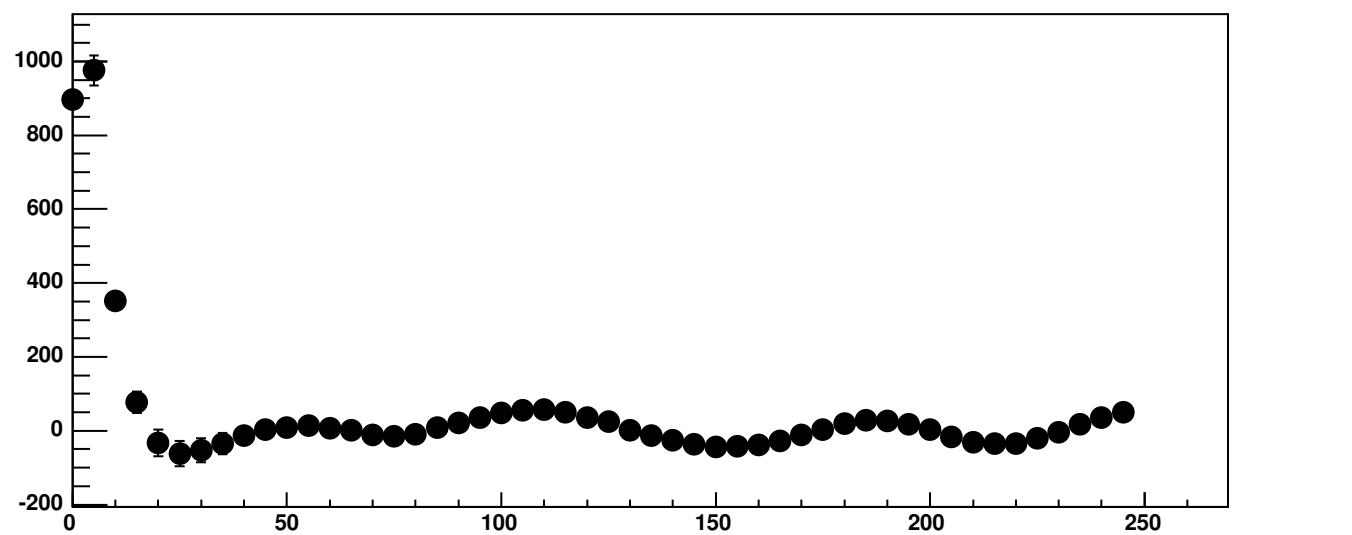
Chip 1, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold

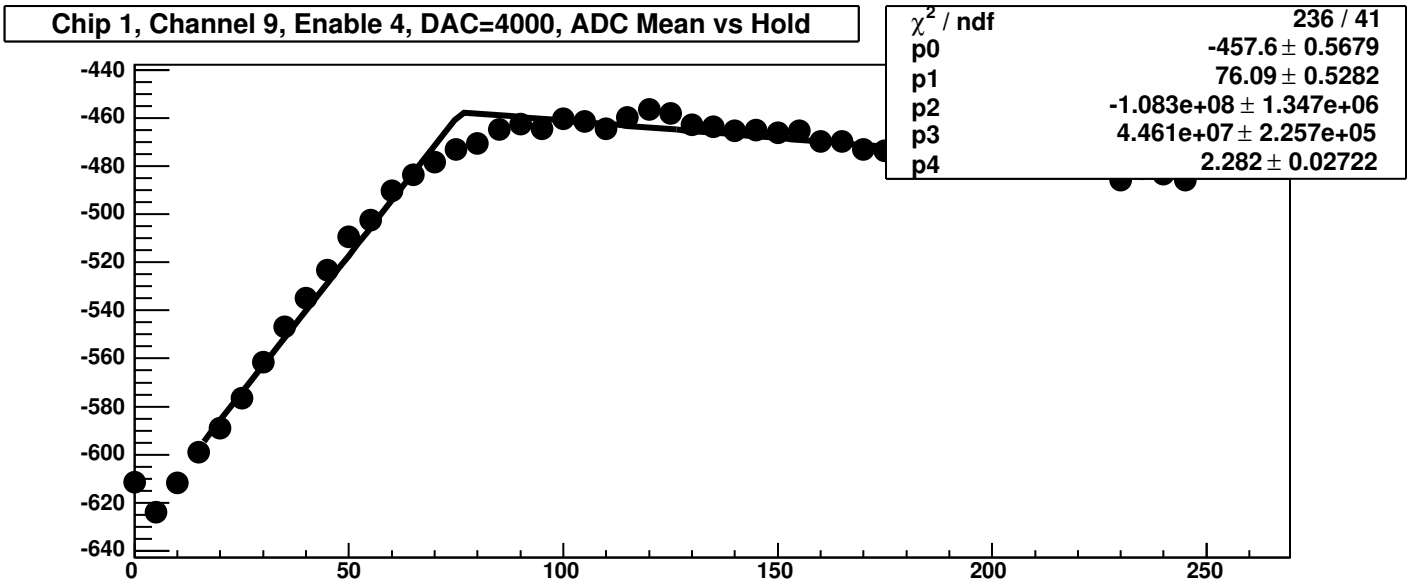


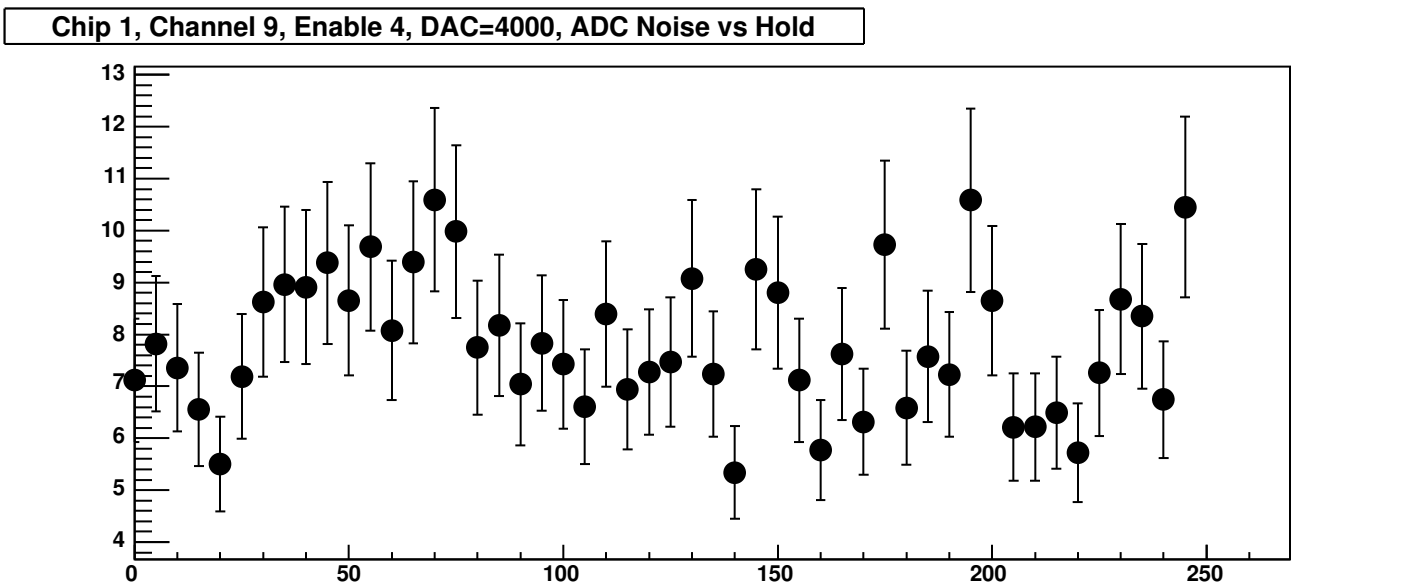
Chip 1, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold

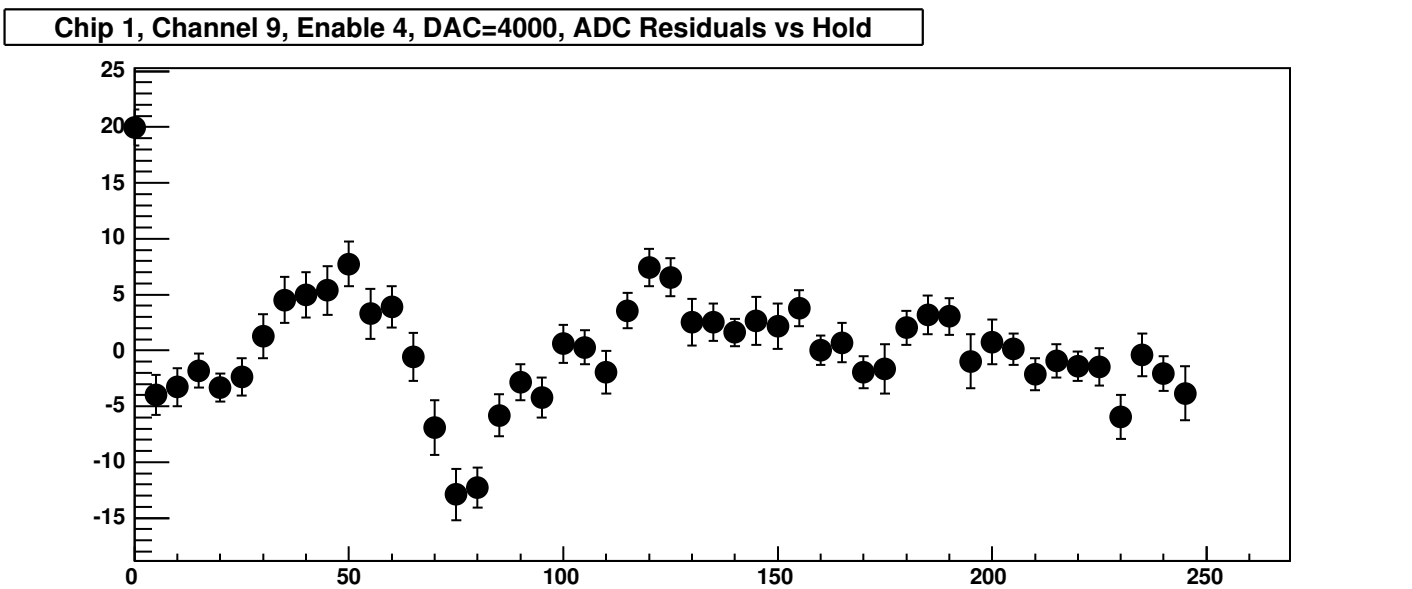


Chip 1, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold

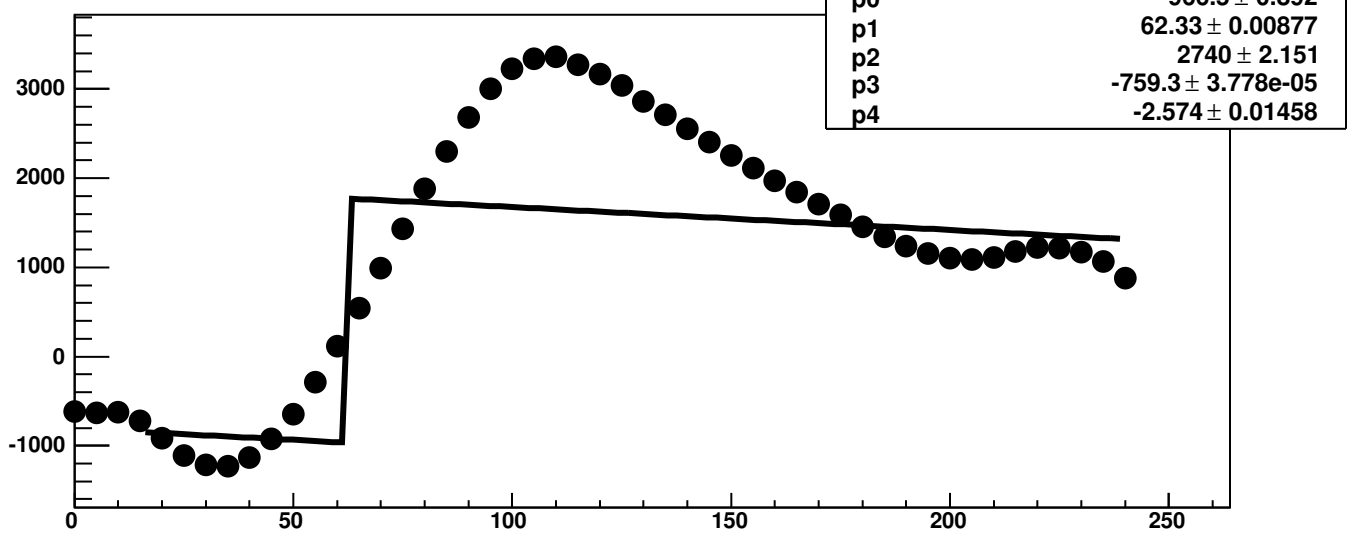



Chip 1, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 236 / 41
p0 -457.6 ± 0.5679
p1 76.09 ± 0.5282
p2 -1.083e+08 ± 1.347e+06
p3 4.461e+07 ± 2.257e+05
p4 2.282 ± 0.02722
-440
-460
-480
-500
-520
-540
-560
-580
-600
-620
-640
0
50
100
150
200
250

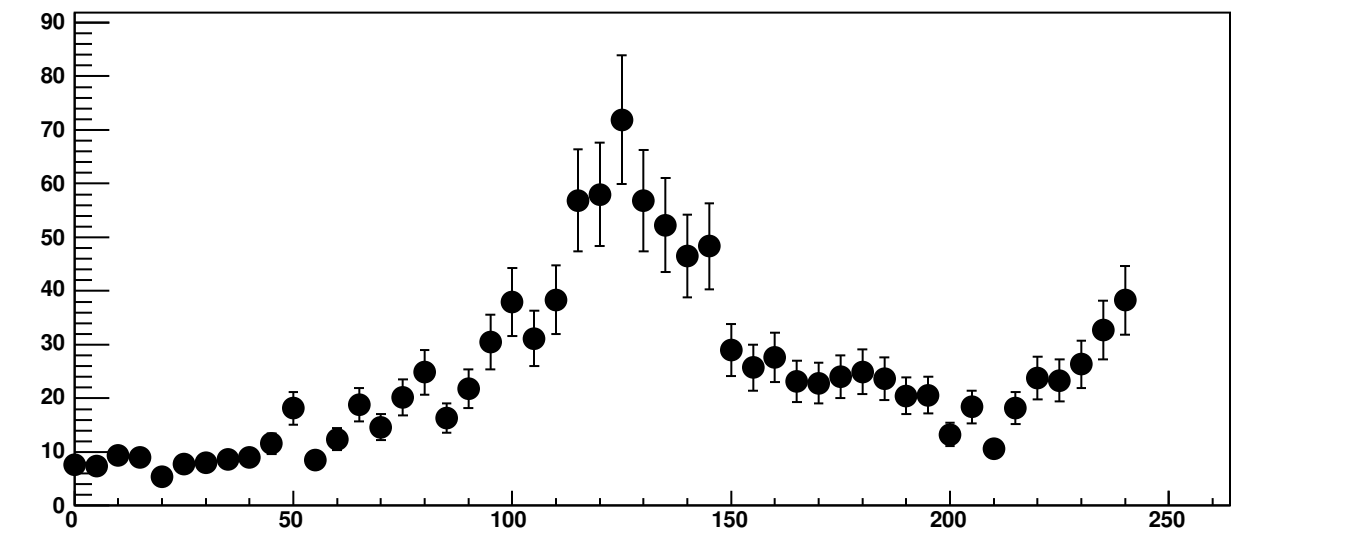

Chip 1, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 1, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

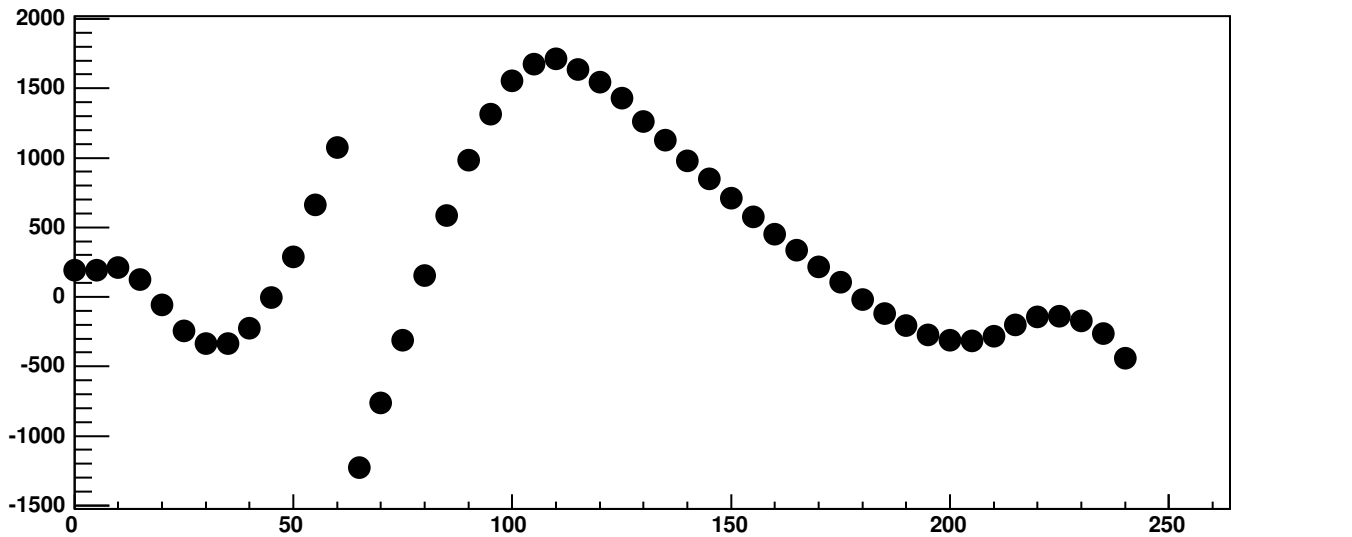
Chip 1, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold



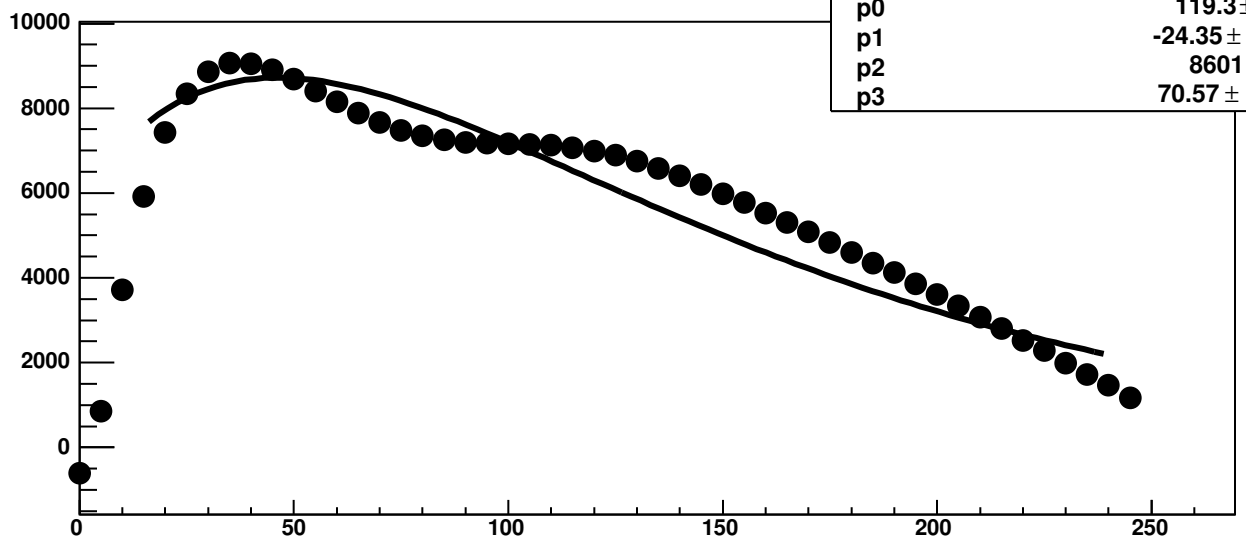
Chip 1, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



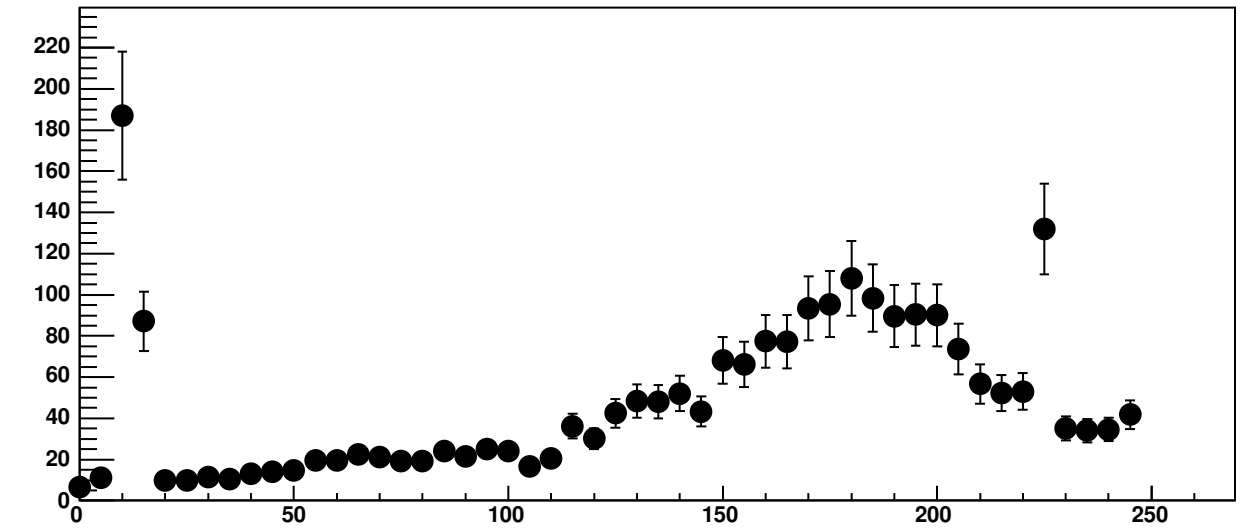
Chip 1, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold



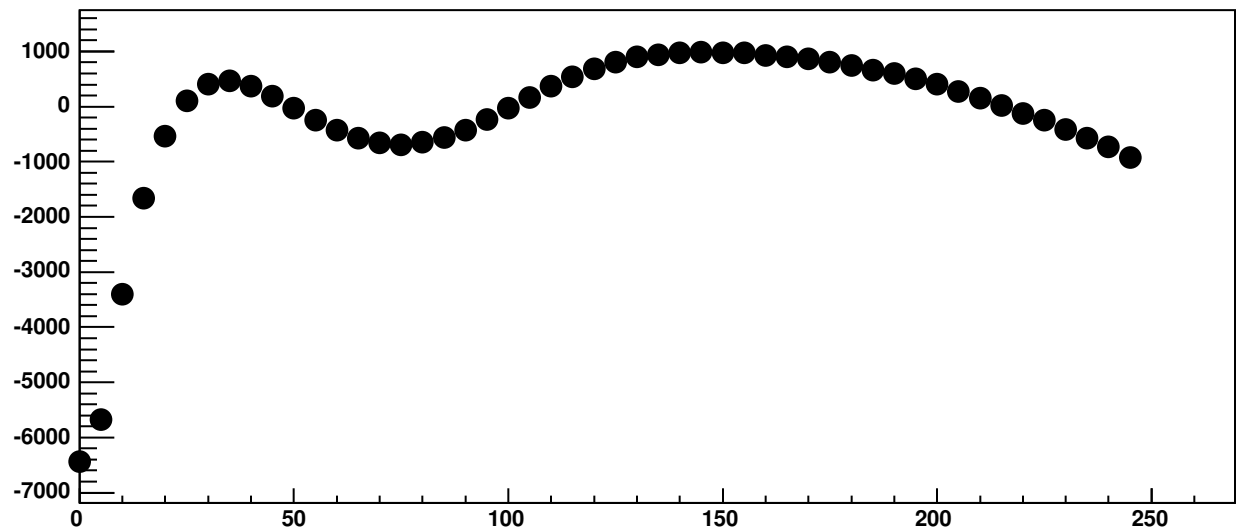
Chip 1, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold



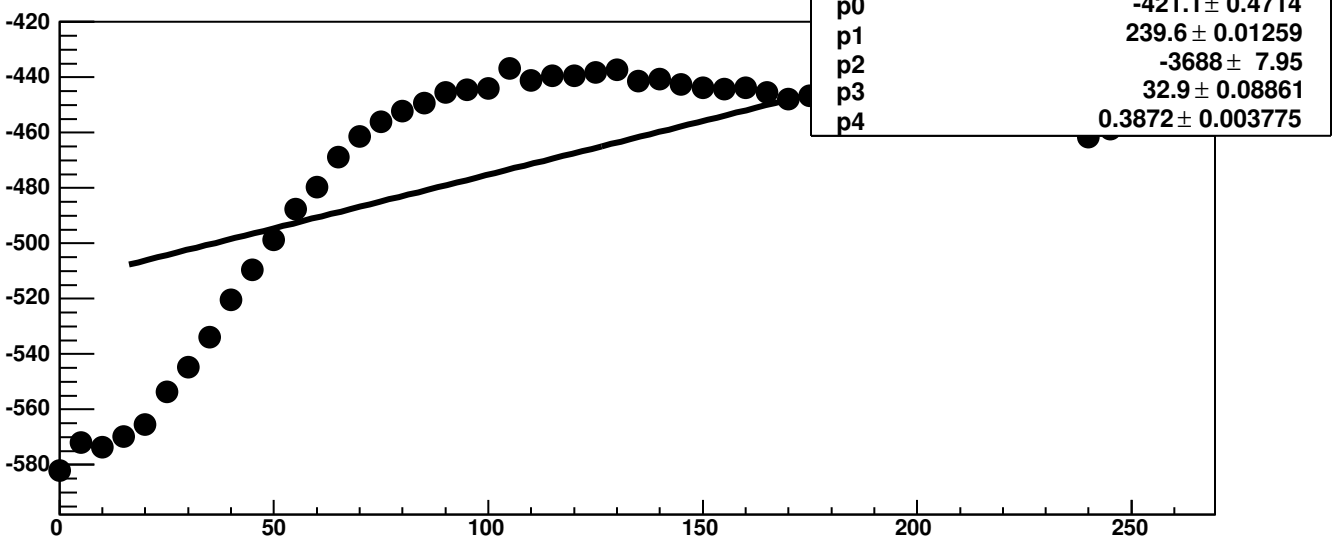
Chip 1, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold



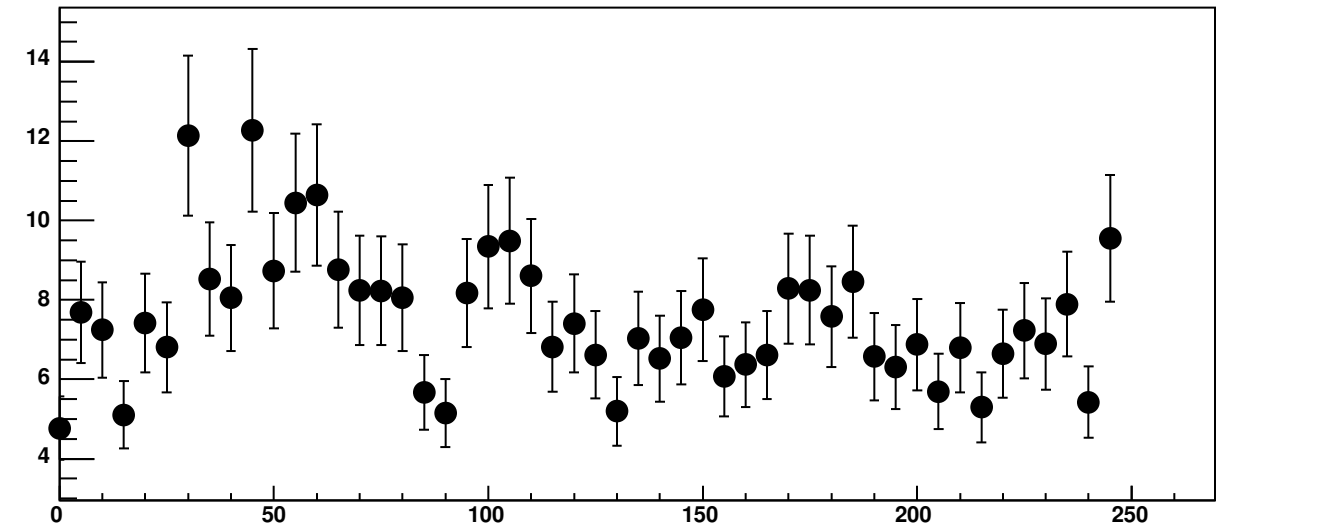
Chip 1, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold



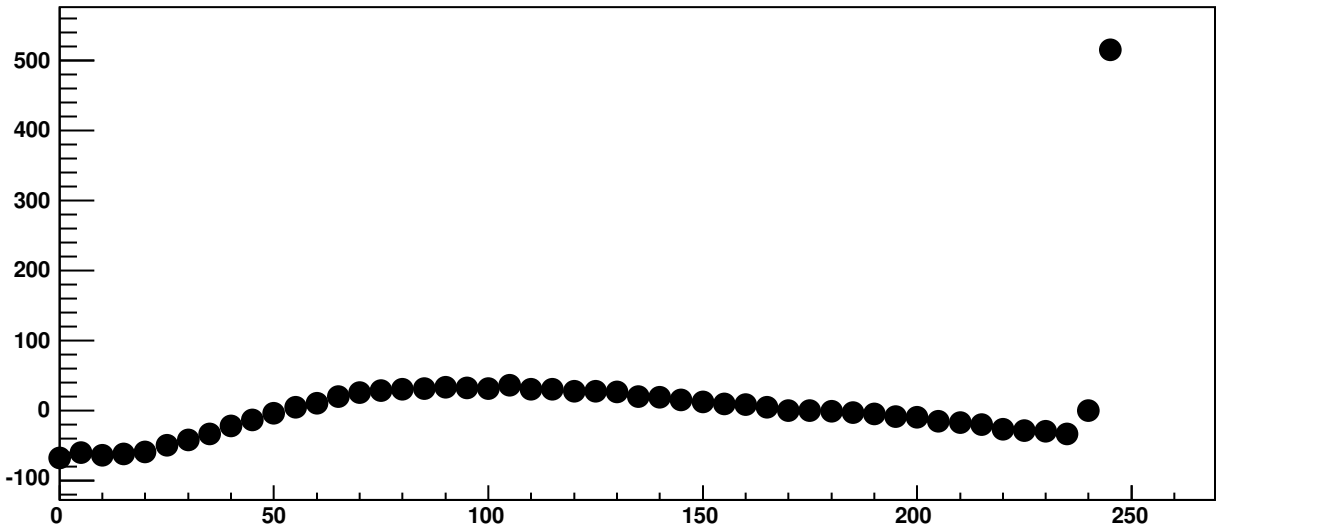
Chip 1, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold

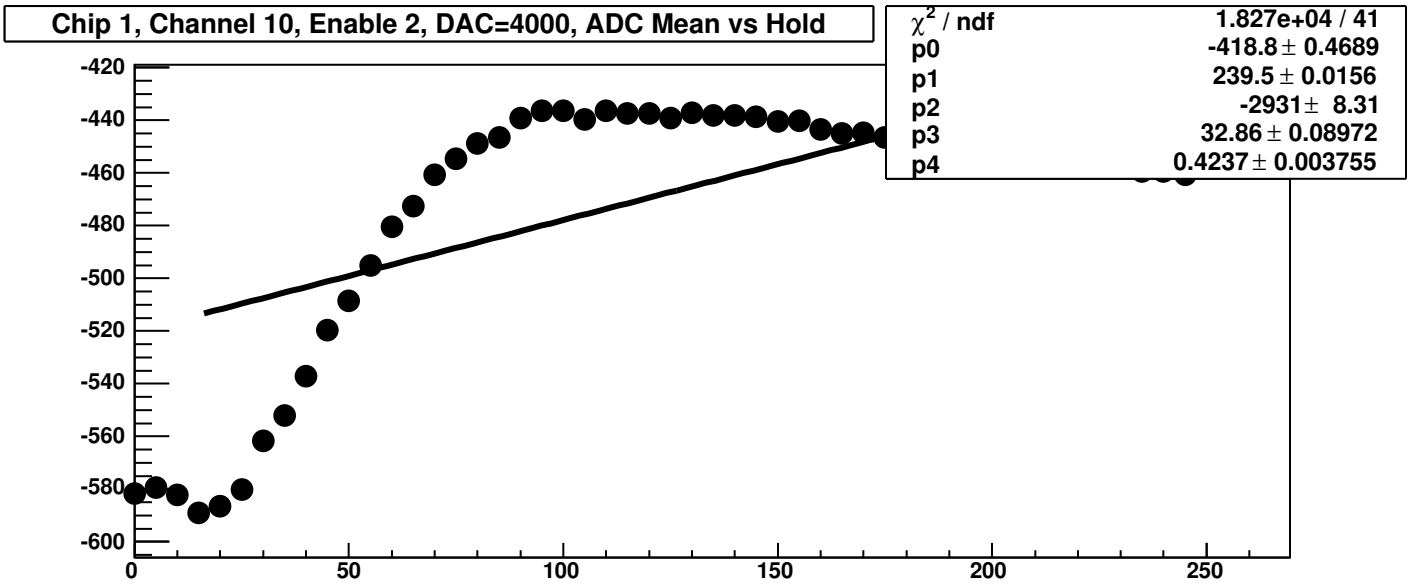


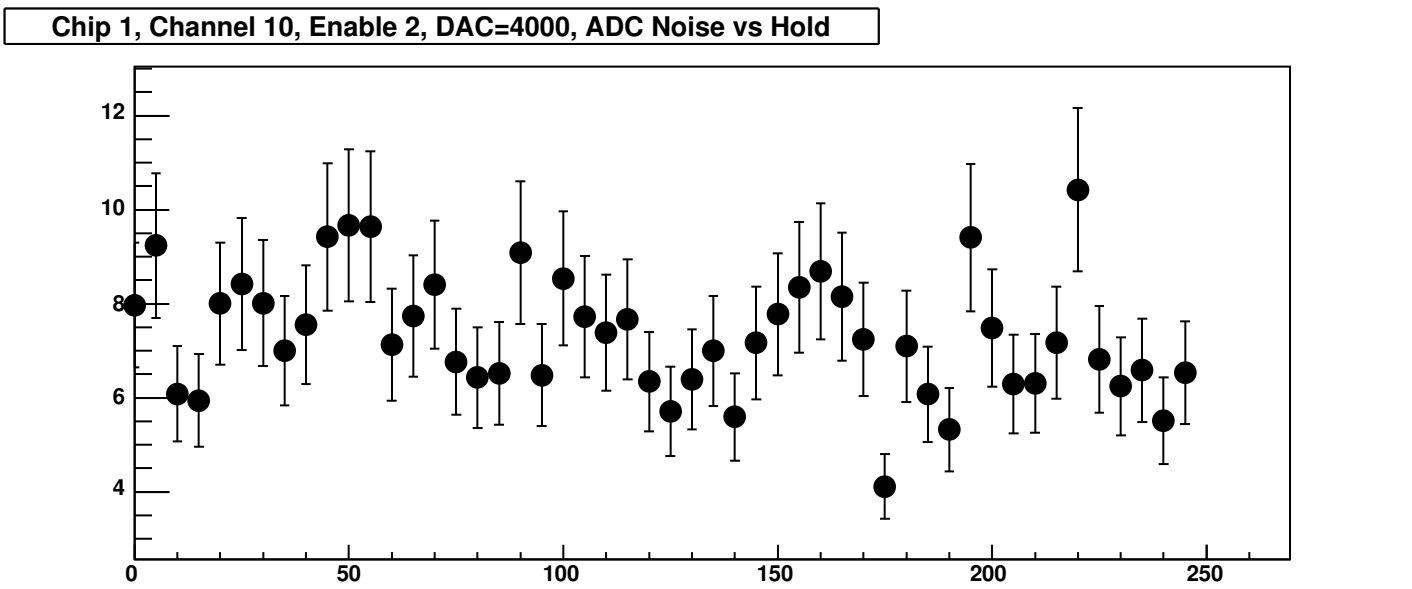
Chip 1, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold

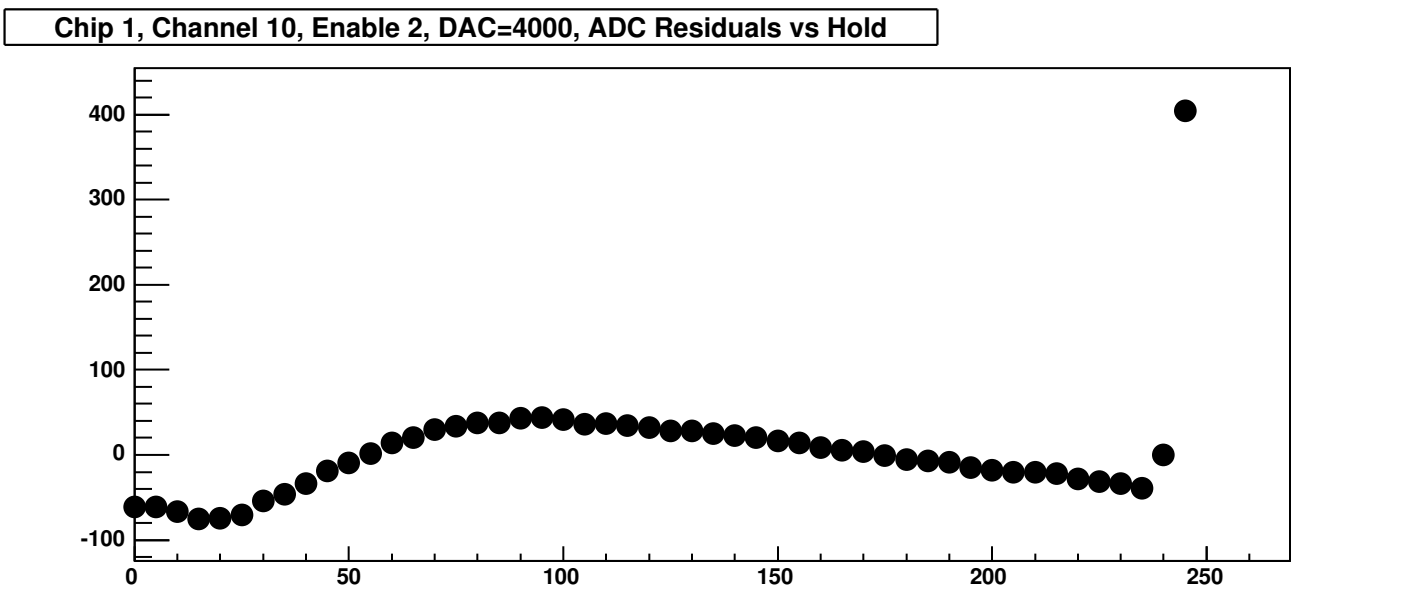


Chip 1, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

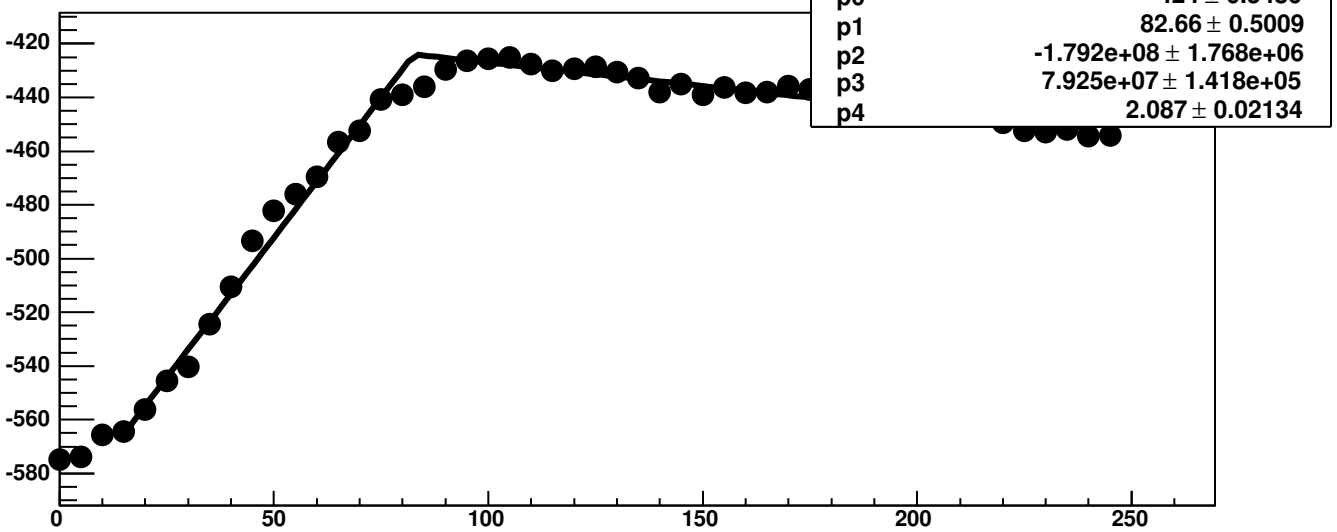



Chip 1, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.827e+04 / 41
p0 -418.8 ± 0.4689
p1 239.5 ± 0.0156
p2 -2931 ± 8.31
p3 32.86 ± 0.08972
p4 0.4237 ± 0.003755
-420
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
50
100
150
200
250

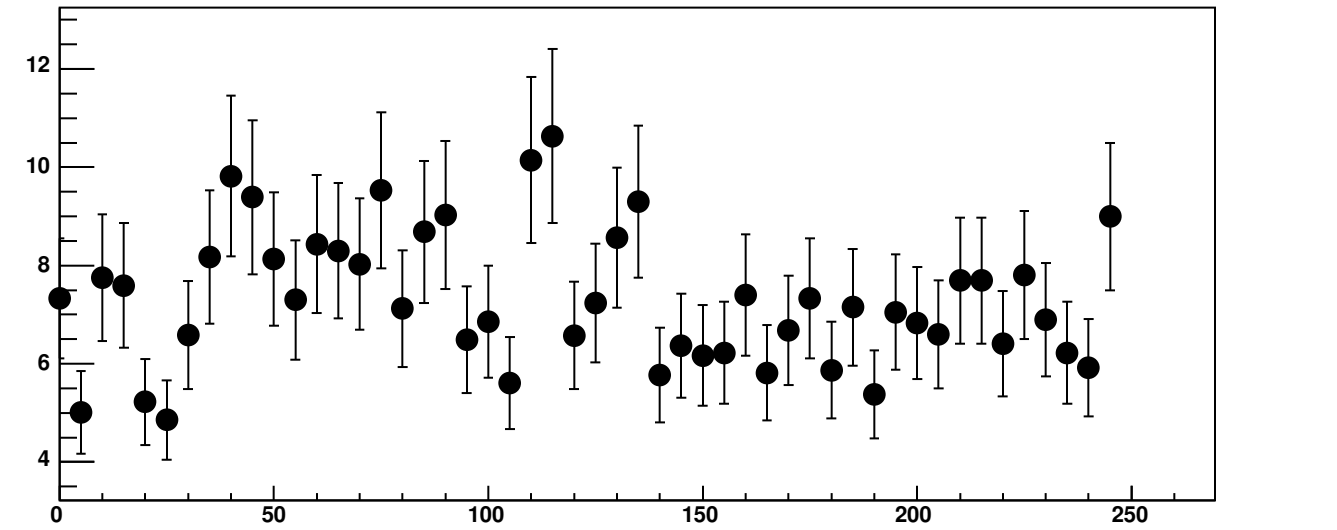

Chip 1, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 1, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold
400
300
200
100
0
-100
0
50
100
150
200
250

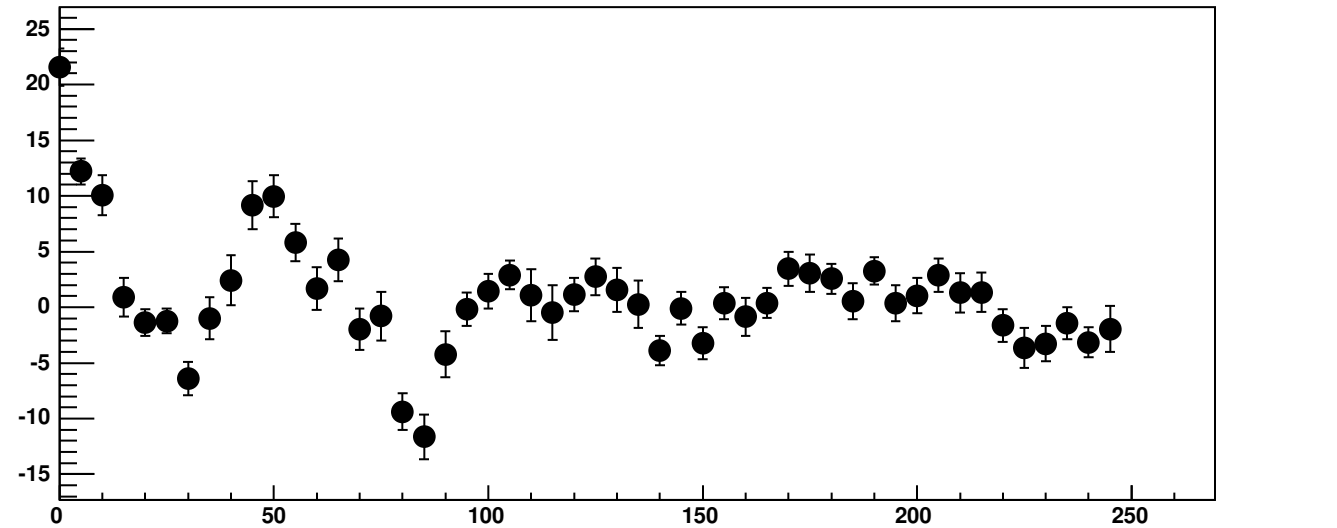
Chip 1, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold



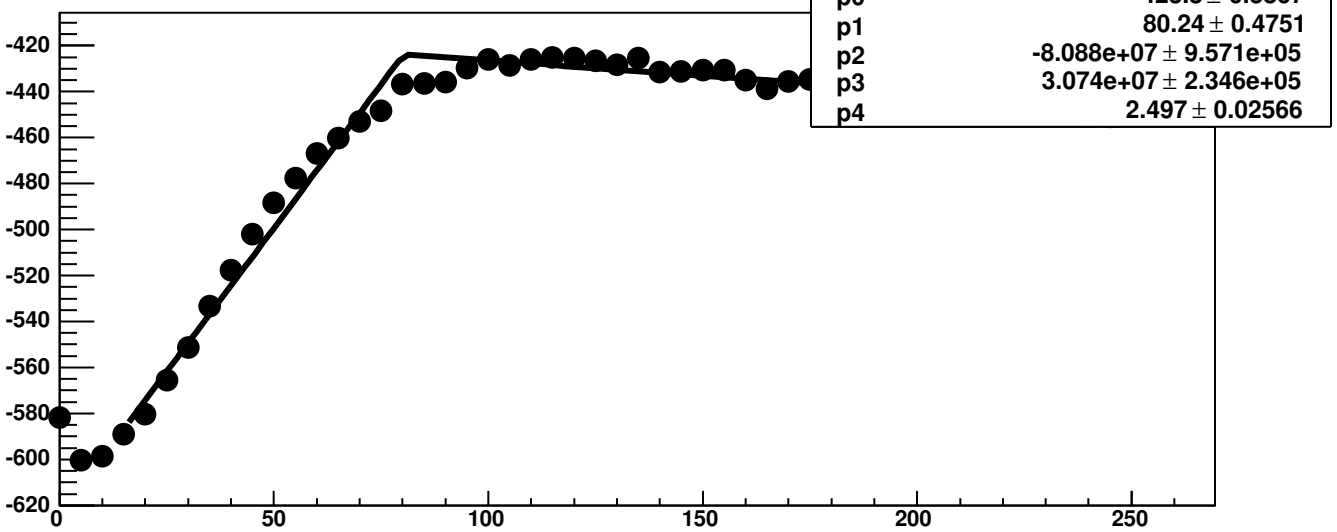
Chip 1, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold



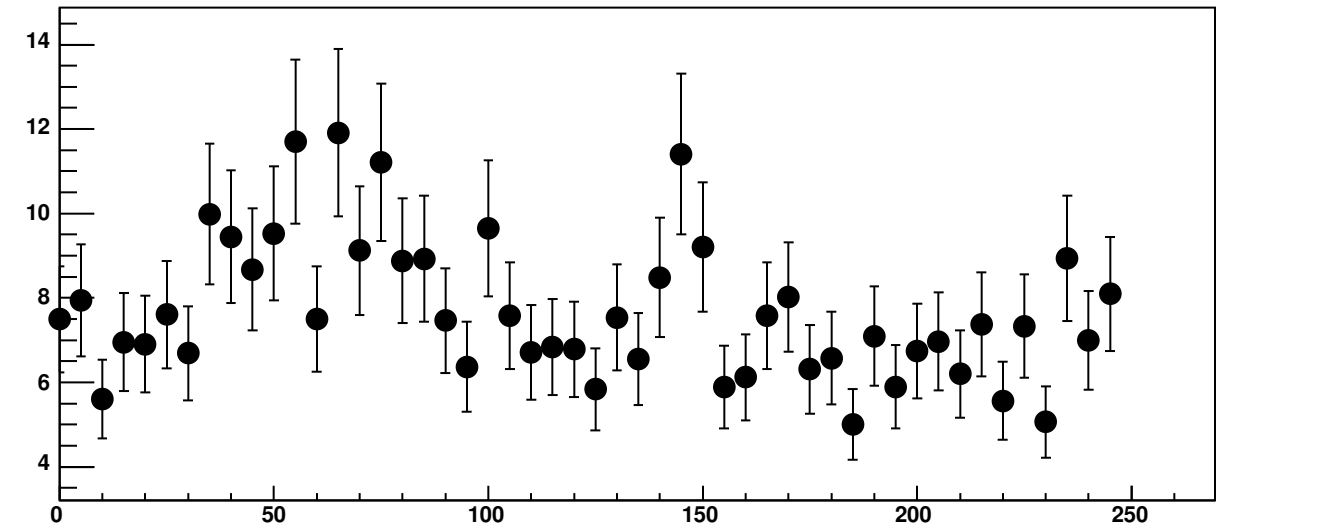
Chip 1, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold



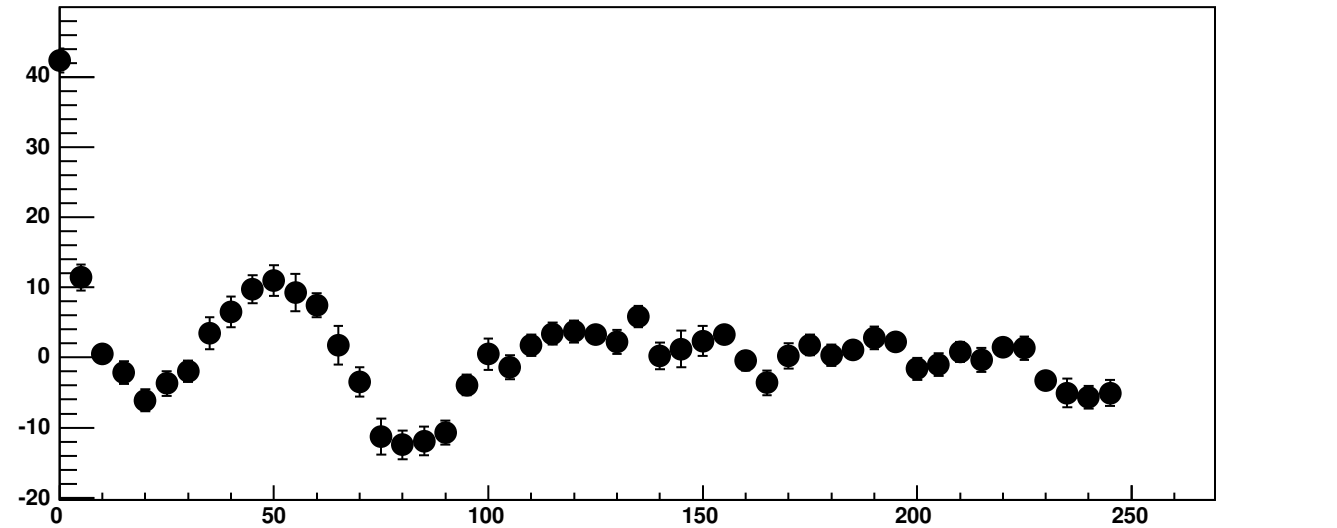
Chip 1, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold



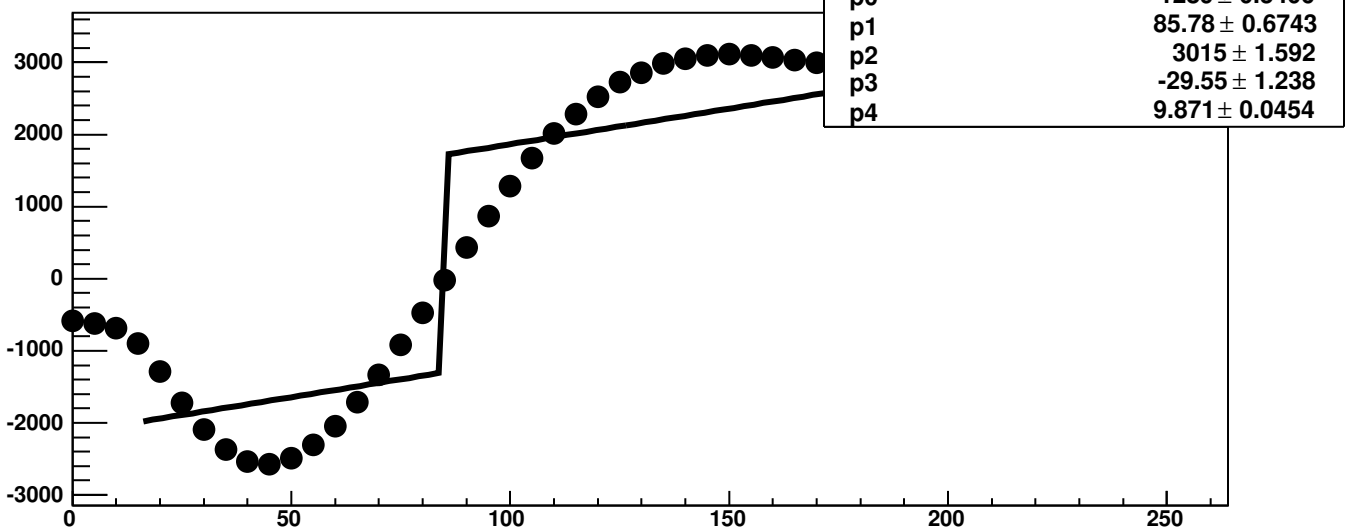
Chip 1, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



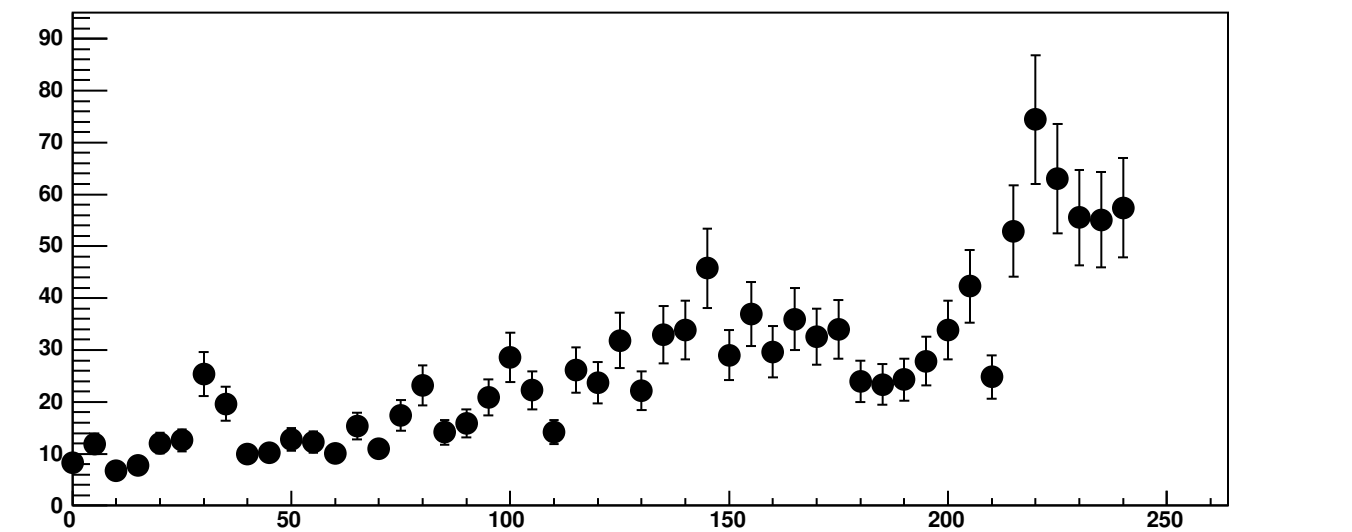
Chip 1, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold



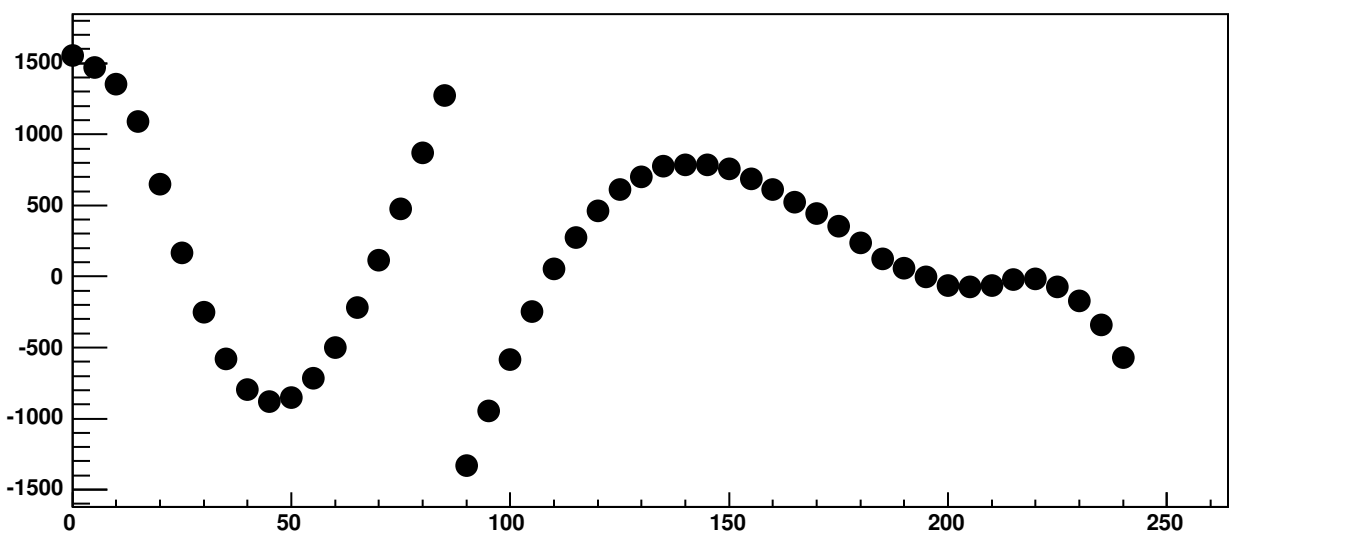
Chip 1, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold



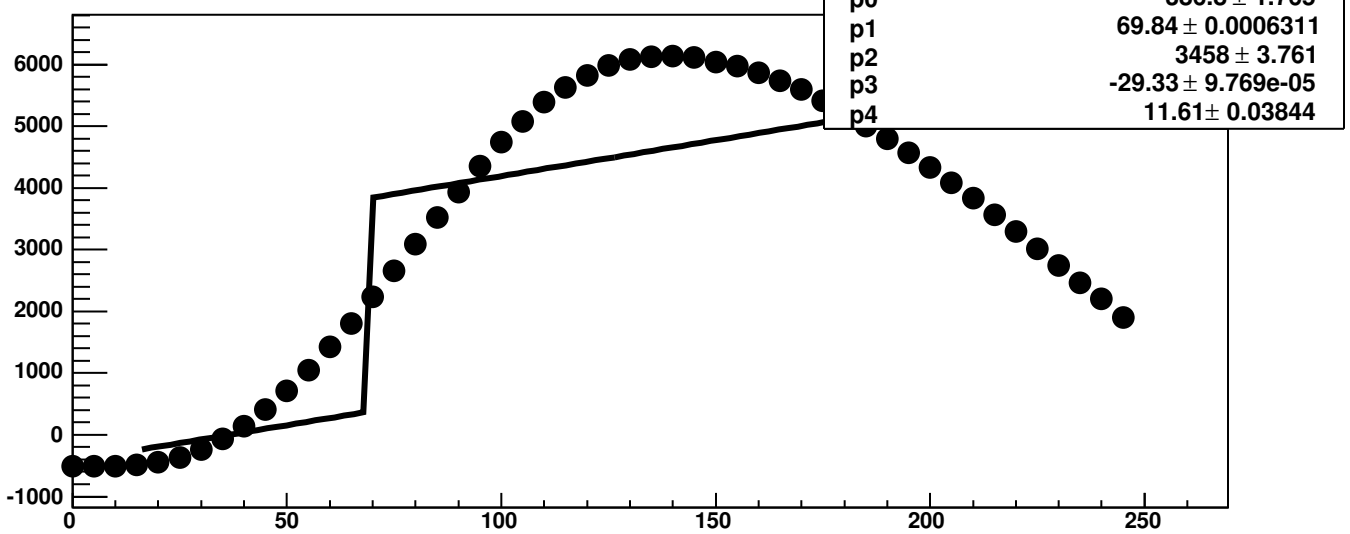
Chip 1, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold



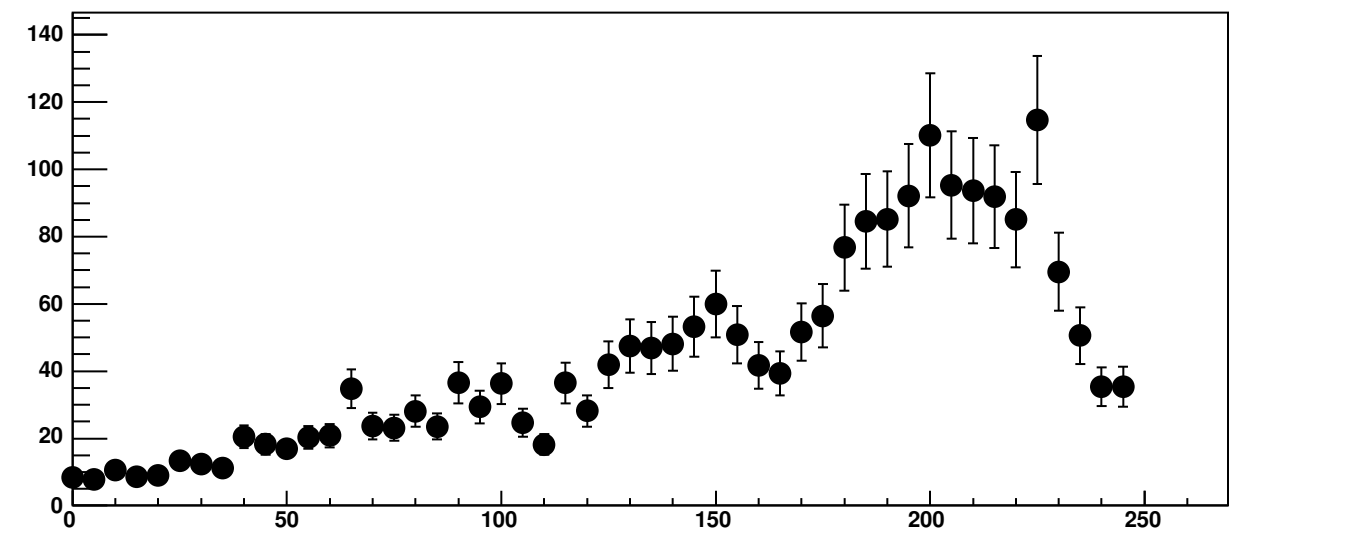
Chip 1, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold



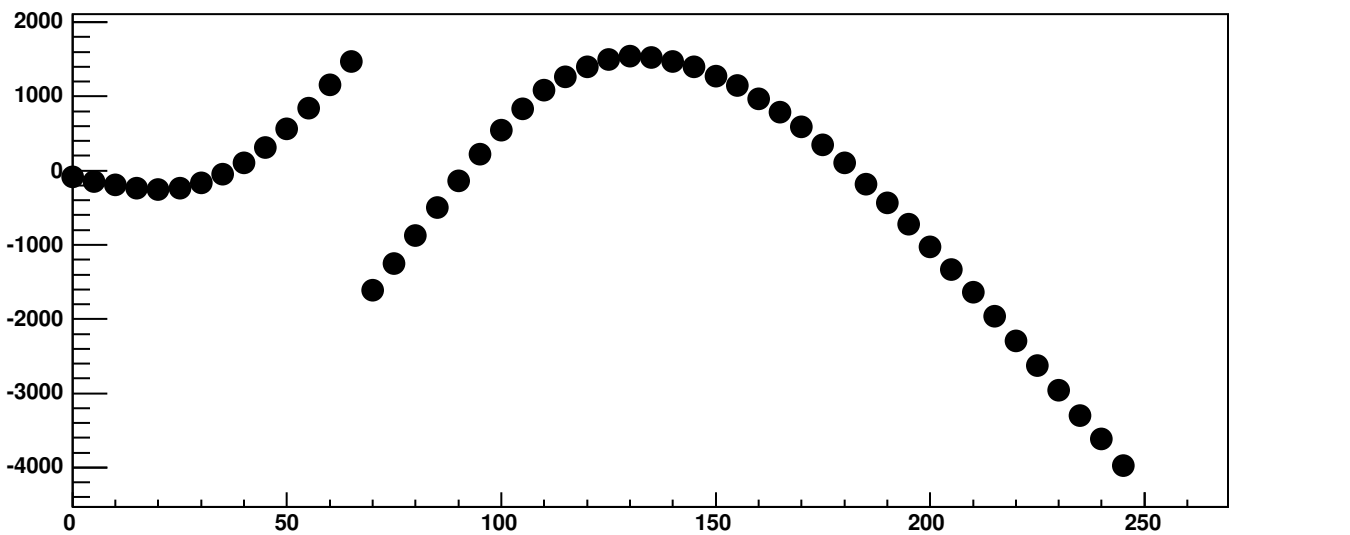
Chip 1, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold



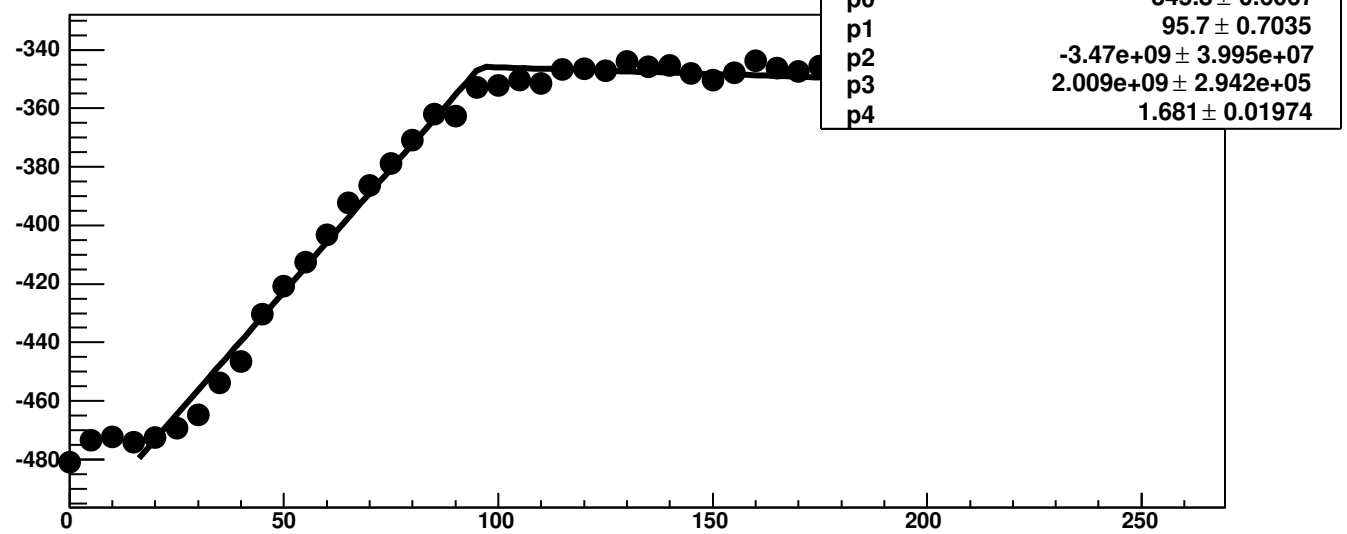
Chip 1, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold



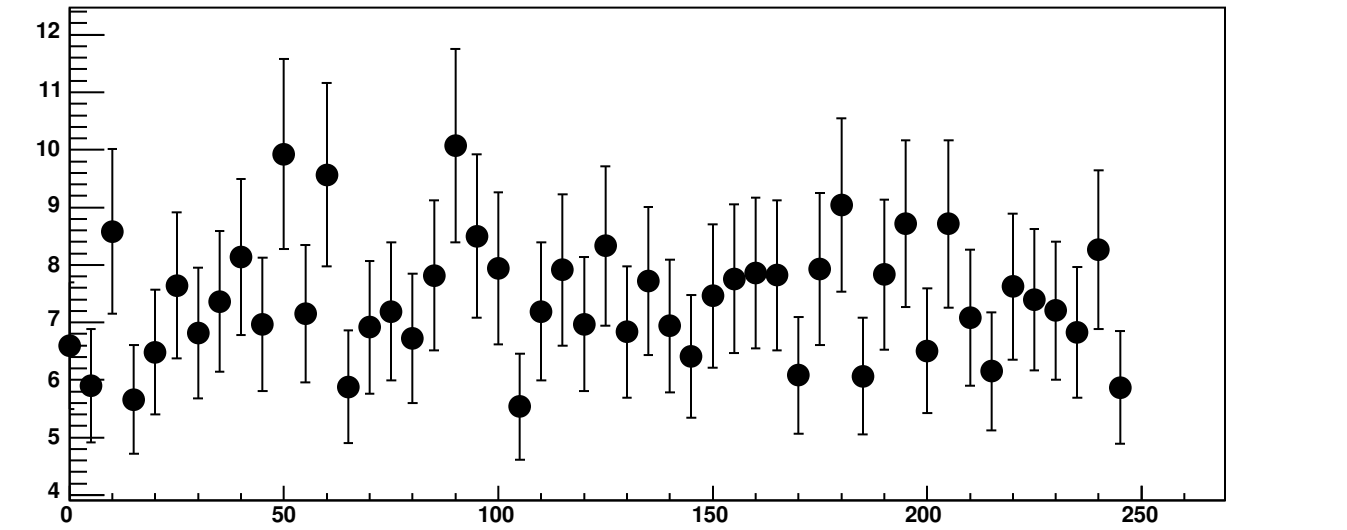
Chip 1, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold



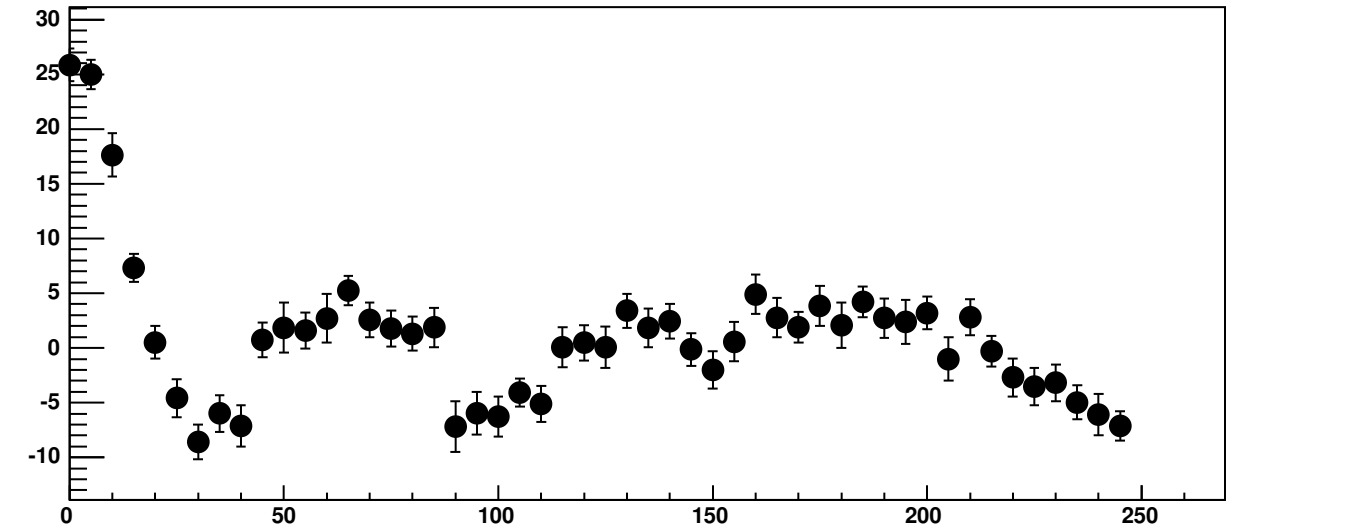
Chip 1, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold



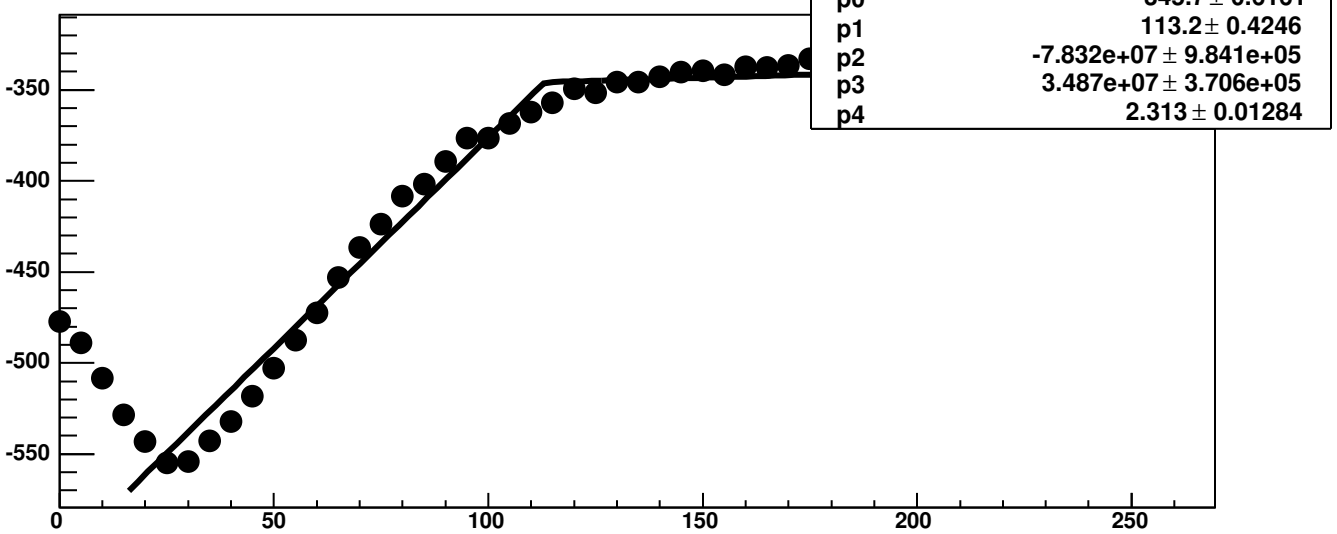
Chip 1, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



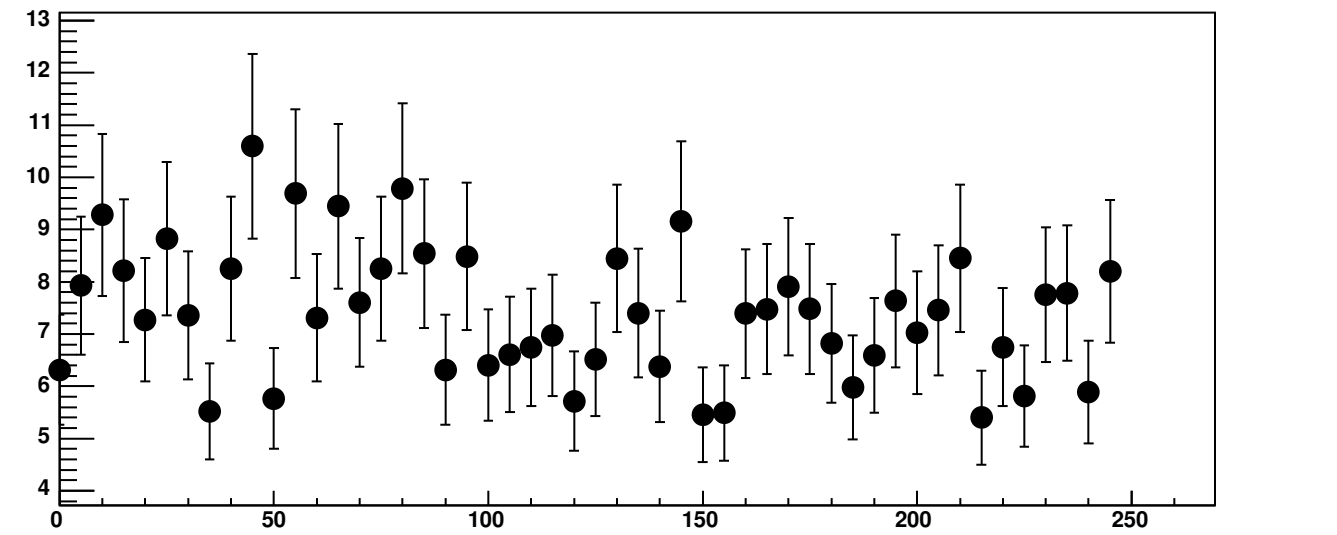
Chip 1, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold



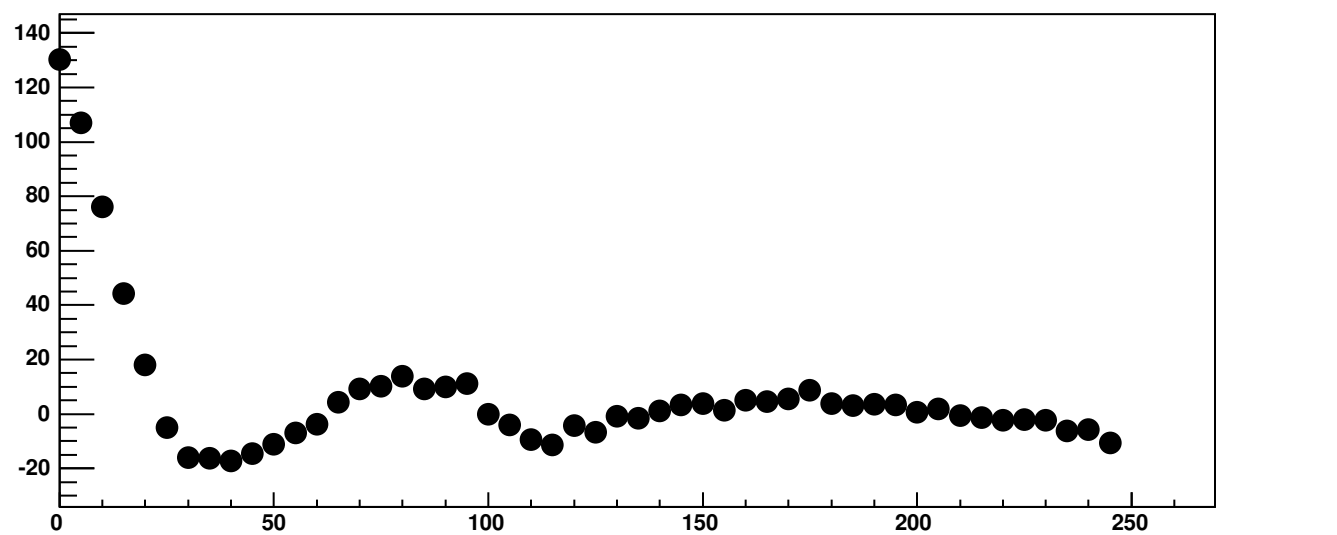
Chip 1, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold



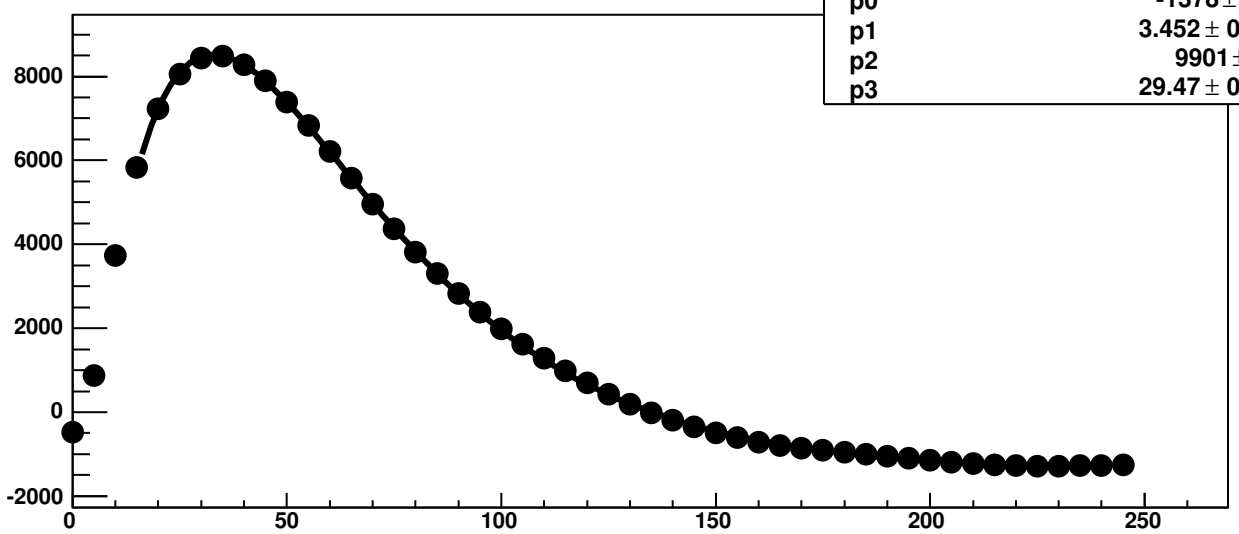
Chip 1, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



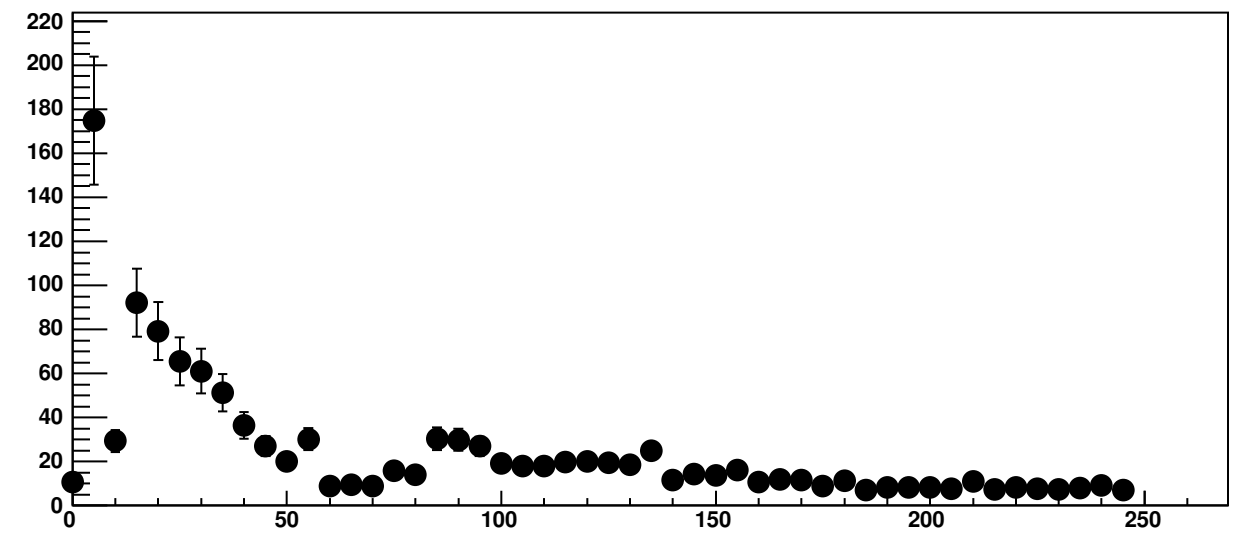
Chip 1, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold



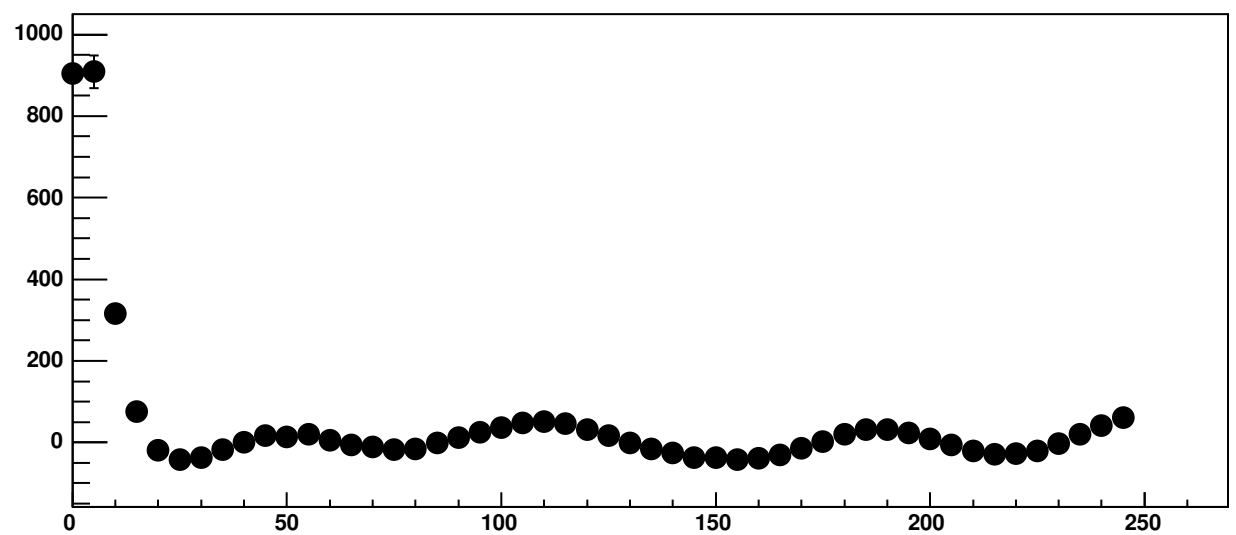
Chip 1, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold



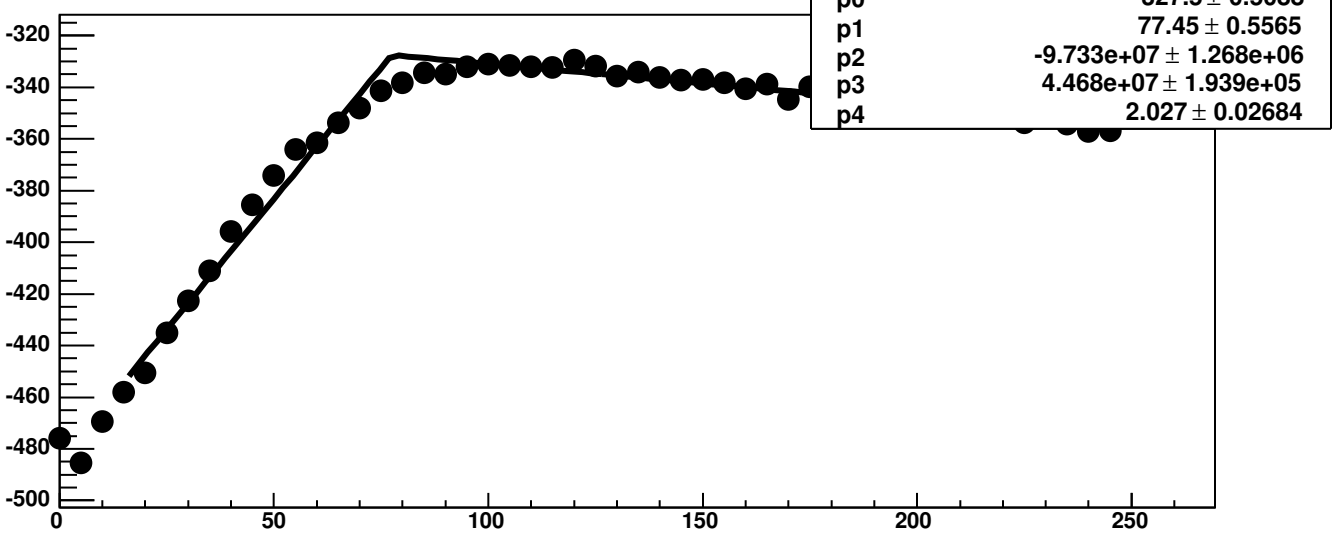
Chip 1, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold



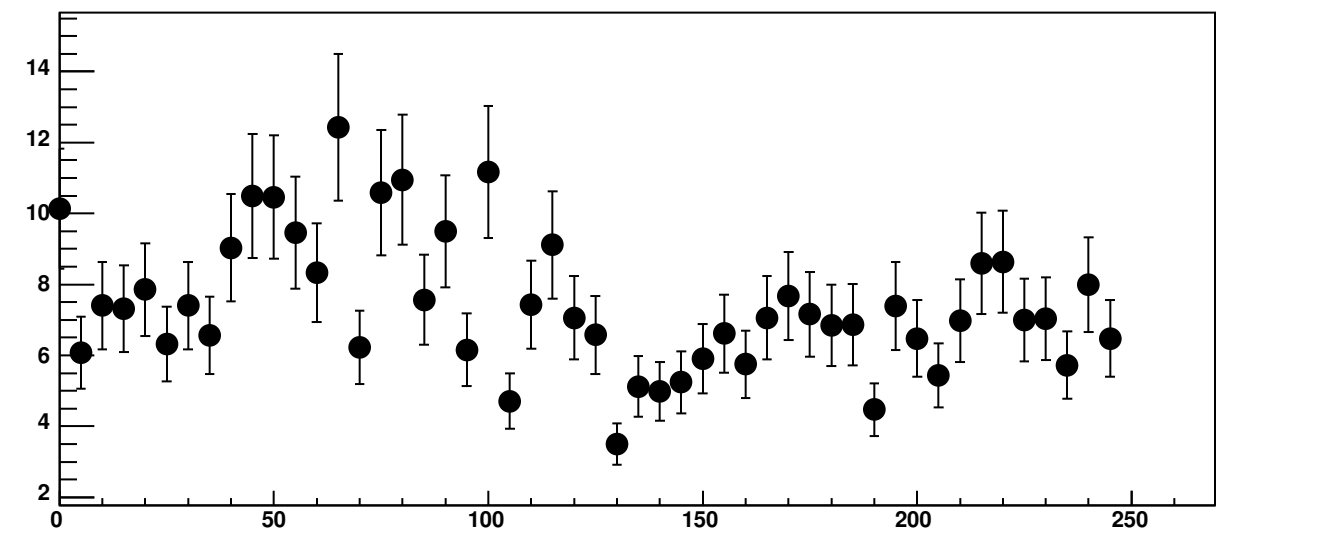
Chip 1, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold



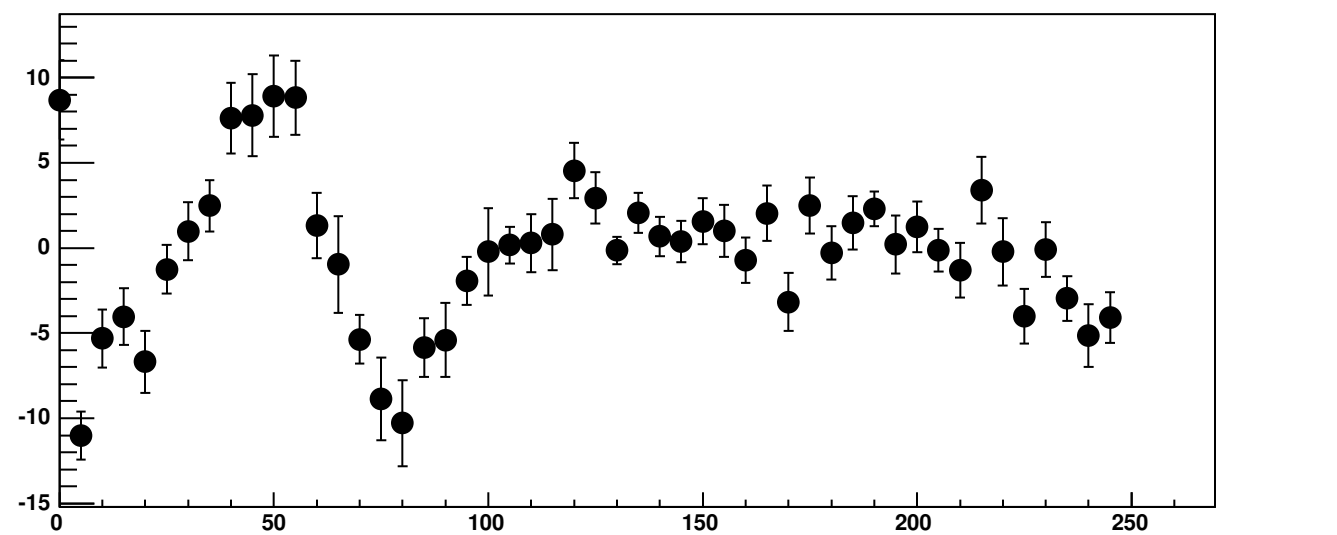
Chip 1, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold



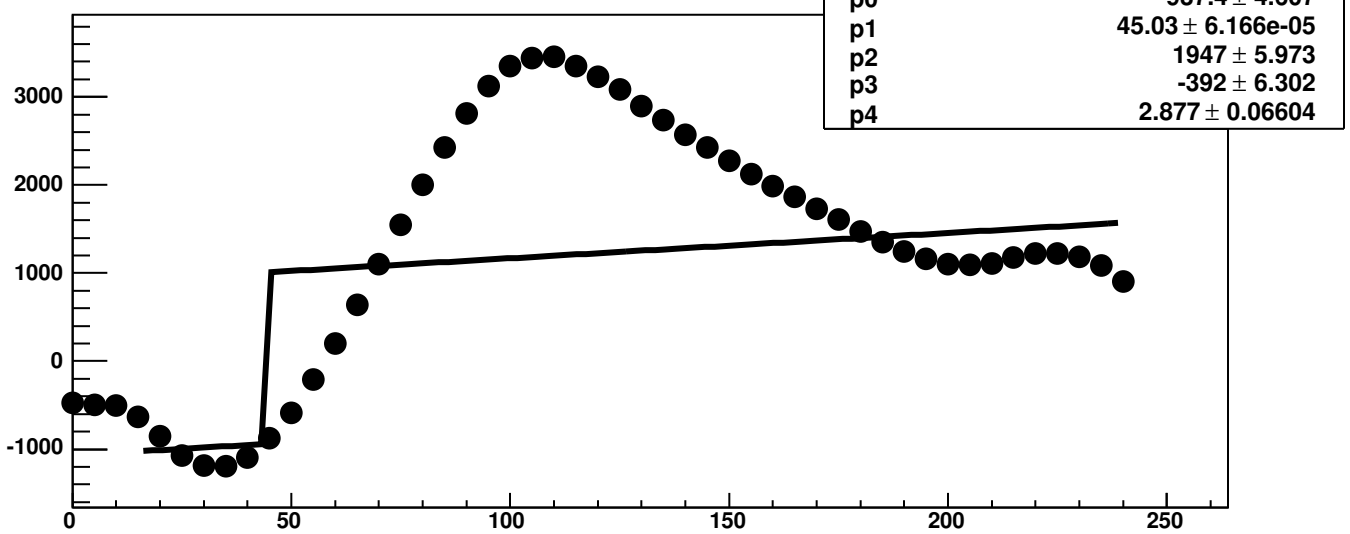
Chip 1, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



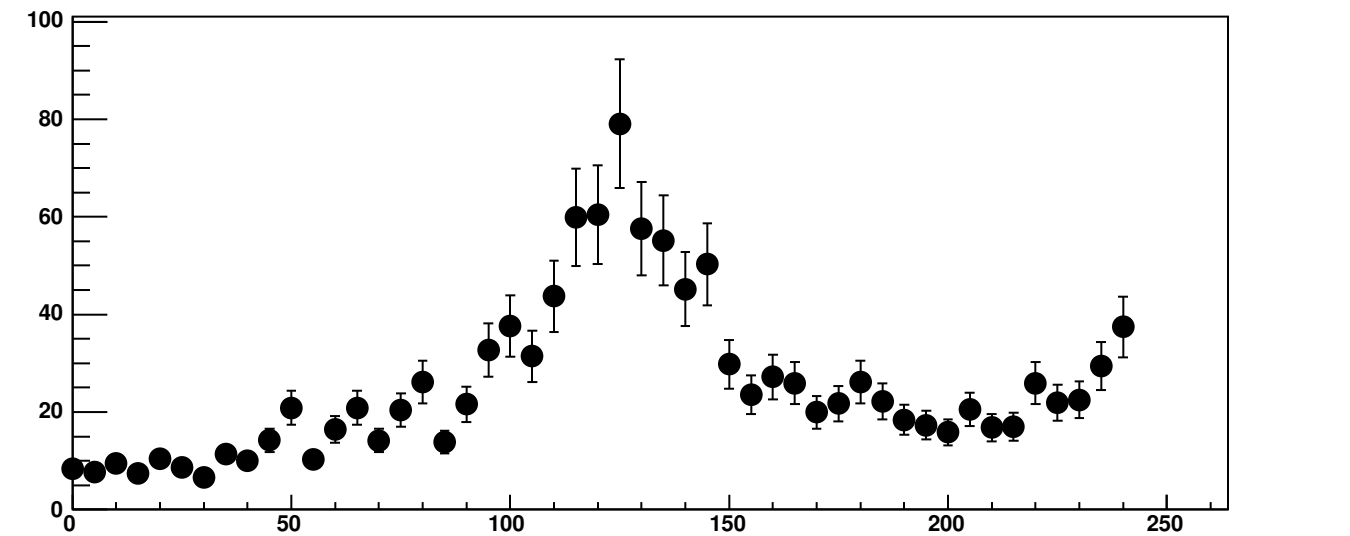
Chip 1, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold



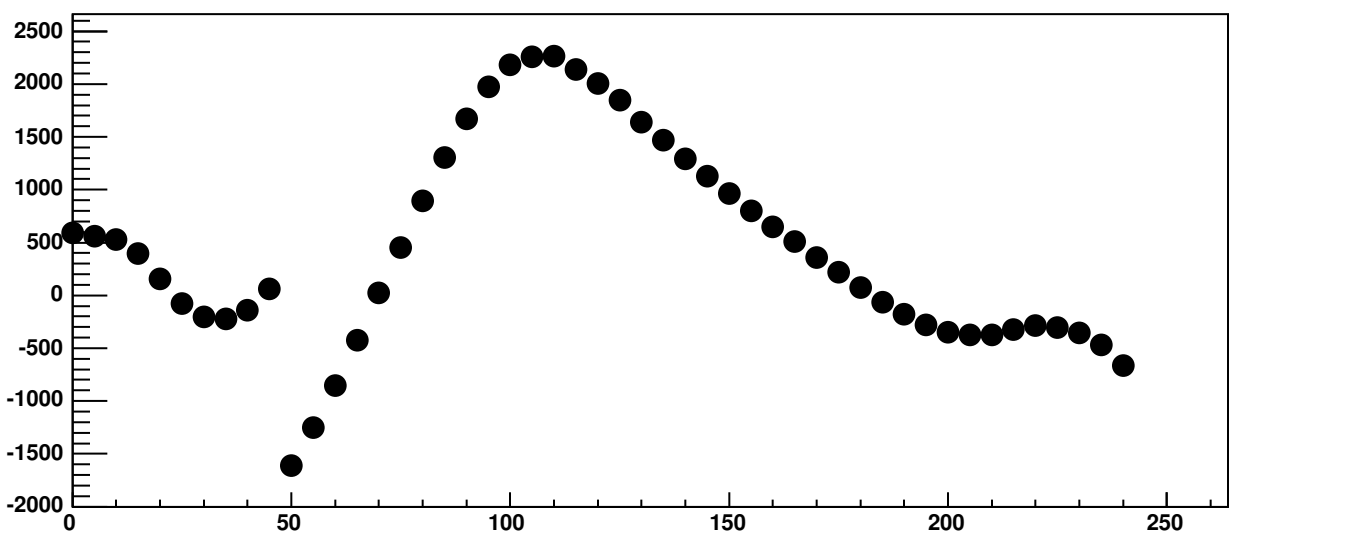
Chip 1, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold



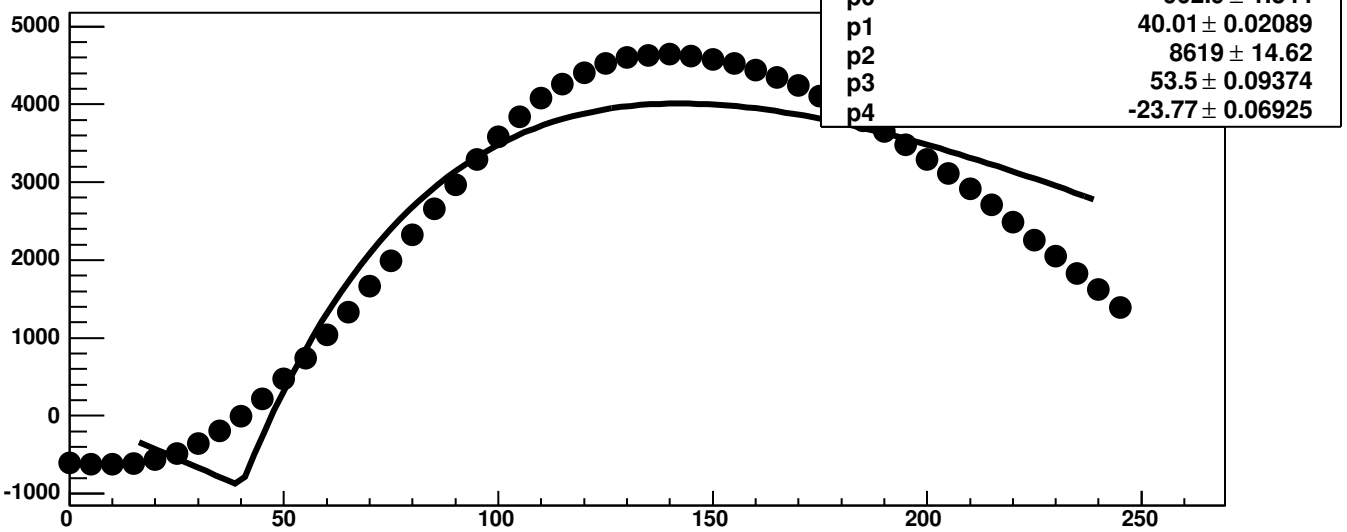
Chip 1, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



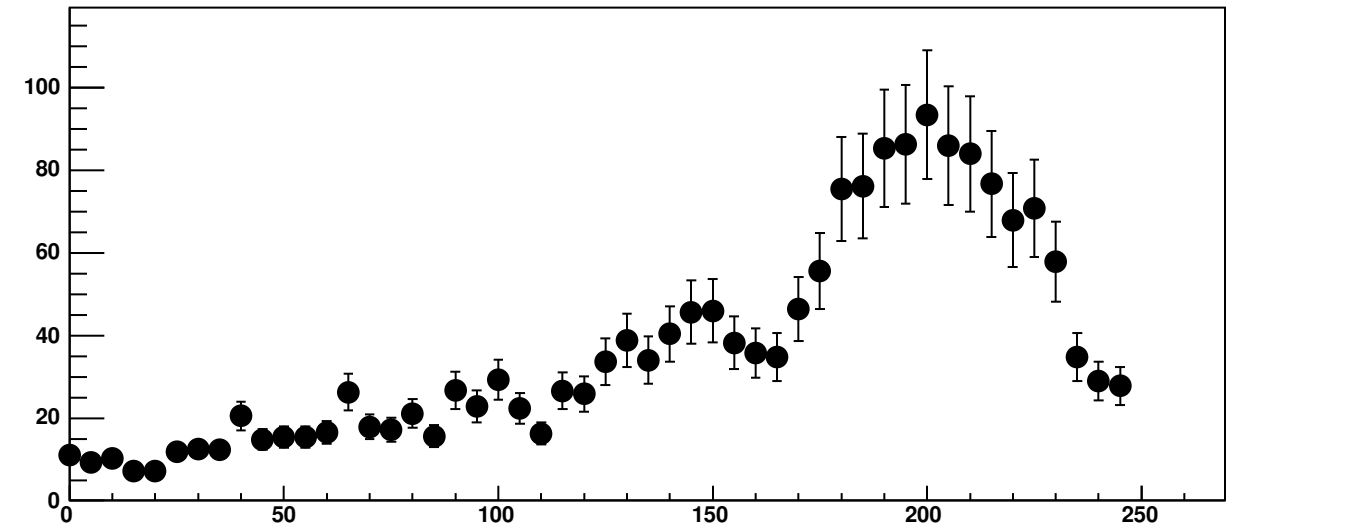
Chip 1, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold



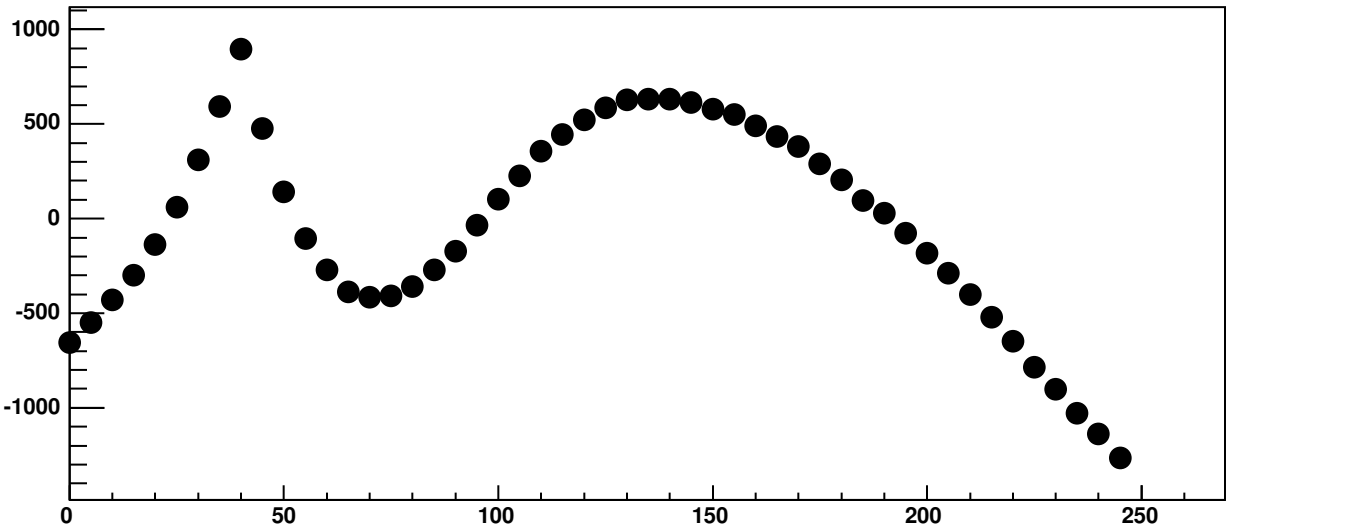
Chip 1, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold



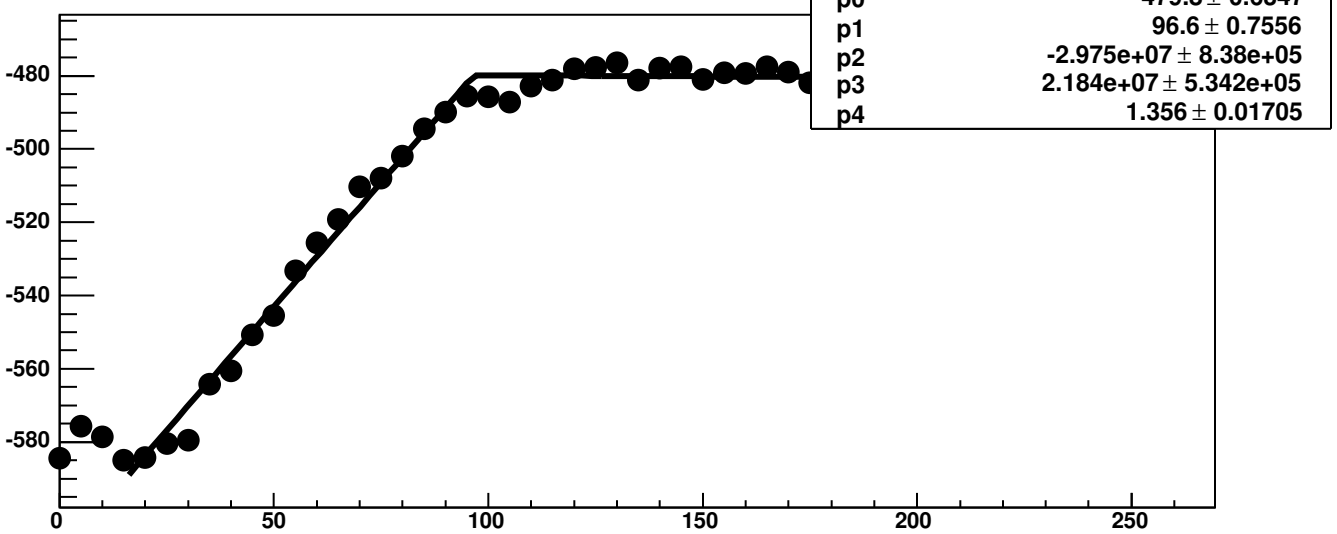
Chip 1, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



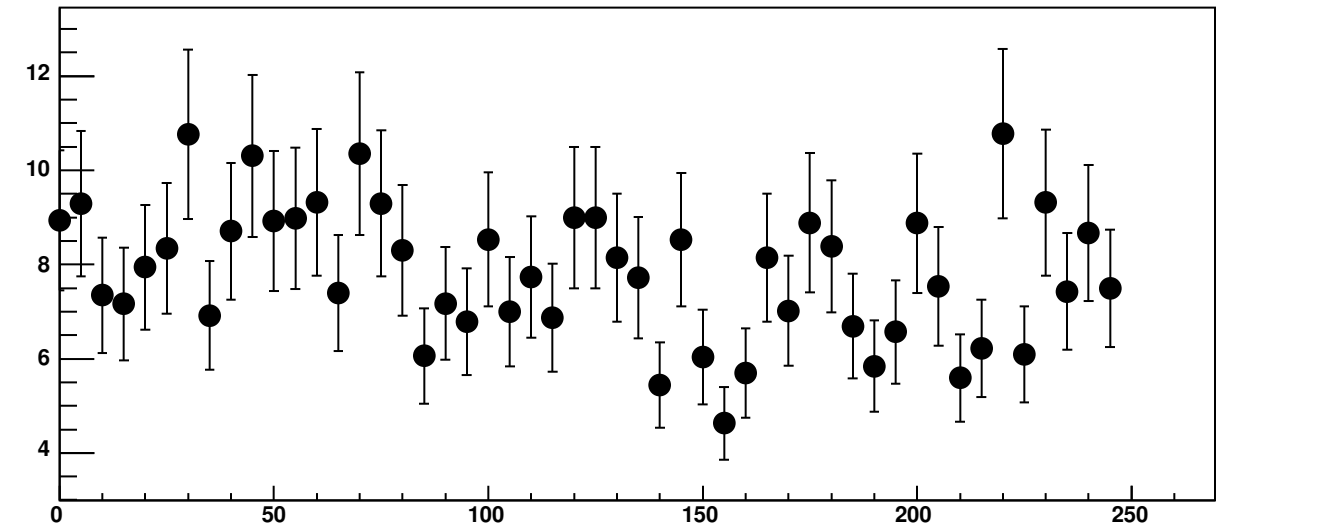
Chip 1, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold



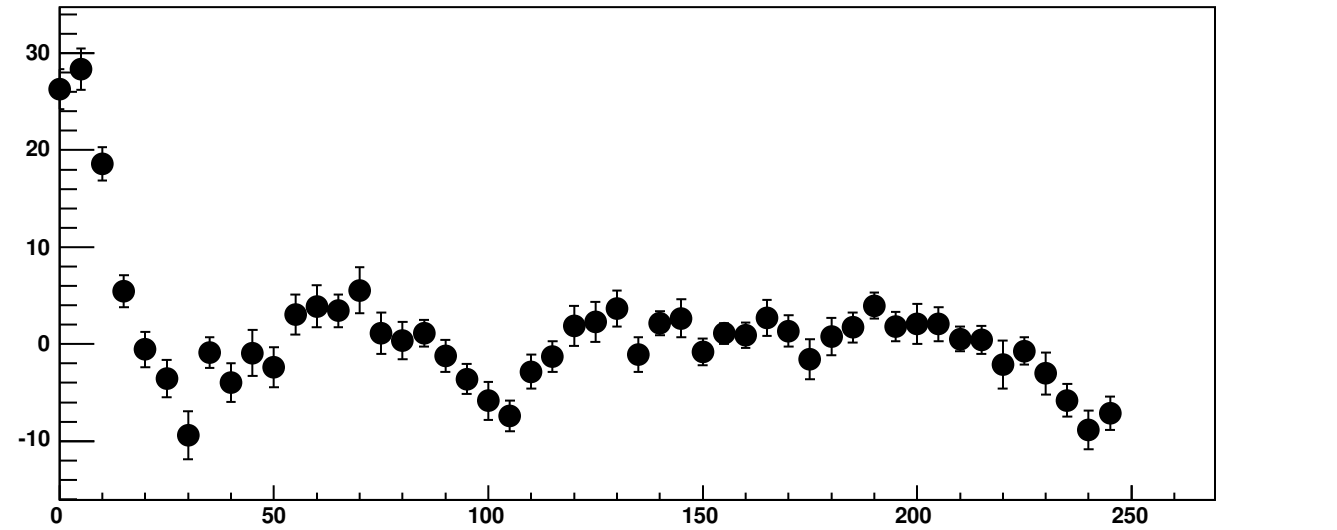
Chip 1, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold



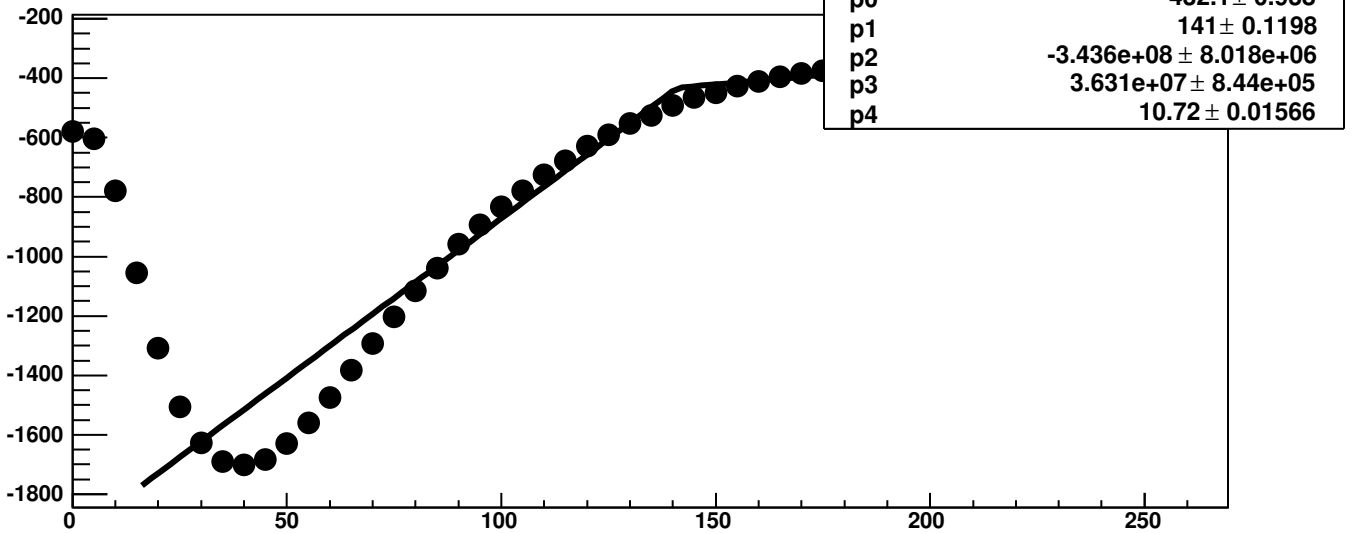
Chip 1, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold



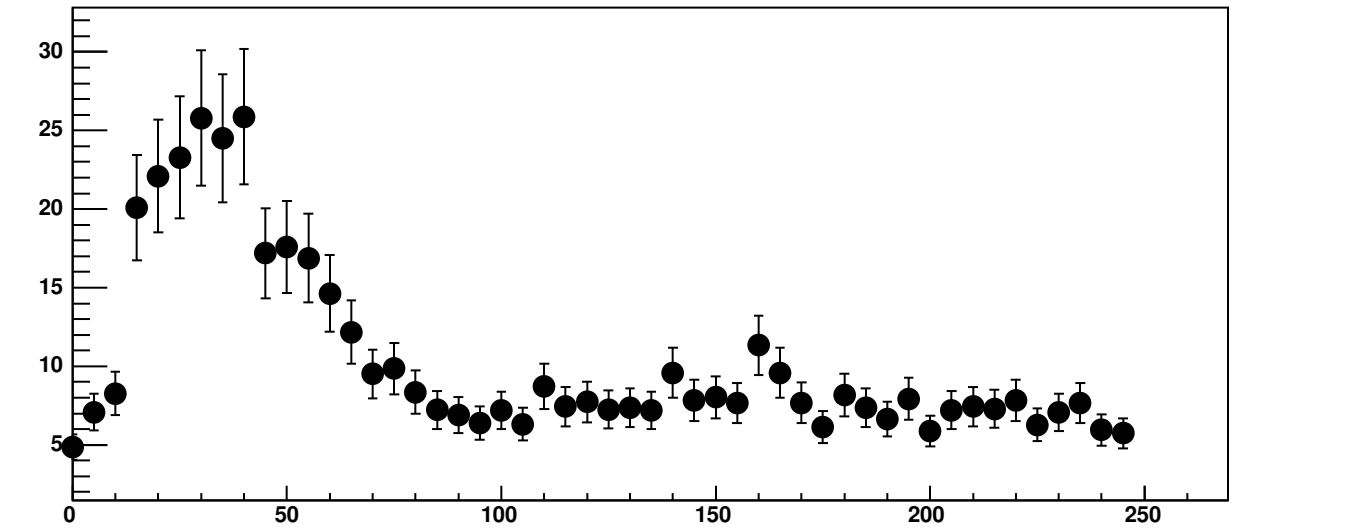
Chip 1, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold



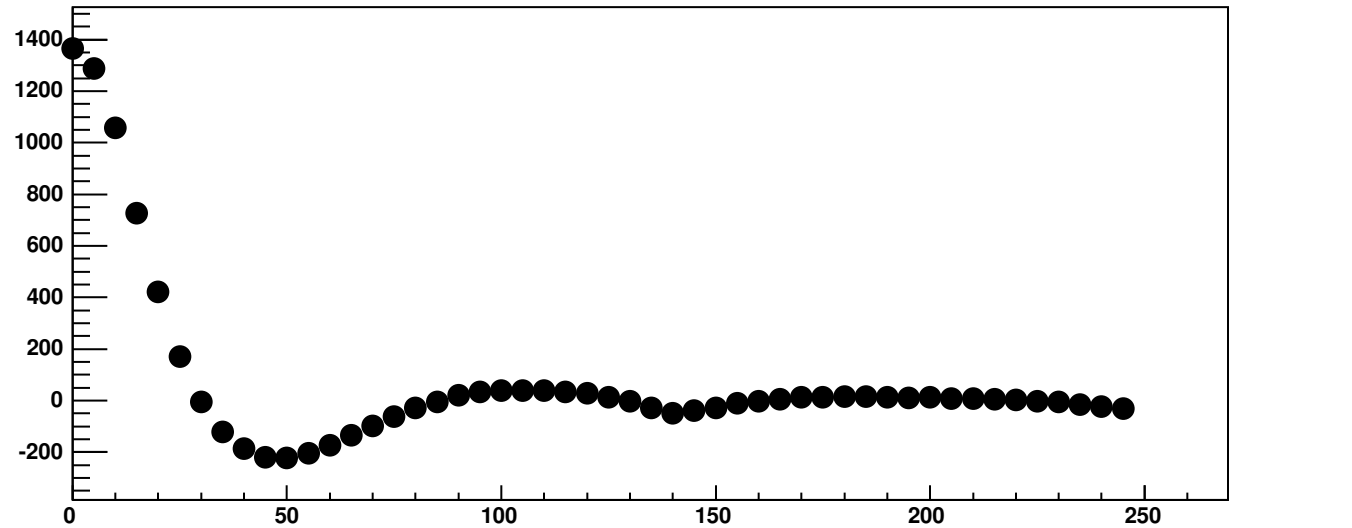
Chip 1, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

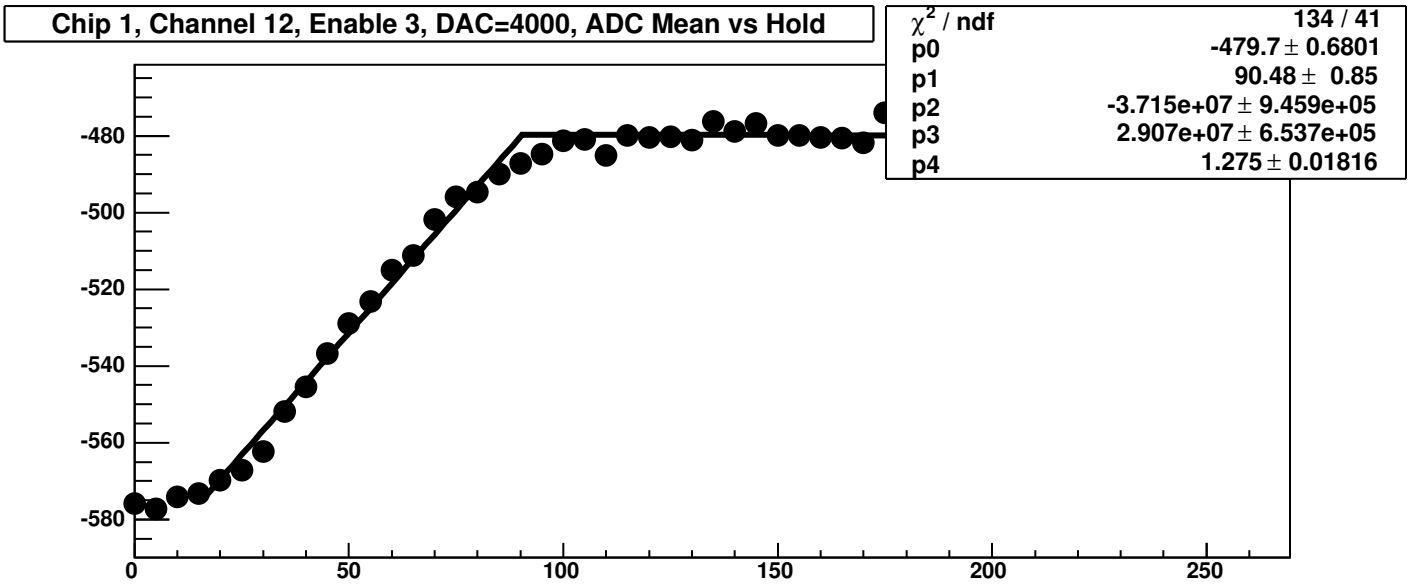


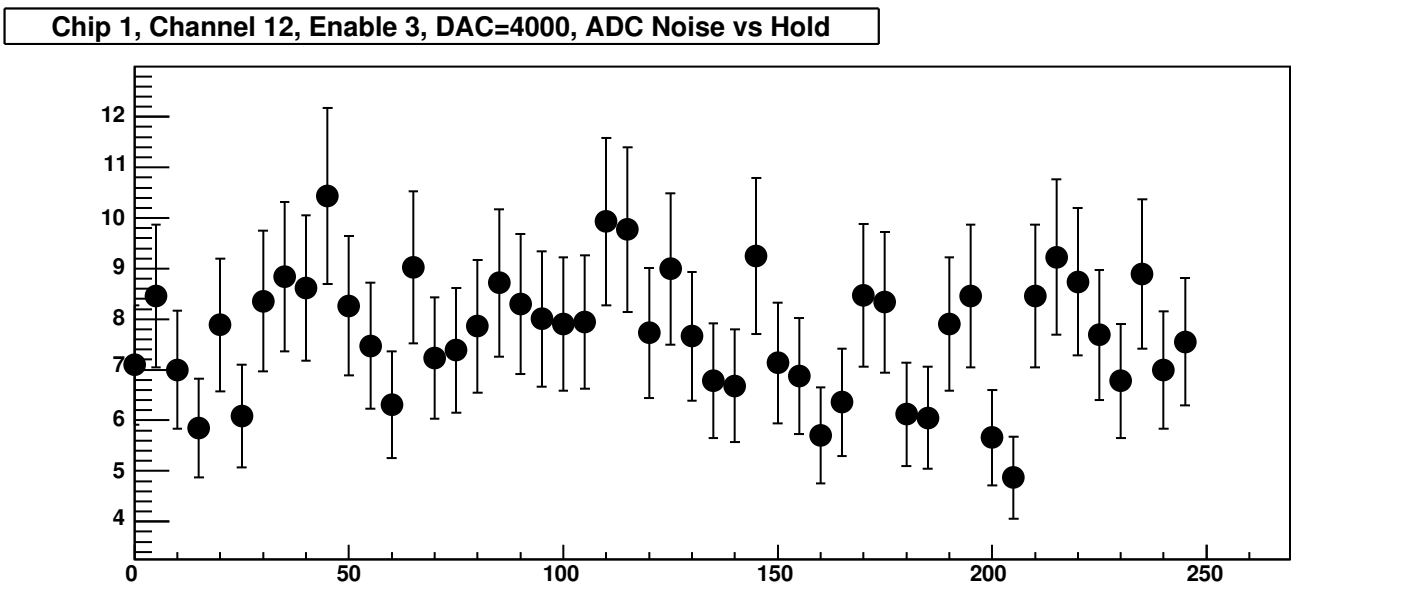
Chip 1, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold

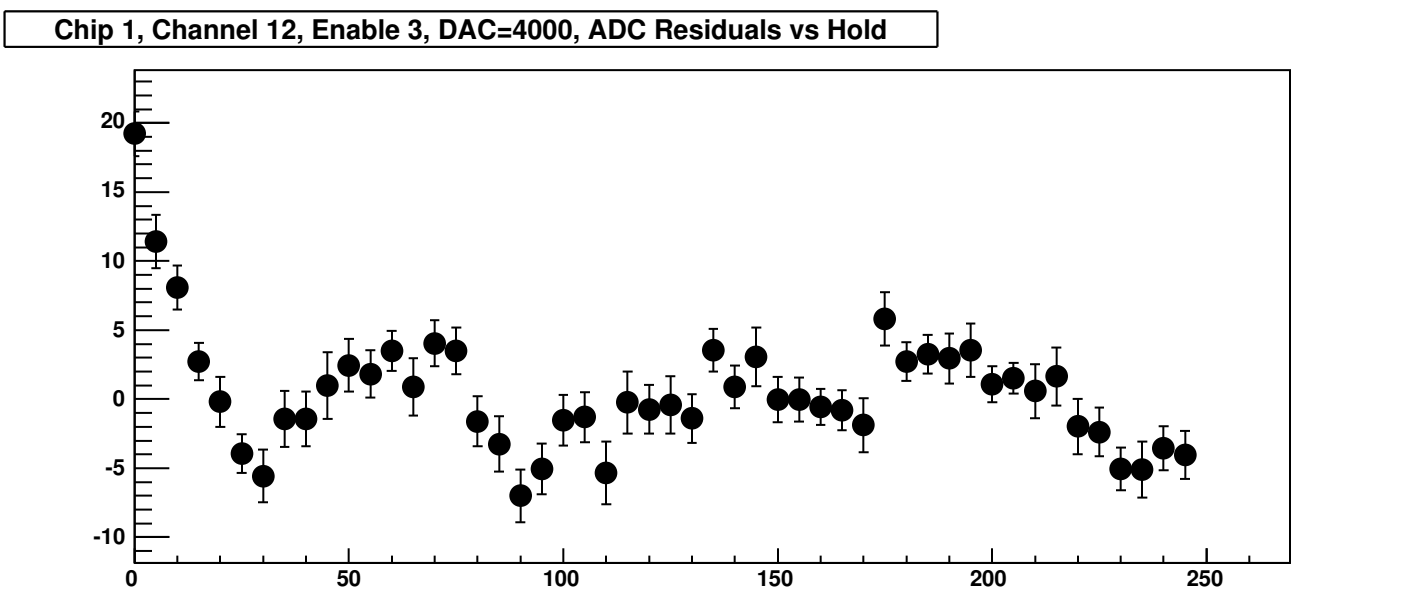


Chip 1, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold



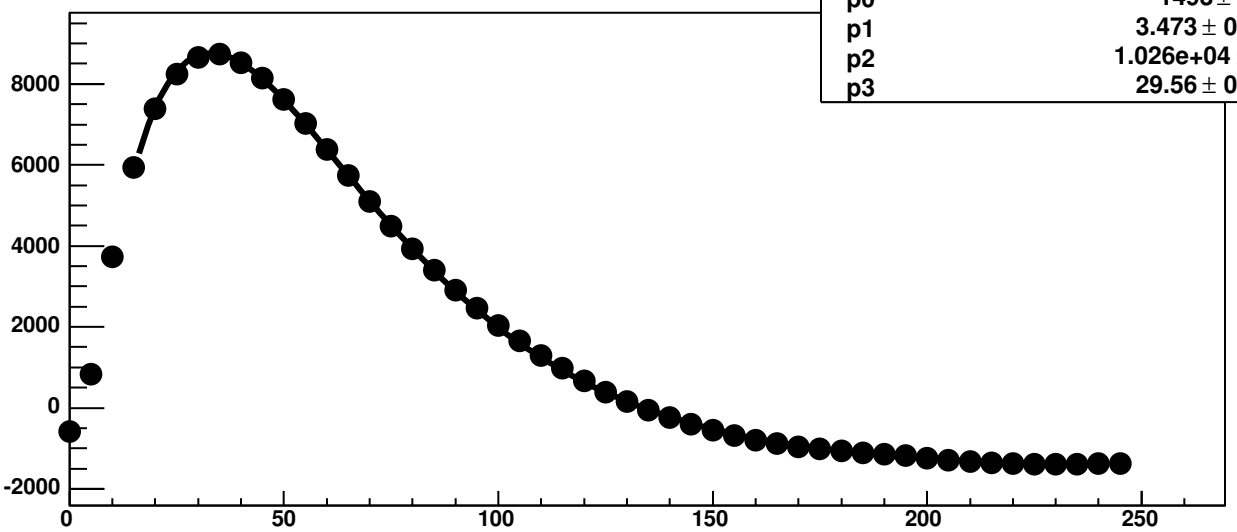

Chip 1, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 134 / 41
p0 -479.7 ± 0.6801
p1 90.48 ± 0.85
p2 -3.715e+07 ± 9.459e+05
p3 2.907e+07 ± 6.537e+05
p4 1.275 ± 0.01816
-480
-500
-520
-540
-560
-580
0
50
100
150
200
250


Chip 1, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

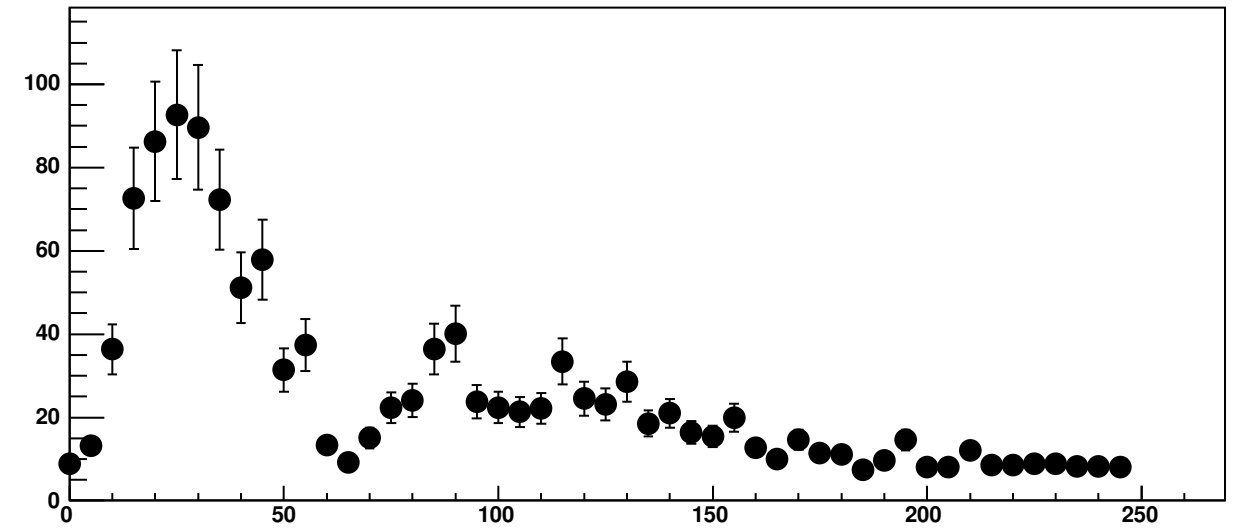

Chip 1, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
0
50
100
150
200
250

Chip 1, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold

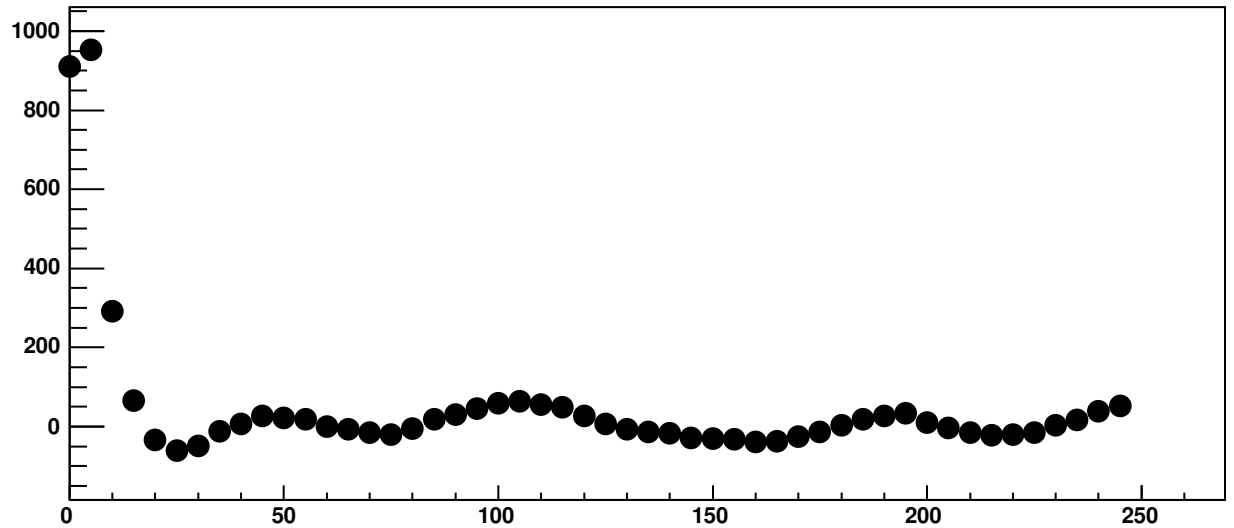
χ^2 / ndf	2597 / 42
p0	-1493 ± 0.7907
p1	3.473 ± 0.03259
p2	1.026e+04 ± 4.14
p3	29.56 ± 0.01434

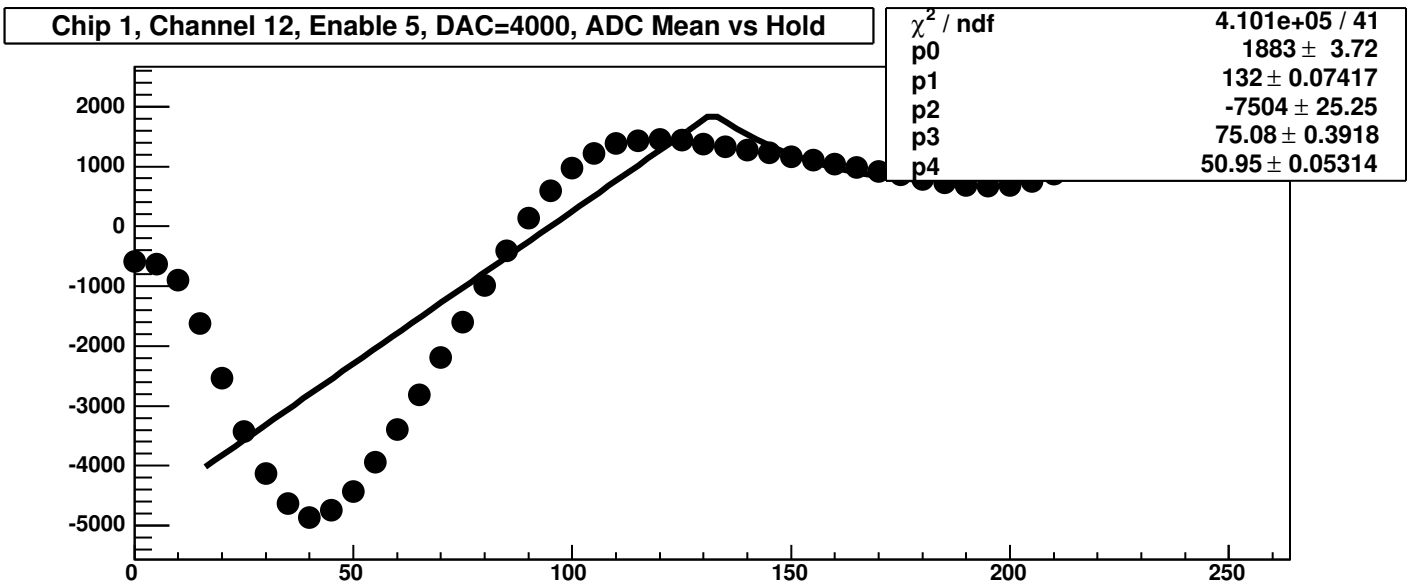


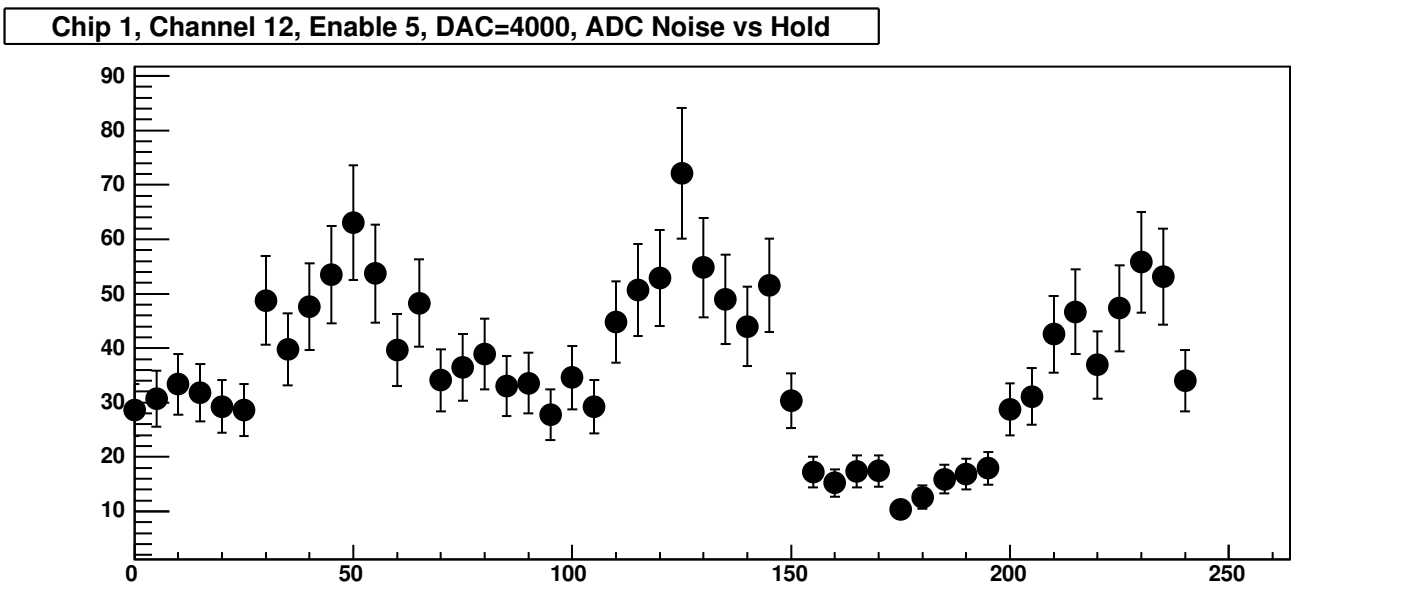
Chip 1, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold

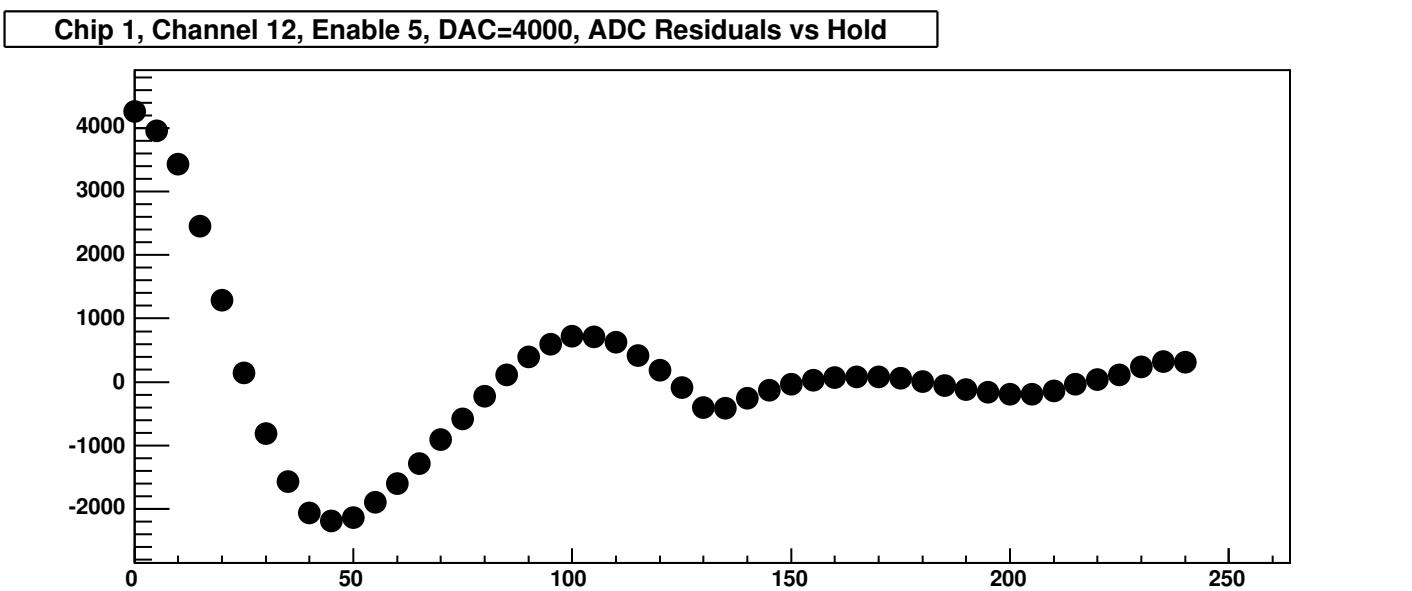


Chip 1, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold

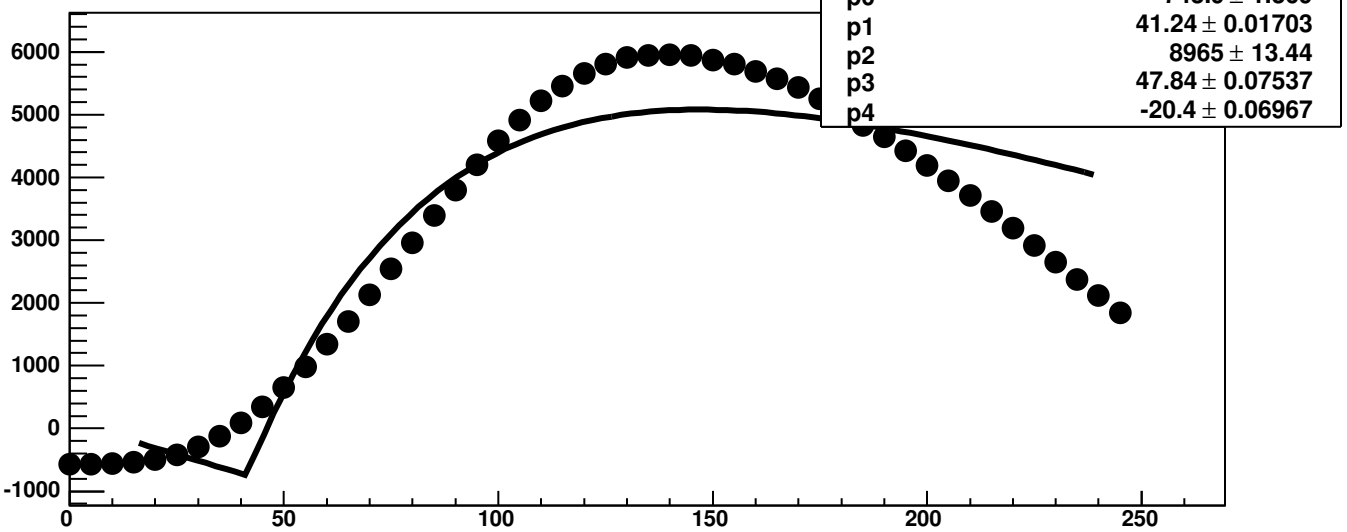



Chip 1, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.101e+05 / 41
p0 1883 ± 3.72
p1 132 ± 0.07417
p2 -7504 ± 25.25
p3 75.08 ± 0.3918
p4 50.95 ± 0.05314
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

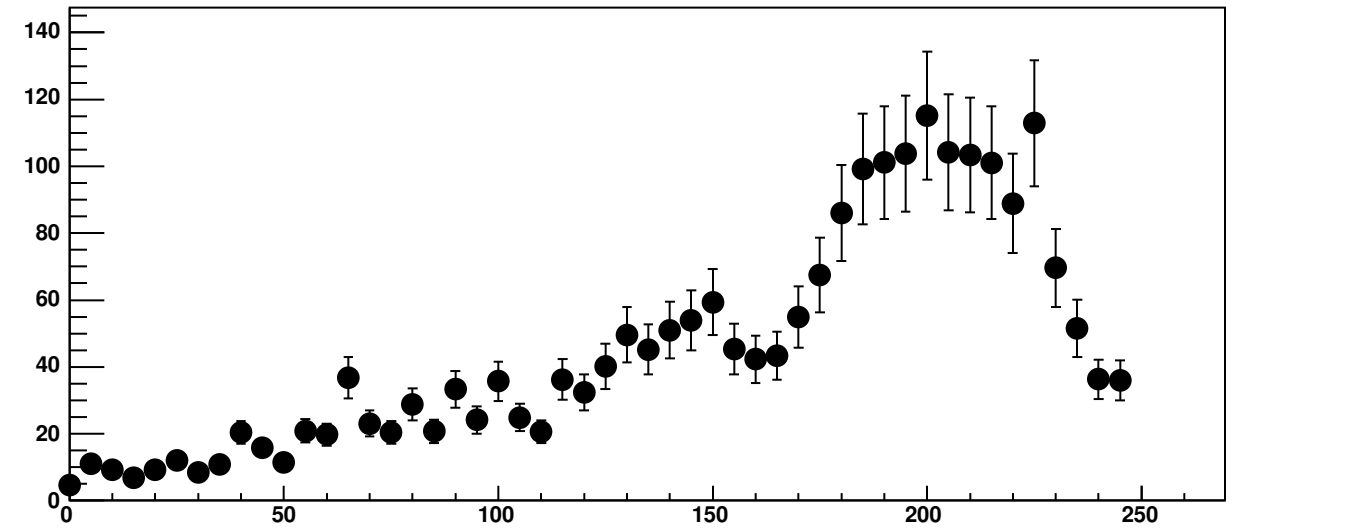

Chip 1, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold
90
80
70
60
50
40
30
20
10
0
50
100
150
200
250


Chip 1, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold
4000
3000
2000
1000
0
-1000
-2000
0
50
100
150
200
250

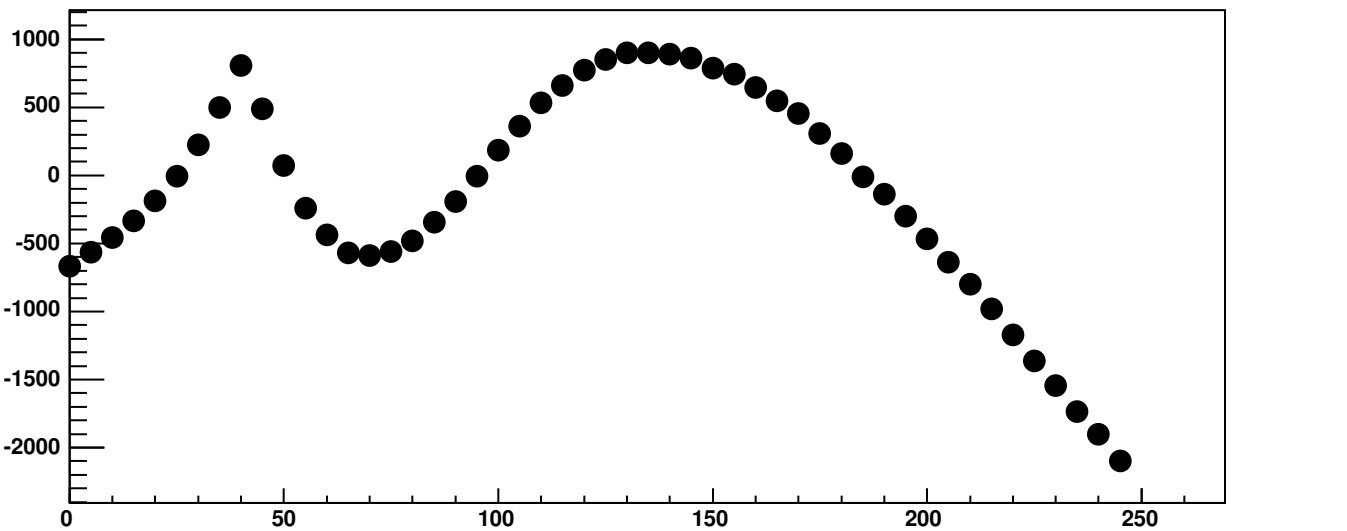
Chip 1, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold



Chip 1, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold

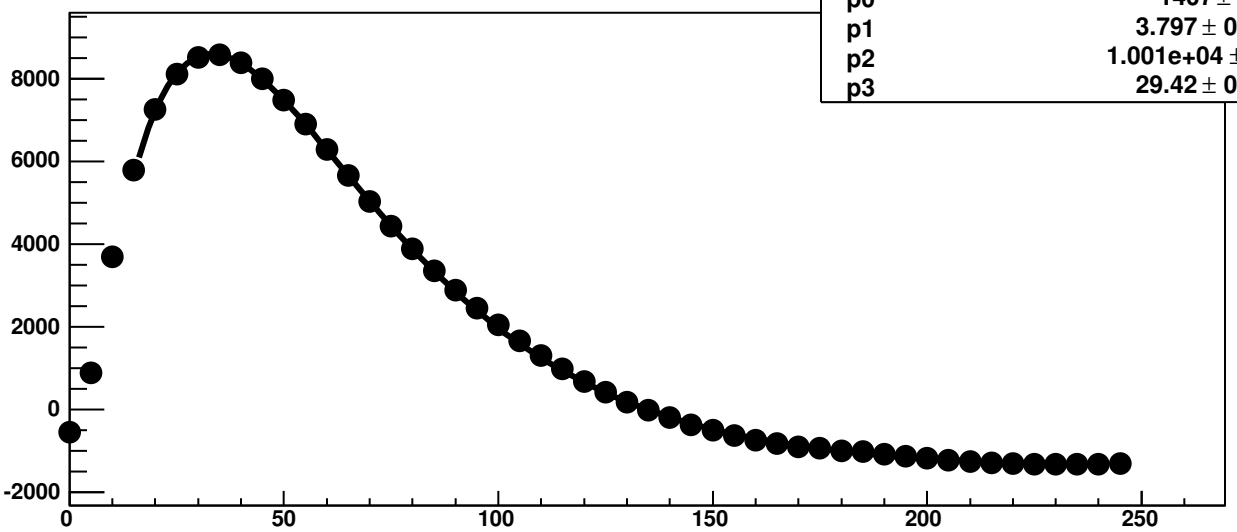


Chip 1, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold

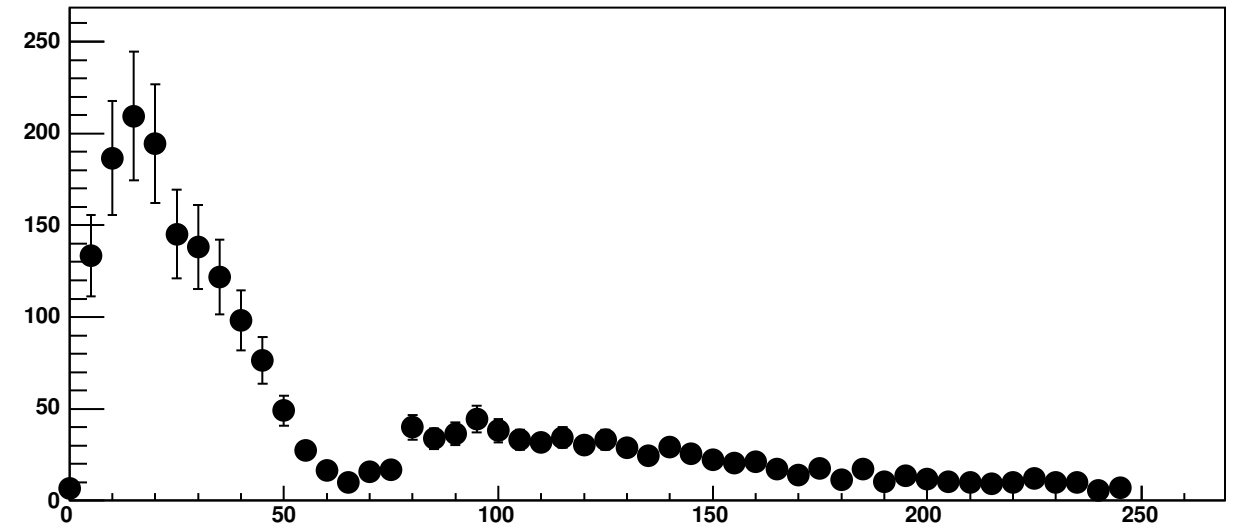


Chip 1, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold

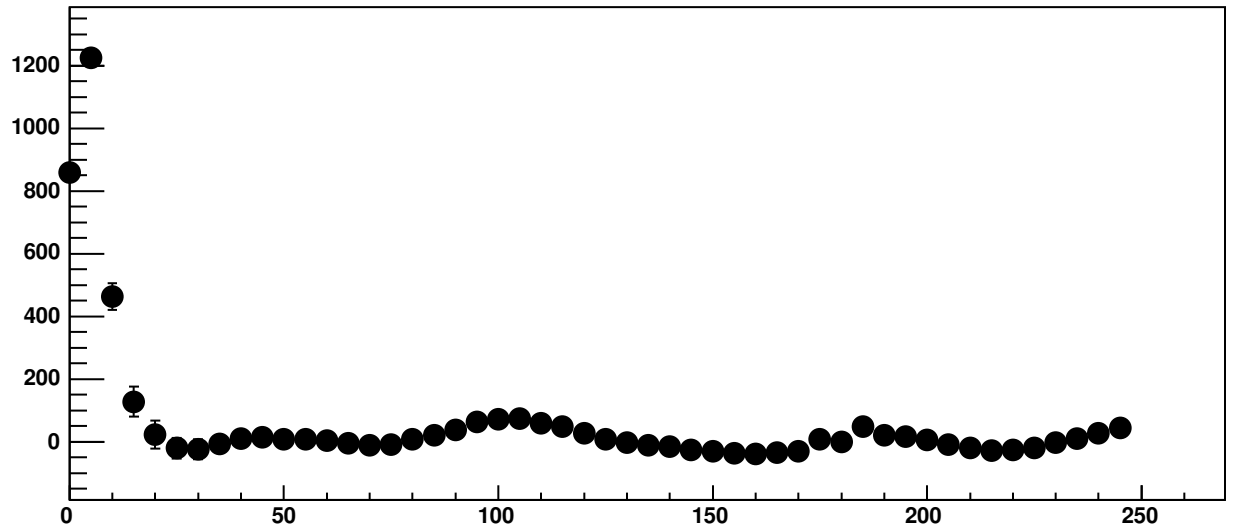
χ^2 / ndf	1898 / 42
p0	-1407 ± 0.8857
p1	3.797 ± 0.06785
p2	1.001e+04 ± 7.993
p3	29.42 ± 0.02082



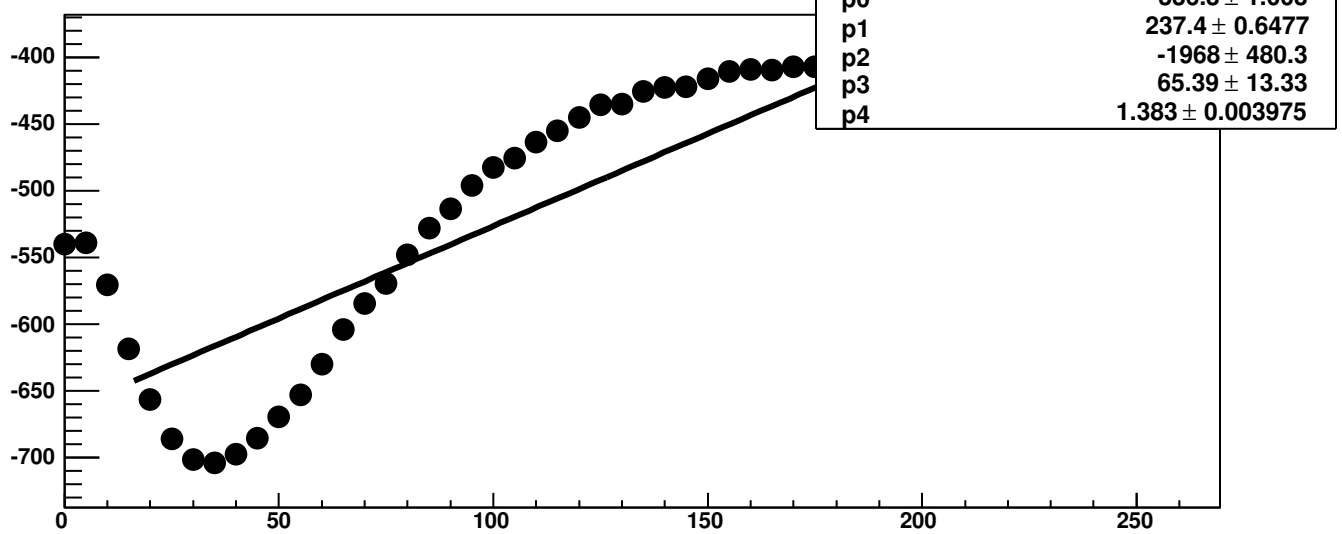
Chip 1, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold



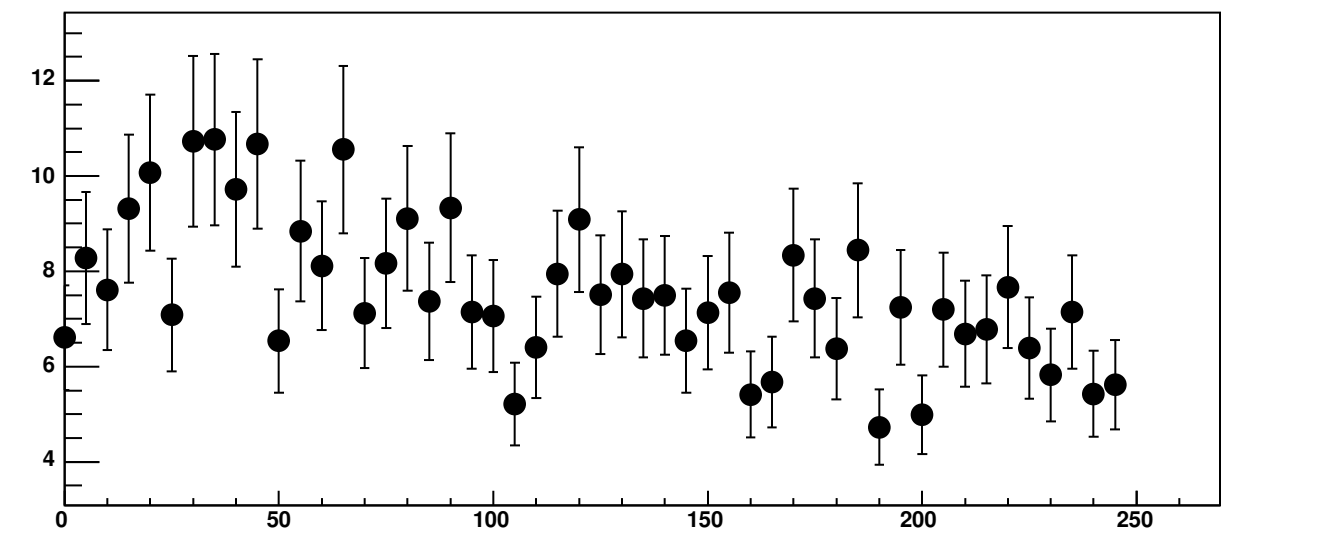
Chip 1, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold



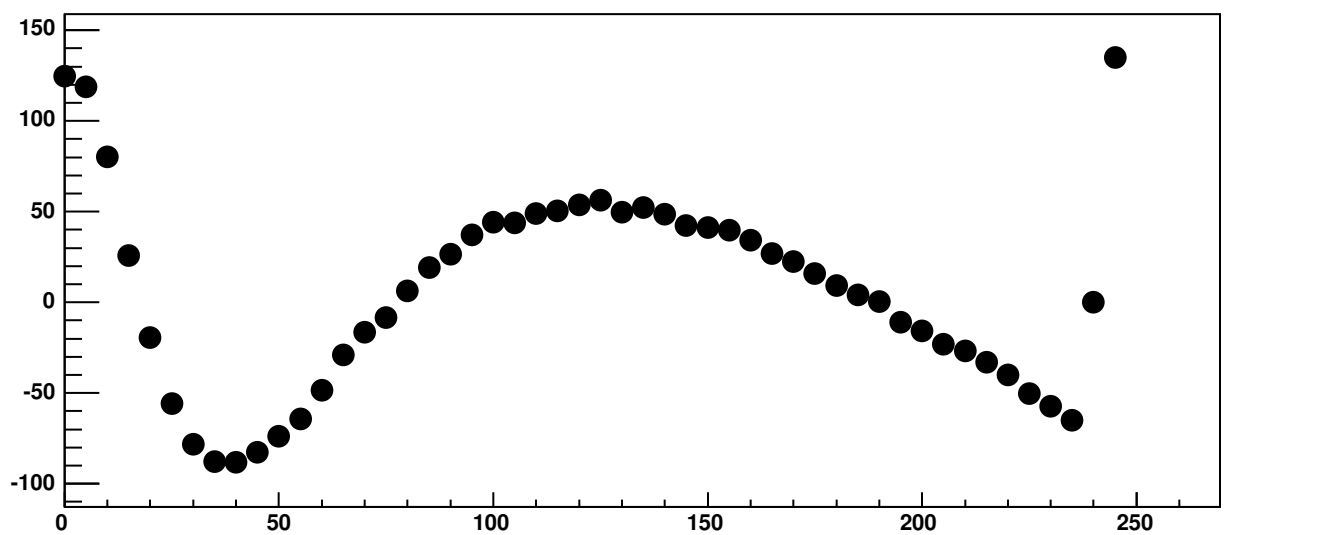
Chip 1, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold



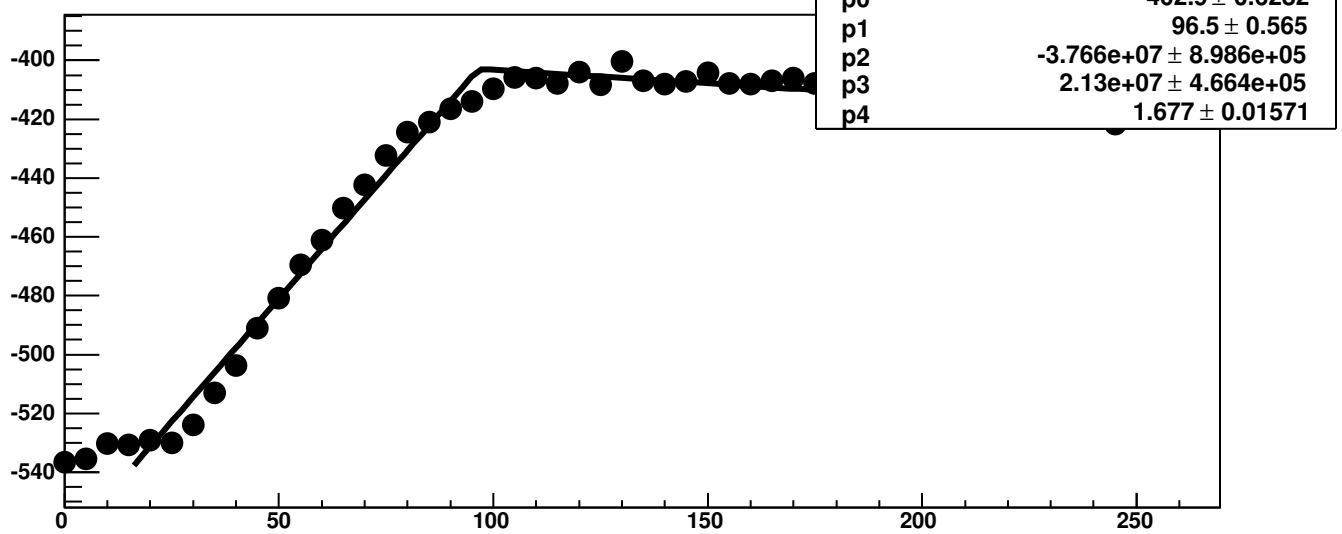
Chip 1, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



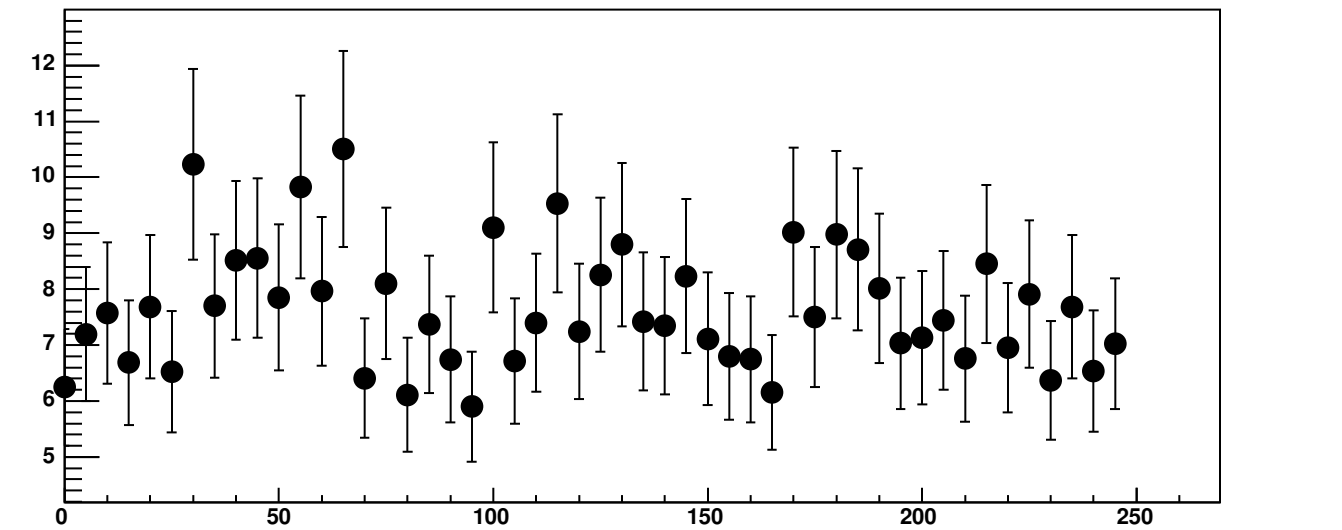
Chip 1, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold



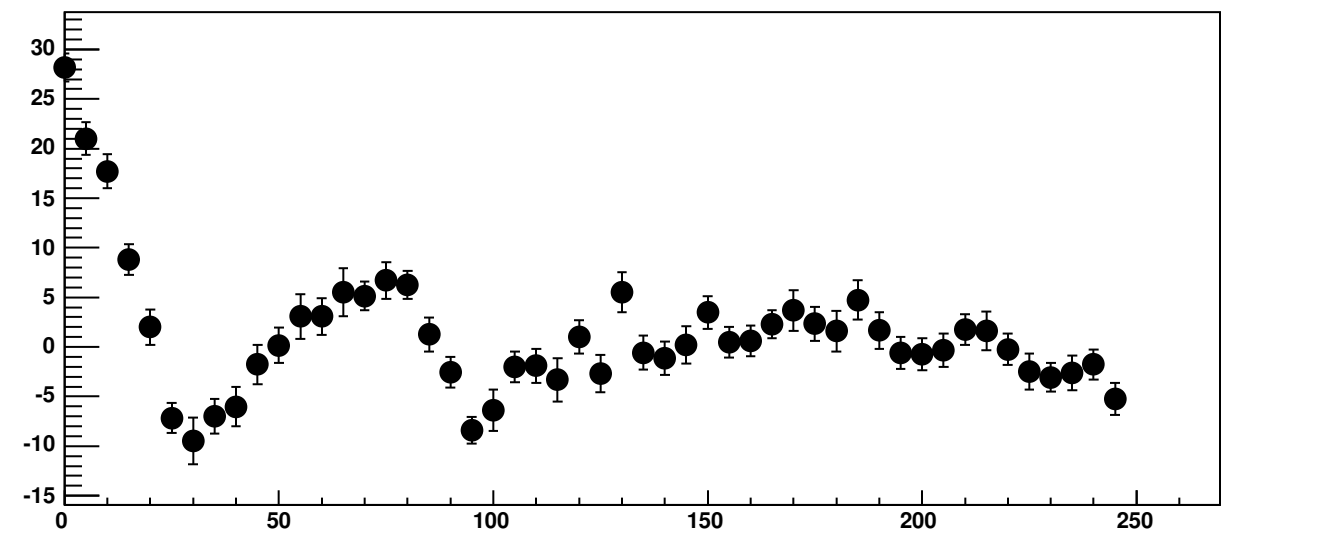
Chip 1, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

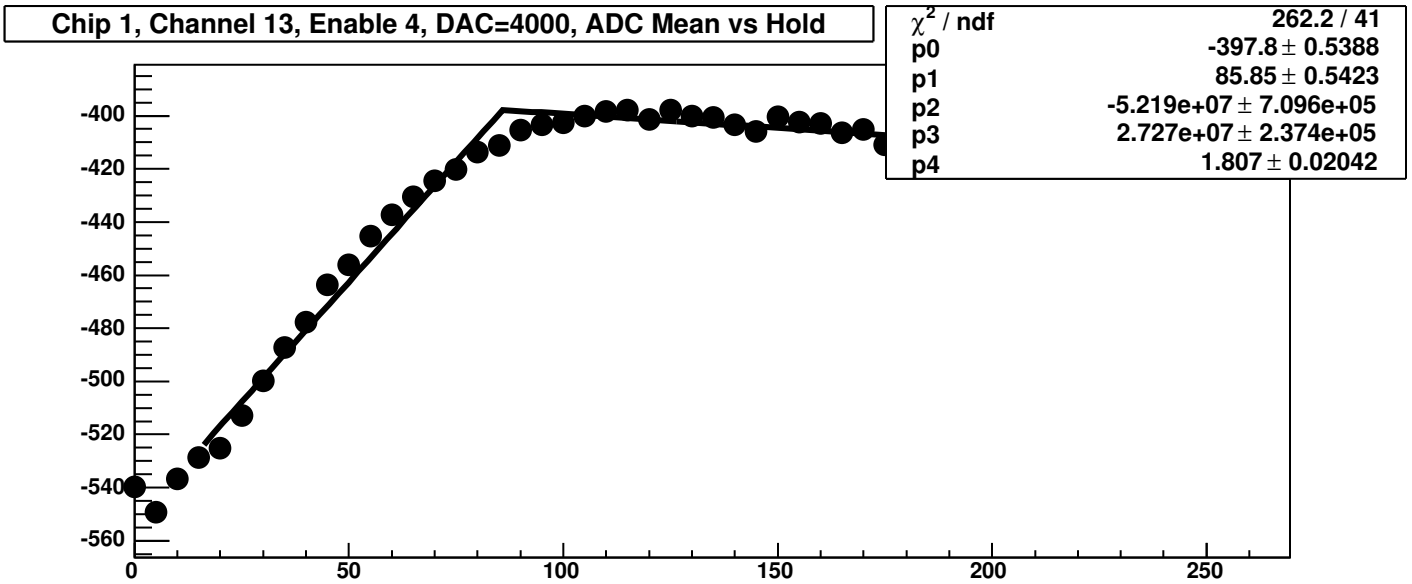


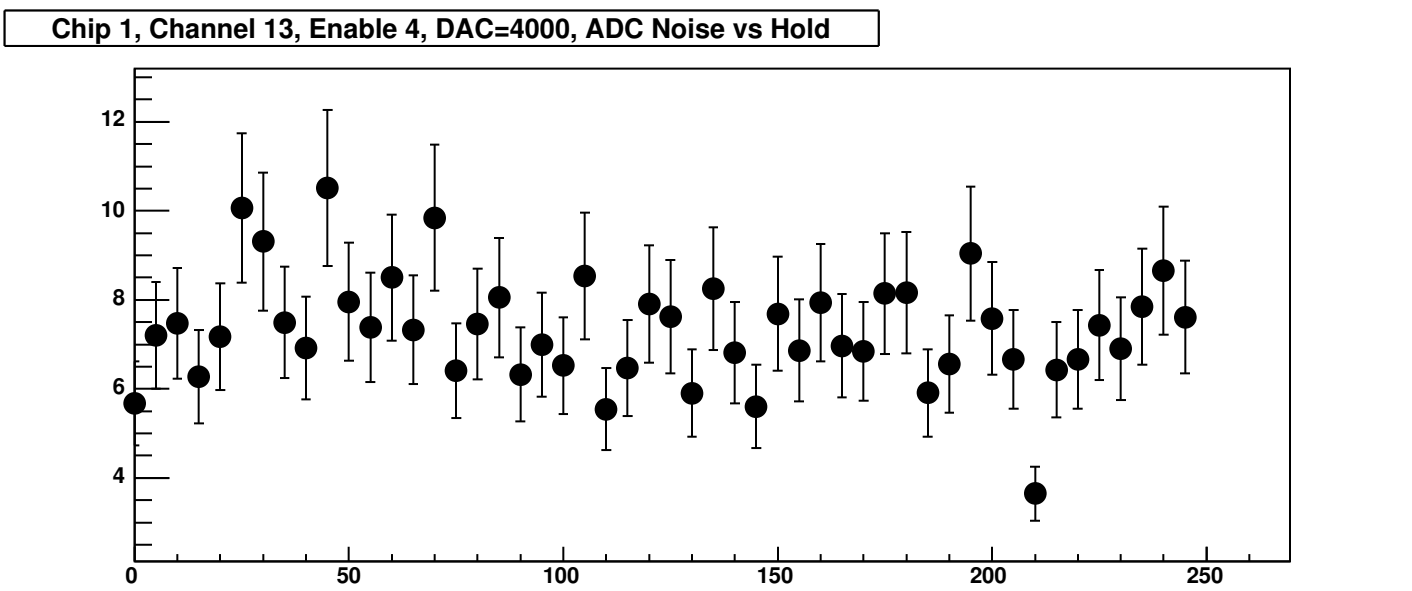
Chip 1, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold

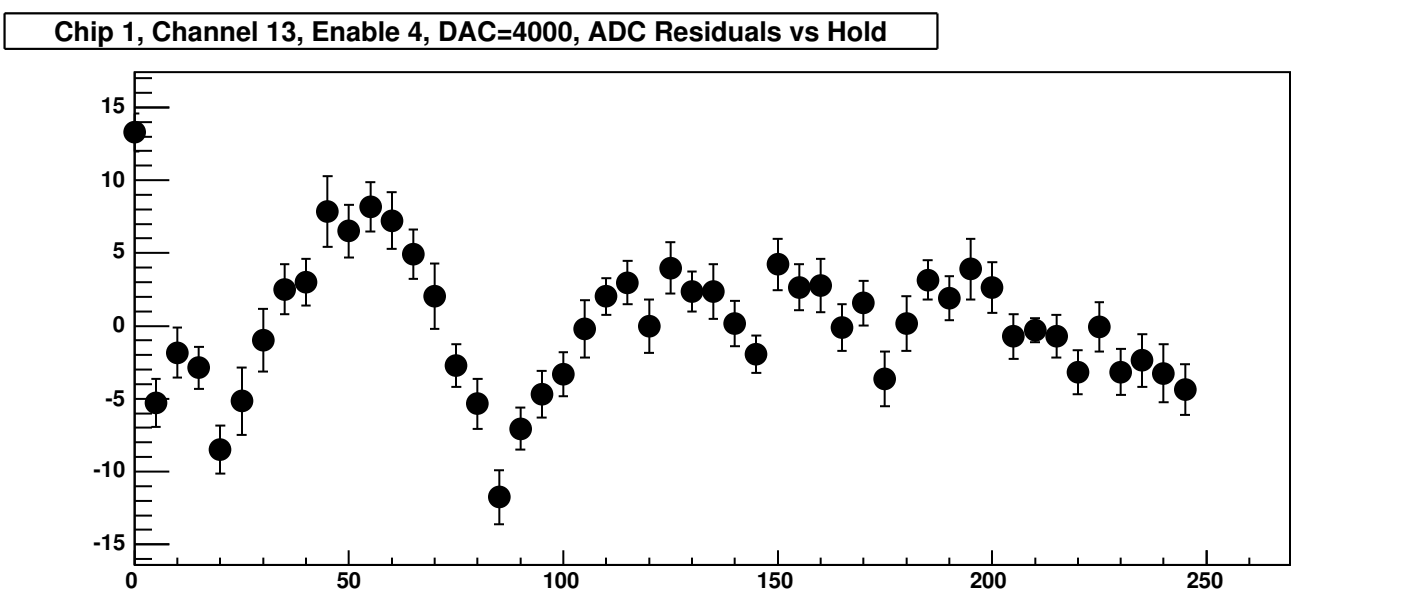


Chip 1, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

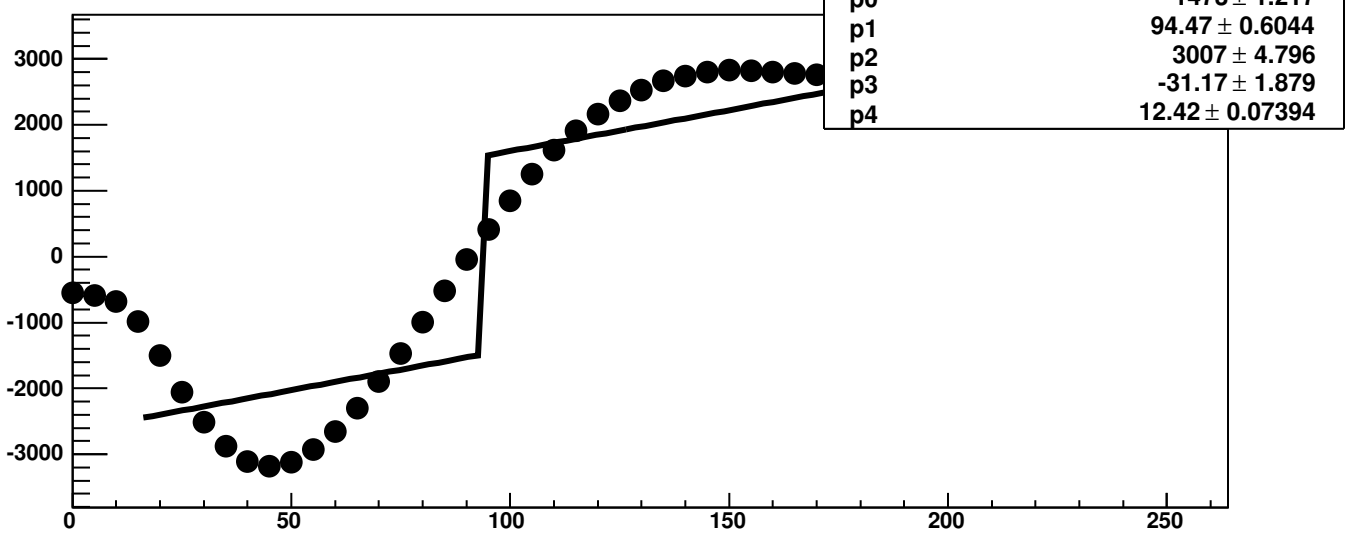



Chip 1, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 262.2 / 41
p0 -397.8 ± 0.5388
p1 85.85 ± 0.5423
p2 -5.219e+07 ± 7.096e+05
p3 2.727e+07 ± 2.374e+05
p4 1.807 ± 0.02042
-400
-420
-440
-460
-480
-500
-520
-540
-560
0
50
100
150
200
250

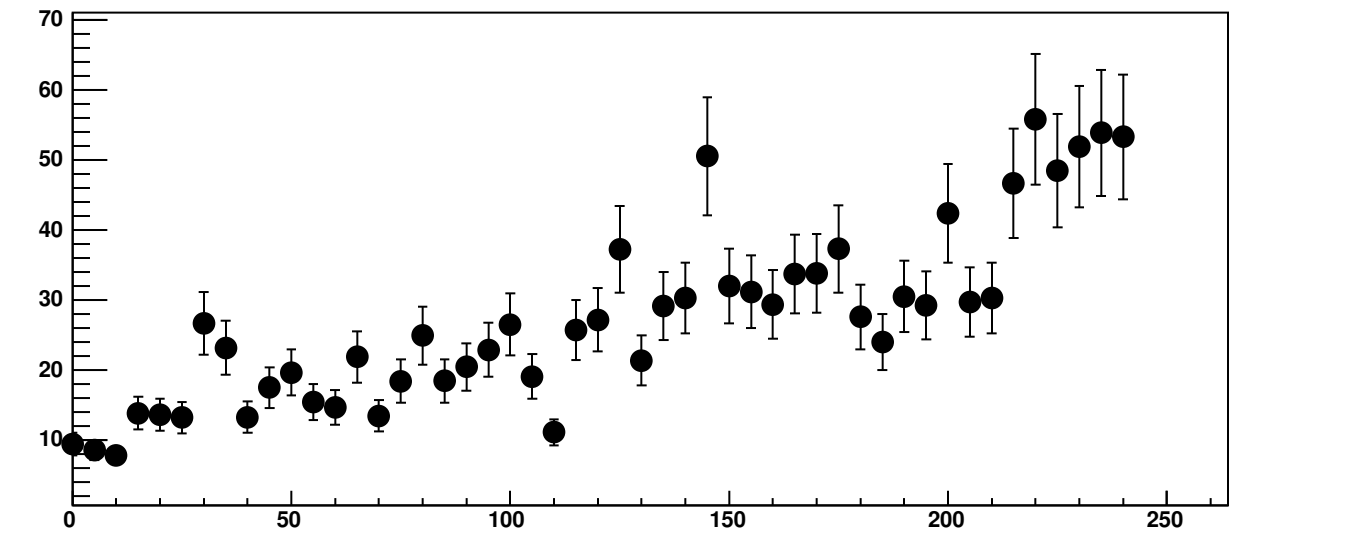

Chip 1, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 1, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

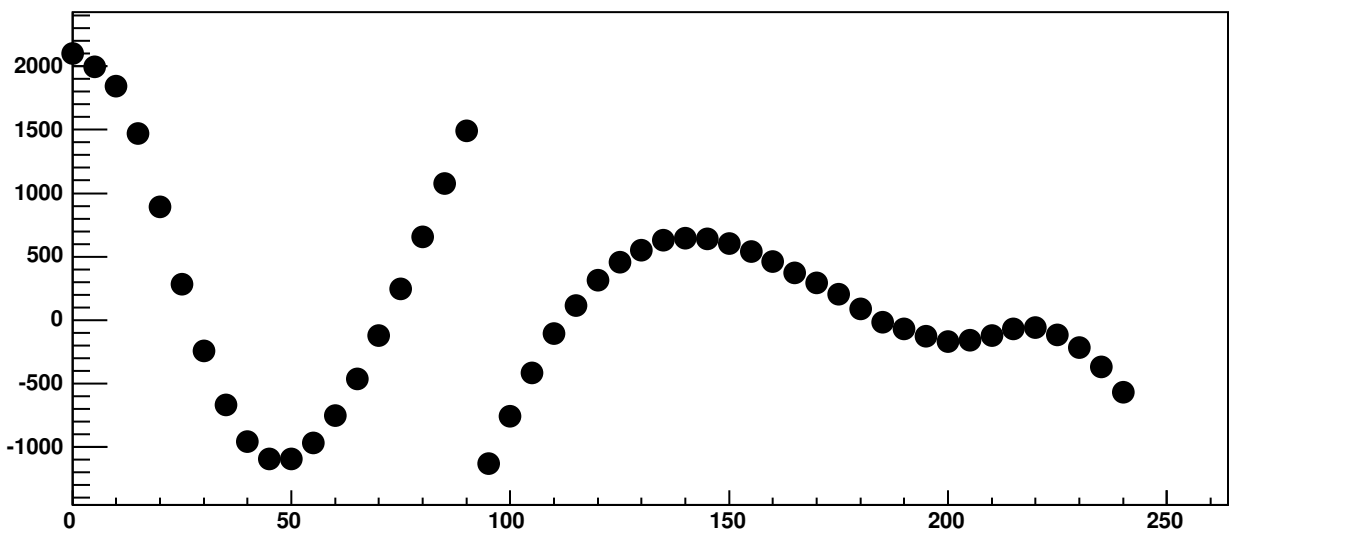
Chip 1, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold



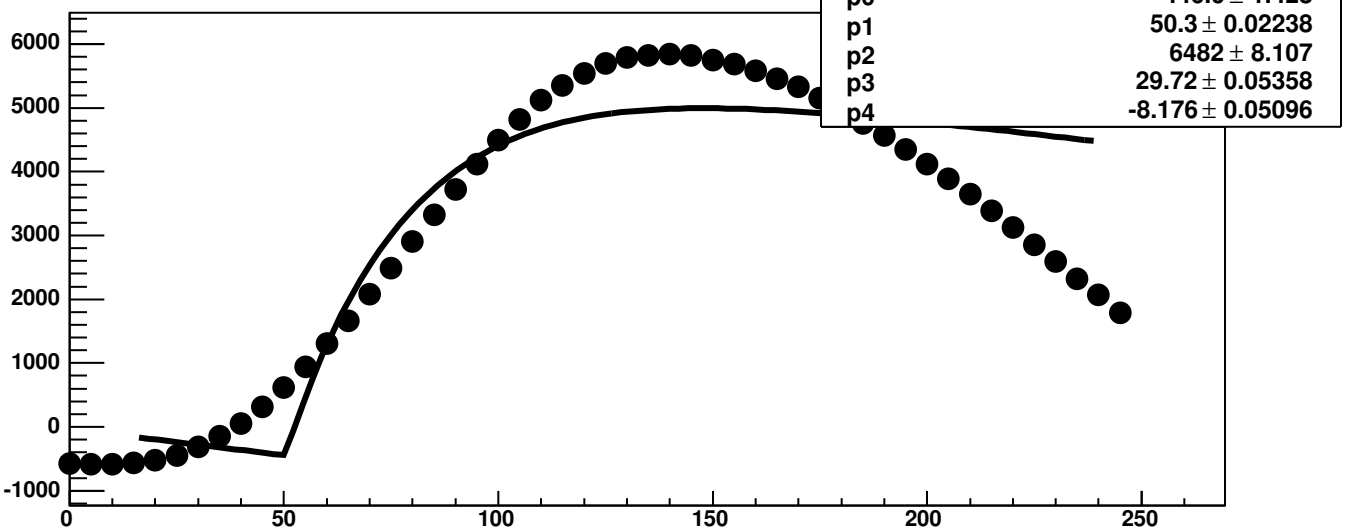
Chip 1, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



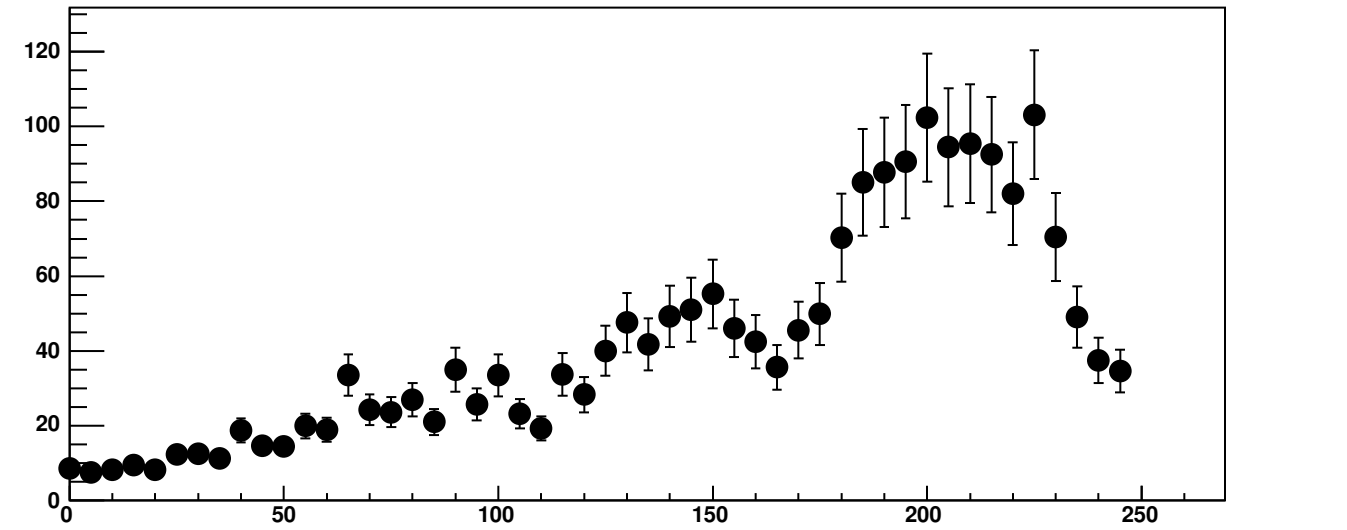
Chip 1, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold



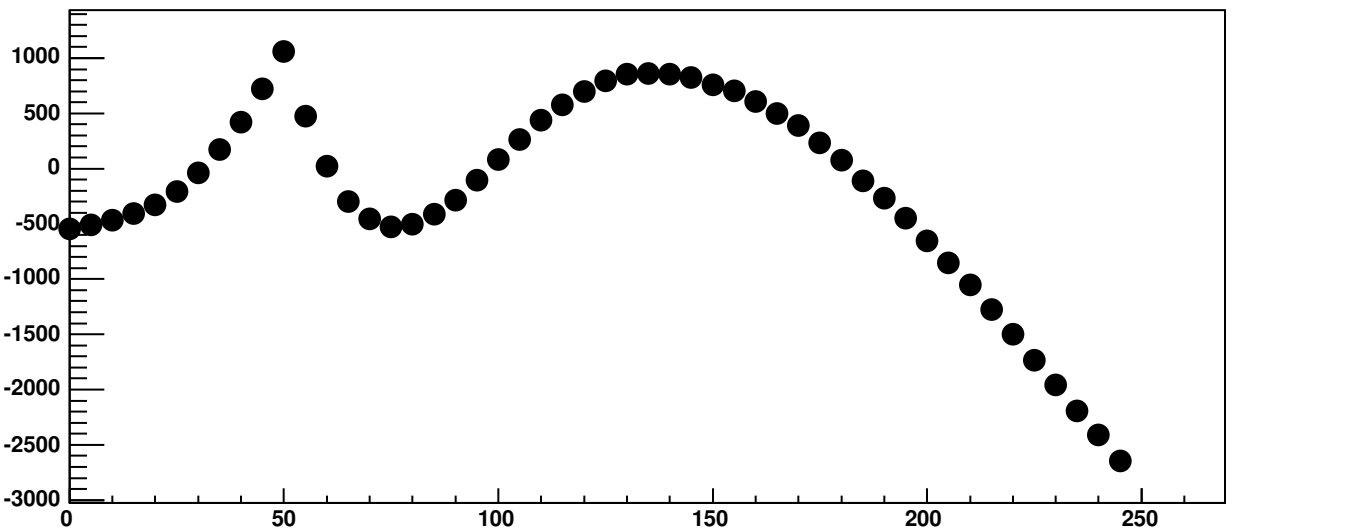
Chip 1, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold



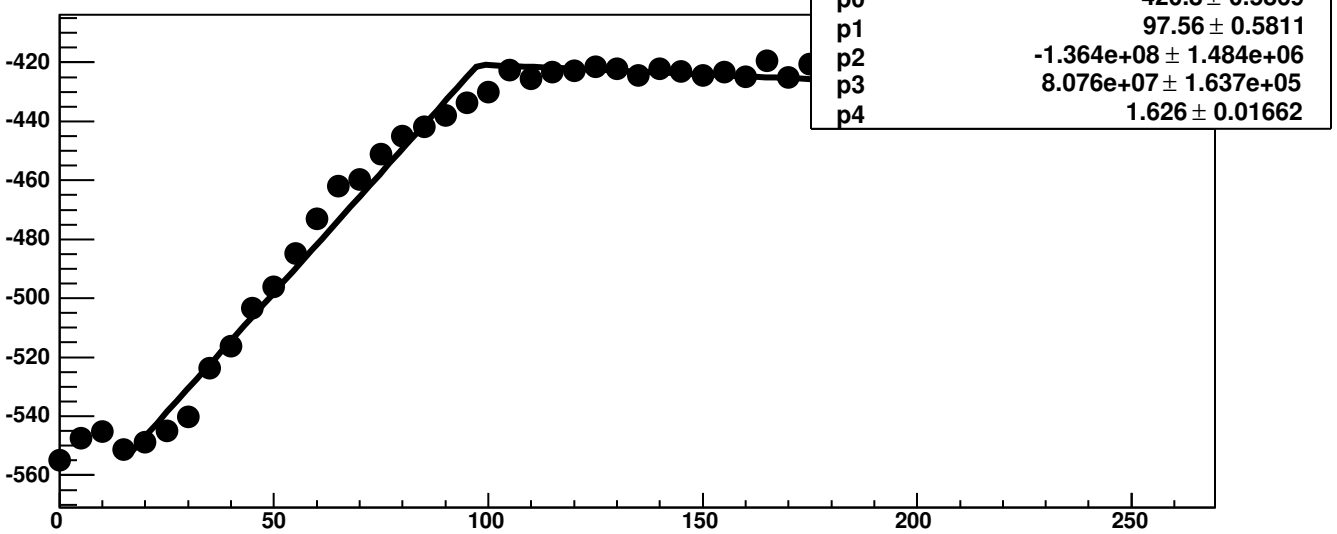
Chip 1, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



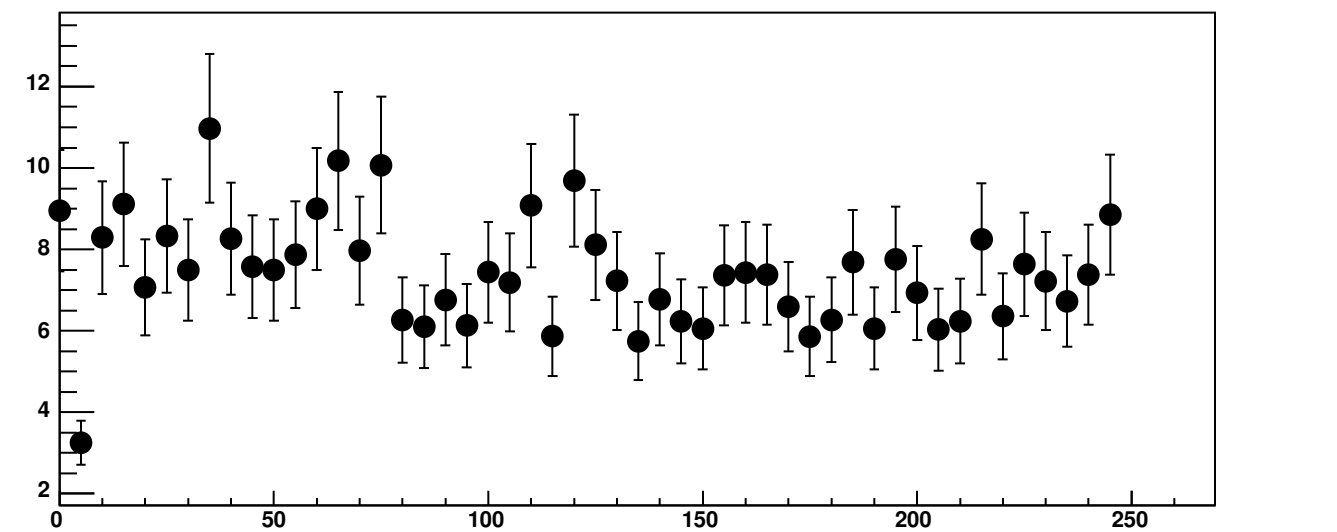
Chip 1, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold



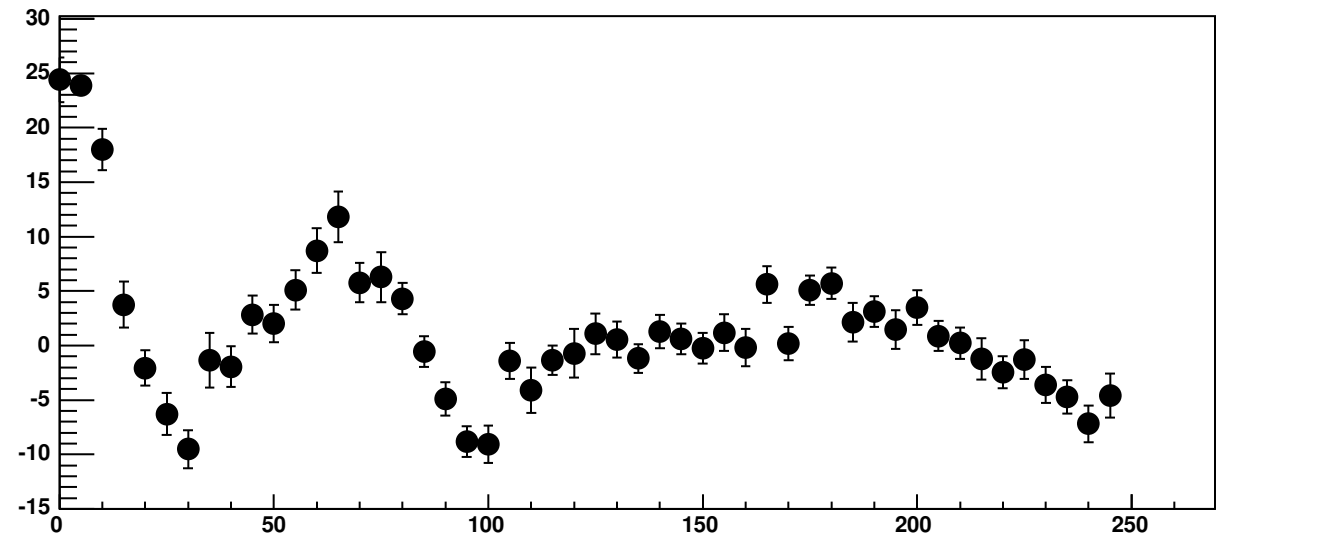
Chip 1, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold



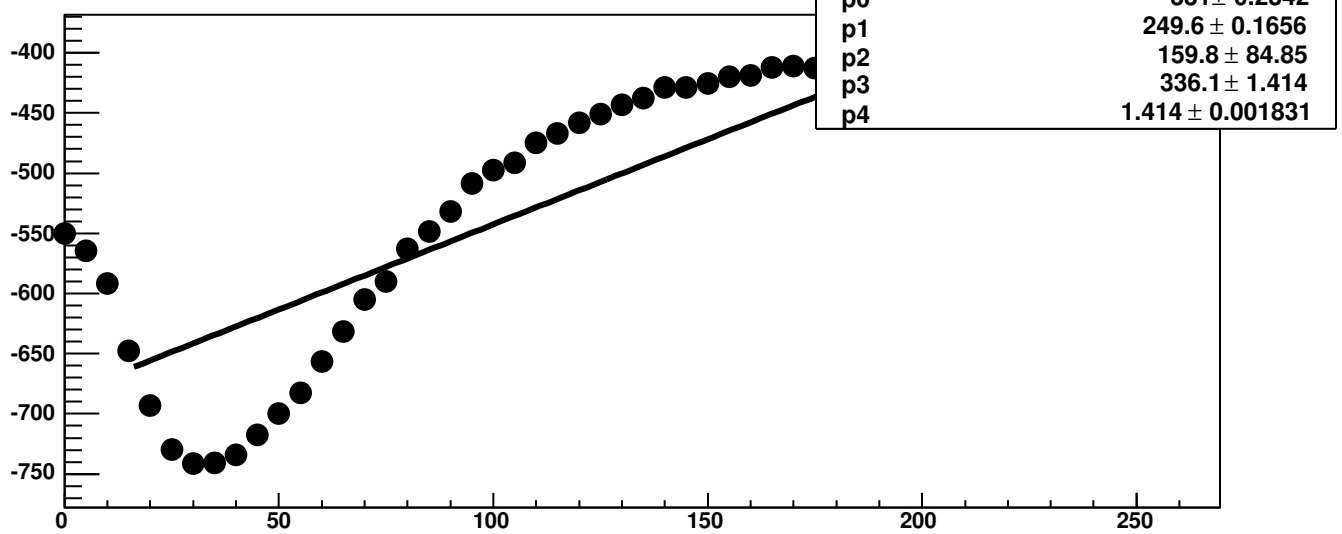
Chip 1, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold



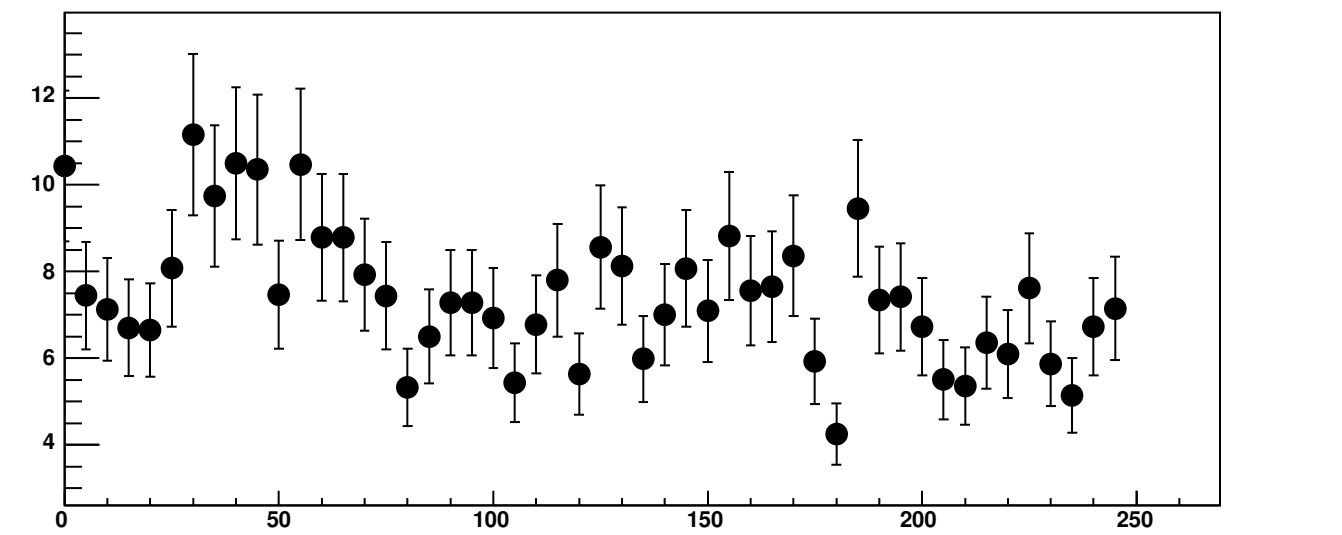
Chip 1, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold



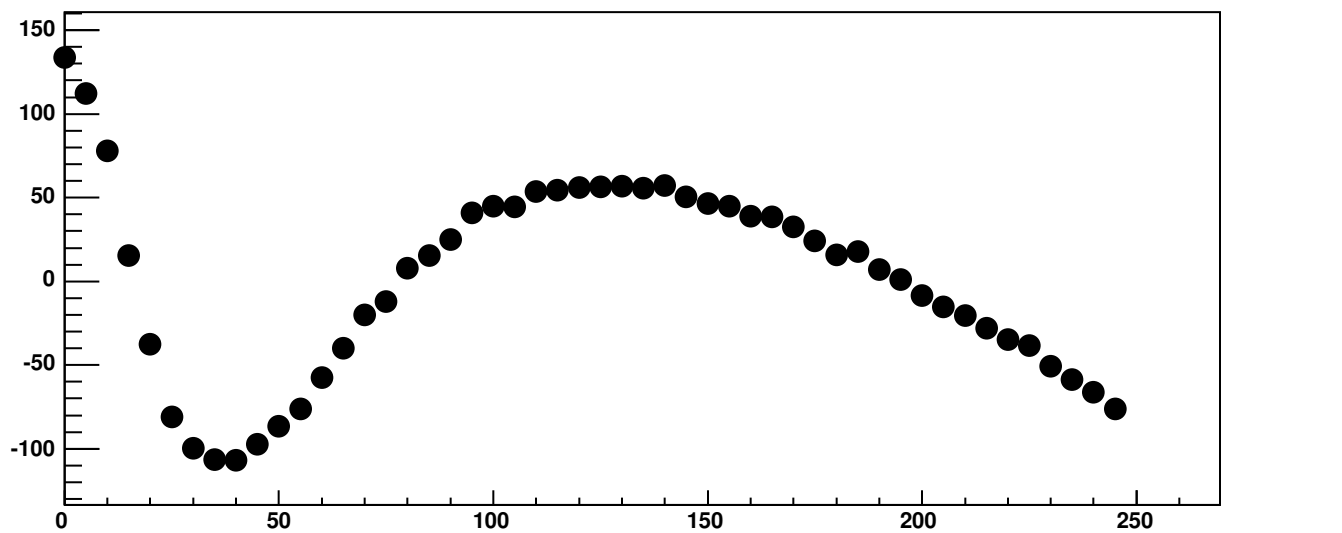
Chip 1, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

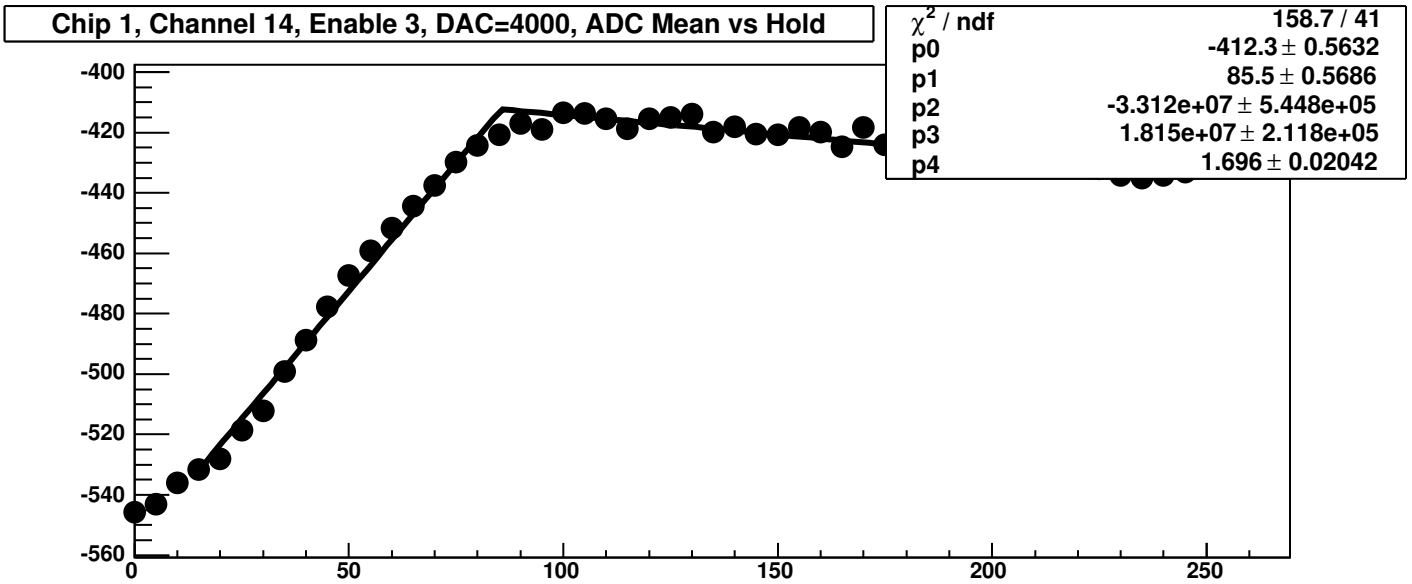


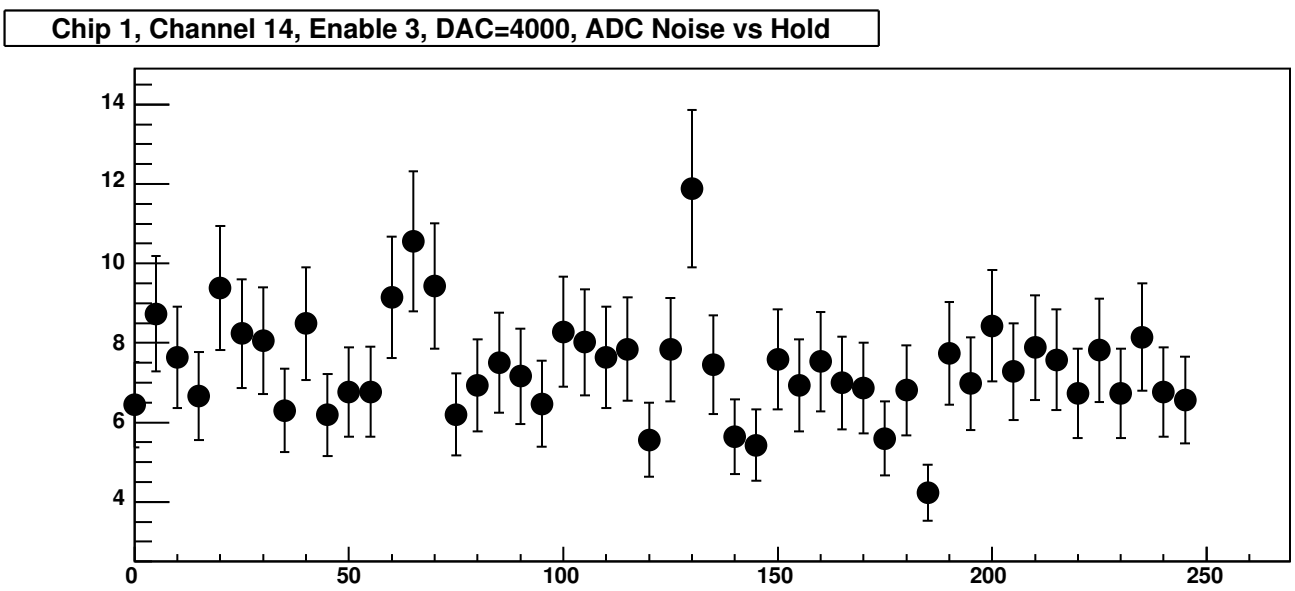
Chip 1, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold

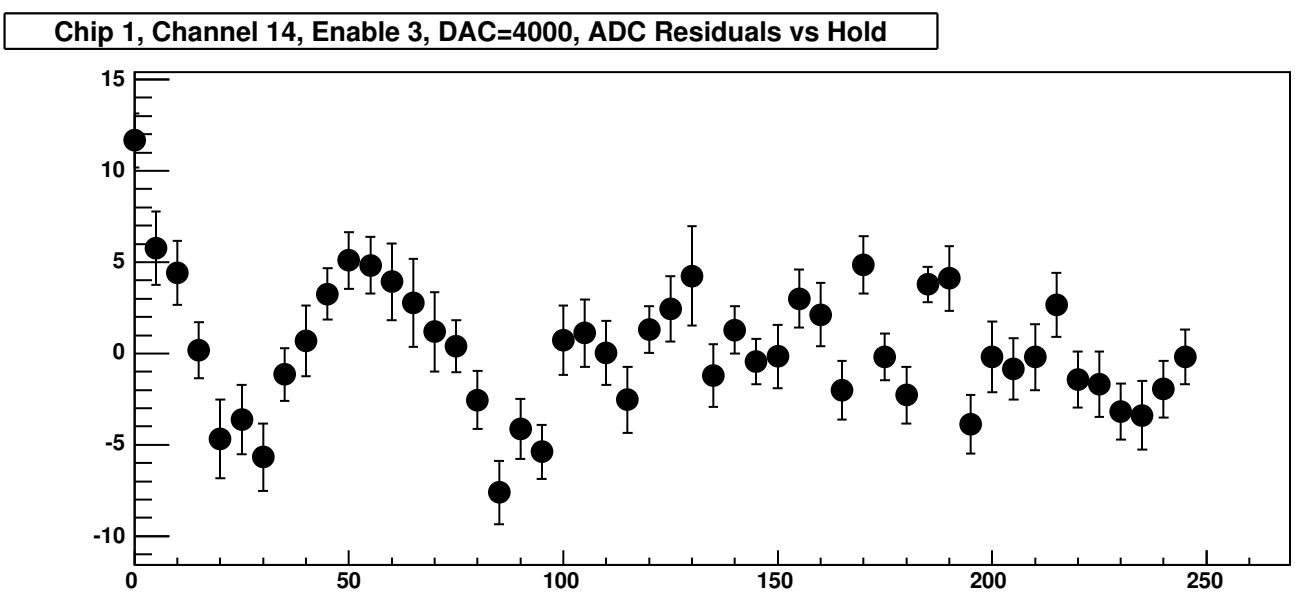


Chip 1, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold

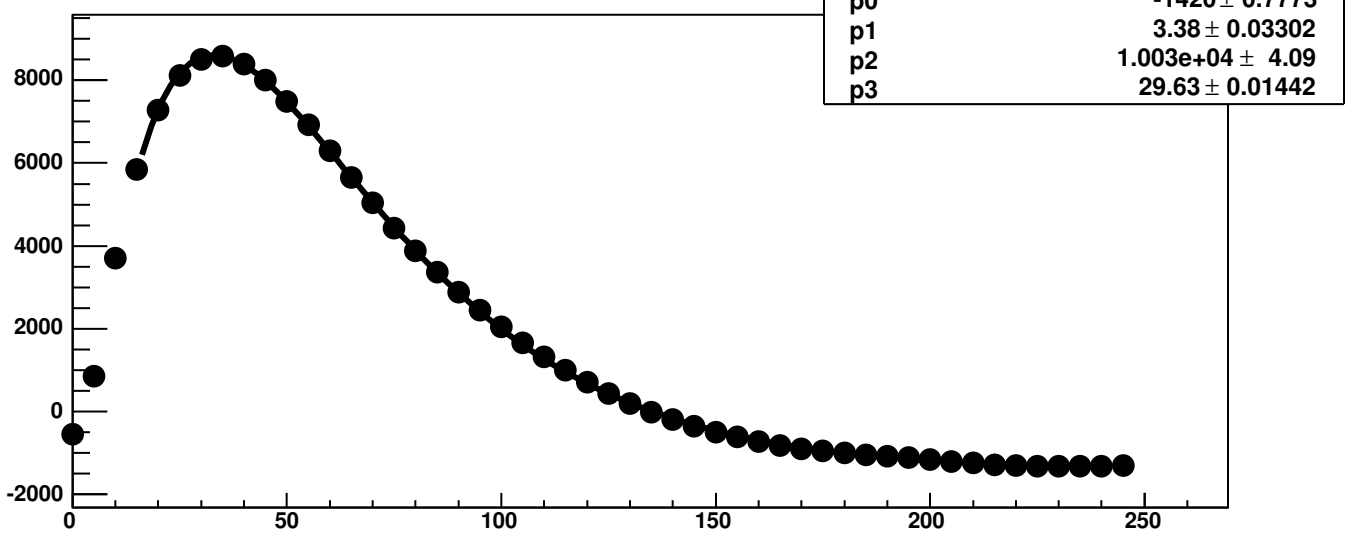



Chip 1, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 158.7 / 41
p0 -412.3 ± 0.5632
p1 85.5 ± 0.5686
p2 -3.312e+07 ± 5.448e+05
p3 1.815e+07 ± 2.118e+05
p4 1.696 ± 0.02042
-400
-420
-440
-460
-480
-500
-520
-540
-560
0
50
100
150
200
250

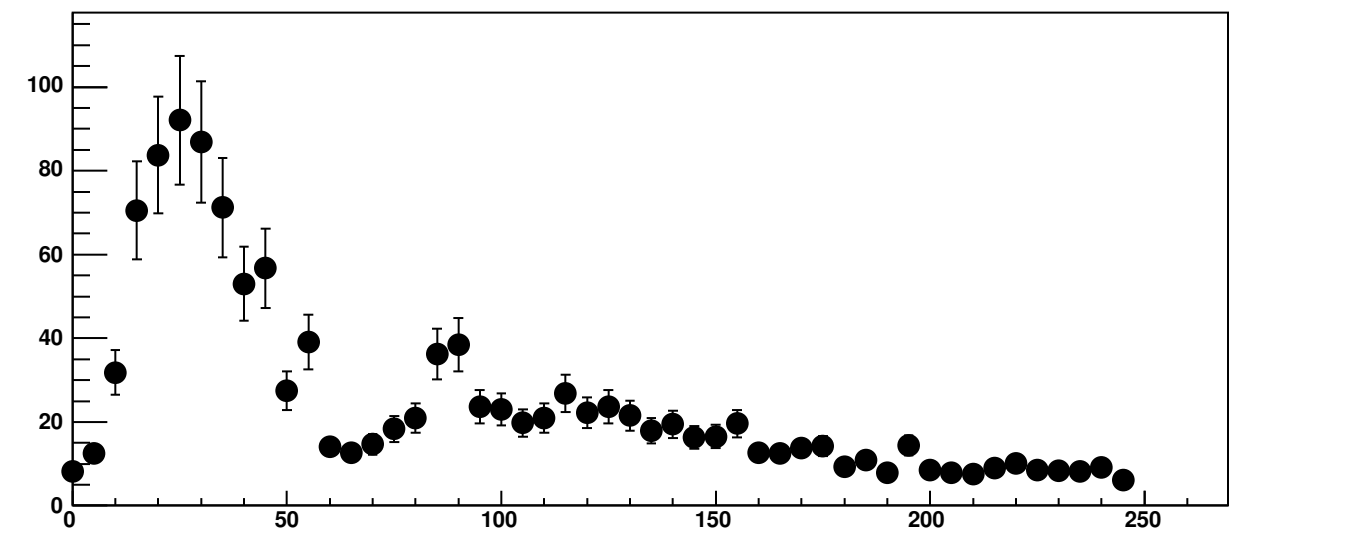

Chip 1, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 1, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
0
50
100
150
200
250

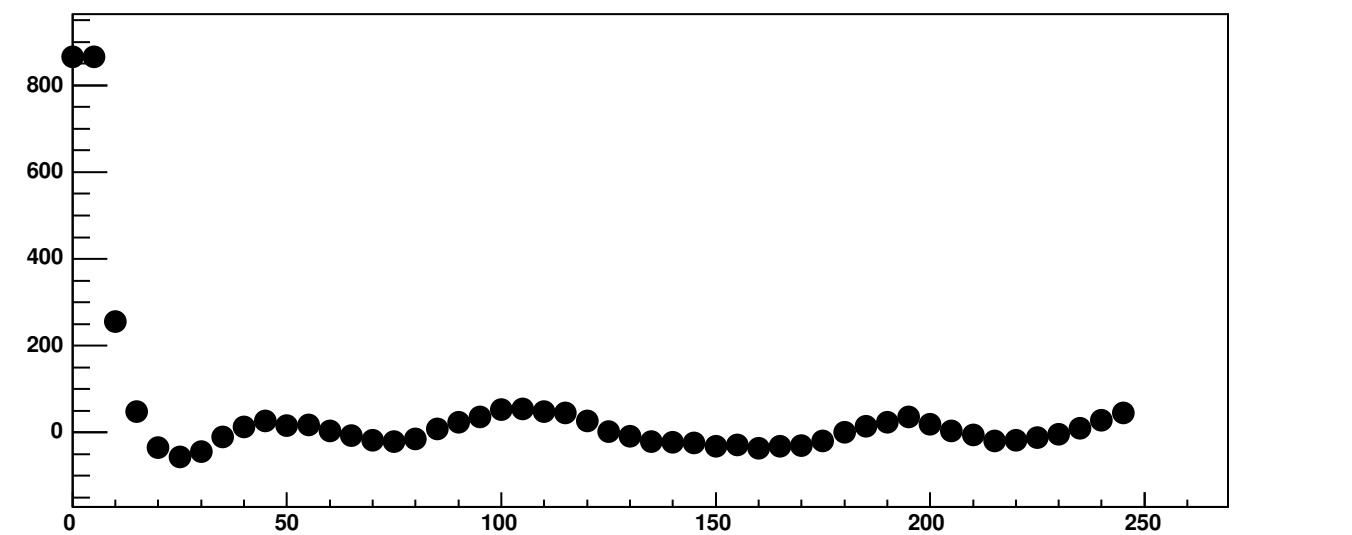
Chip 1, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold

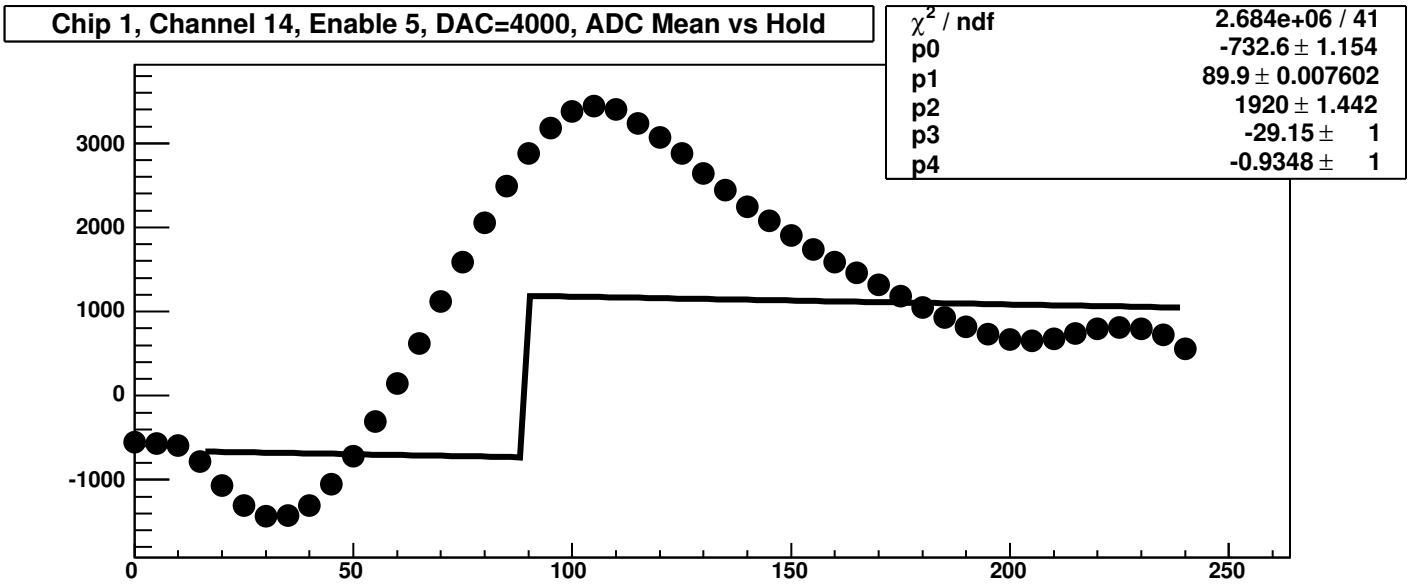


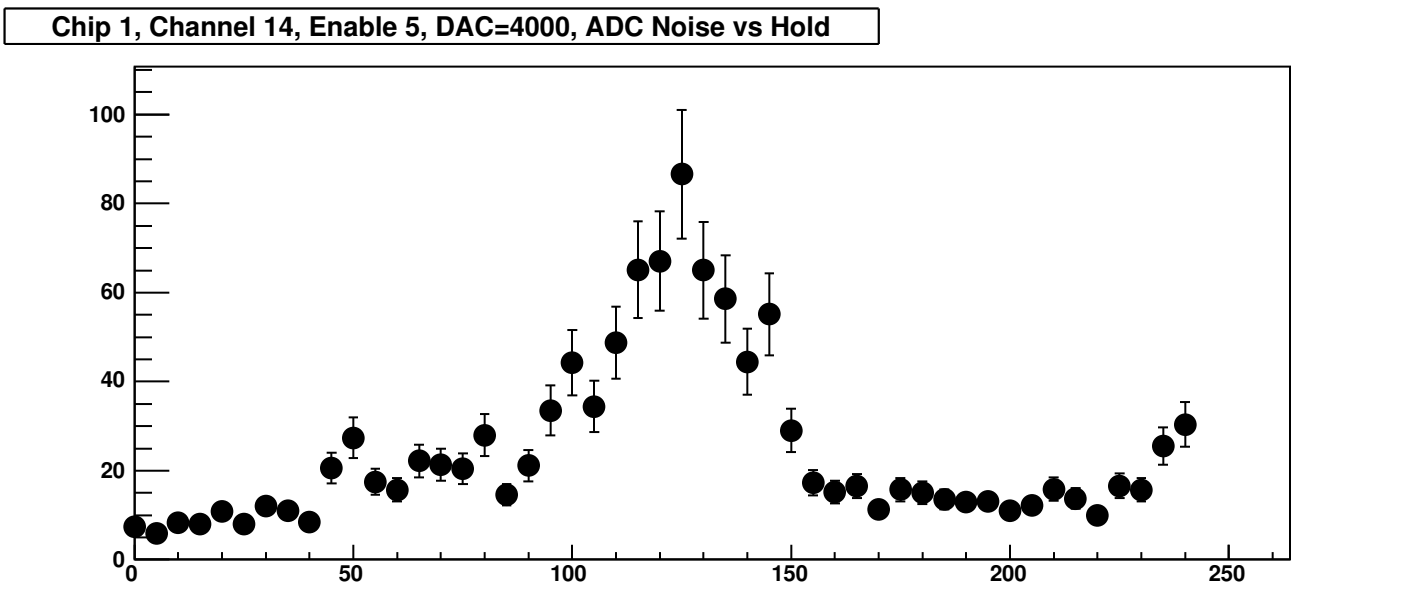
Chip 1, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold

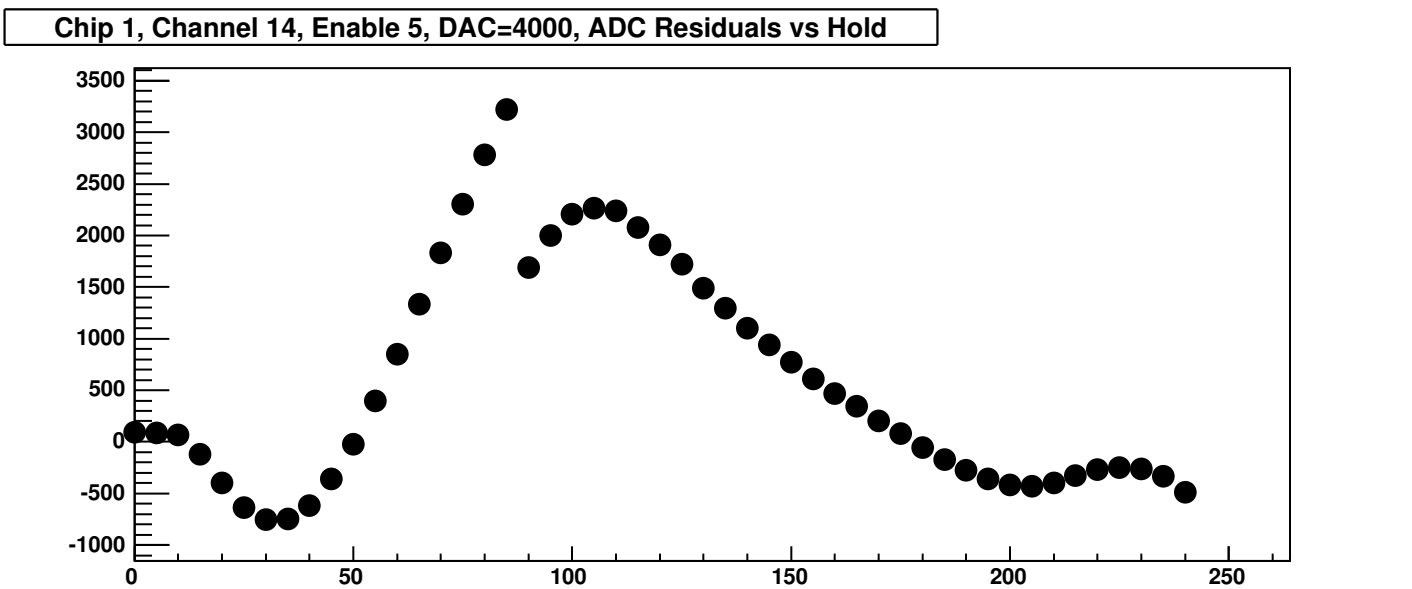


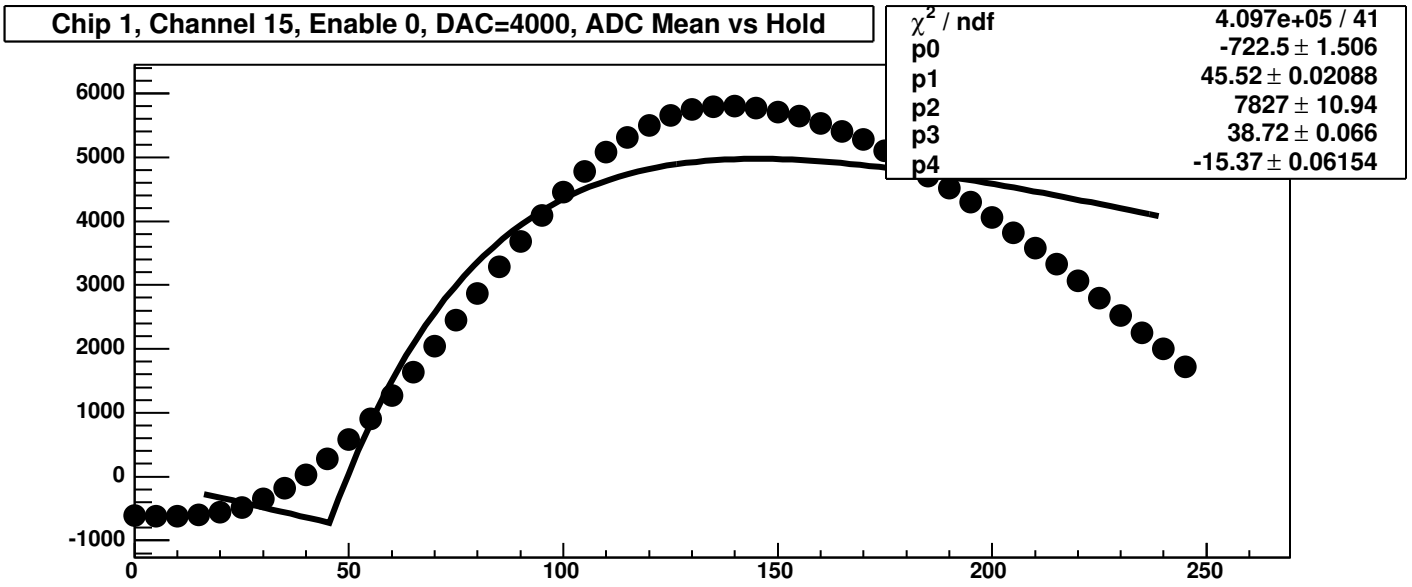
Chip 1, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold

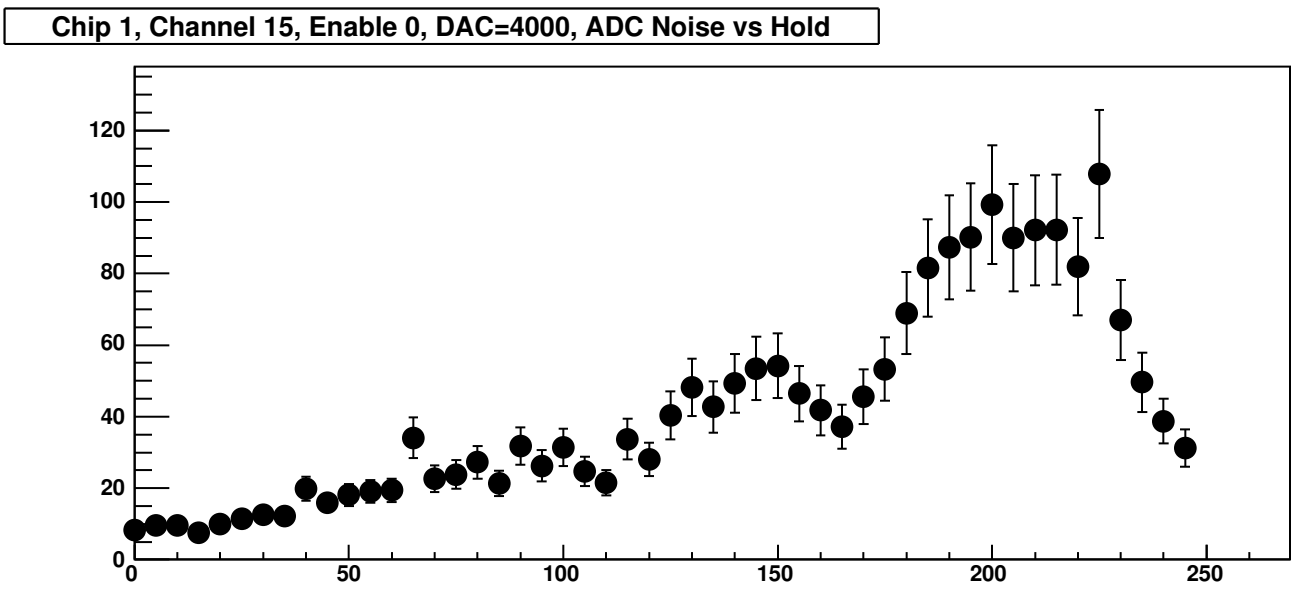


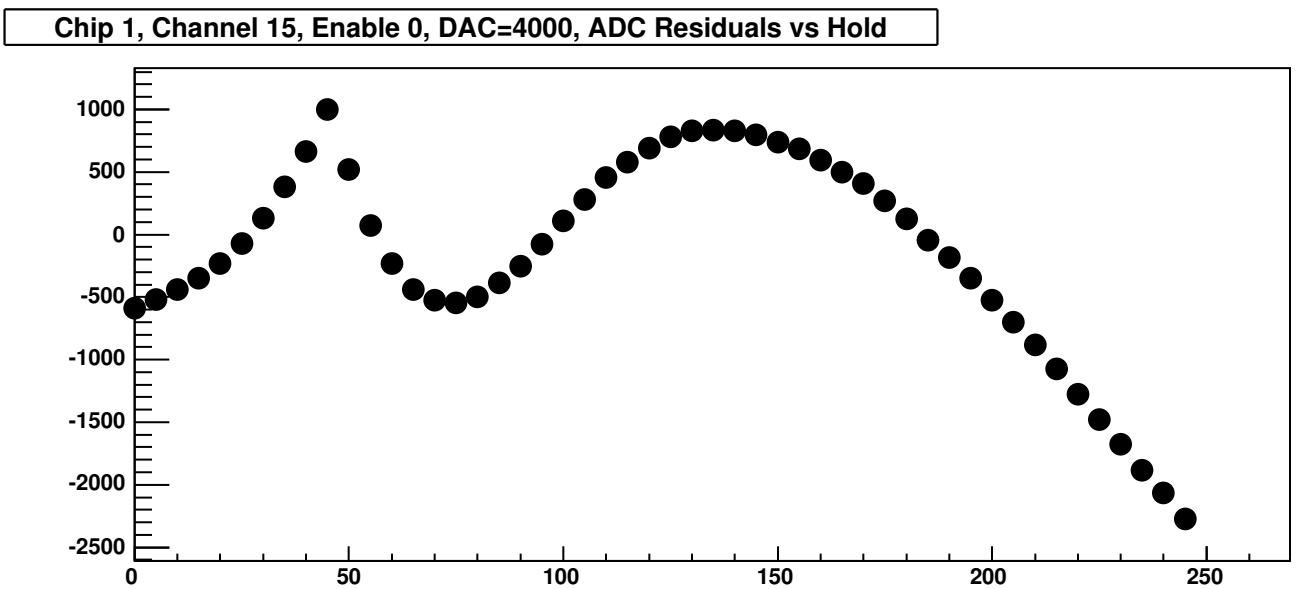

Chip 1, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.684e+06 / 41
p0 -732.6 ± 1.154
p1 89.9 ± 0.007602
p2 1920 ± 1.442
p3 -29.15 ± 1
p4 -0.9348 ± 1
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 1, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250

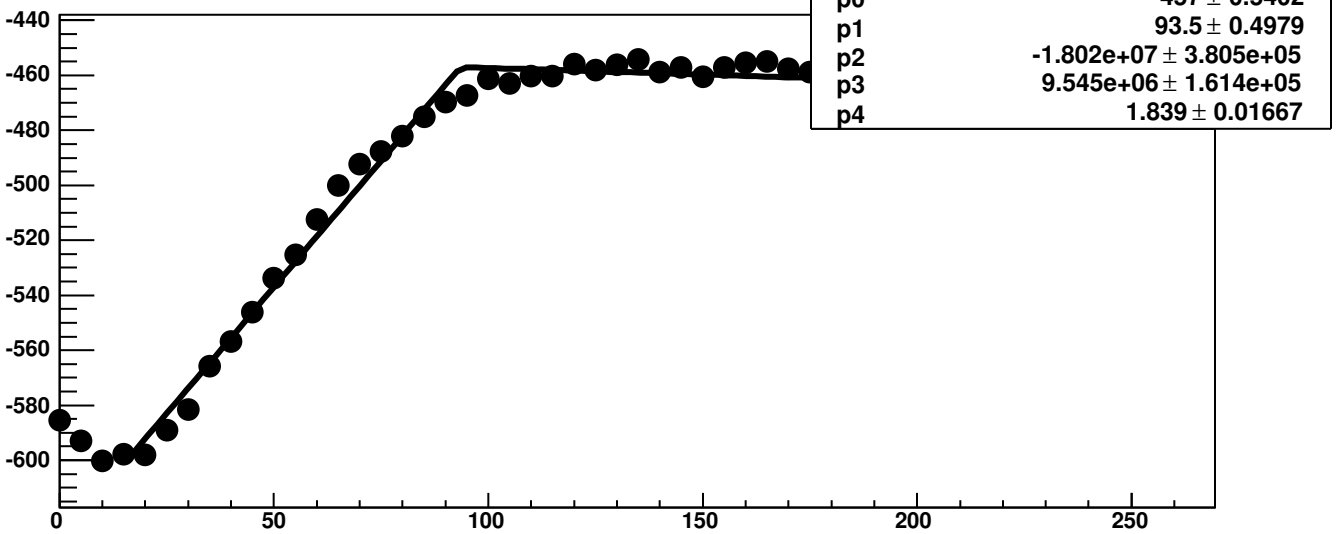

Chip 1, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold
3500
3000
2500
2000
1500
1000
500
0
-500
-1000
0
50
100
150
200
250


Chip 1, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.097e+05 / 41
p0 -722.5 ± 1.506
p1 45.52 ± 0.02088
p2 7827 ± 10.94
p3 38.72 ± 0.066
p4 -15.37 ± 0.06154
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

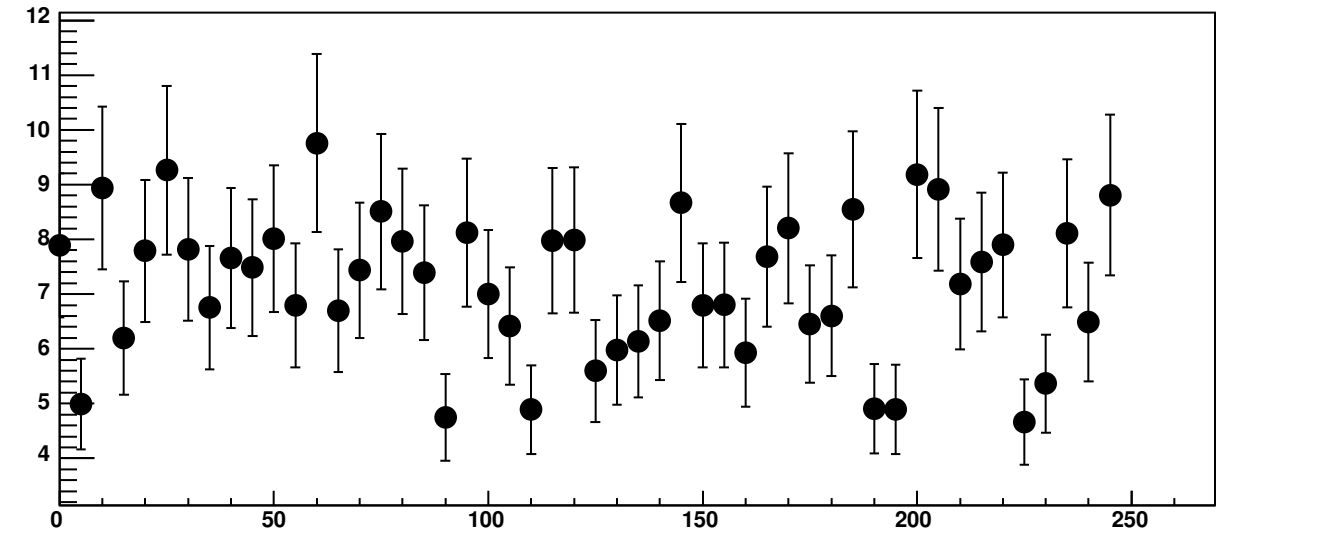

Chip 1, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 1, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
0
50
100
150
200
250

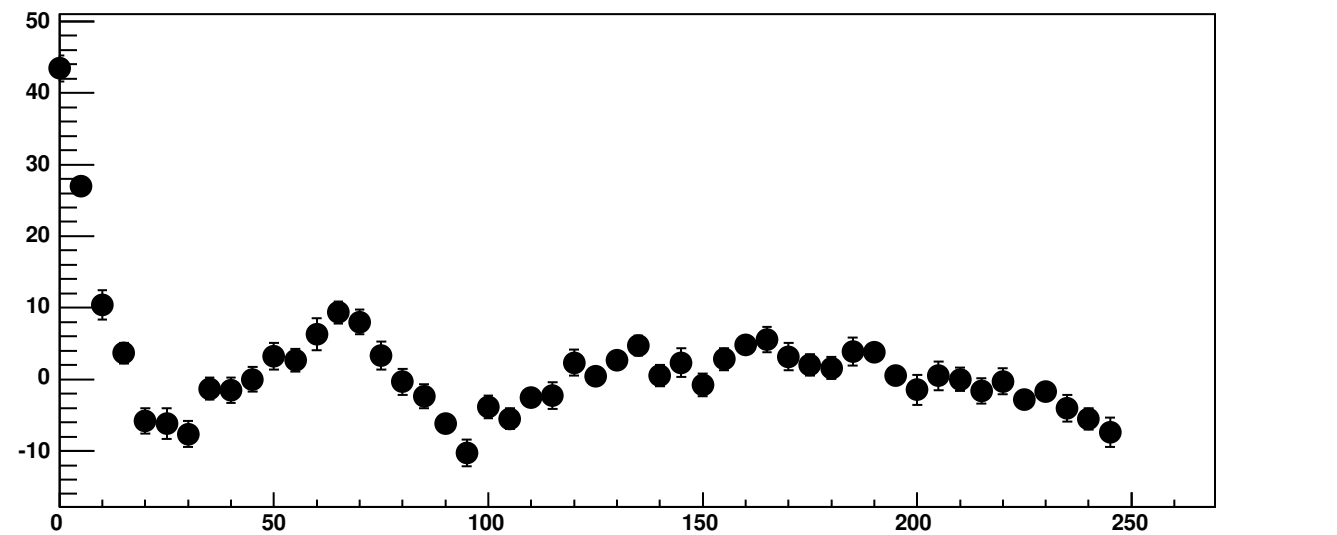
Chip 1, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

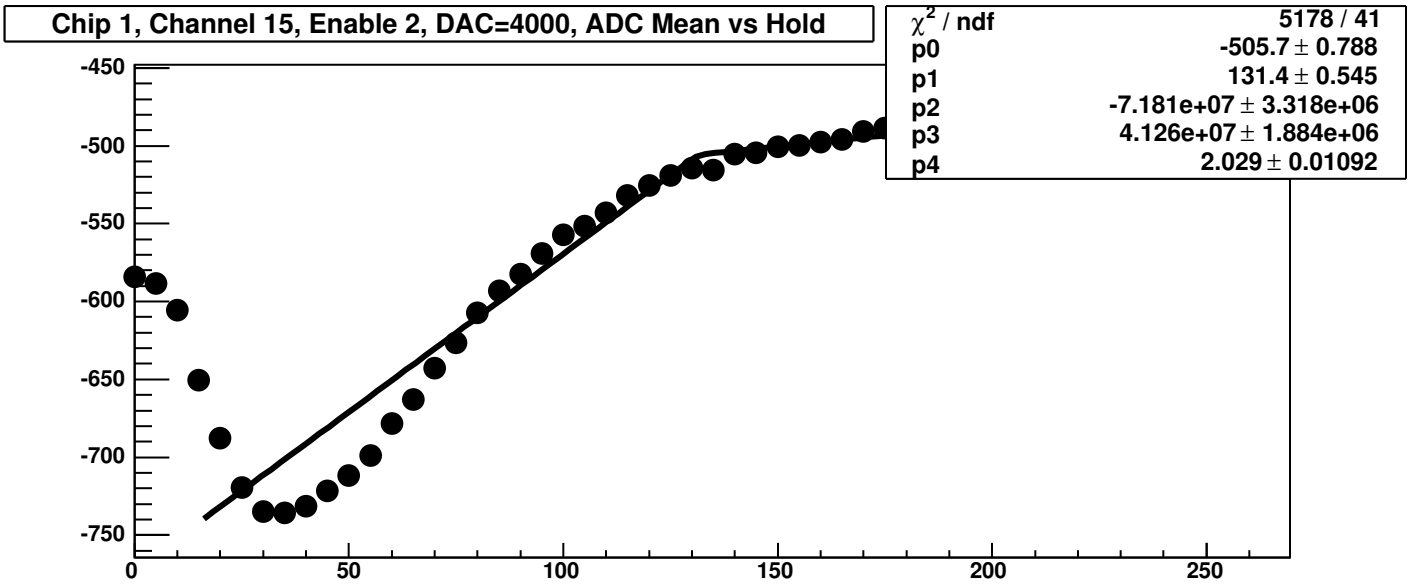


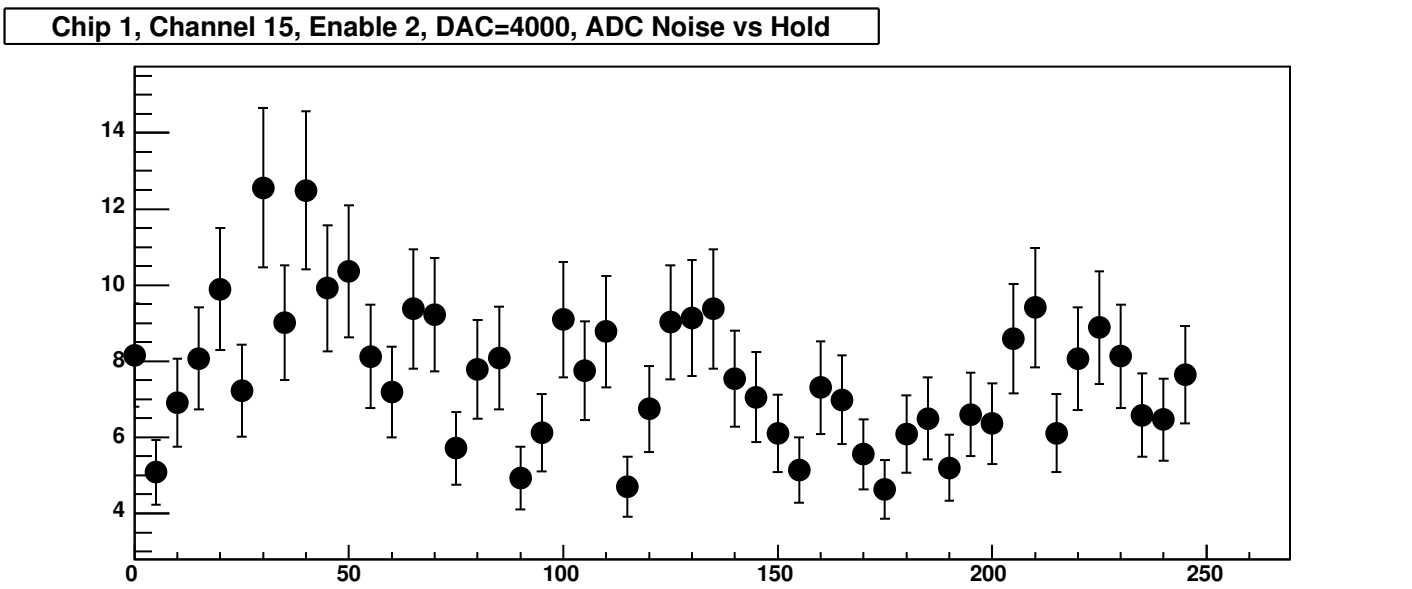
Chip 1, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold

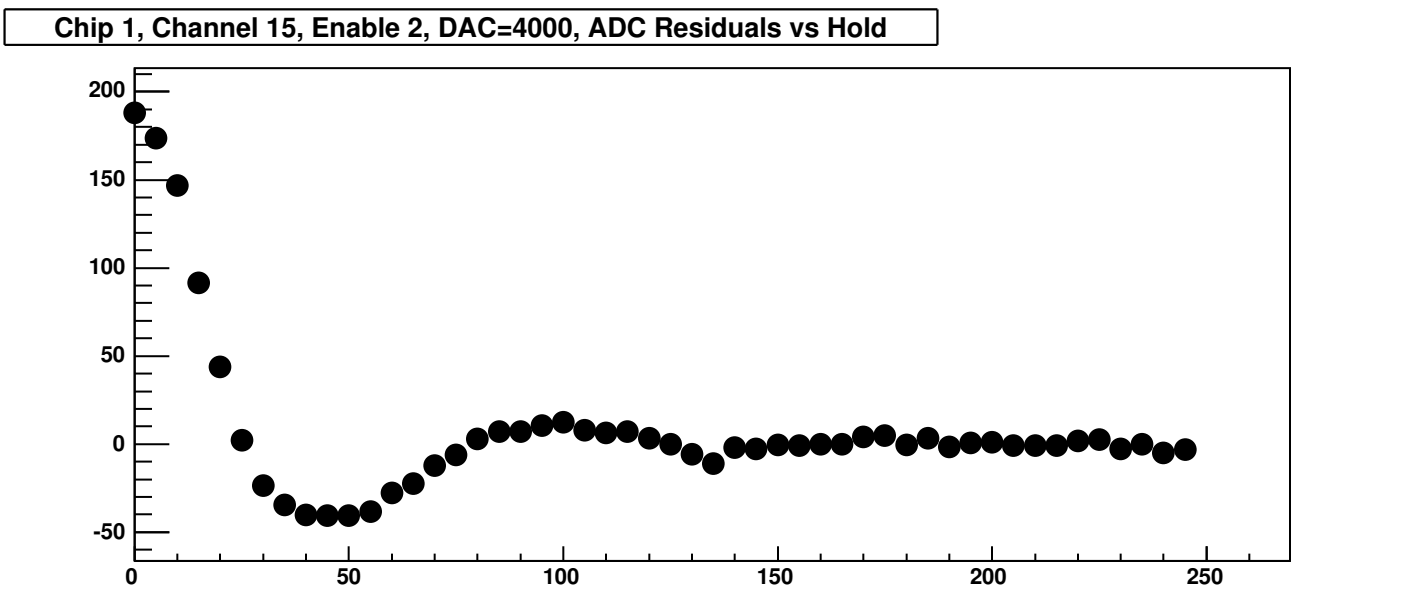


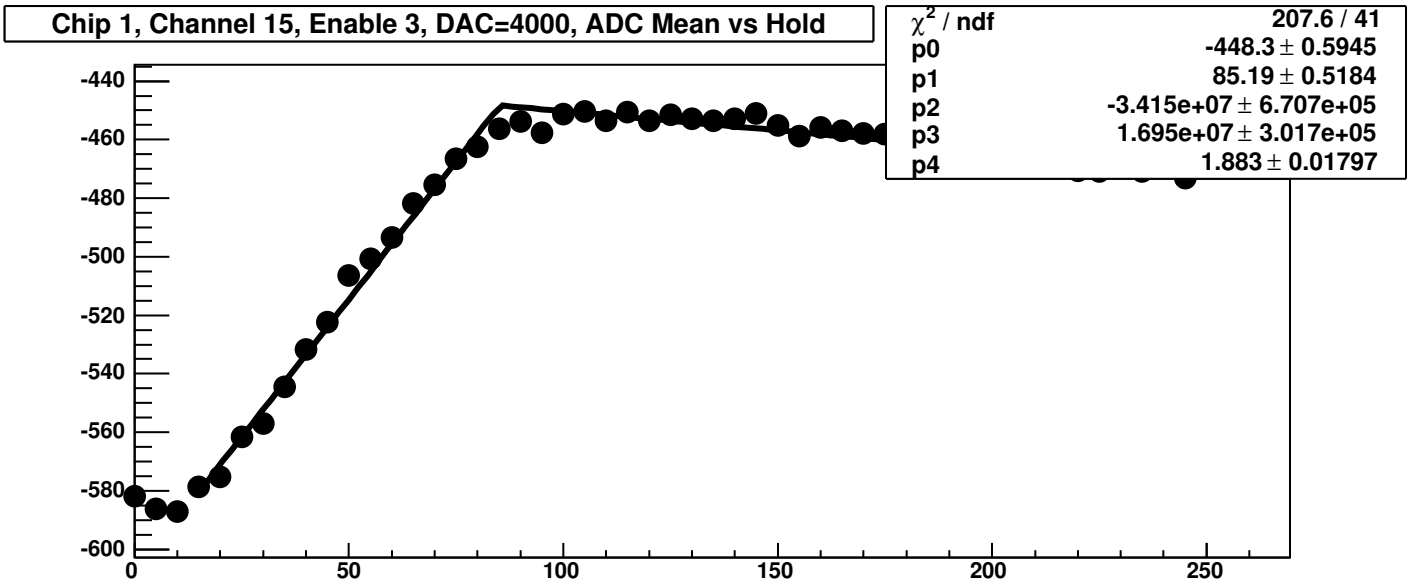
Chip 1, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

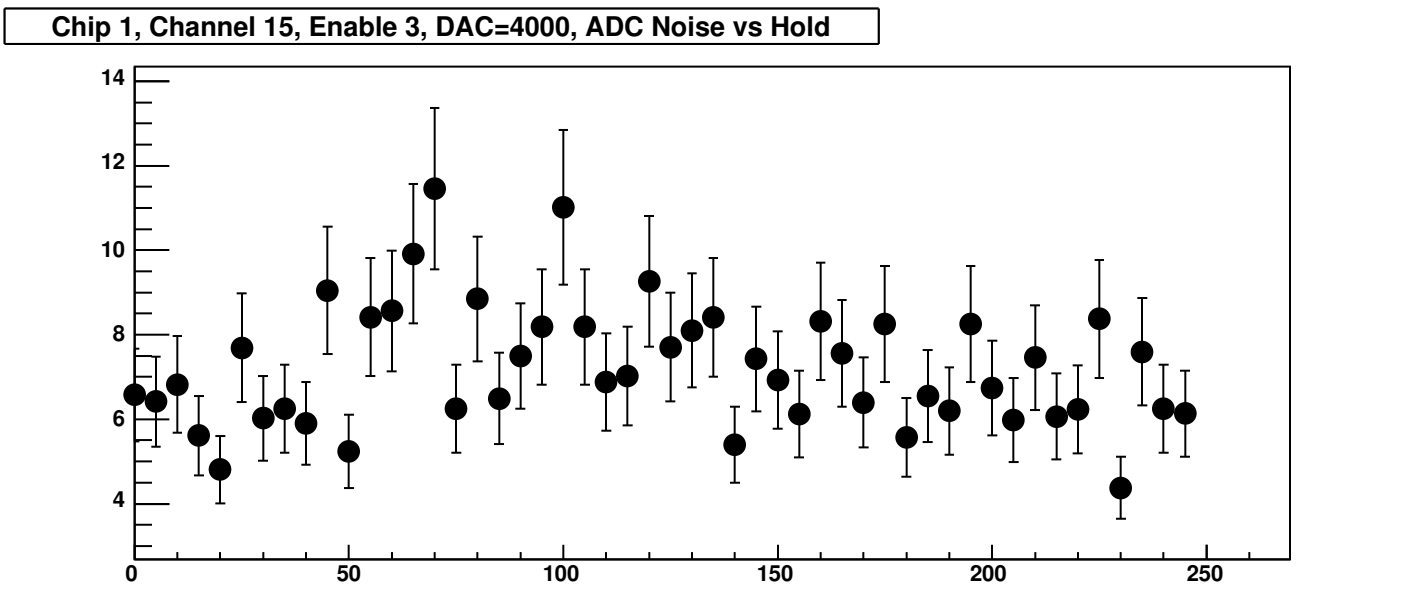


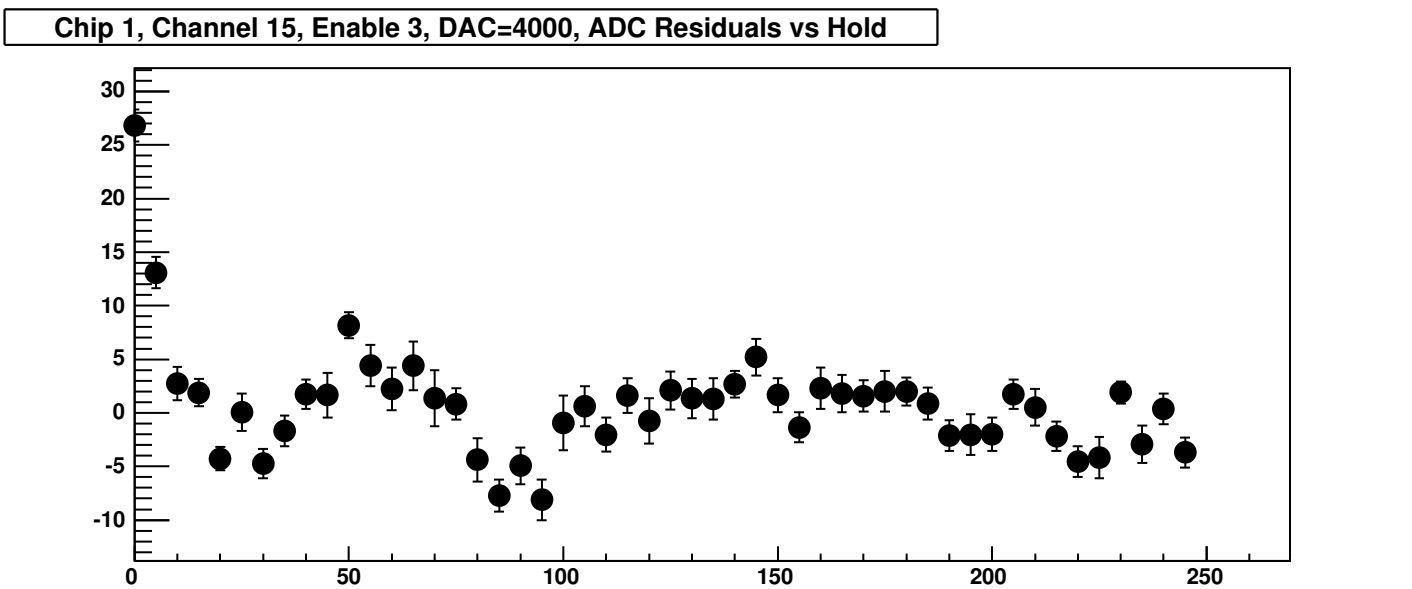

Chip 1, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 5178 / 41
p0 -505.7 ± 0.788
p1 131.4 ± 0.545
p2 -7.181e+07 ± 3.318e+06
p3 4.126e+07 ± 1.884e+06
p4 2.029 ± 0.01092
-450
-500
-550
-600
-650
-700
-750
0
50
100
150
200
250

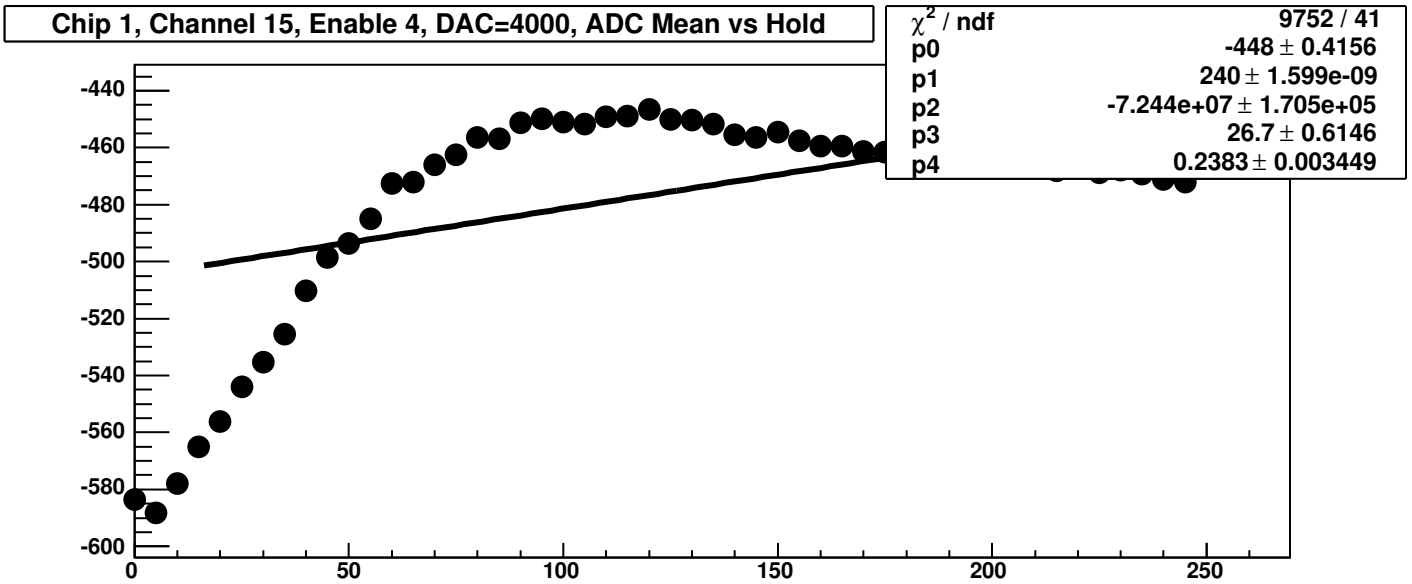

Chip 1, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

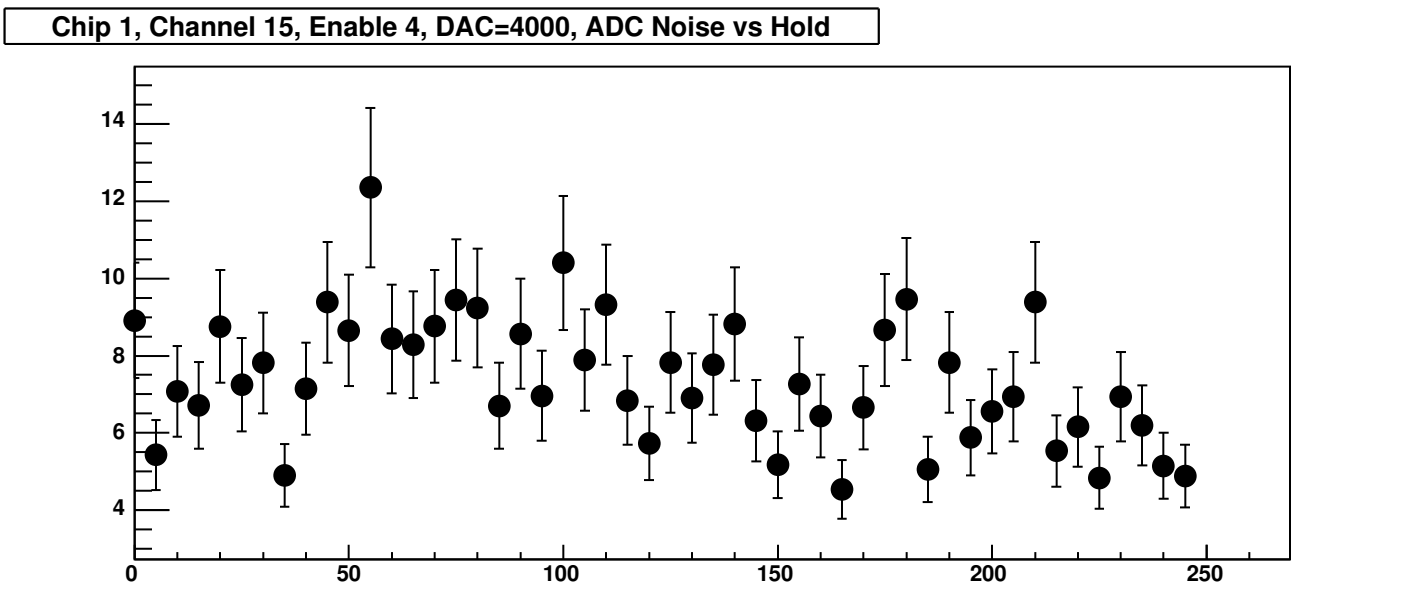

Chip 1, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold
200
150
100
50
0
-50
0
50
100
150
200
250

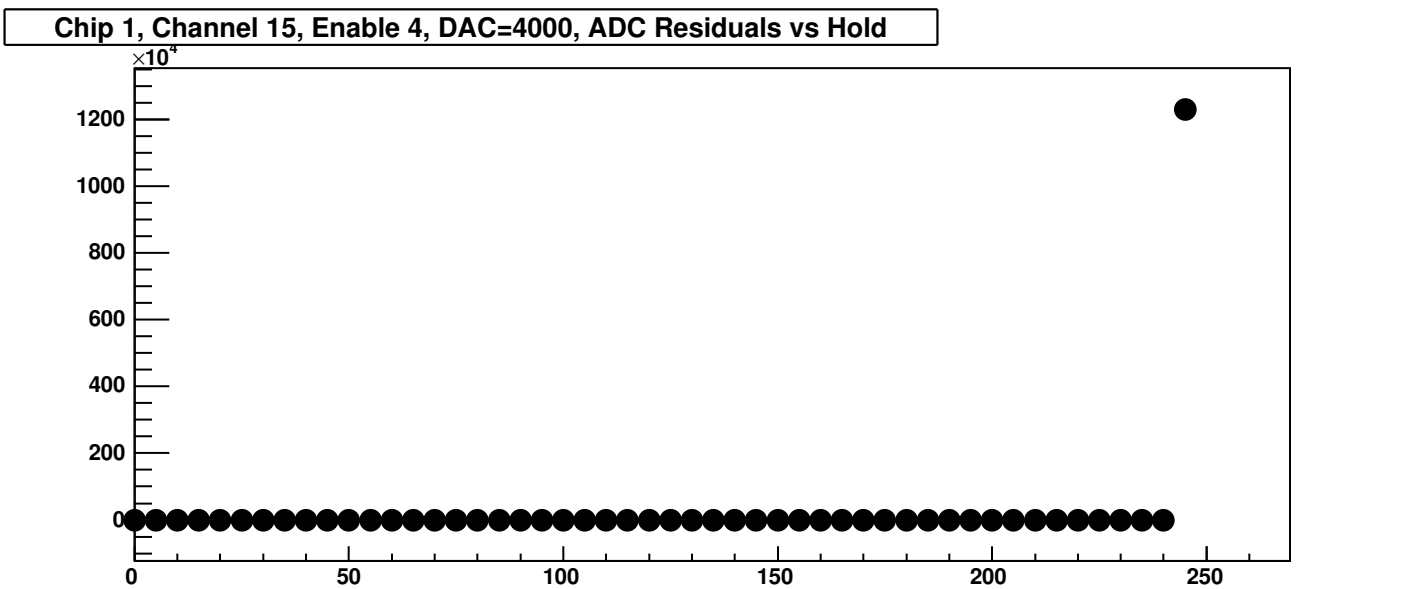

Chip 1, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 207.6 / 41
p0 -448.3 ± 0.5945
p1 85.19 ± 0.5184
p2 -3.415e+07 ± 6.707e+05
p3 1.695e+07 ± 3.017e+05
p4 1.883 ± 0.01797
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
50
100
150
200
250


Chip 1, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

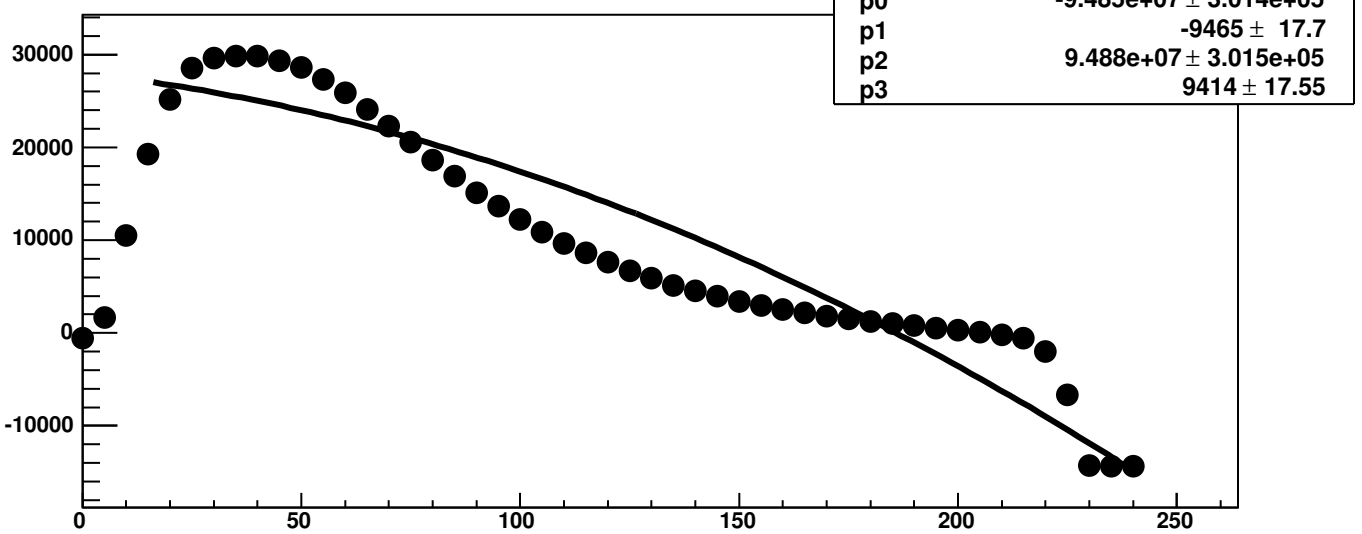

Chip 1, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
0
50
100
150
200
250


Chip 1, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 9752 / 41
p0 -448 ± 0.4156
p1 240 ± 1.599e-09
p2 -7.244e+07 ± 1.705e+05
p3 26.7 ± 0.6146
p4 0.2383 ± 0.003449
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
50
100
150
200
250

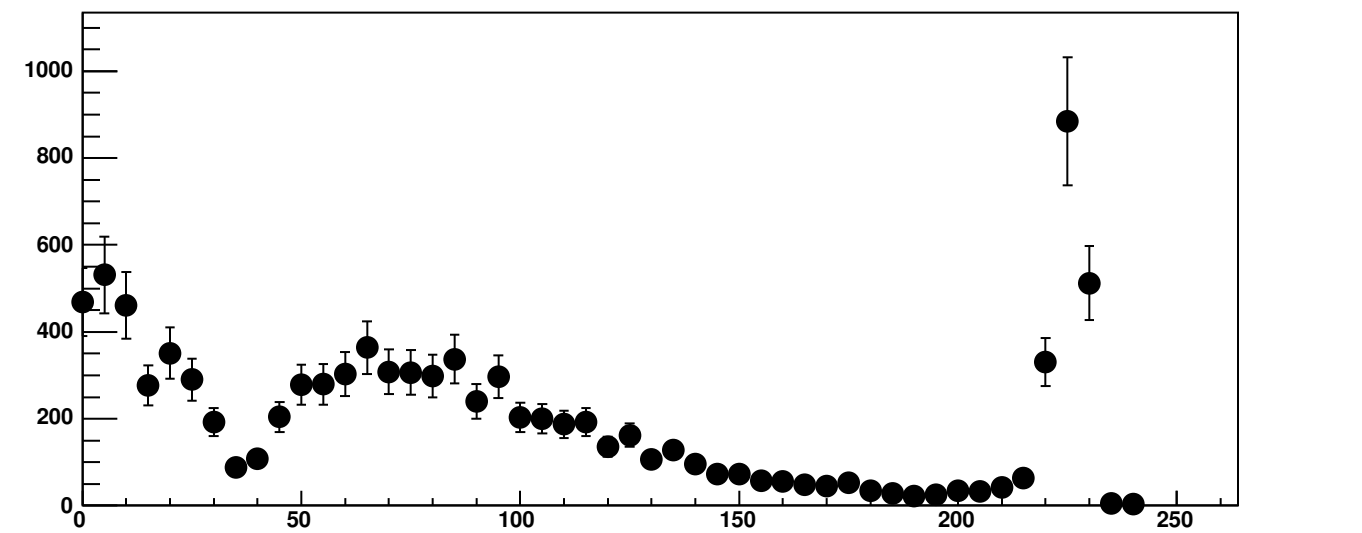

Chip 1, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 1, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold
×10⁴
1200
1000
800
600
400
200
0
0
50
100
150
200
250

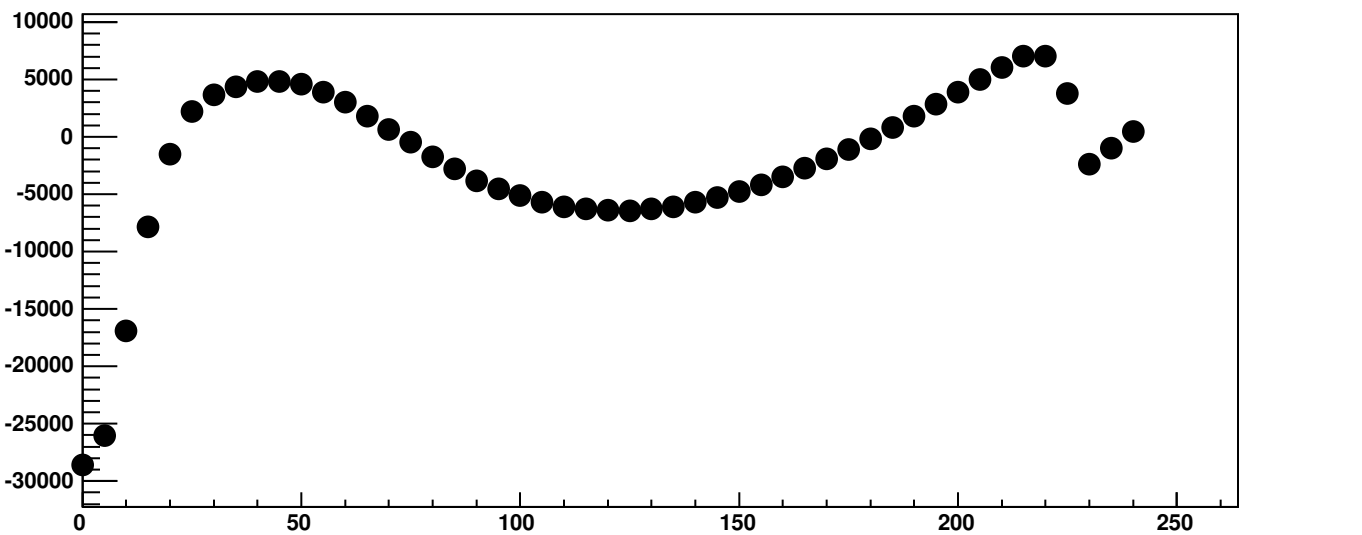
Chip 1, Channel 15, Enable 5!, DAC=4000, ADC Mean vs Hold



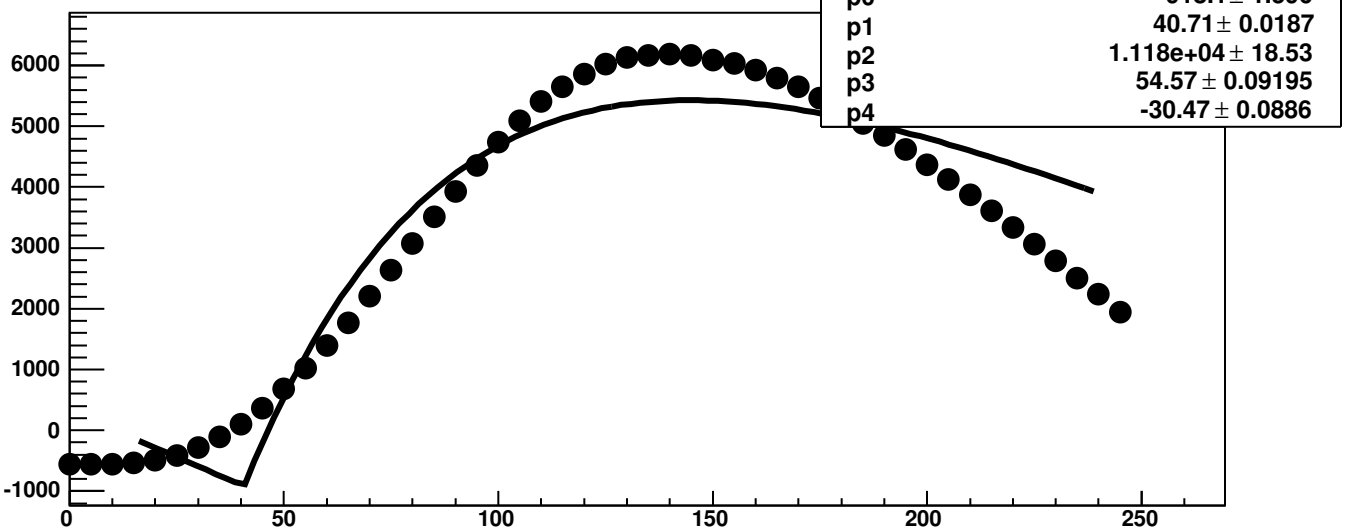
Chip 1, Channel 15, Enable 5!, DAC=4000, ADC Noise vs Hold



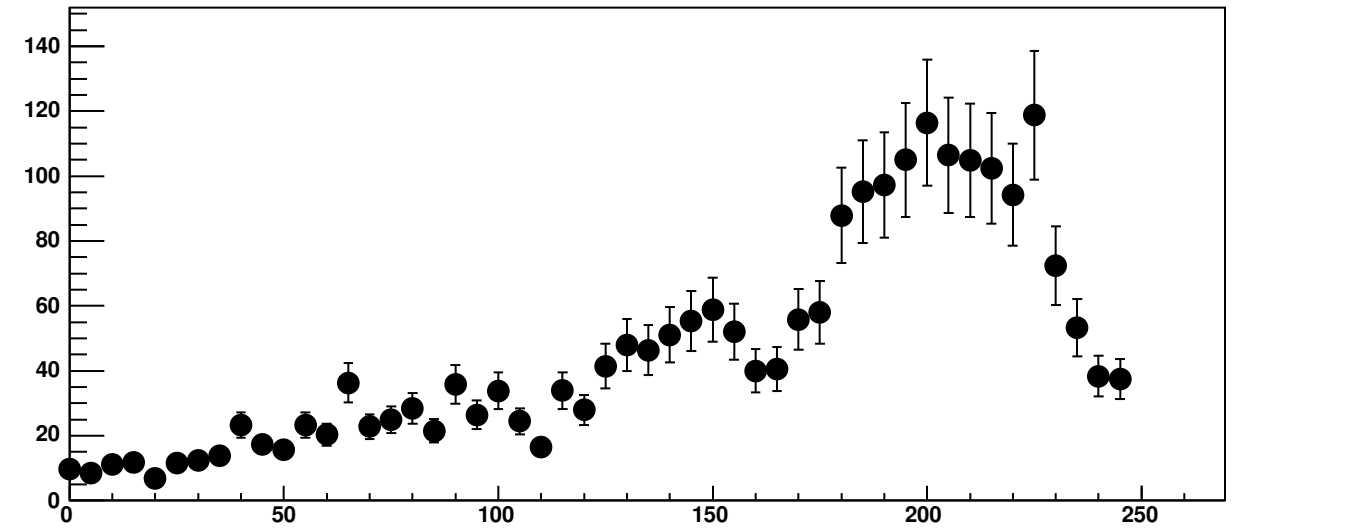
Chip 1, Channel 15, Enable 5!, DAC=4000, ADC Residuals vs Hold



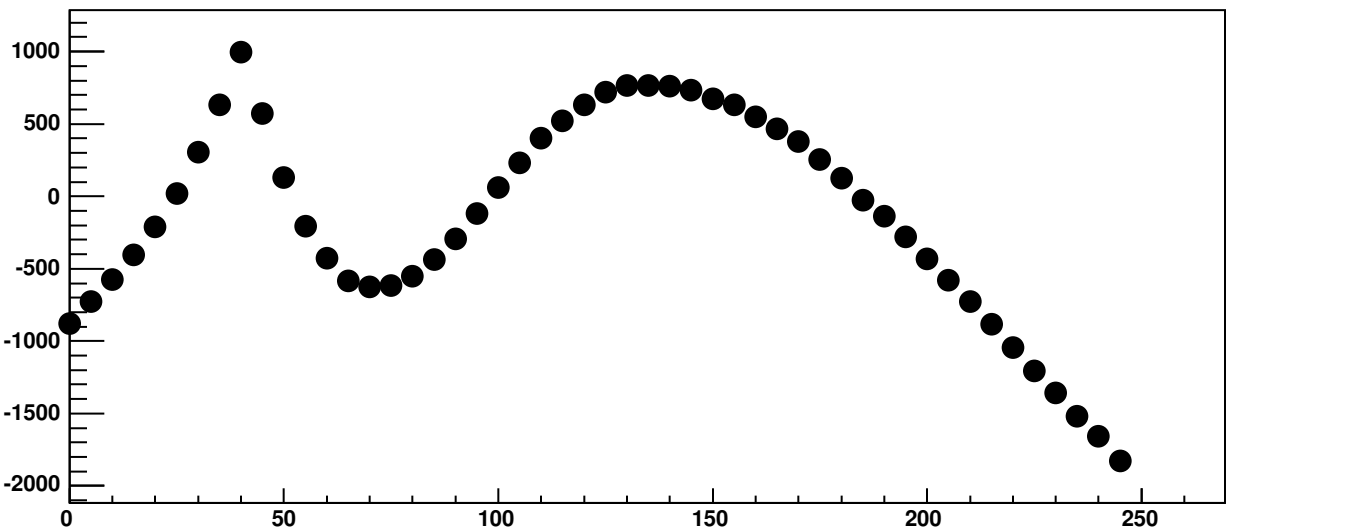
Chip 1, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold



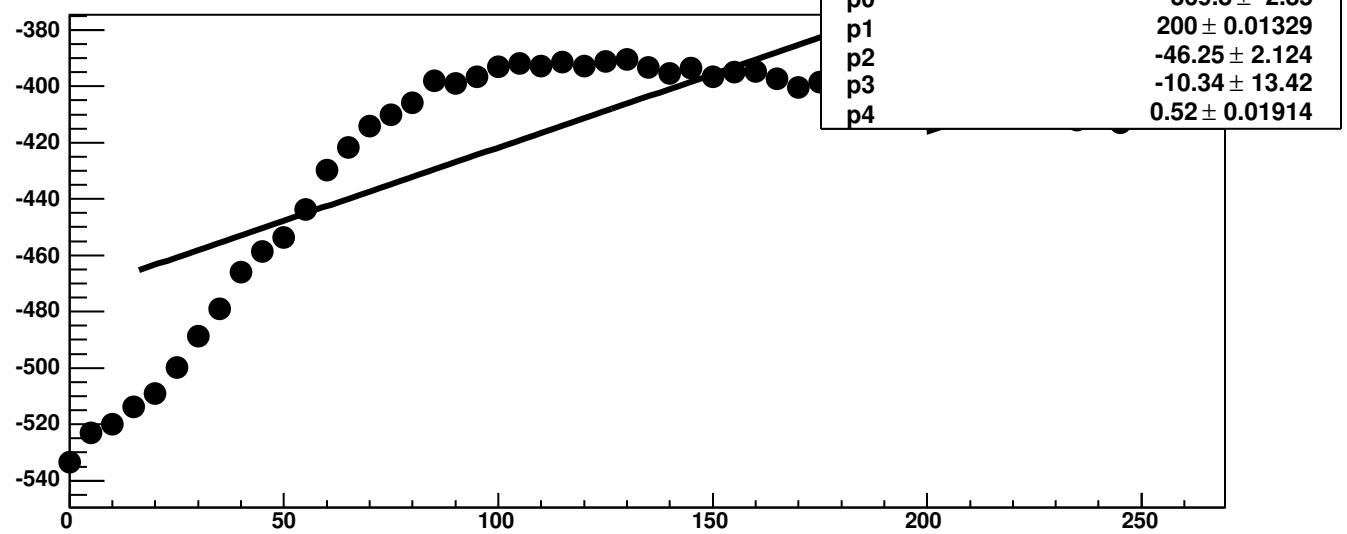
Chip 1, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



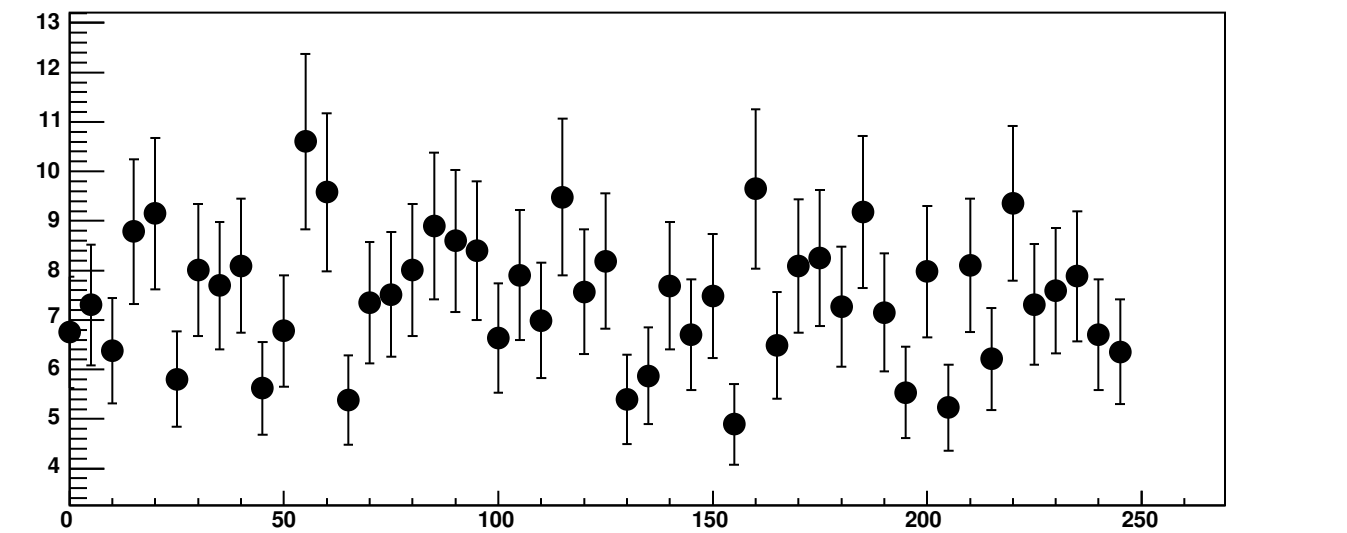
Chip 1, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold



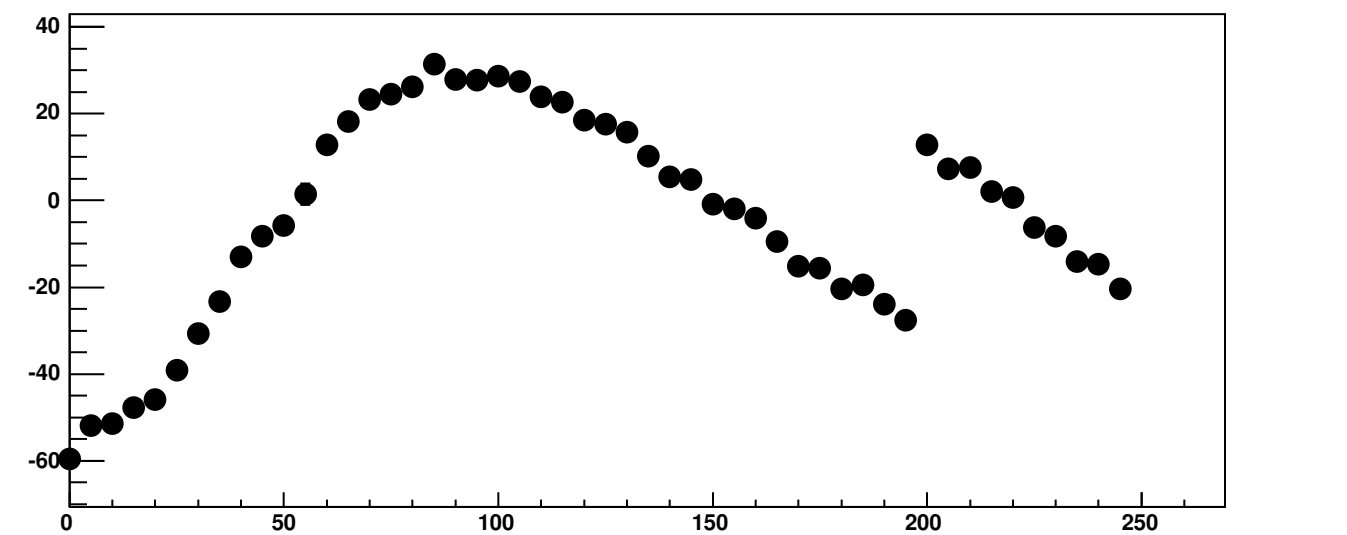
Chip 1, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold



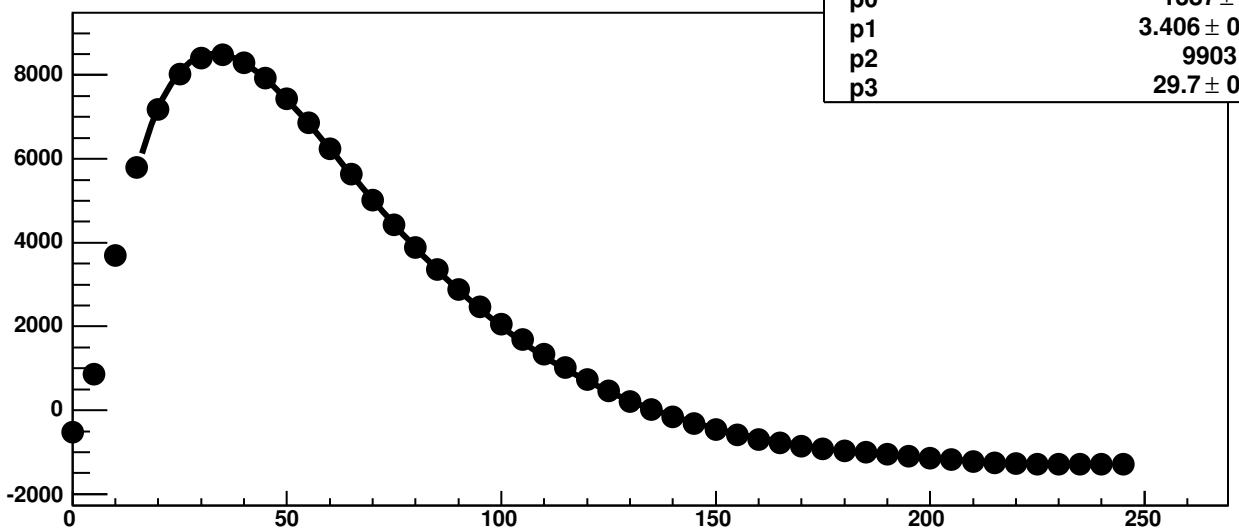
Chip 1, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



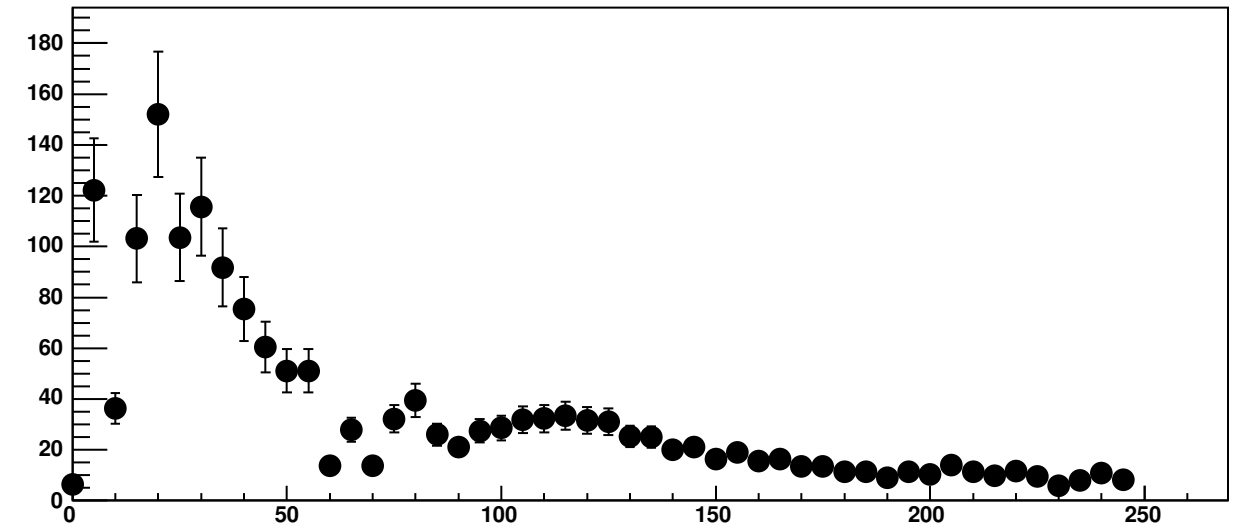
Chip 1, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold



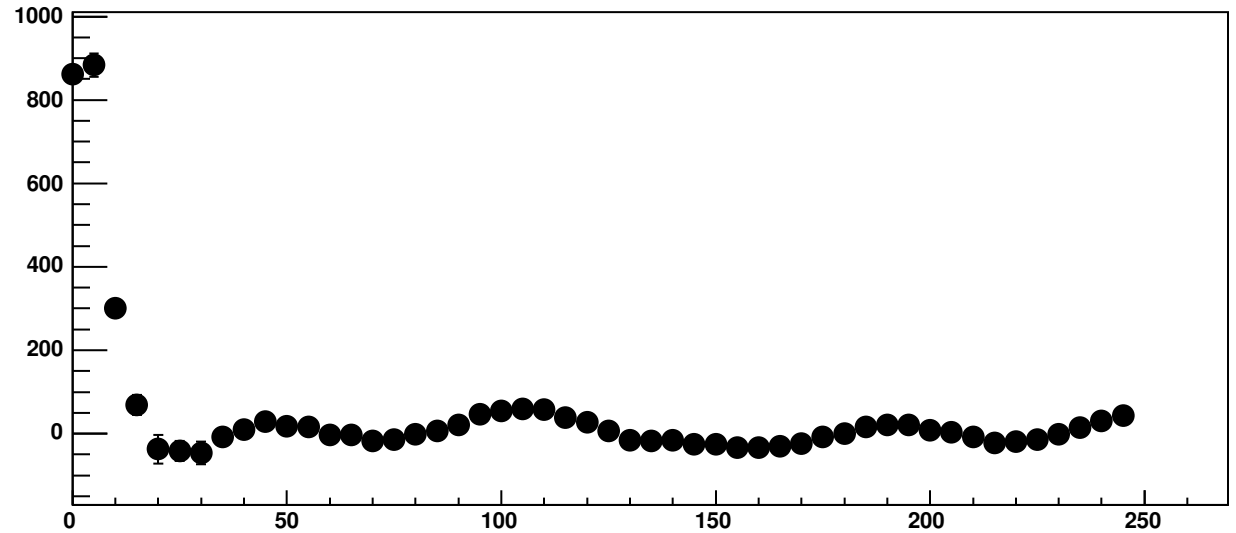
Chip 1, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold



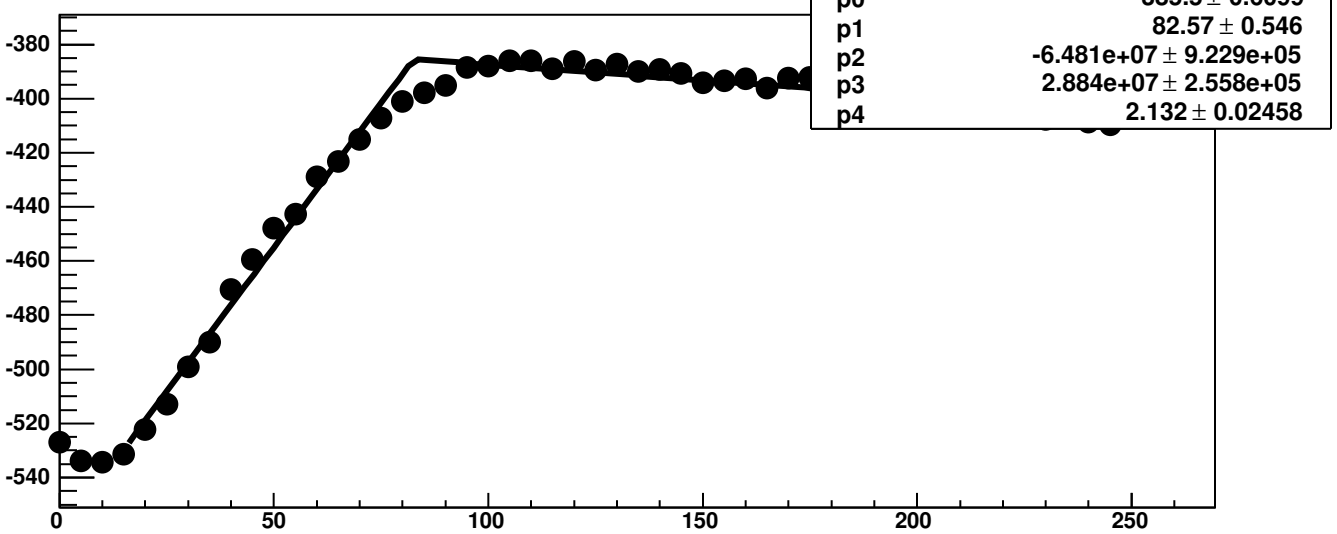
Chip 1, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold



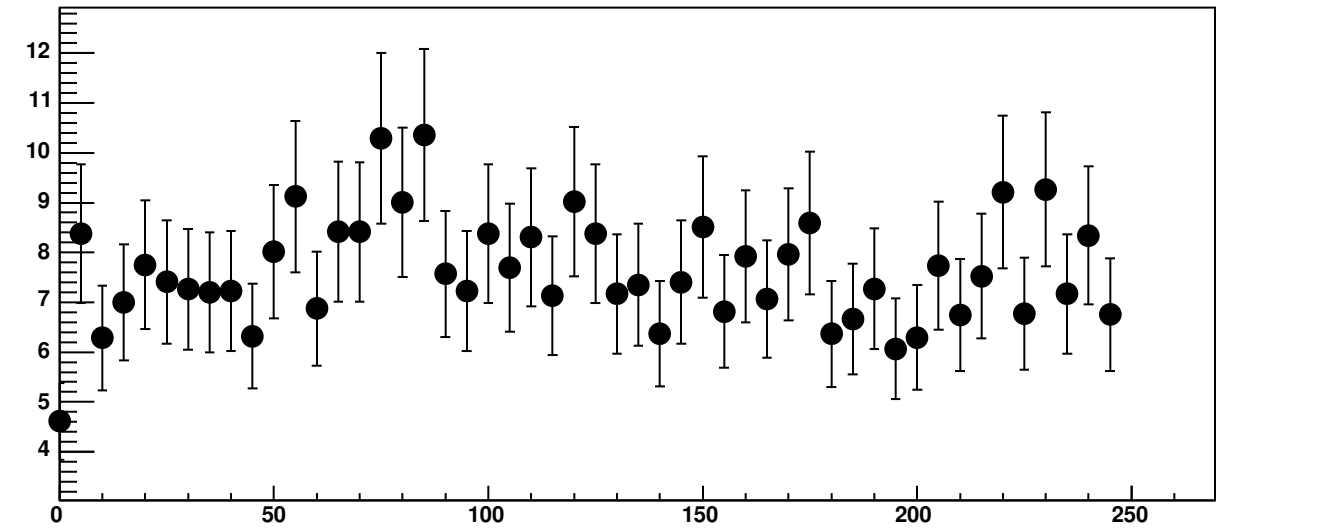
Chip 1, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold



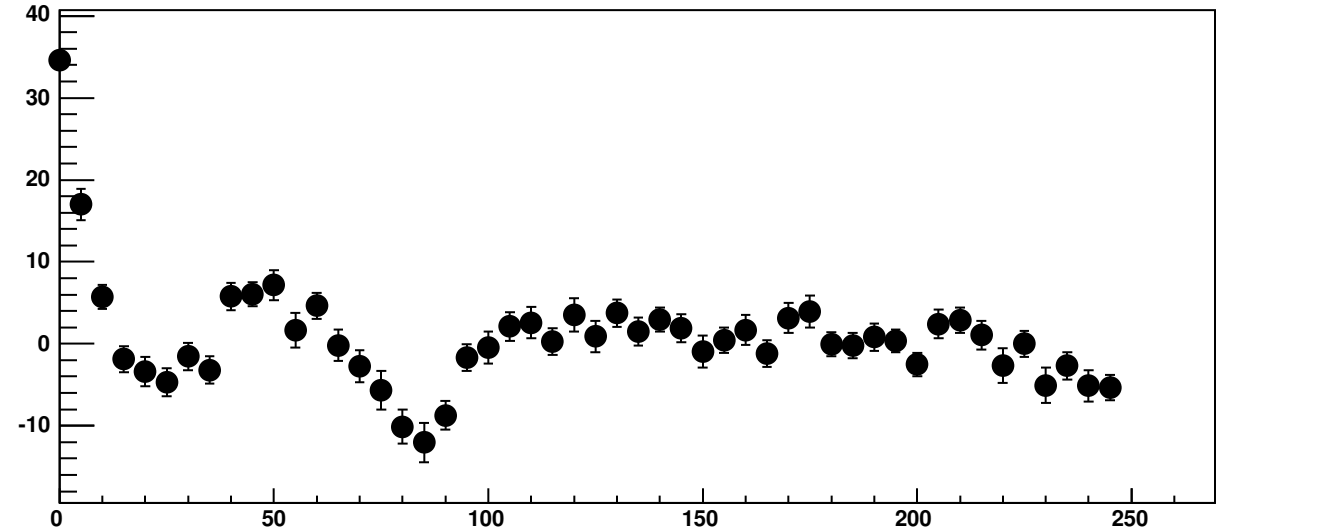
Chip 1, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold



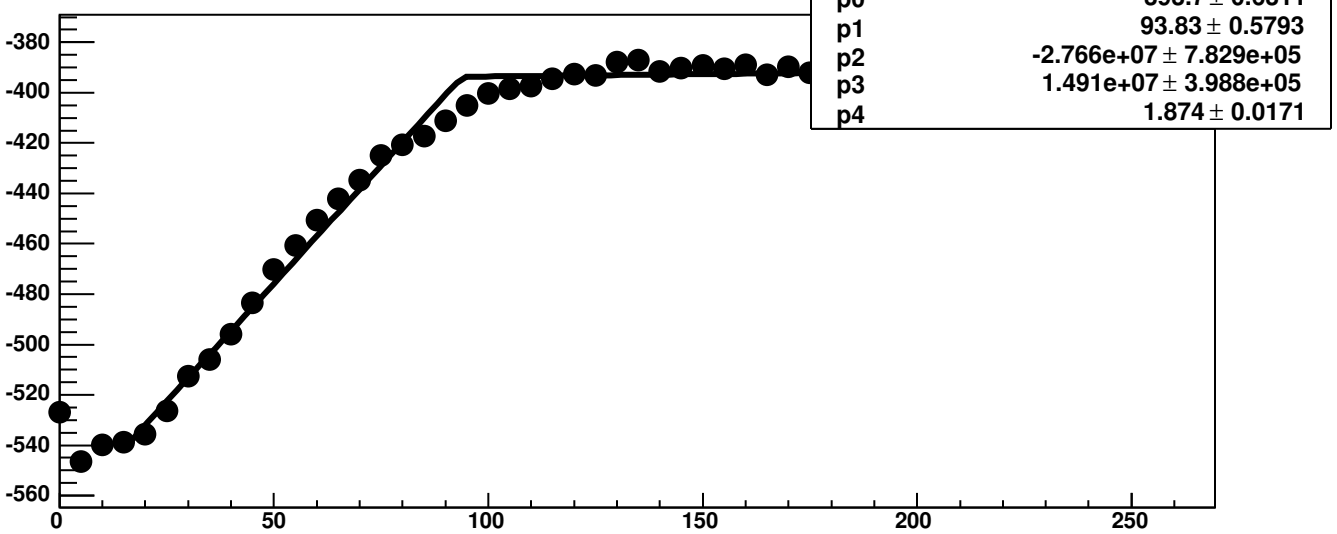
Chip 1, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



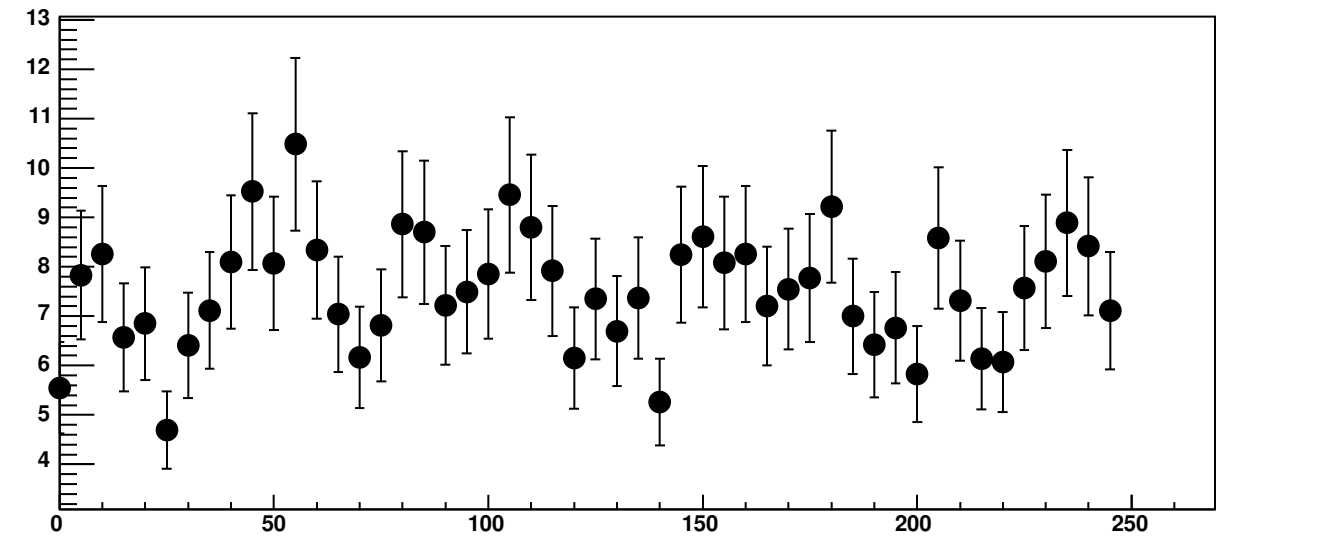
Chip 1, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold



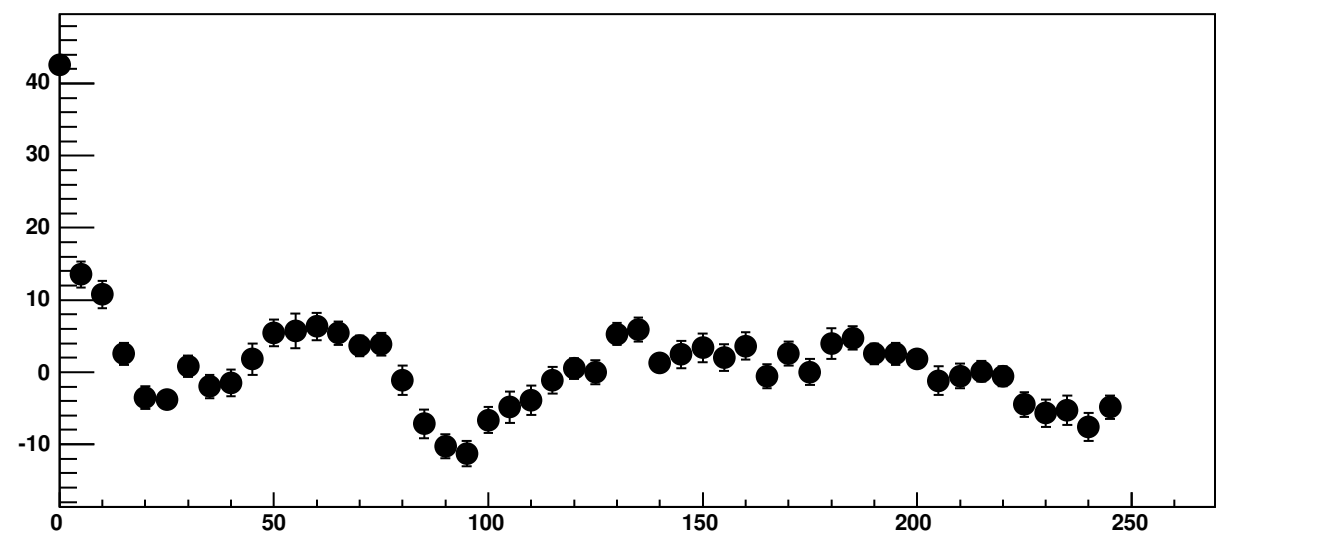
Chip 1, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

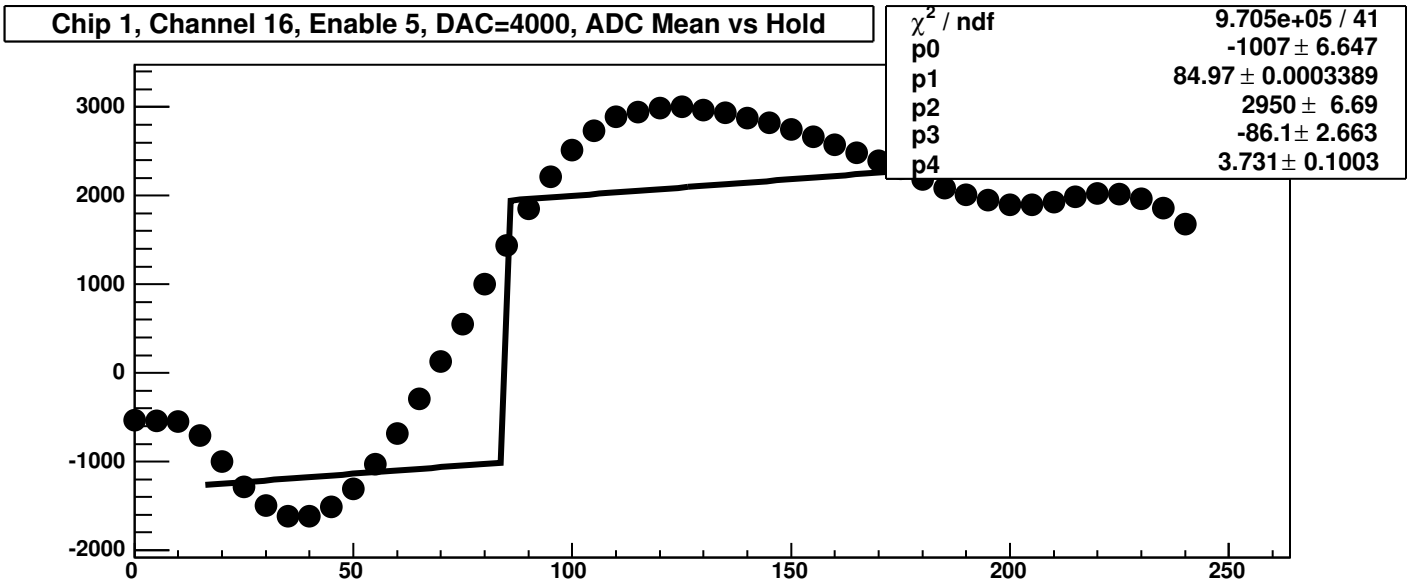


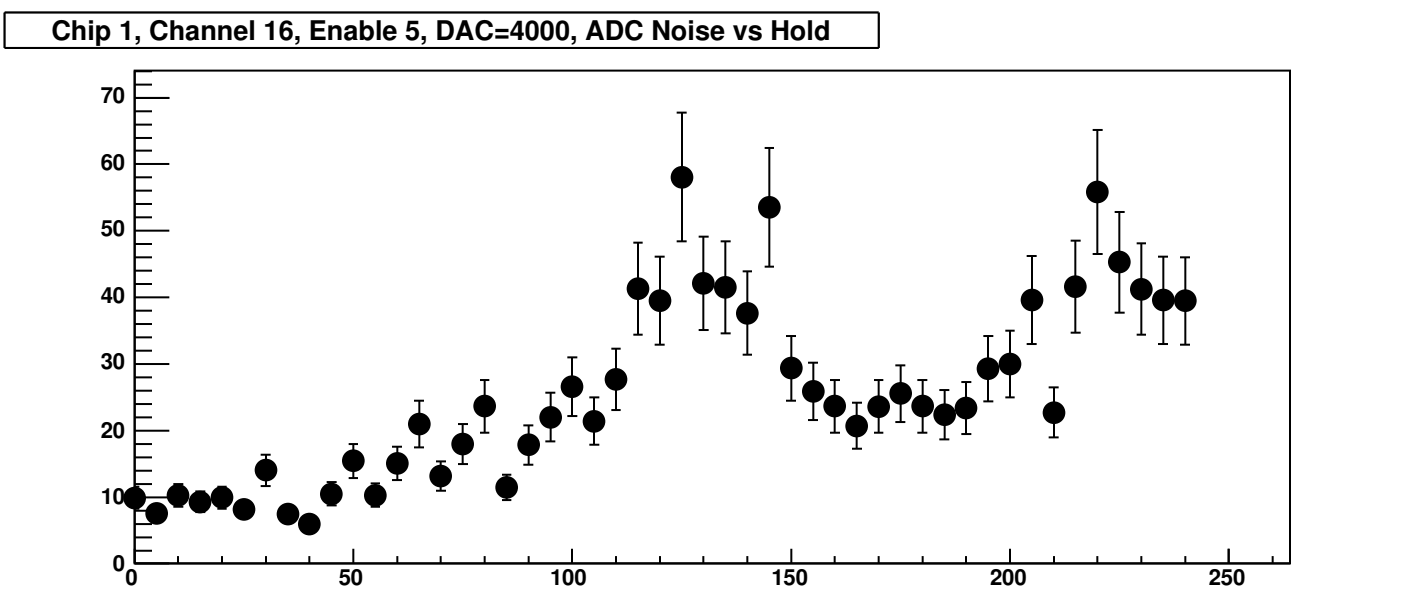
Chip 1, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold

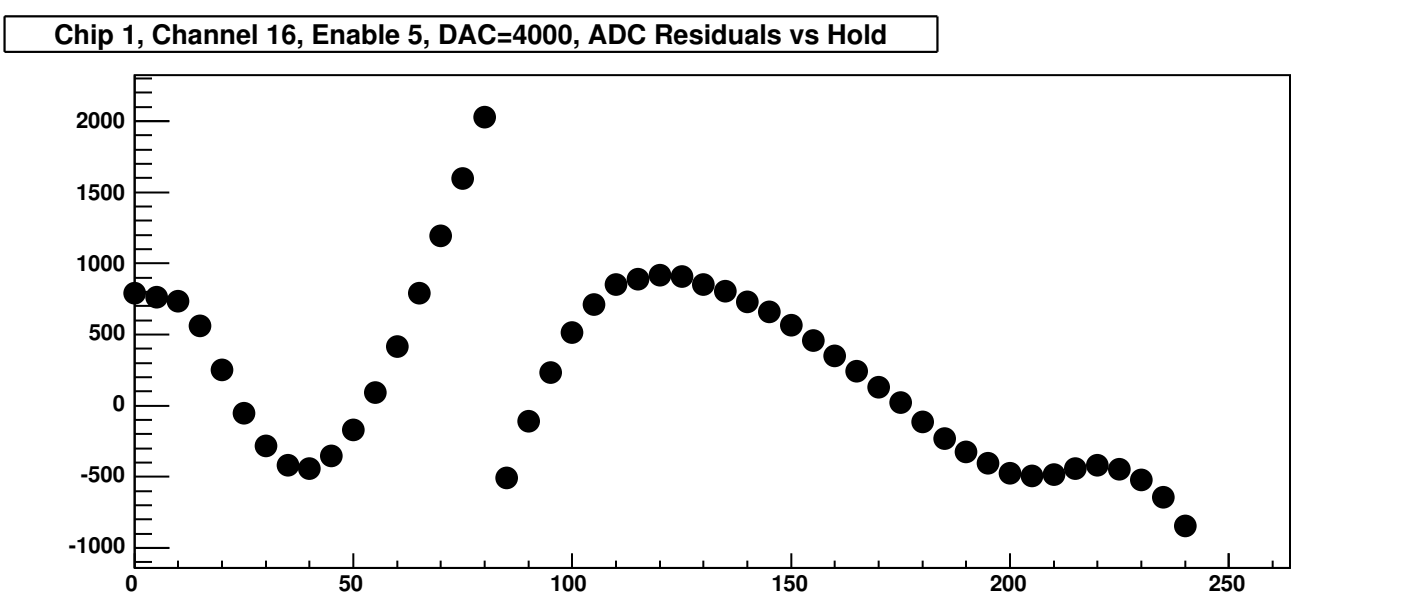


Chip 1, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

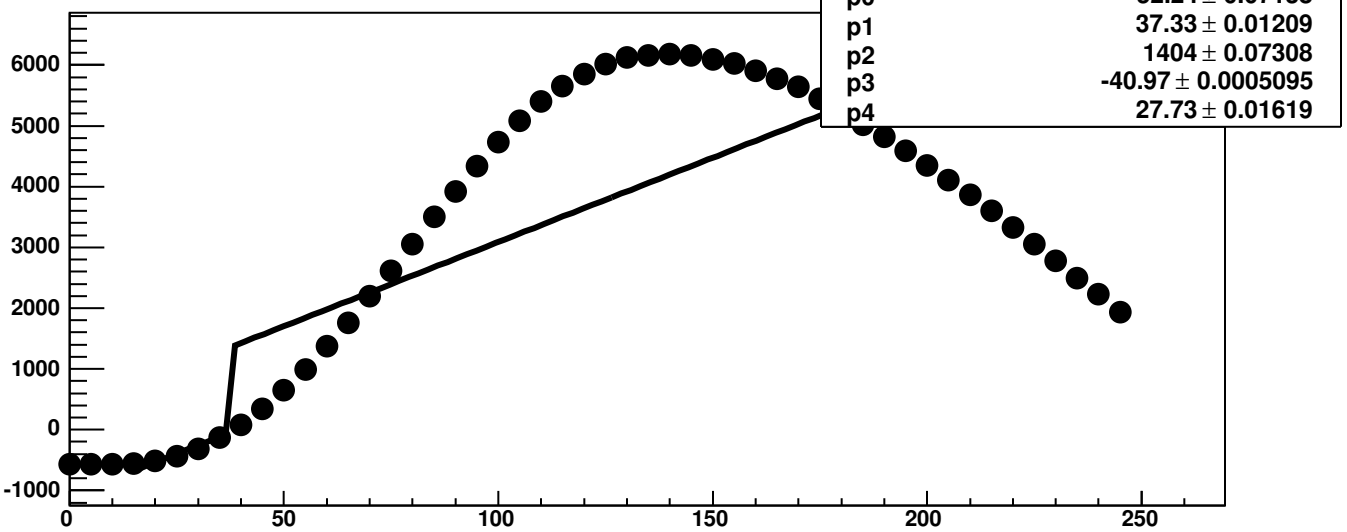



Chip 1, Channel 16, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 9.705e+05 / 41
p0 -1007 ± 6.647
p1 84.97 ± 0.0003389
p2 2950 ± 6.69
p3 -86.1 ± 2.663
p4 3.731 ± 0.1003
3000
2000
1000
0
-1000
-2000
0
50
100
150
200
250

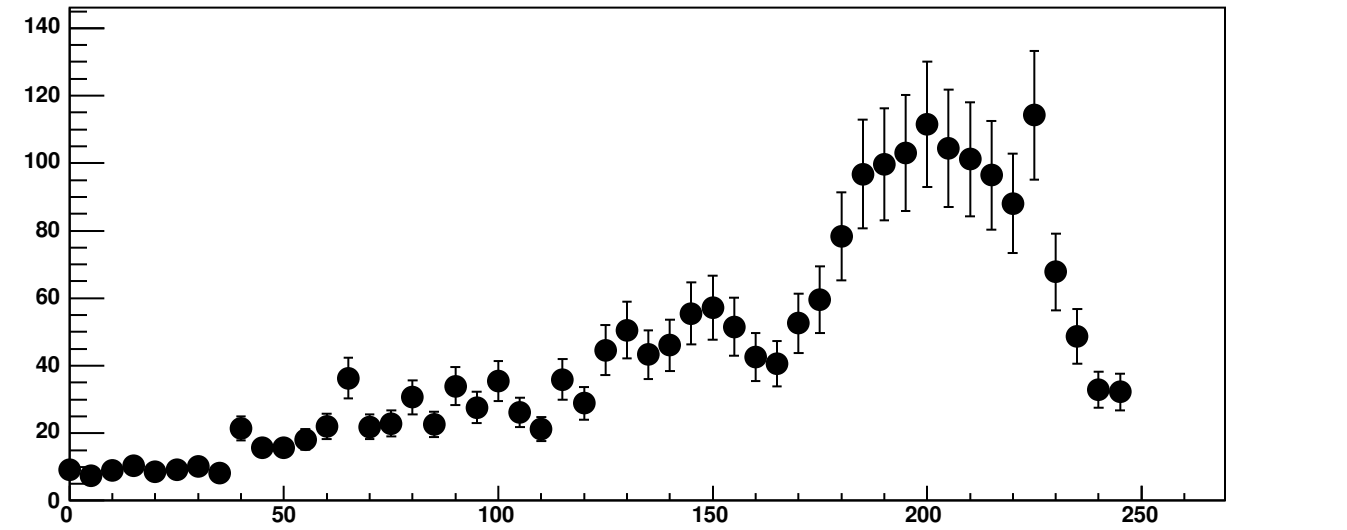

Chip 1, Channel 16, Enable 5, DAC=4000, ADC Noise vs Hold
70
60
50
40
30
20
10
0
0
50
100
150
200
250


Chip 1, Channel 16, Enable 5, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
-500
-1000
0
50
100
150
200
250

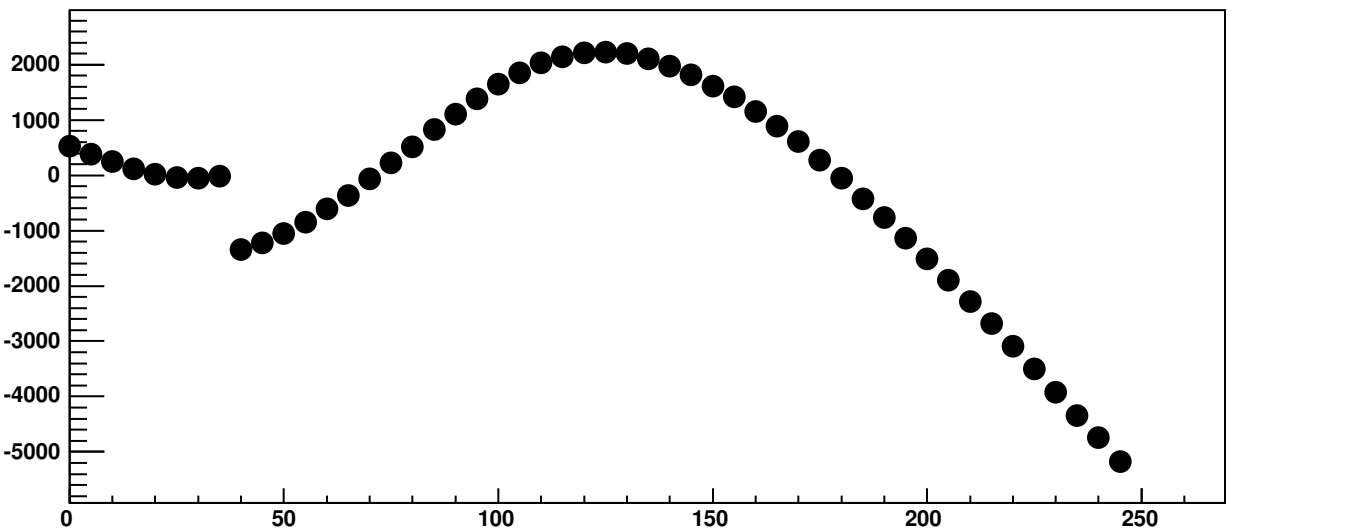
Chip 1, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

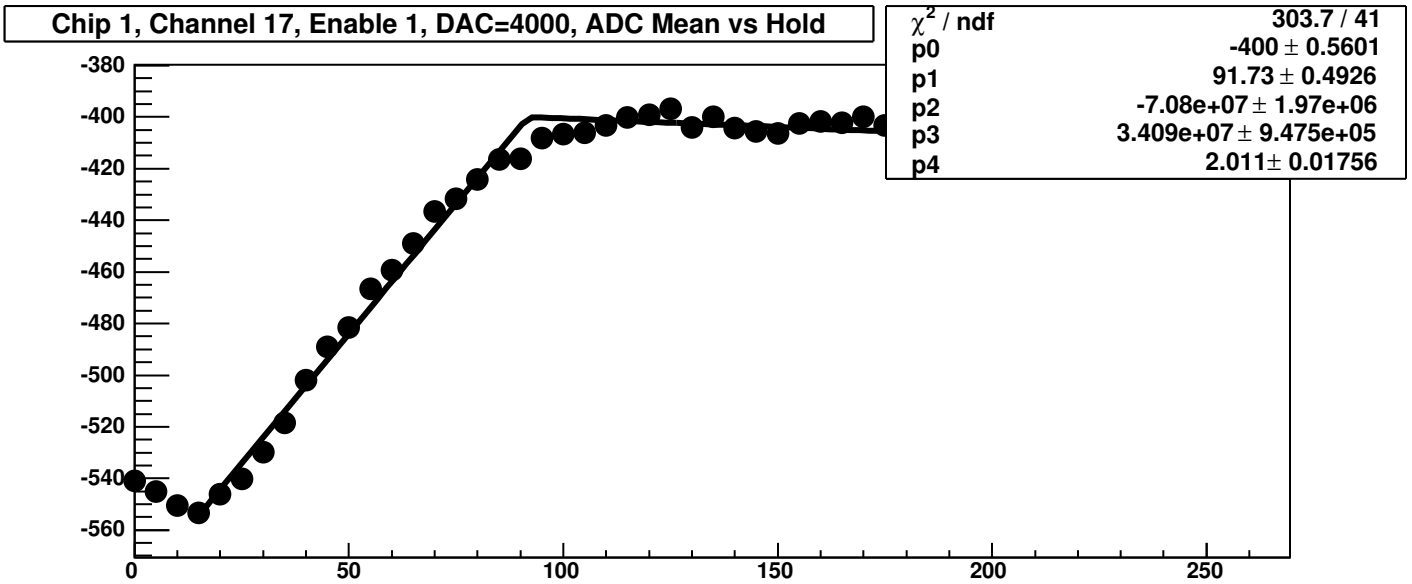


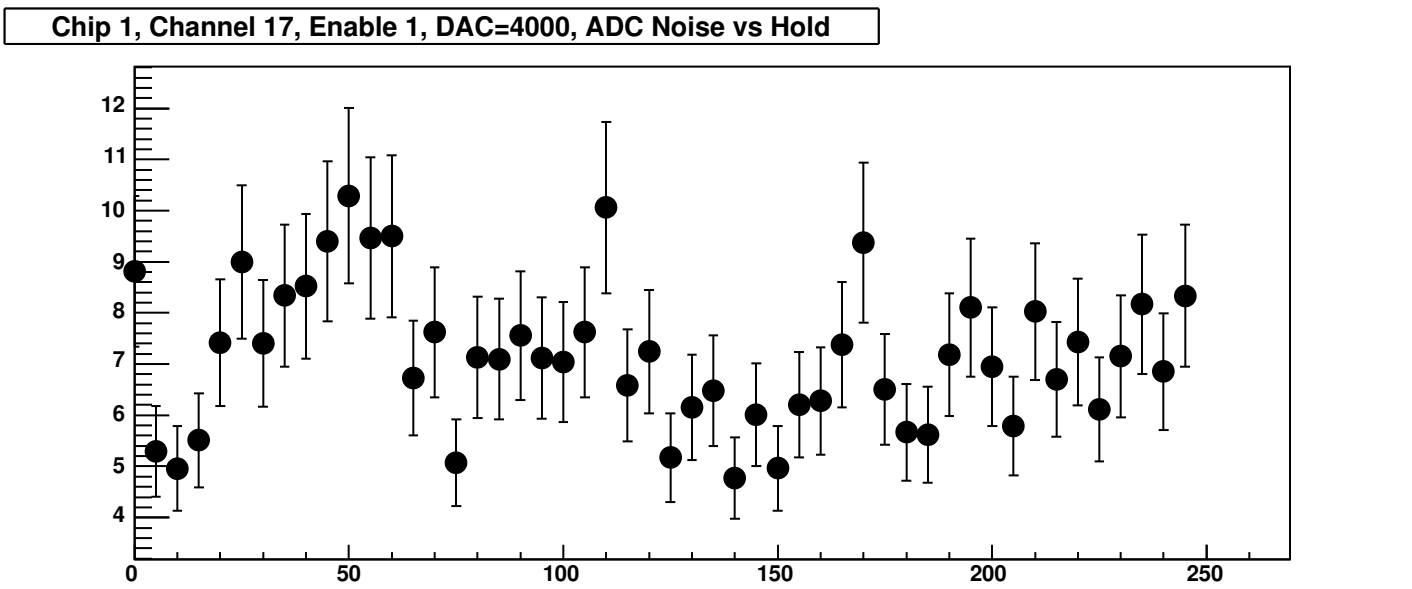
Chip 1, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold

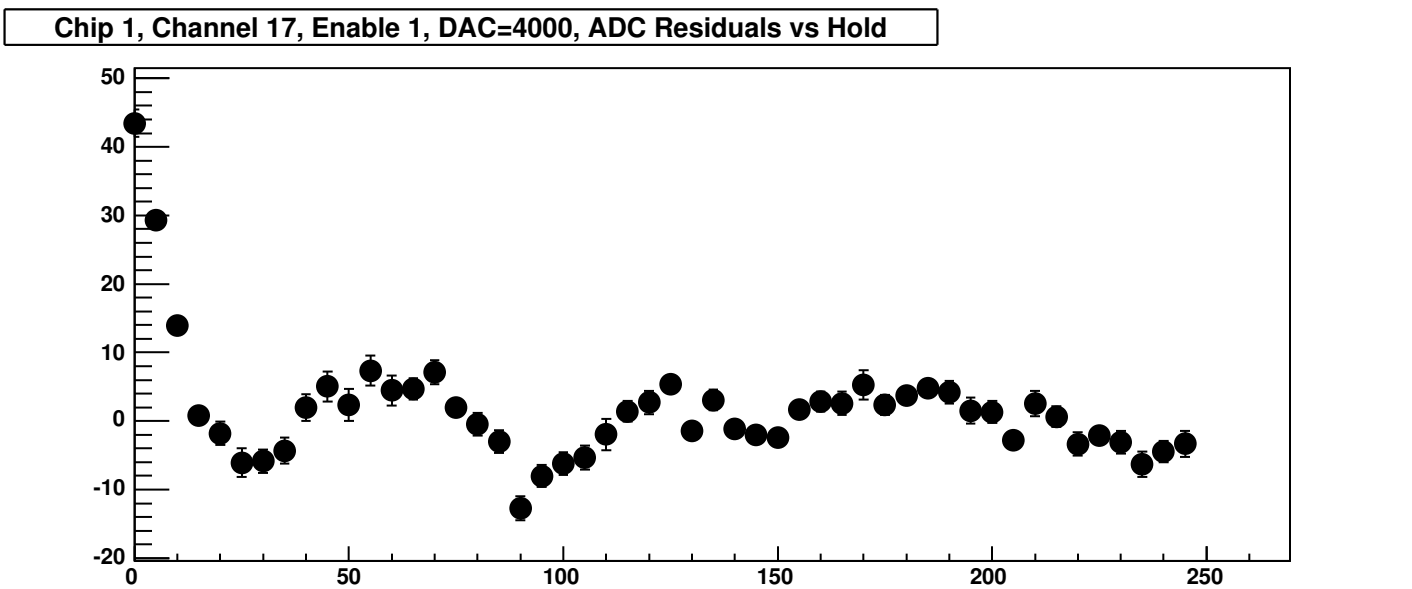


Chip 1, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

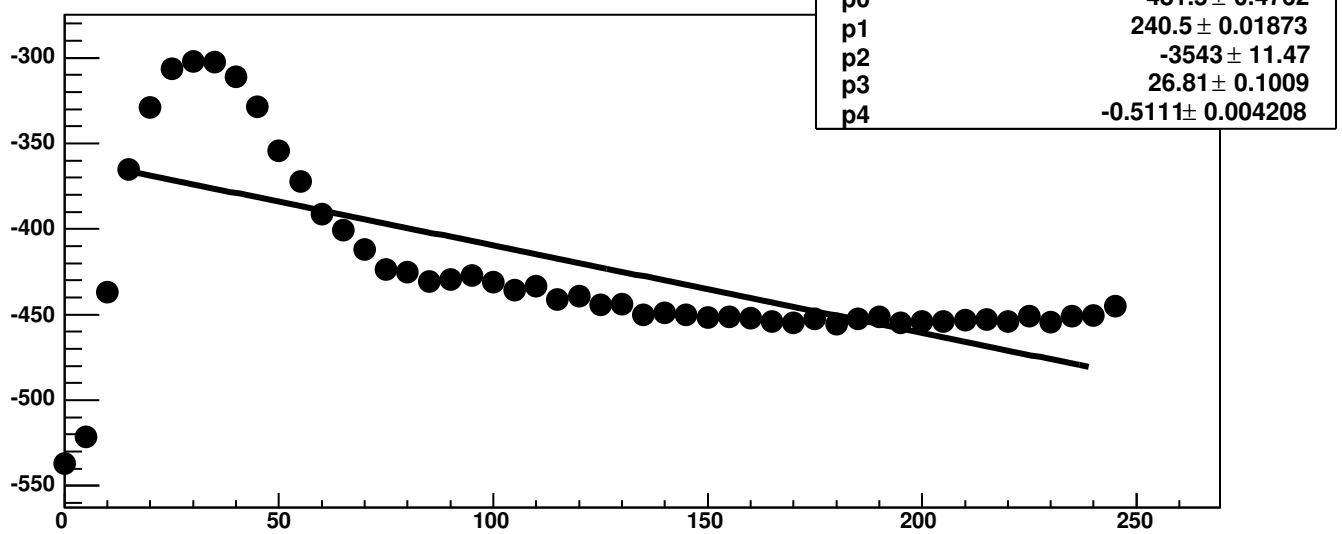



Chip 1, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 303.7 / 41
p0 -400 ± 0.5601
p1 91.73 ± 0.4926
p2 -7.08e+07 ± 1.97e+06
p3 3.409e+07 ± 9.475e+05
p4 2.011 ± 0.01756
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
0
50
100
150
200
250

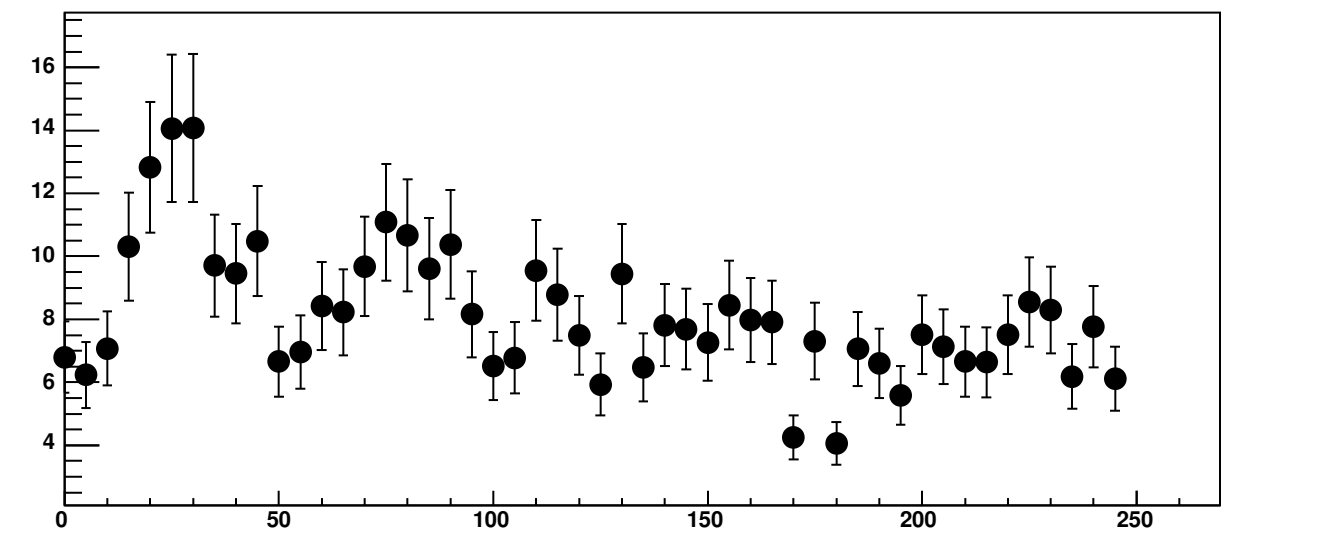

Chip 1, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 1, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
-20
0
50
100
150
200
250

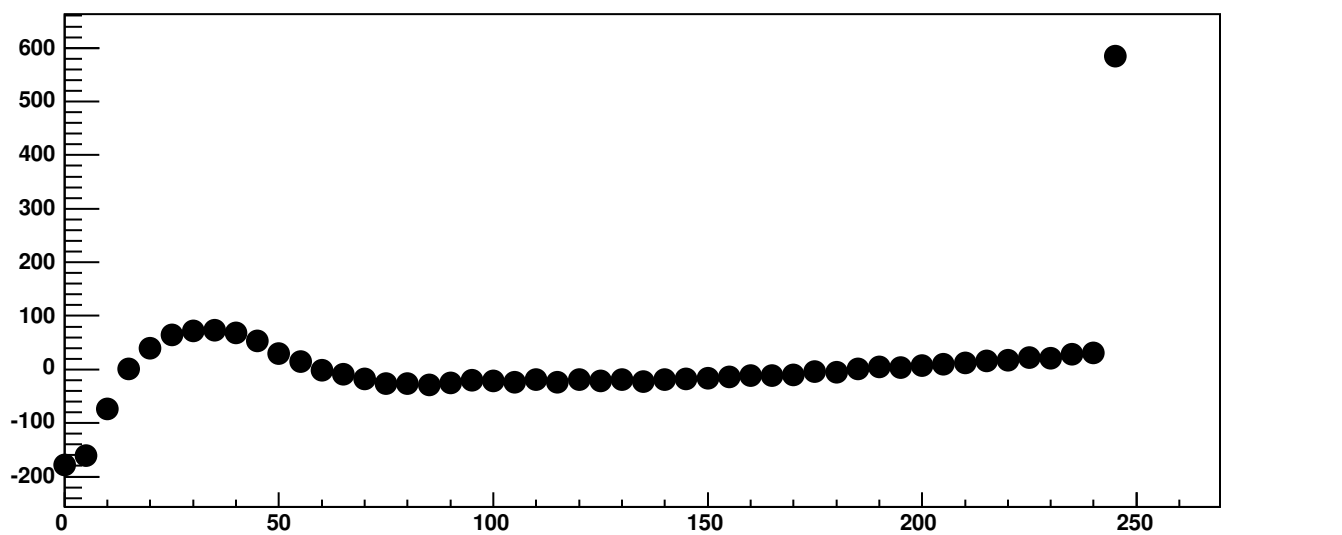
Chip 1, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold

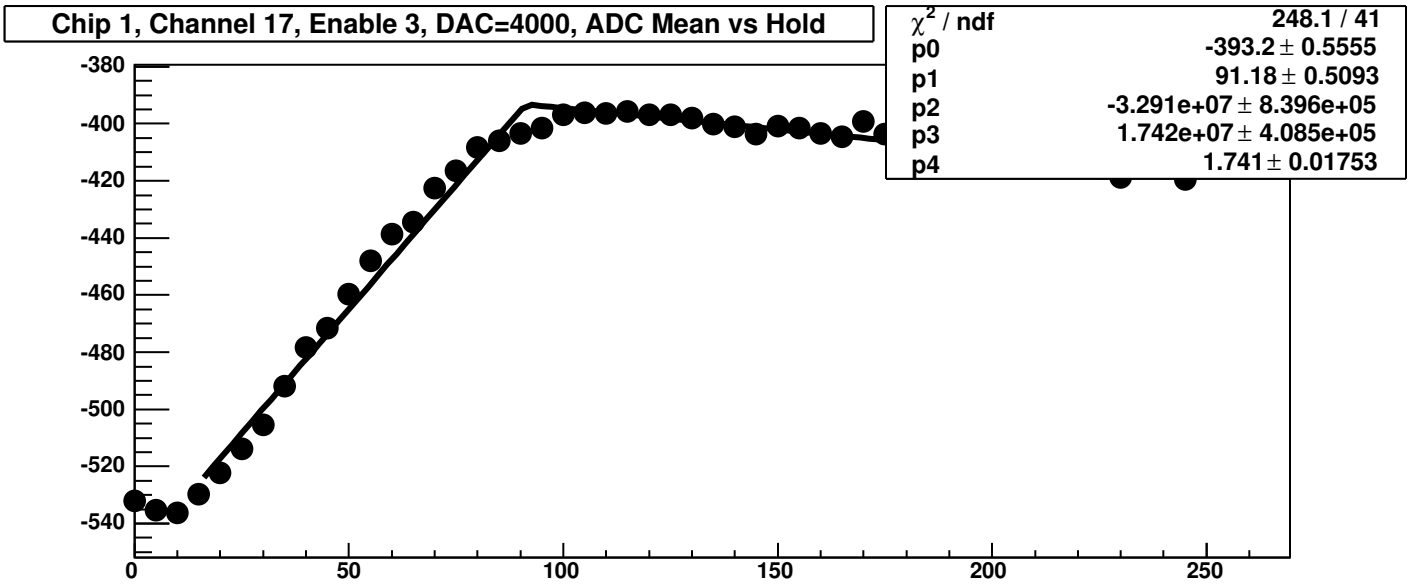


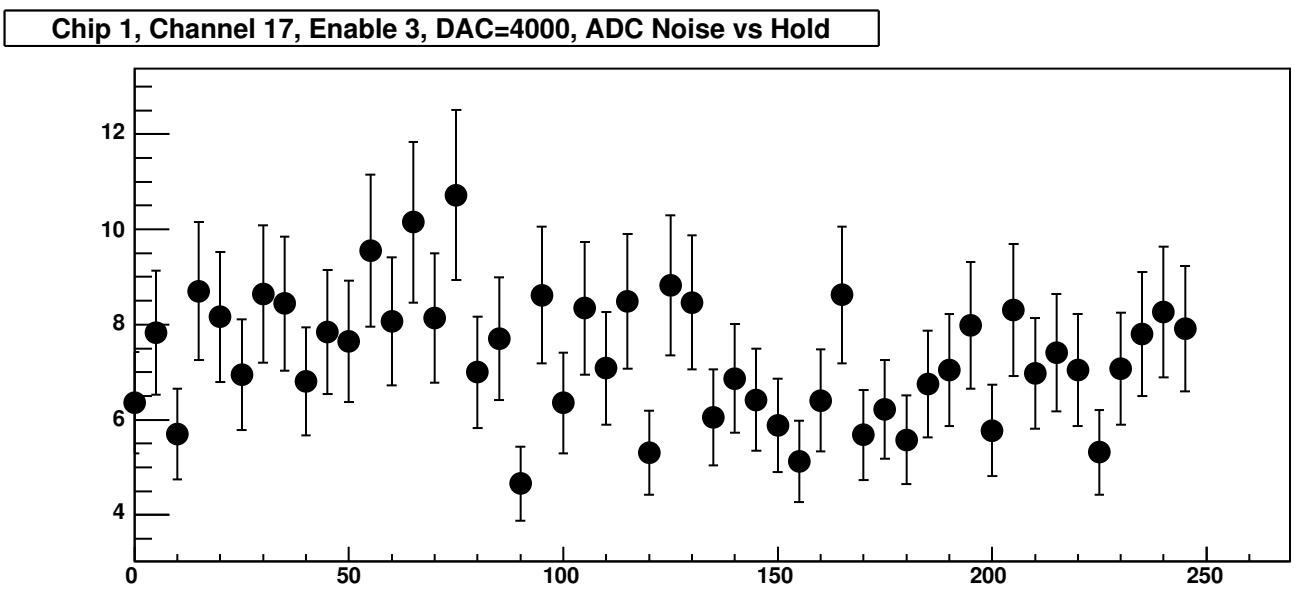
Chip 1, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold

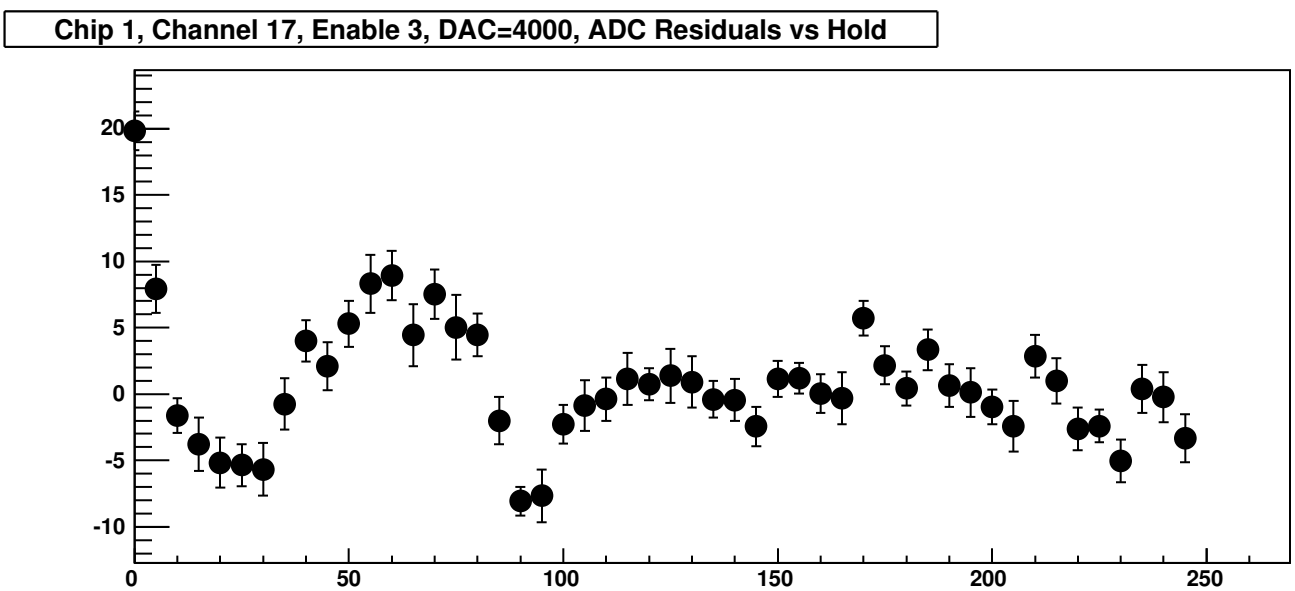


Chip 1, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold

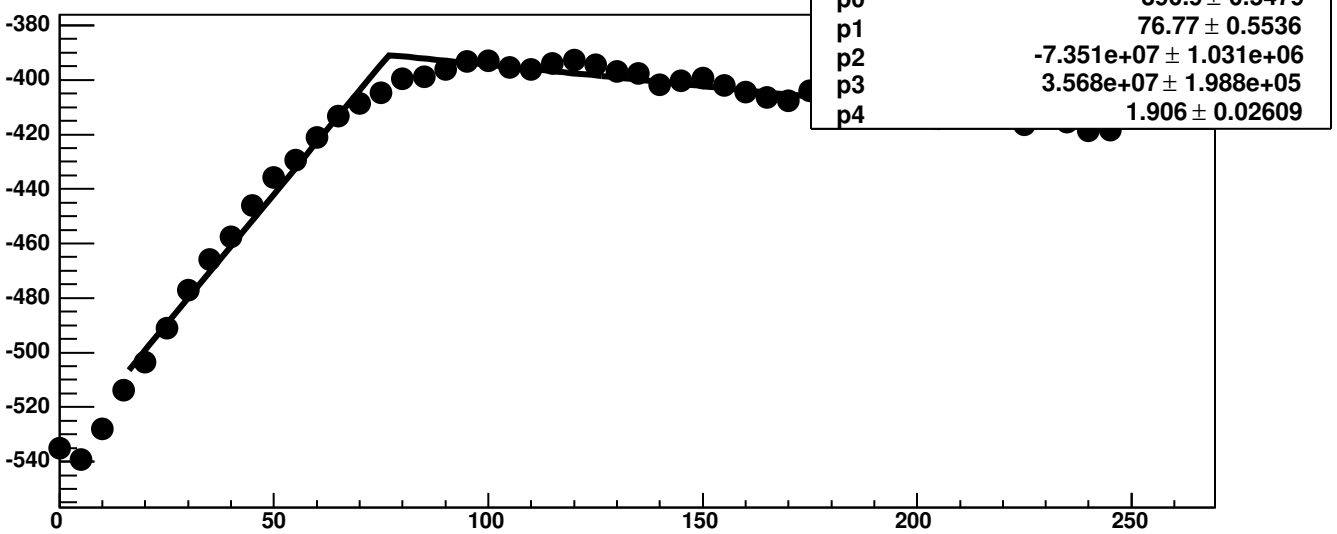



Chip 1, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 248.1 / 41
p0 -393.2 ± 0.5555
p1 91.18 ± 0.5093
p2 -3.291e+07 ± 8.396e+05
p3 1.742e+07 ± 4.085e+05
p4 1.741 ± 0.01753
-380
-400
-420
-440
-460
-480
-500
-520
-540
0
50
100
150
200
250

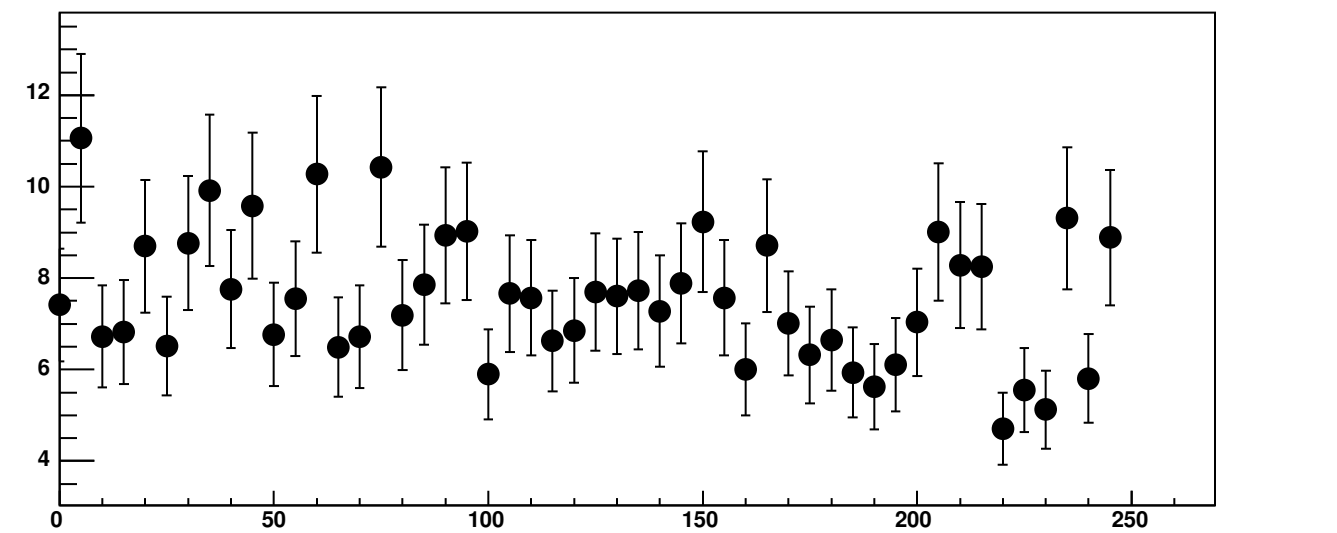

Chip 1, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 1, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
0
50
100
150
200
250

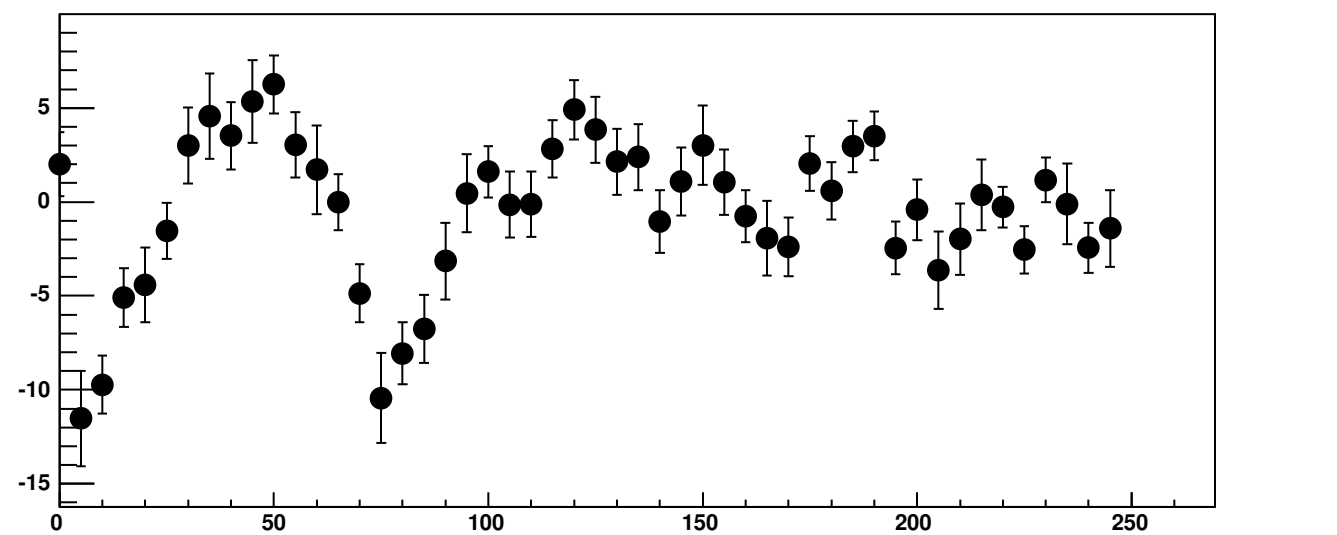
Chip 1, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



Chip 1, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold

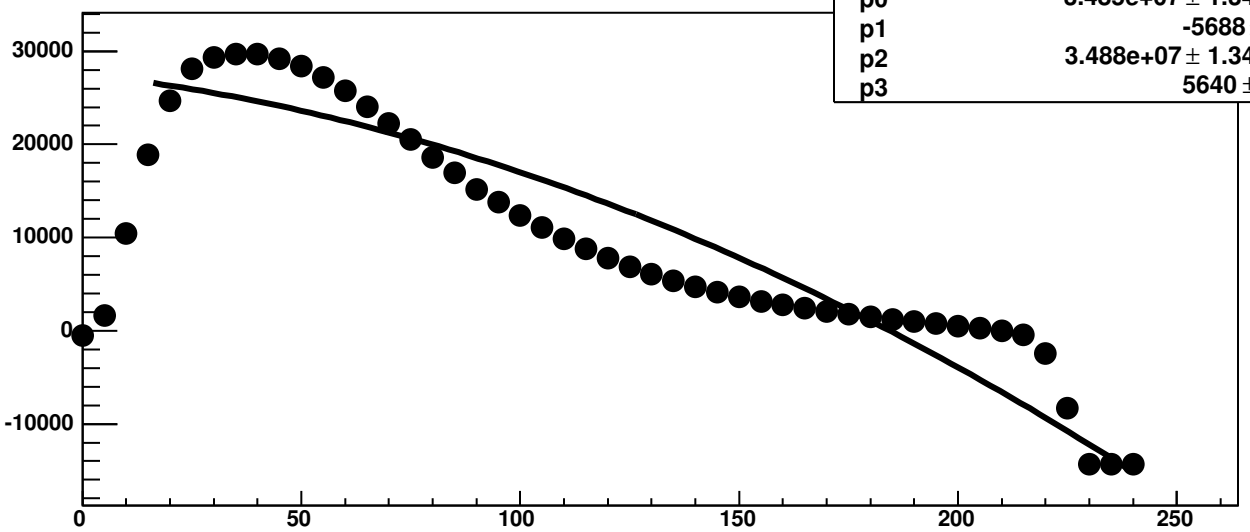


Chip 1, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

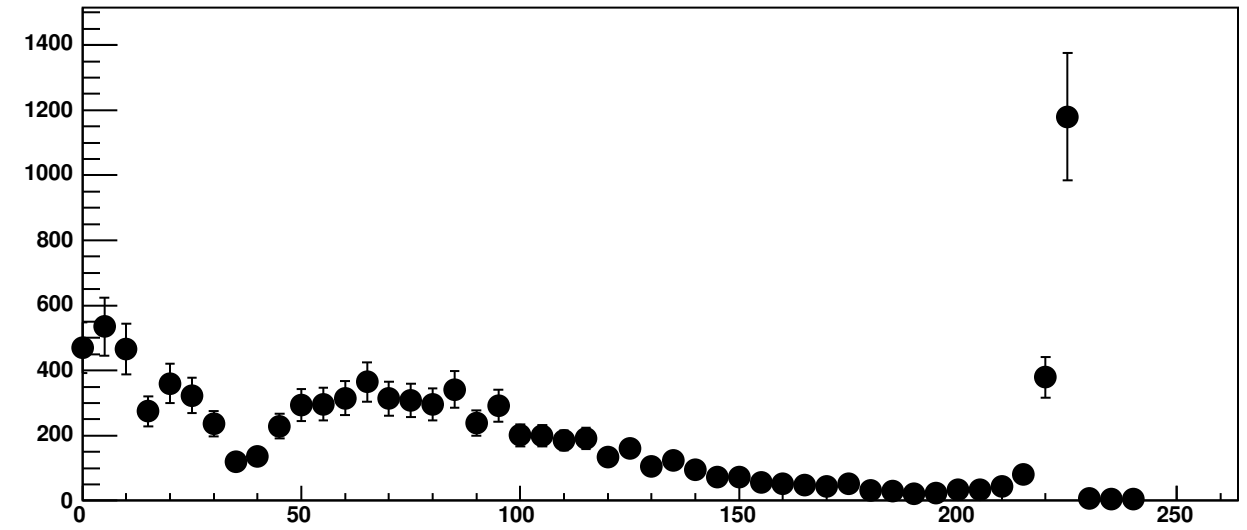


Chip 1, Channel 17, Enable 5!, DAC=4000, ADC Mean vs Hold

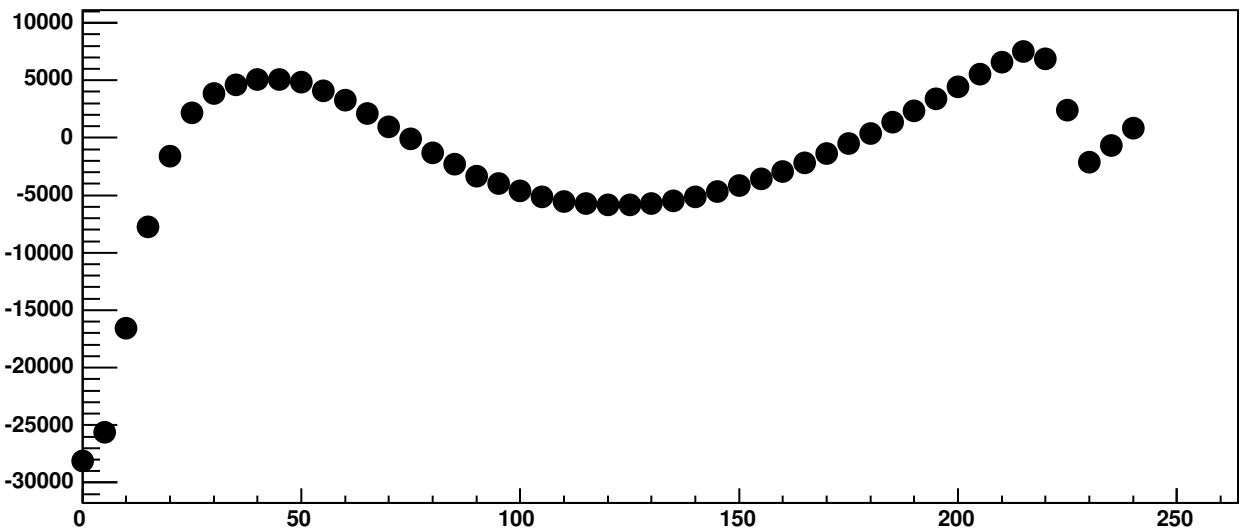
χ^2 / ndf	5.365e+06 / 42
p0	-3.485e+07 ± 1.345e+05
p1	-5688 ± 12.6
p2	3.488e+07 ± 1.345e+05
p3	5640 ± 12.47

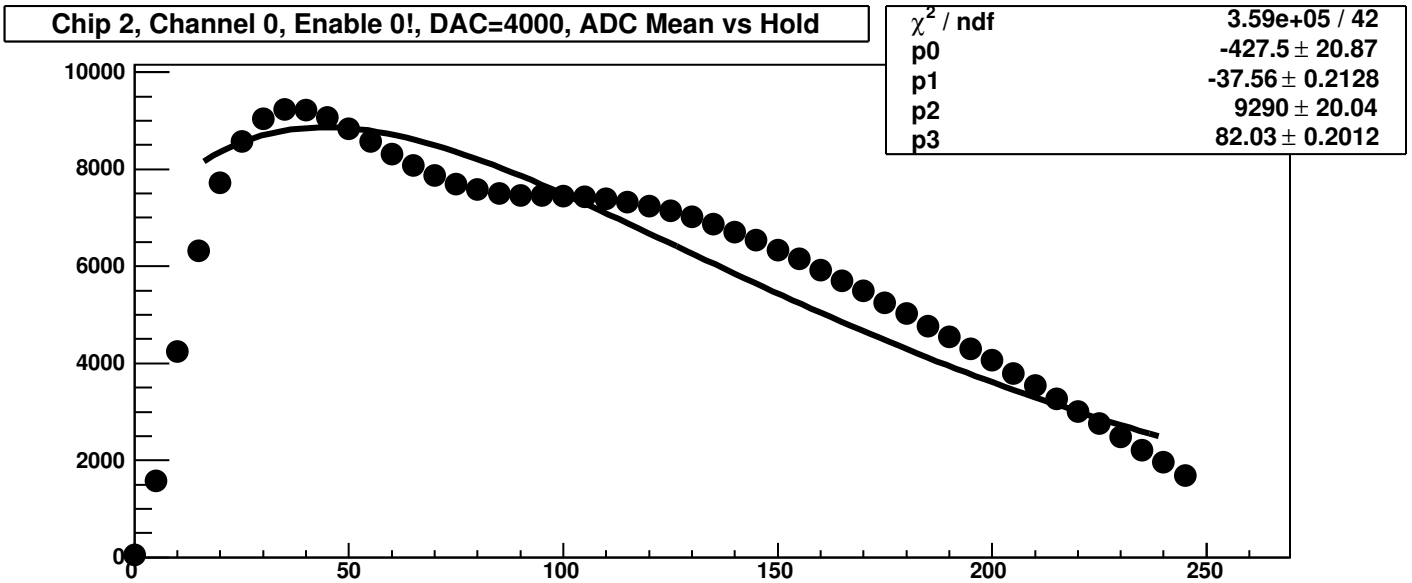


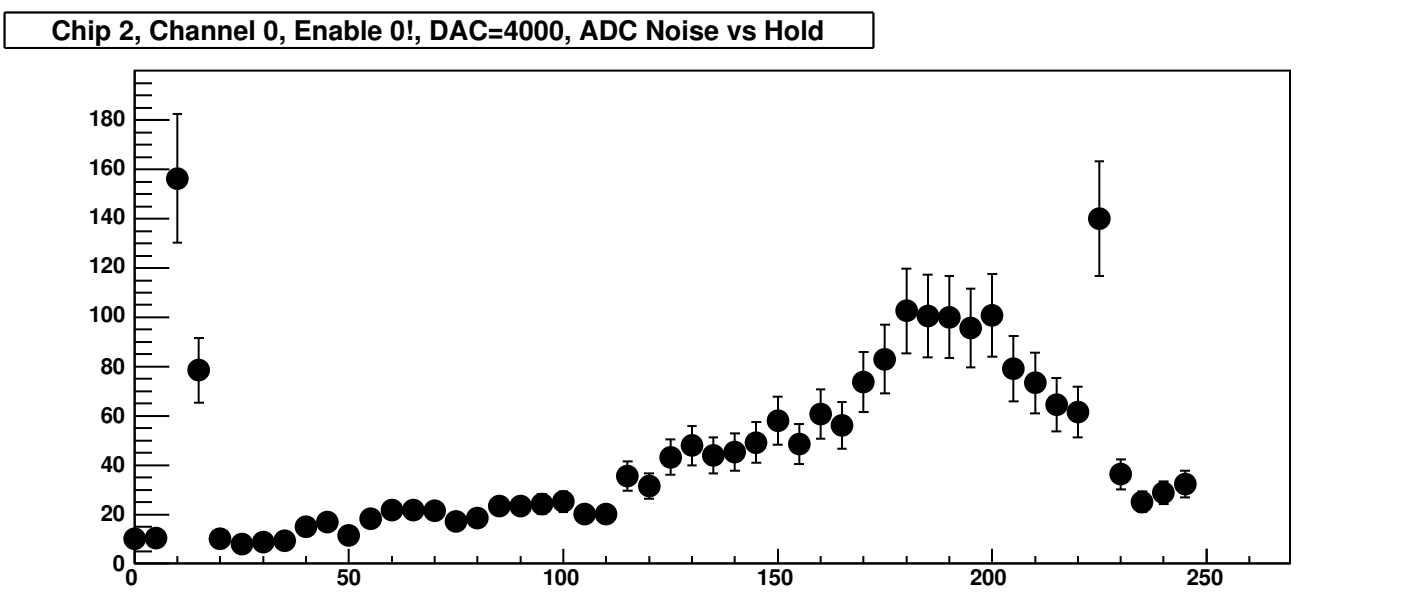
Chip 1, Channel 17, Enable 5!, DAC=4000, ADC Noise vs Hold

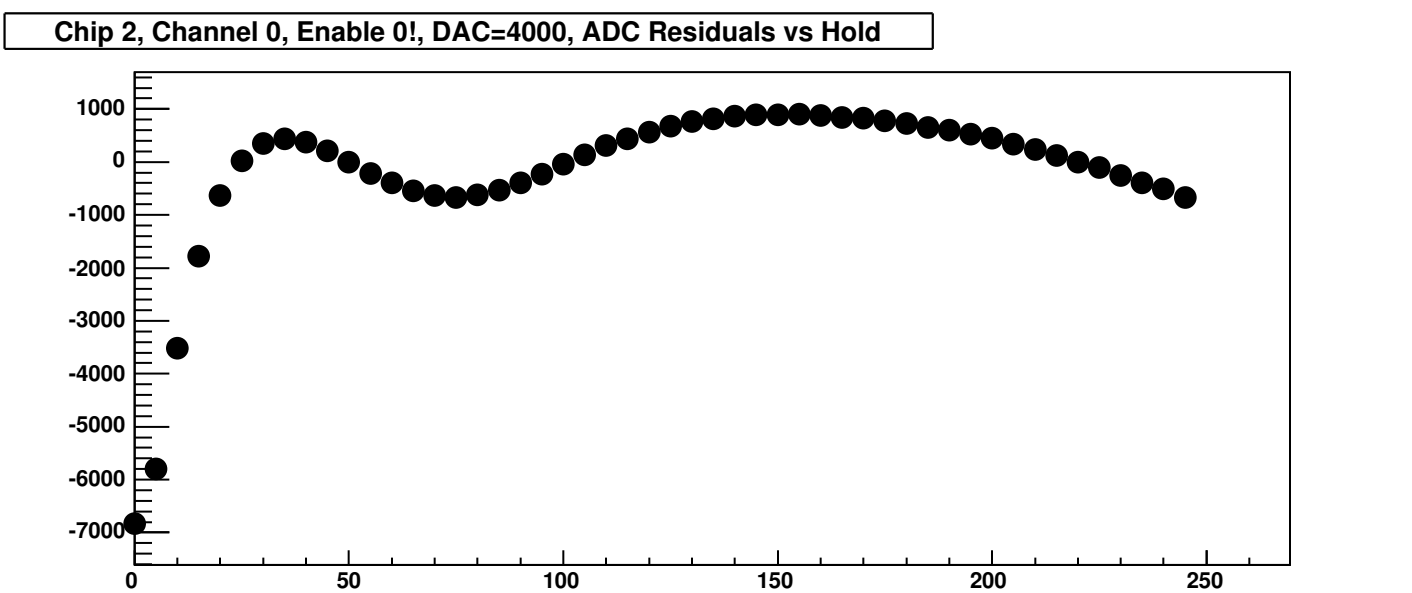


Chip 1, Channel 17, Enable 5!, DAC=4000, ADC Residuals vs Hold

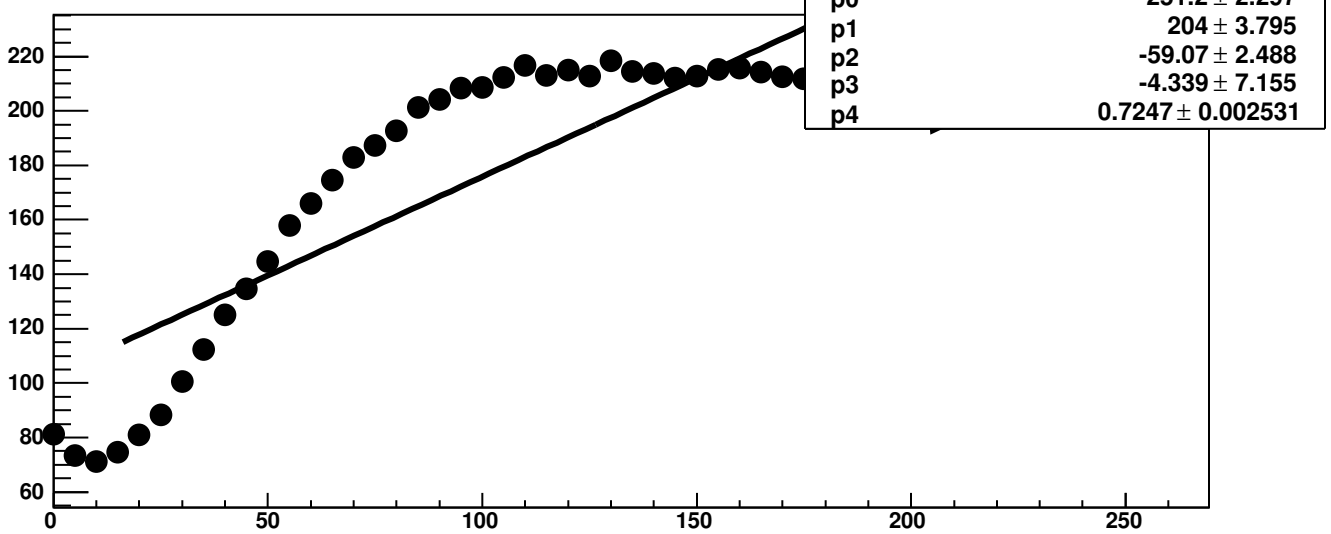



Chip 2, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.59e+05 / 42
p0 -427.5 ± 20.87
p1 -37.56 ± 0.2128
p2 9290 ± 20.04
p3 82.03 ± 0.2012
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

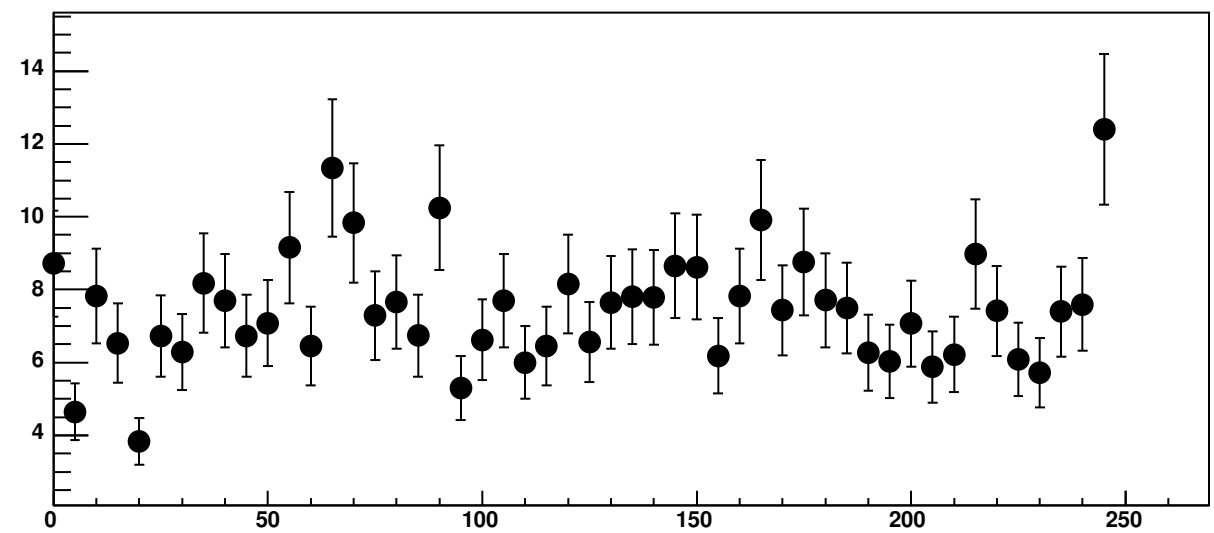

Chip 2, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 2, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
-7000
0
50
100
150
200
250

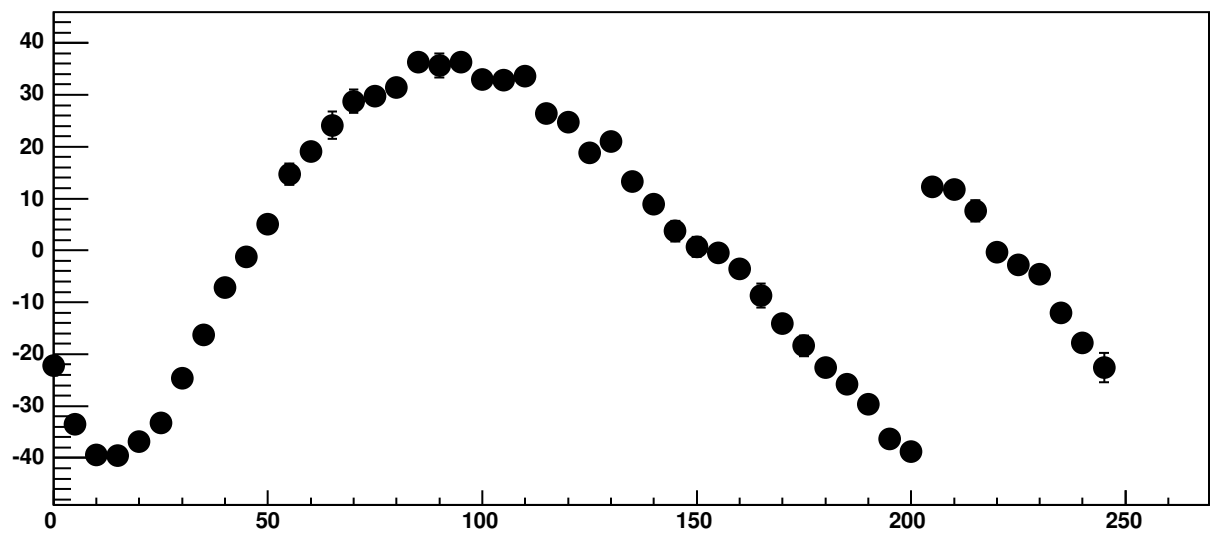
Chip 2, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold



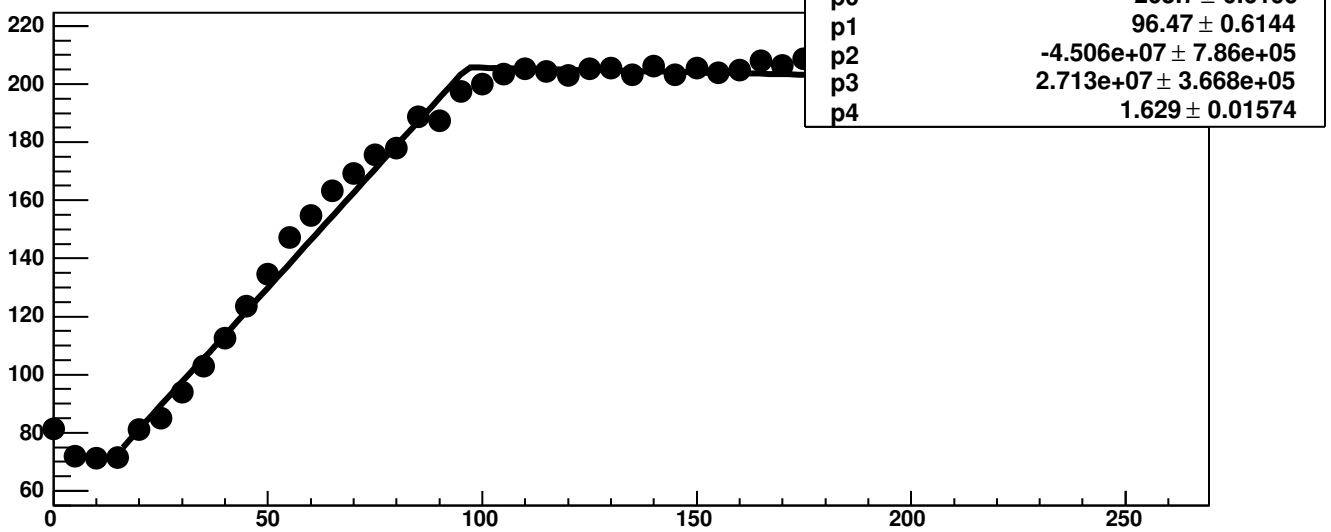
Chip 2, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



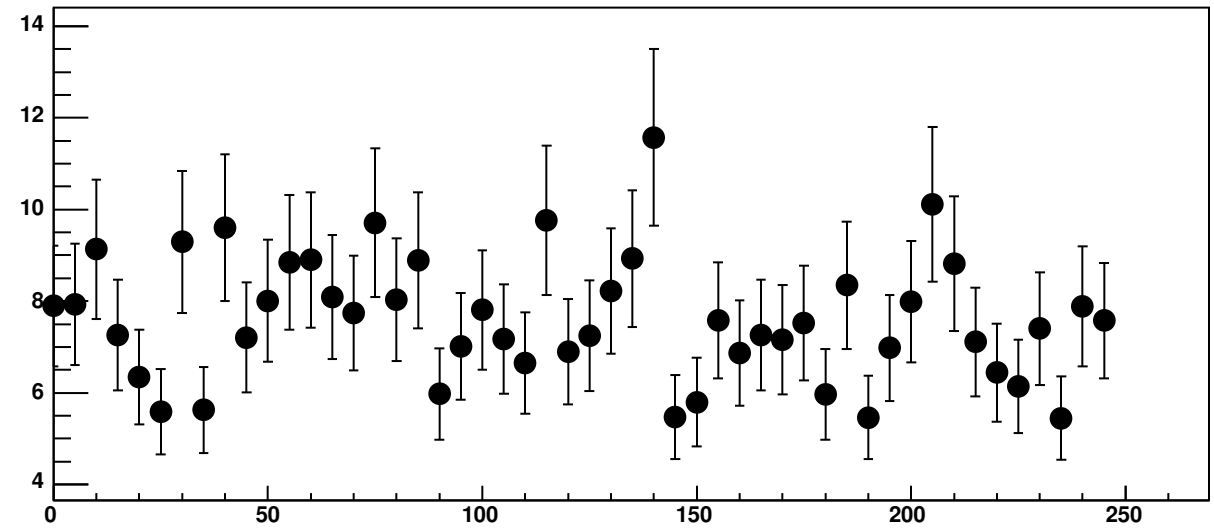
Chip 2, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold



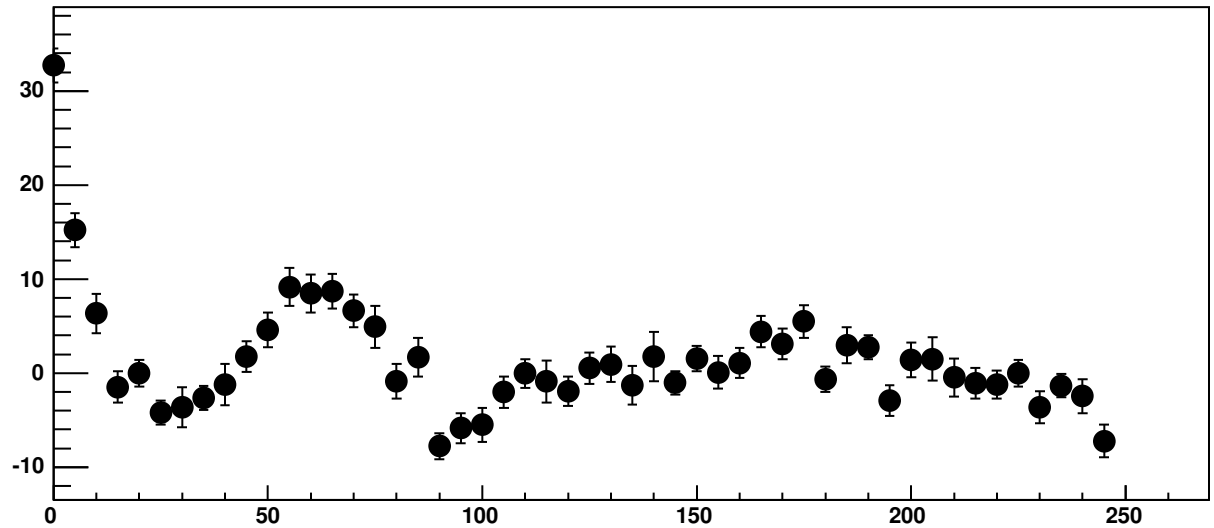
Chip 2, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold



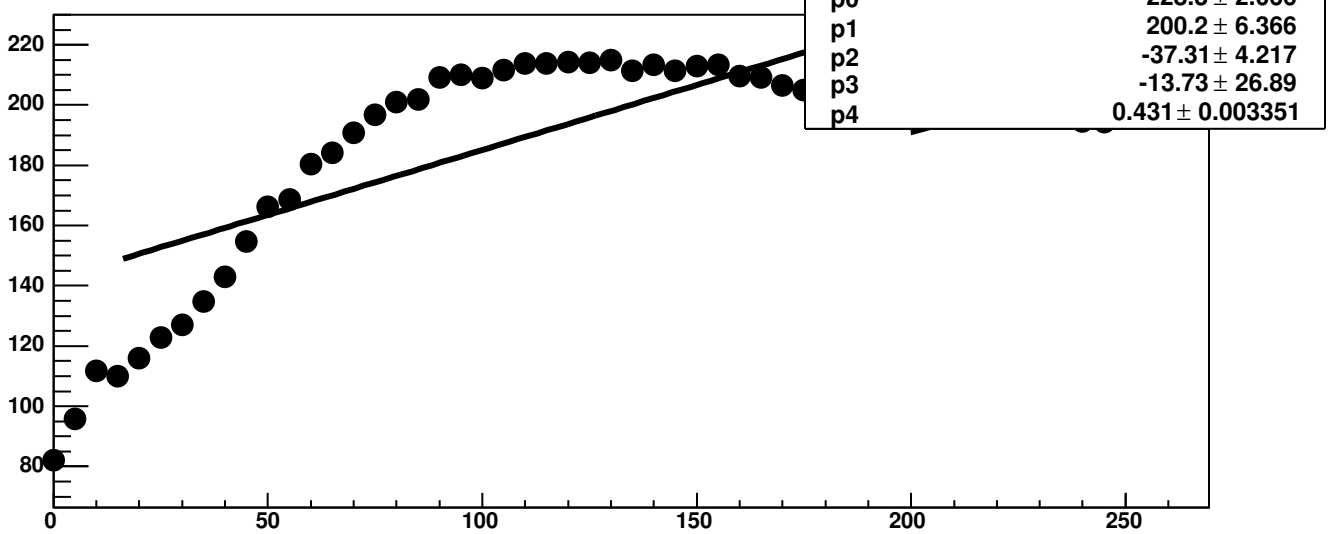
Chip 2, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



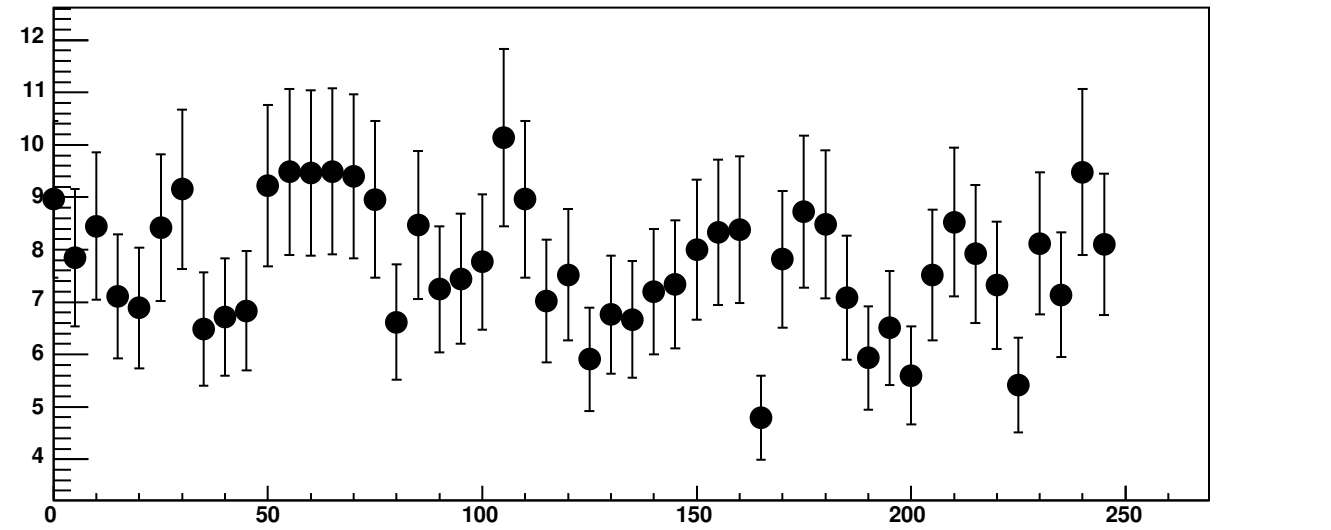
Chip 2, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold



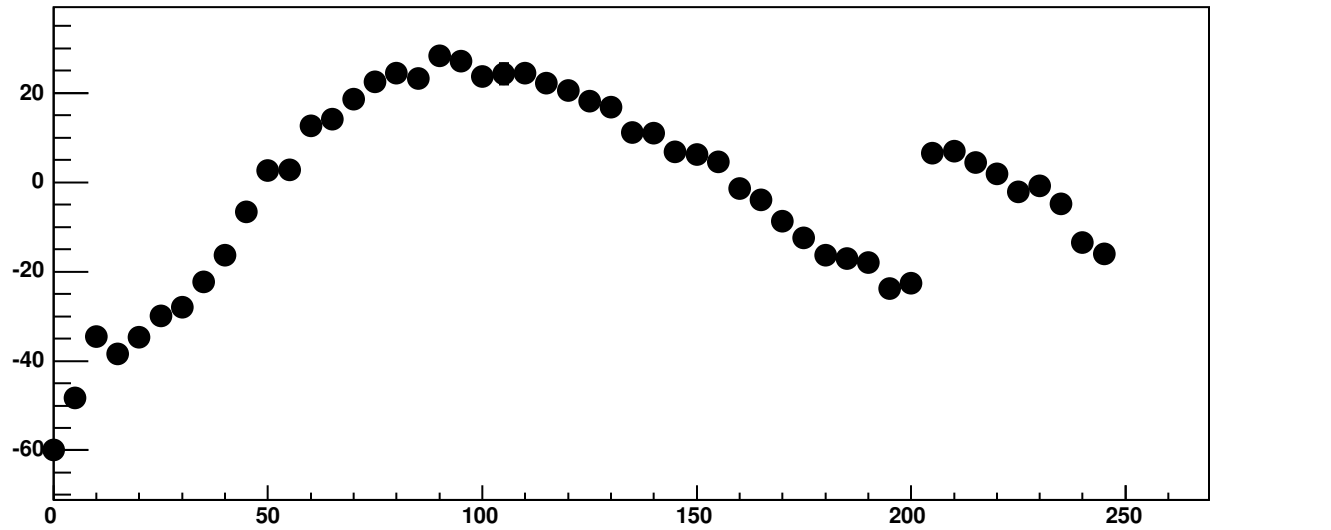
Chip 2, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold



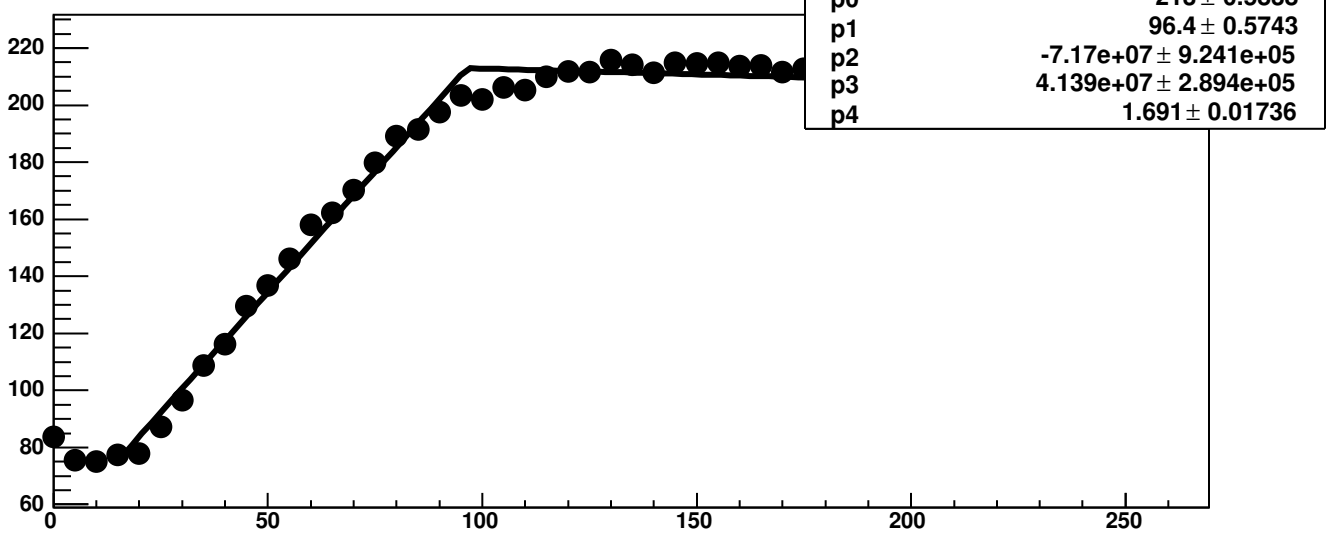
Chip 2, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold



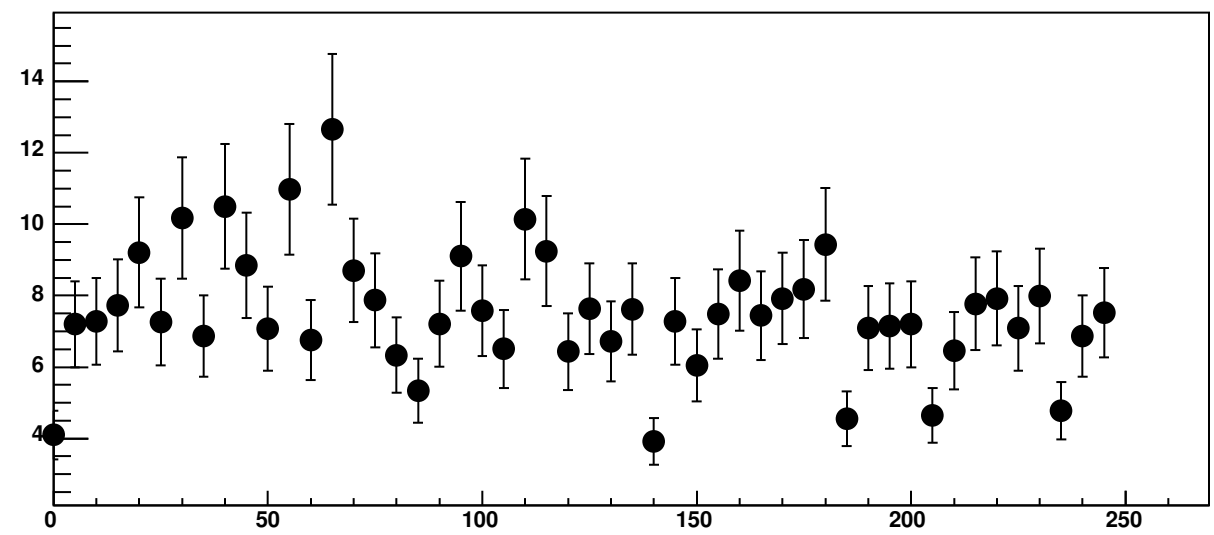
Chip 2, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold



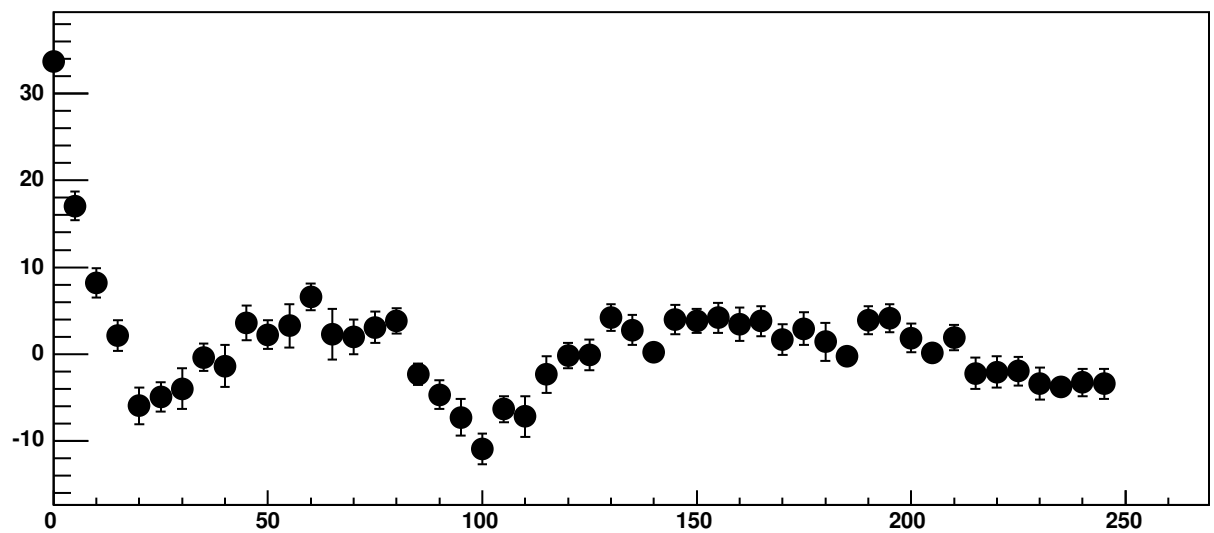
Chip 2, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold



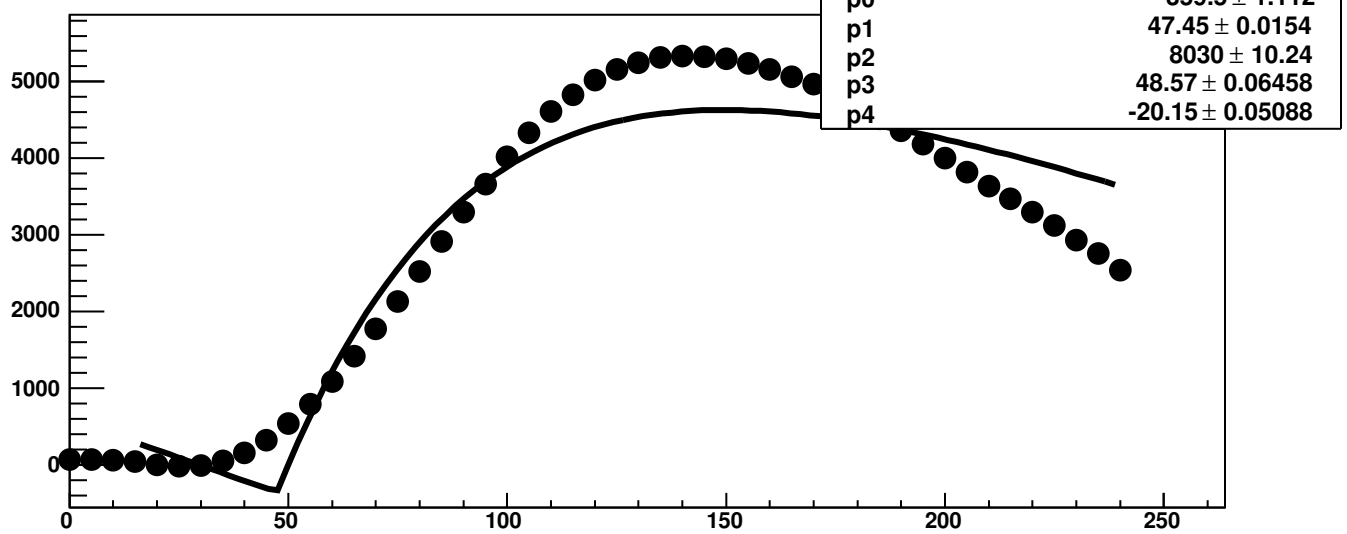
Chip 2, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



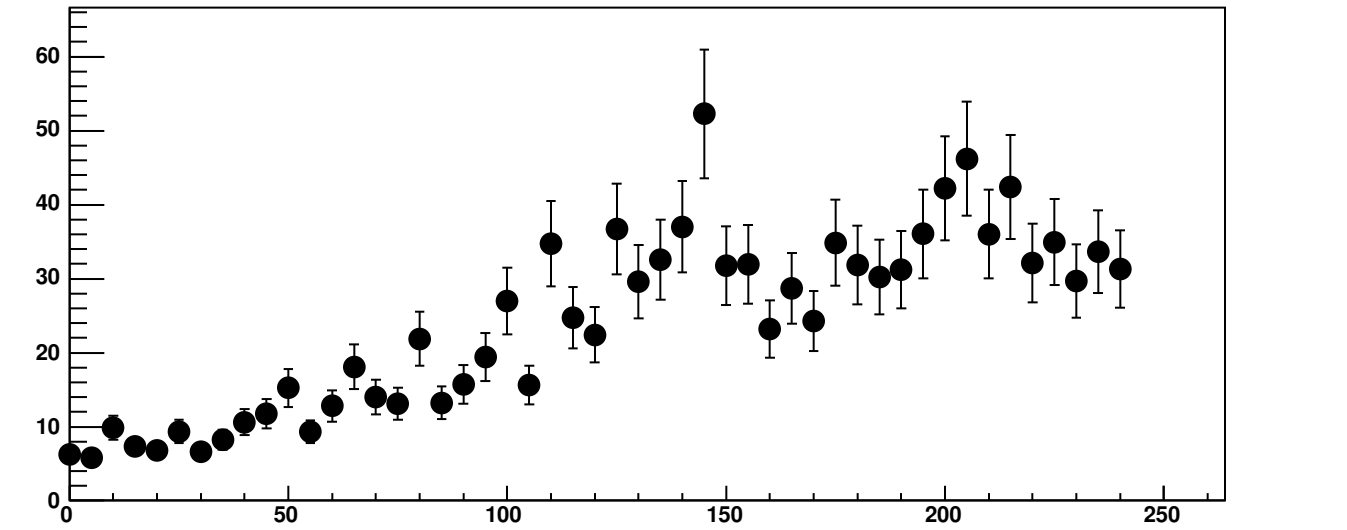
Chip 2, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold



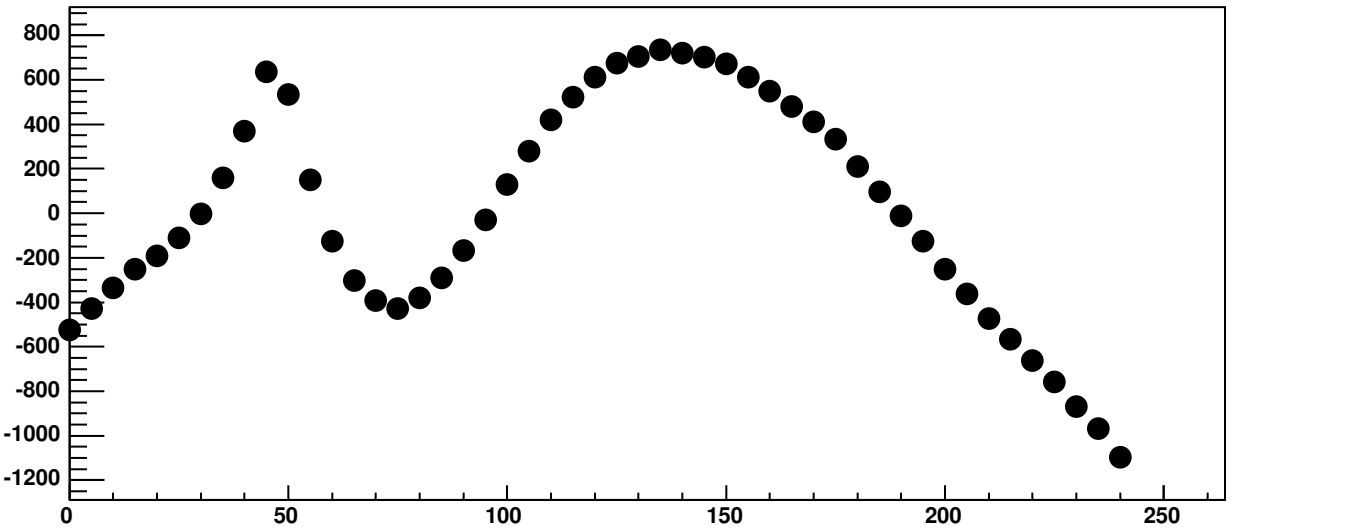
Chip 2, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold



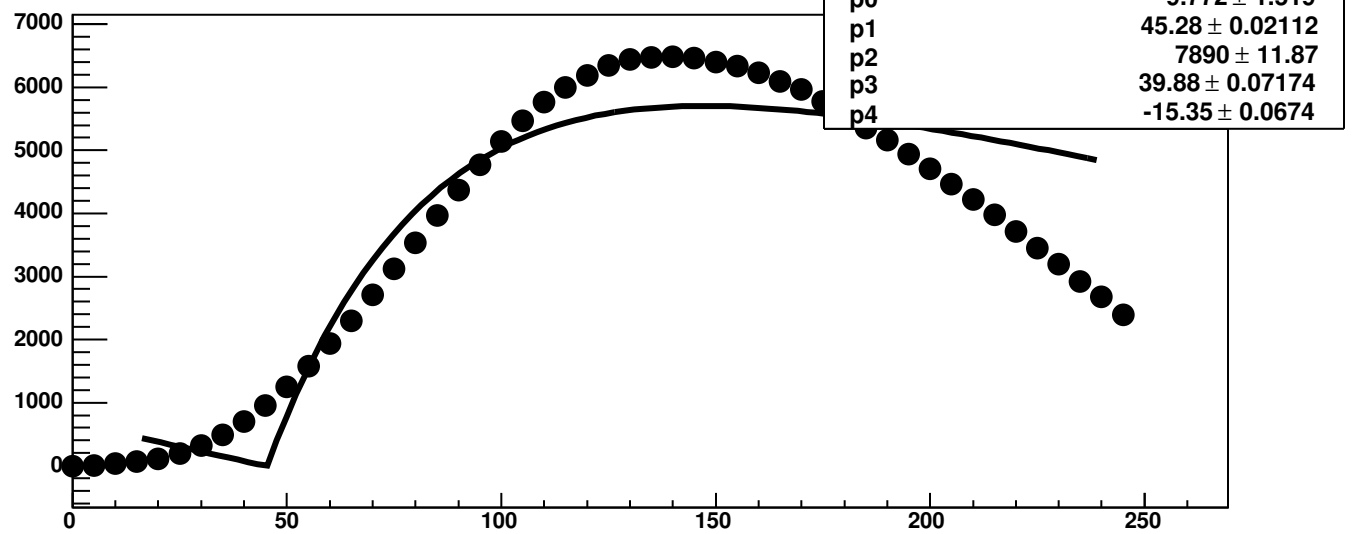
Chip 2, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



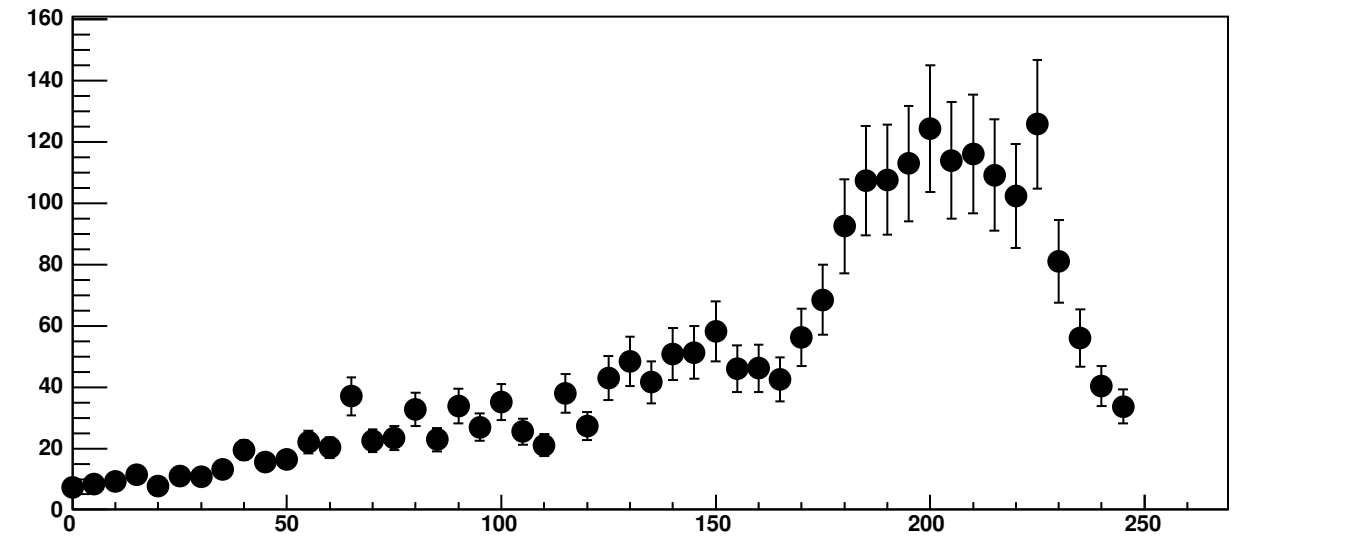
Chip 2, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold



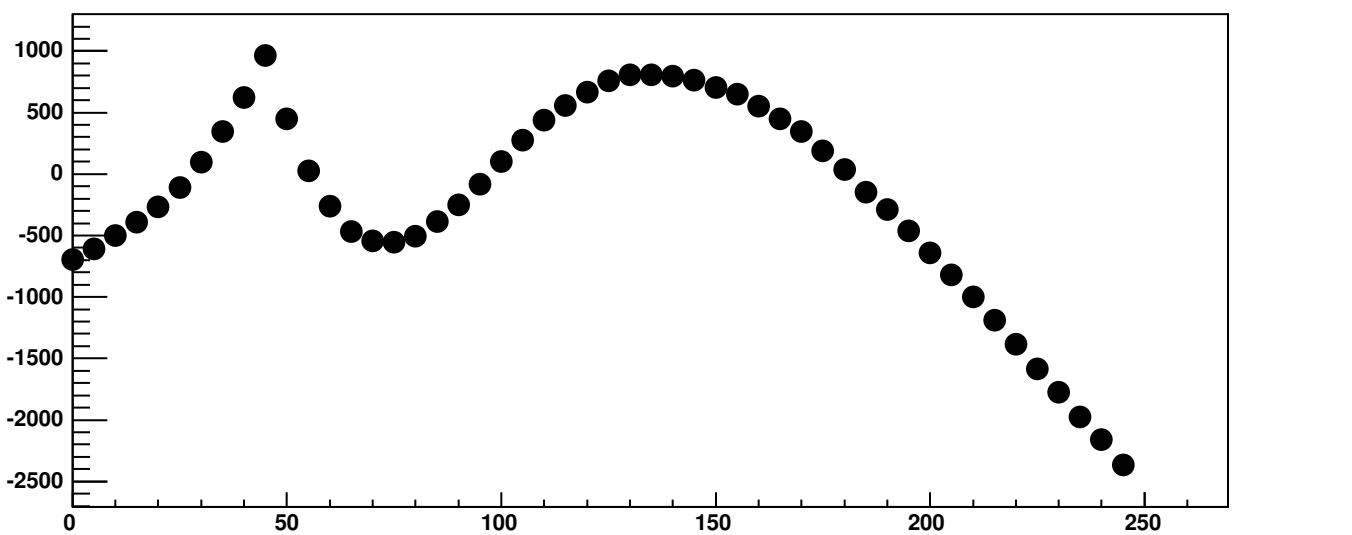
Chip 2, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold



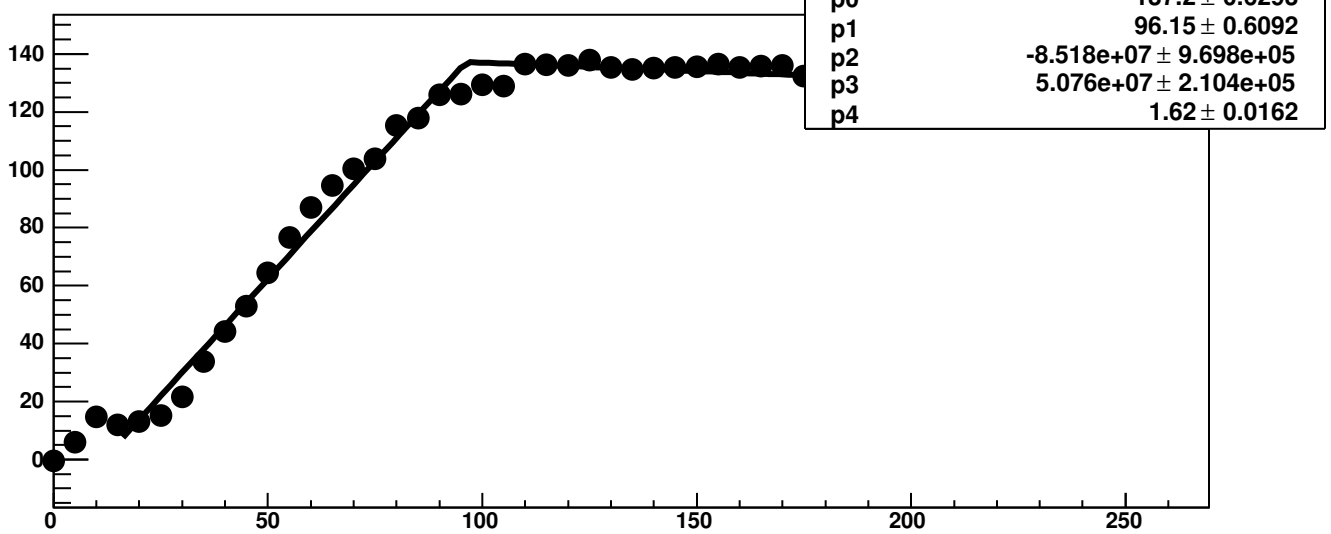
Chip 2, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold



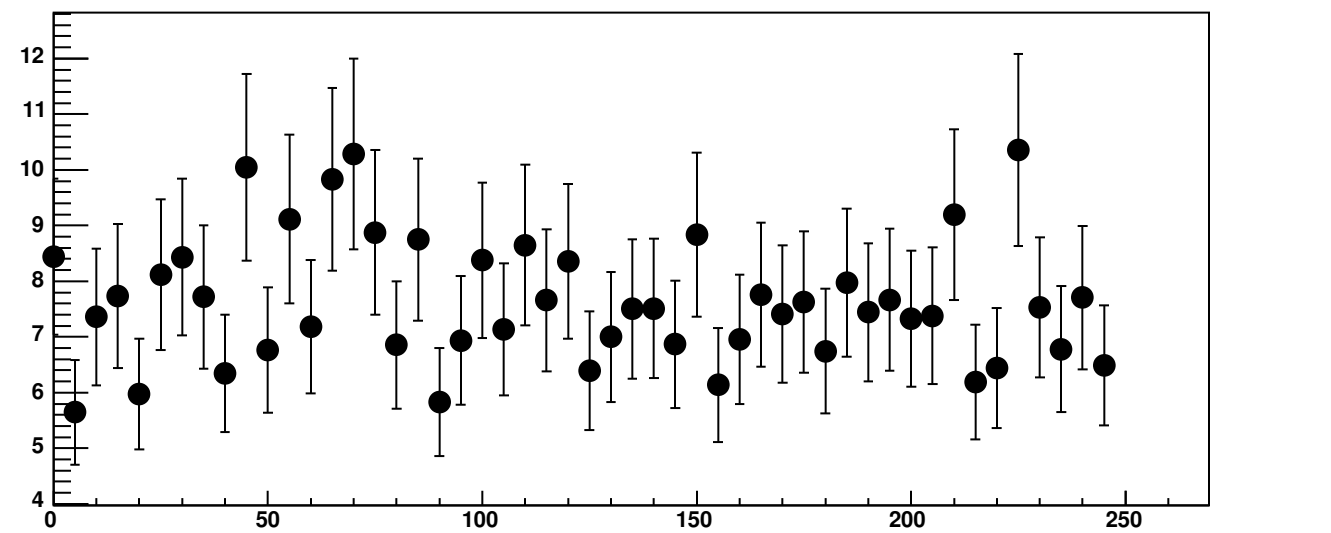
Chip 2, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold



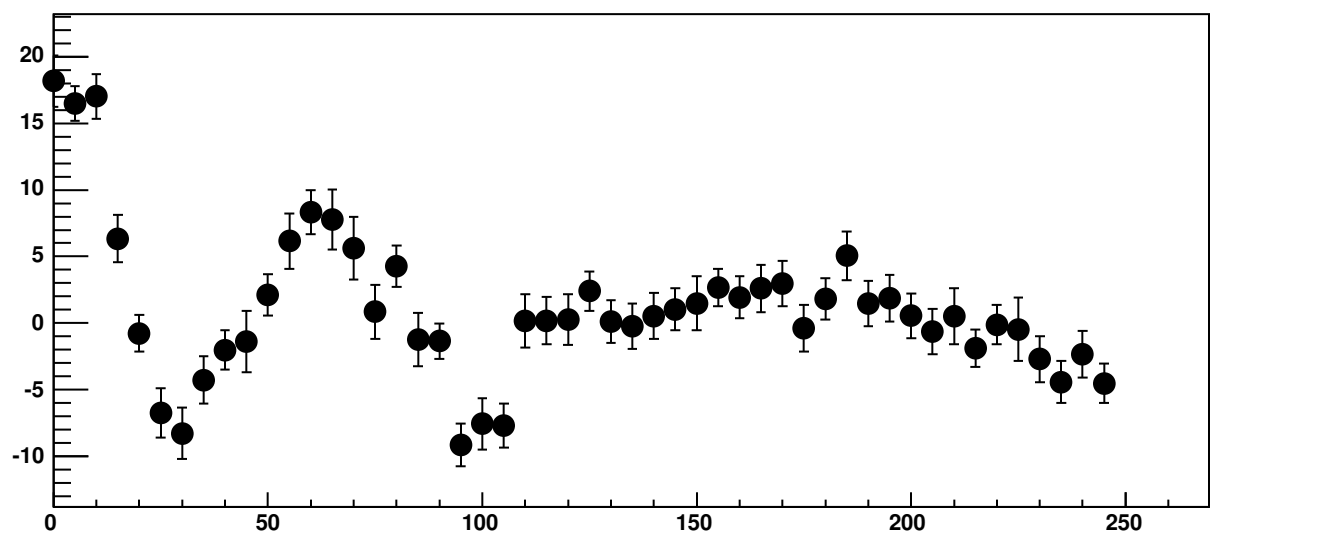
Chip 2, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold



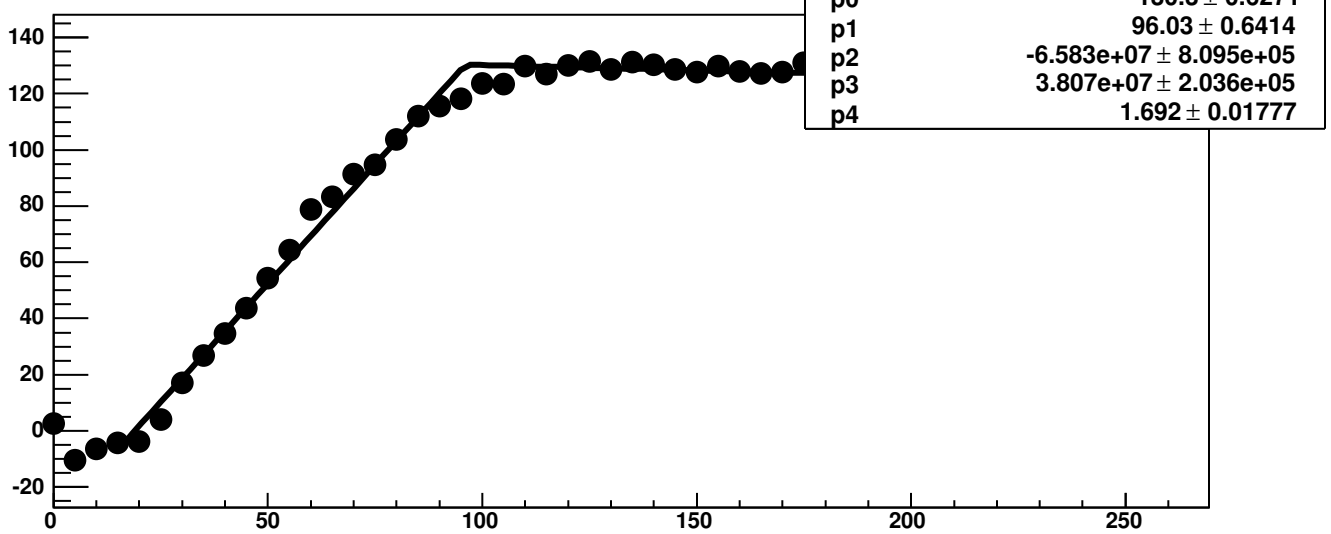
Chip 2, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



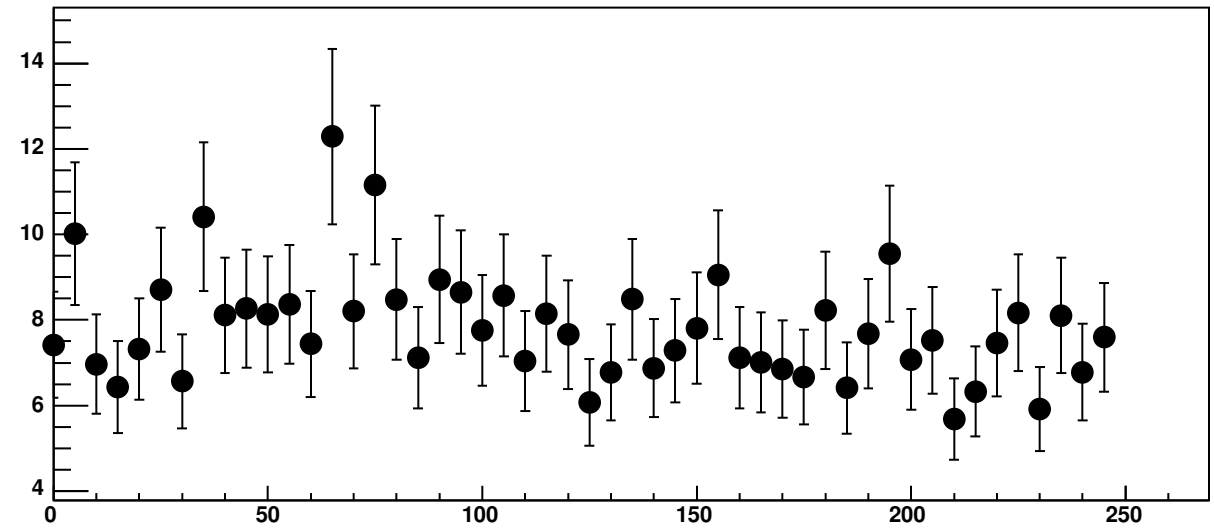
Chip 2, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold



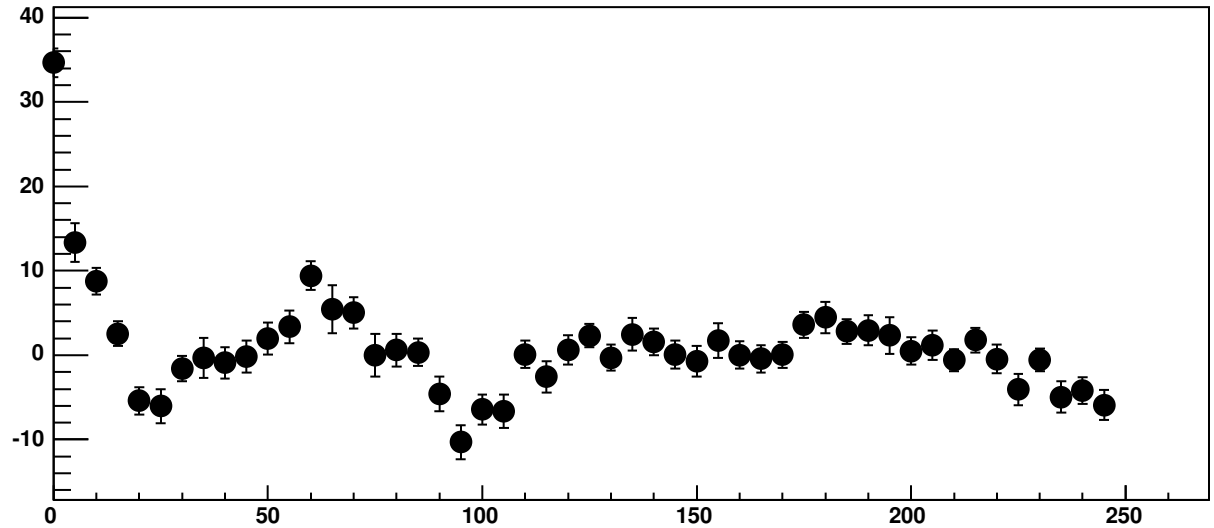
Chip 2, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold



Chip 2, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold

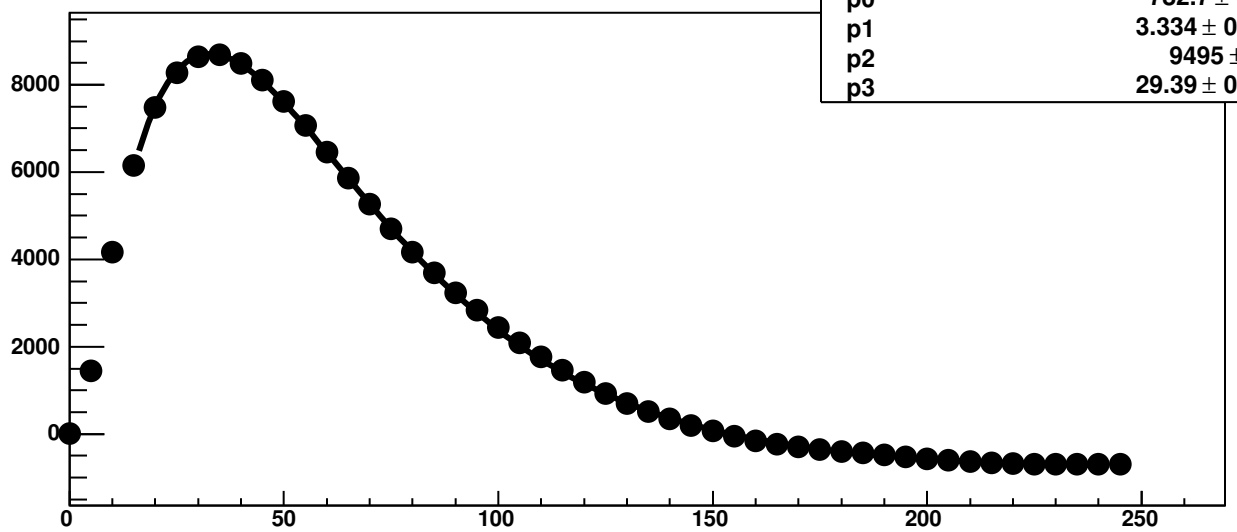


Chip 2, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

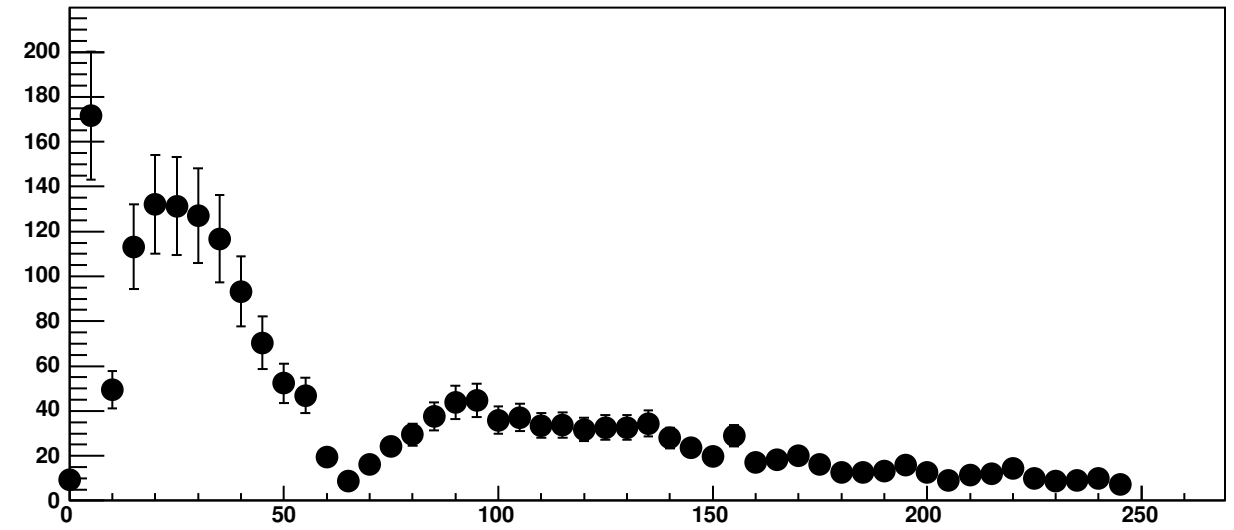


Chip 2, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold

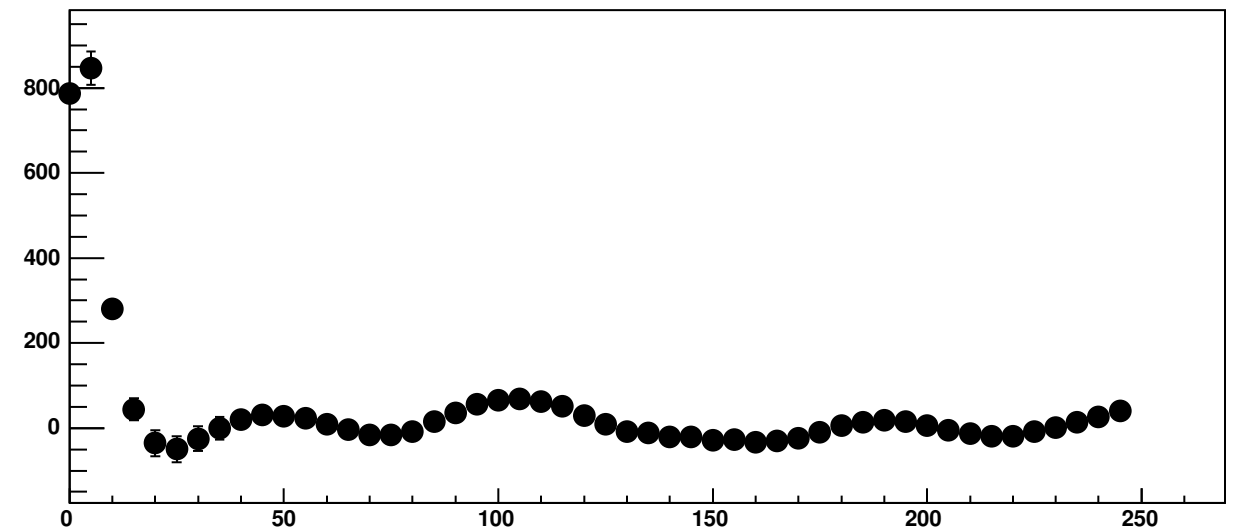
χ^2 / ndf	998.2 / 42
p0	-782.7 ± 0.9856
p1	3.334 ± 0.05352
p2	9495 ± 6.307
p3	29.39 ± 0.02185



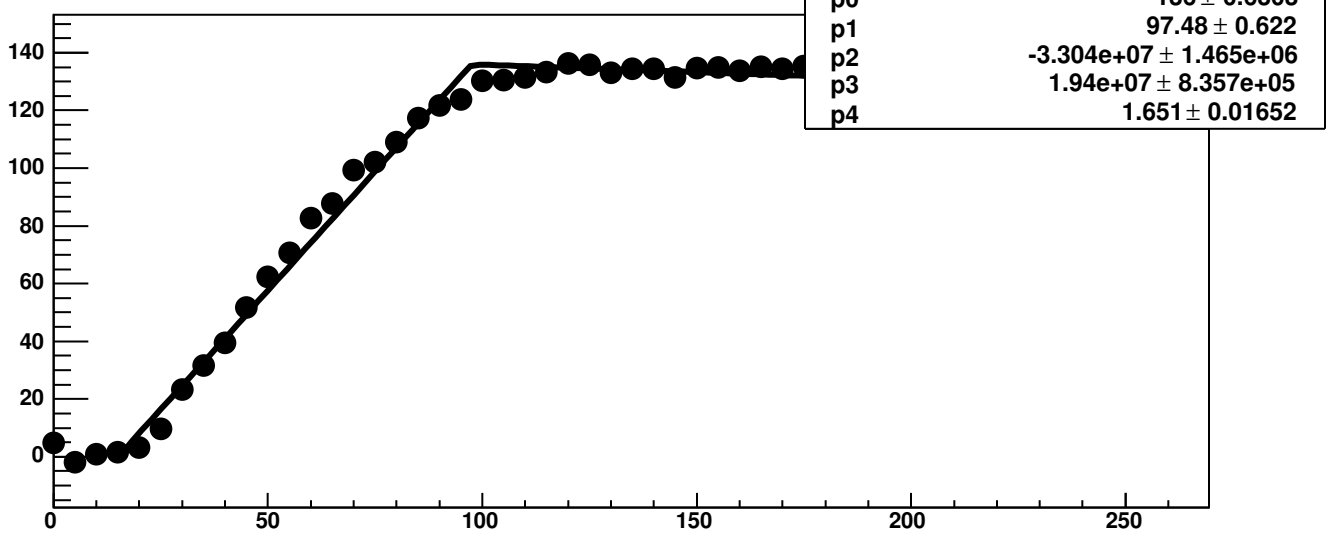
Chip 2, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold



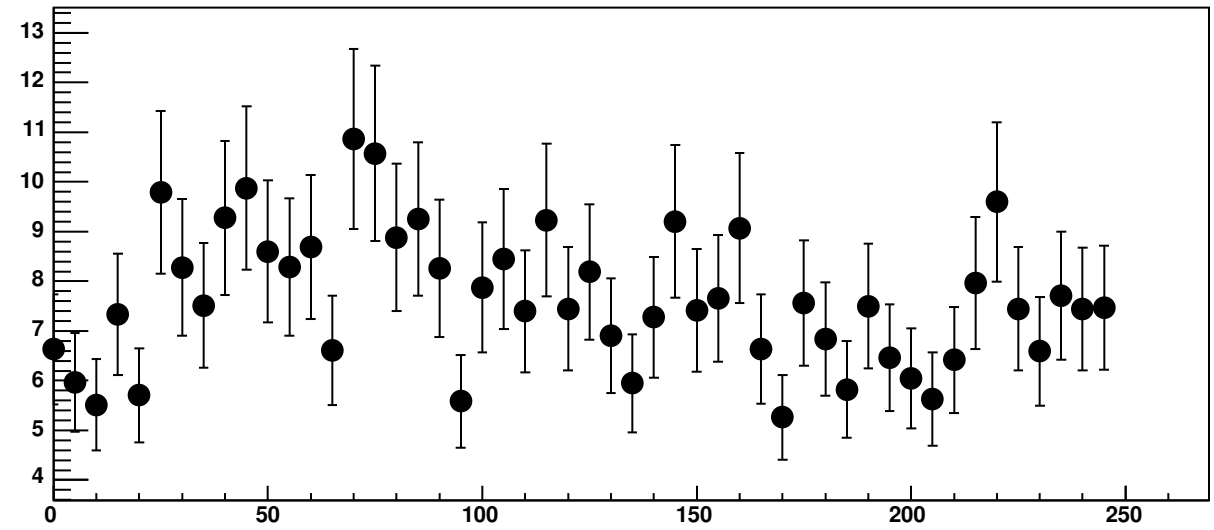
Chip 2, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold



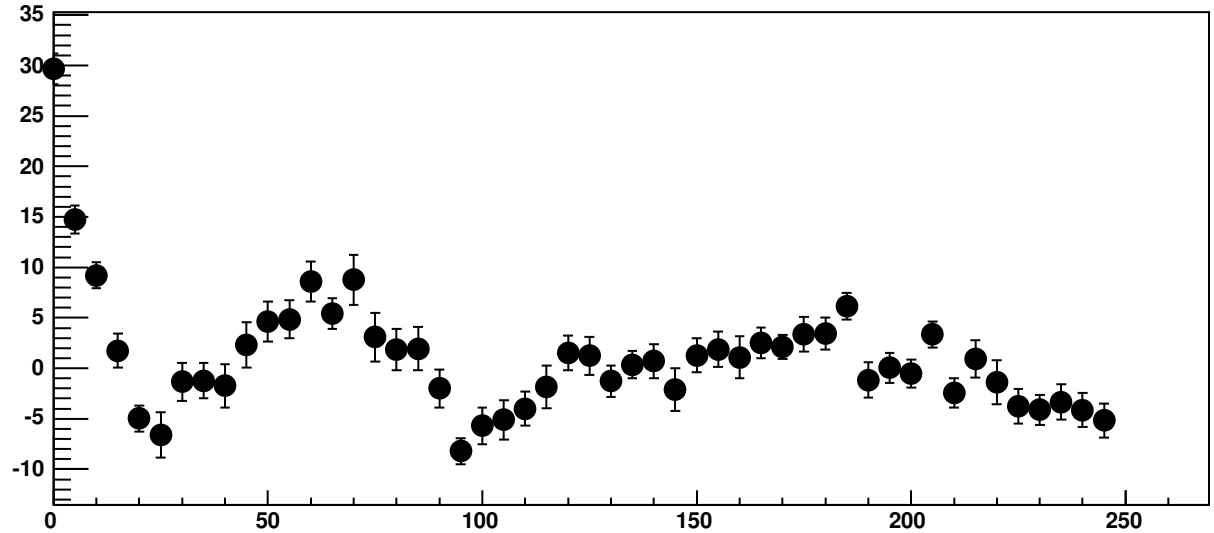
Chip 2, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold



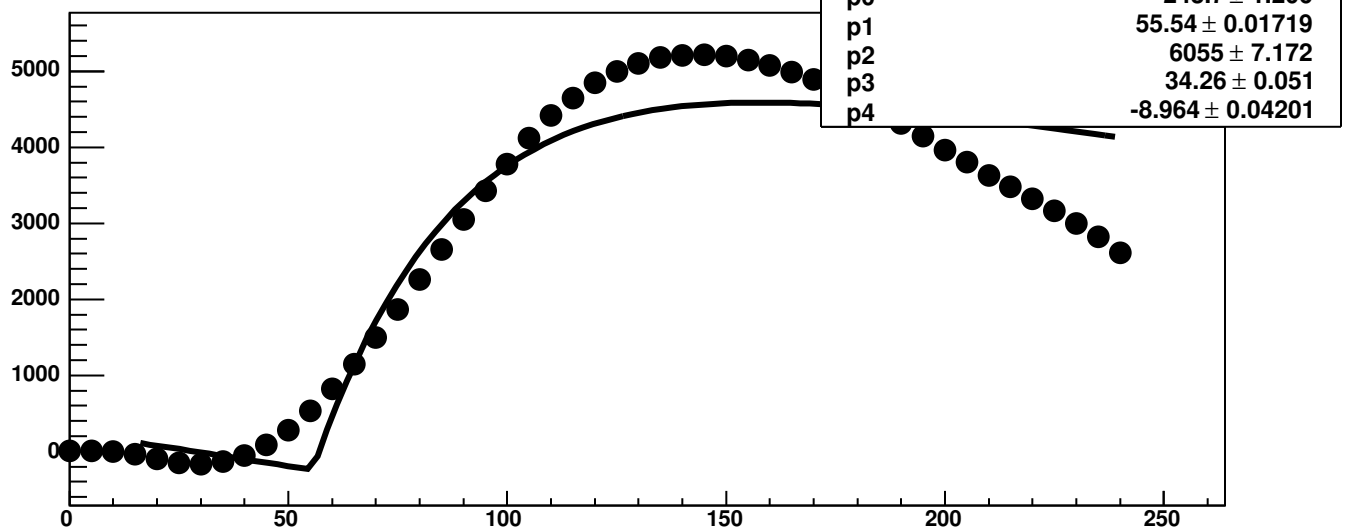
Chip 2, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



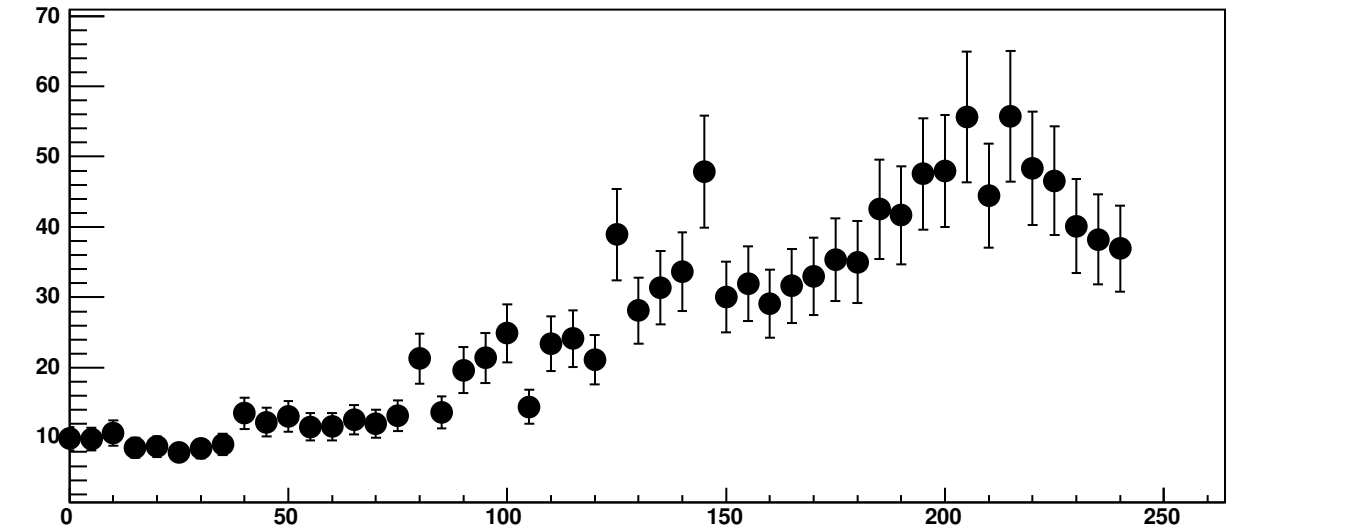
Chip 2, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold



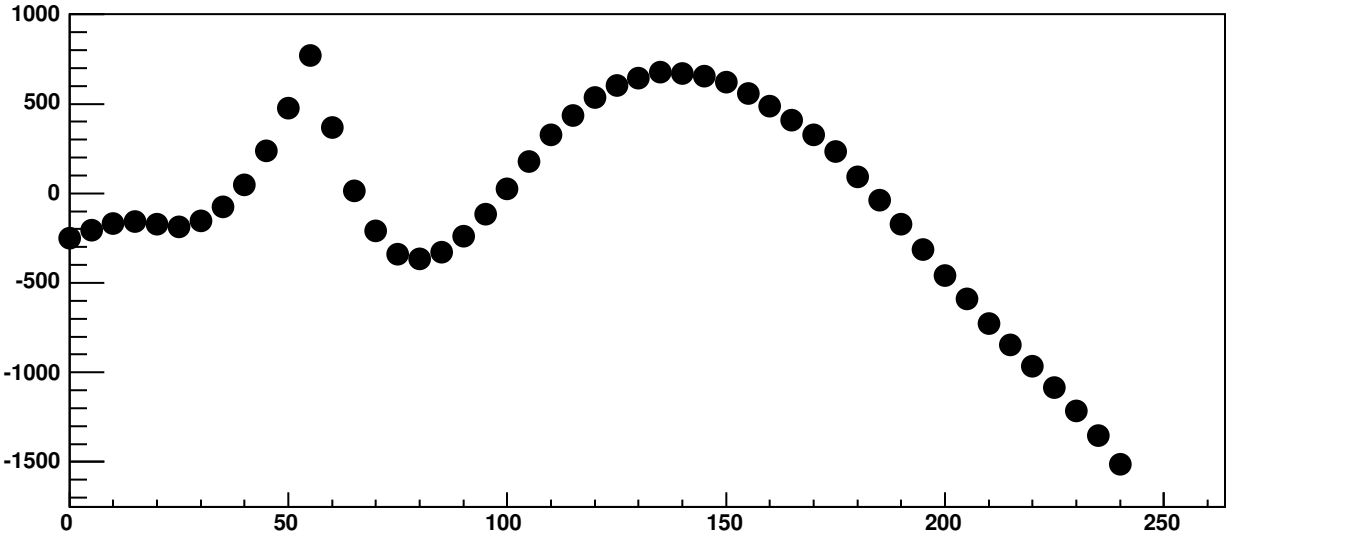
Chip 2, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold



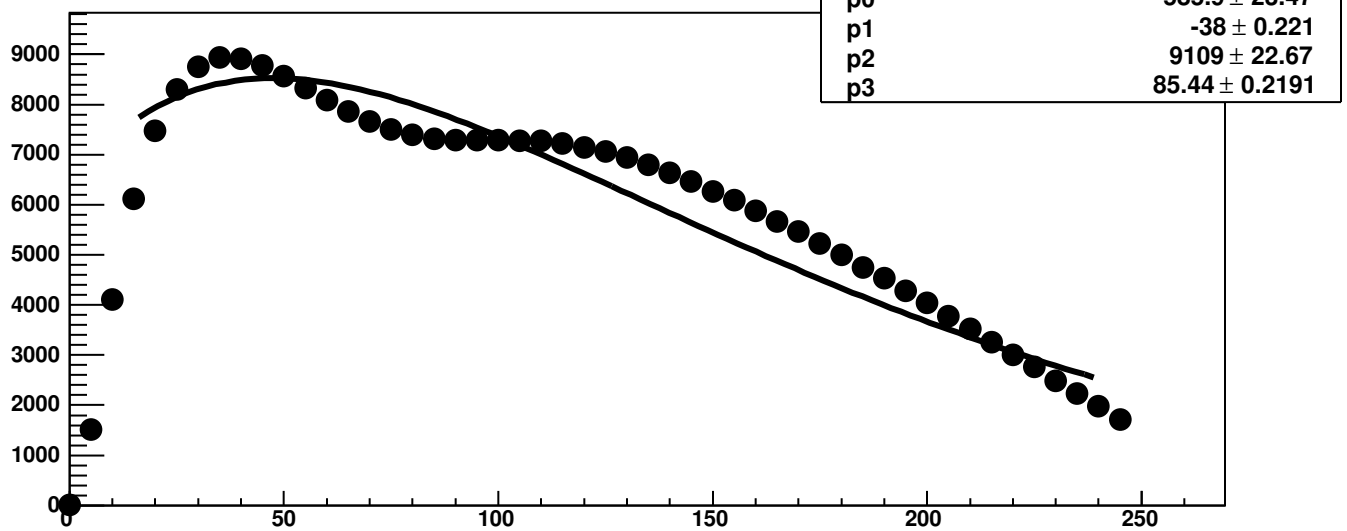
Chip 2, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



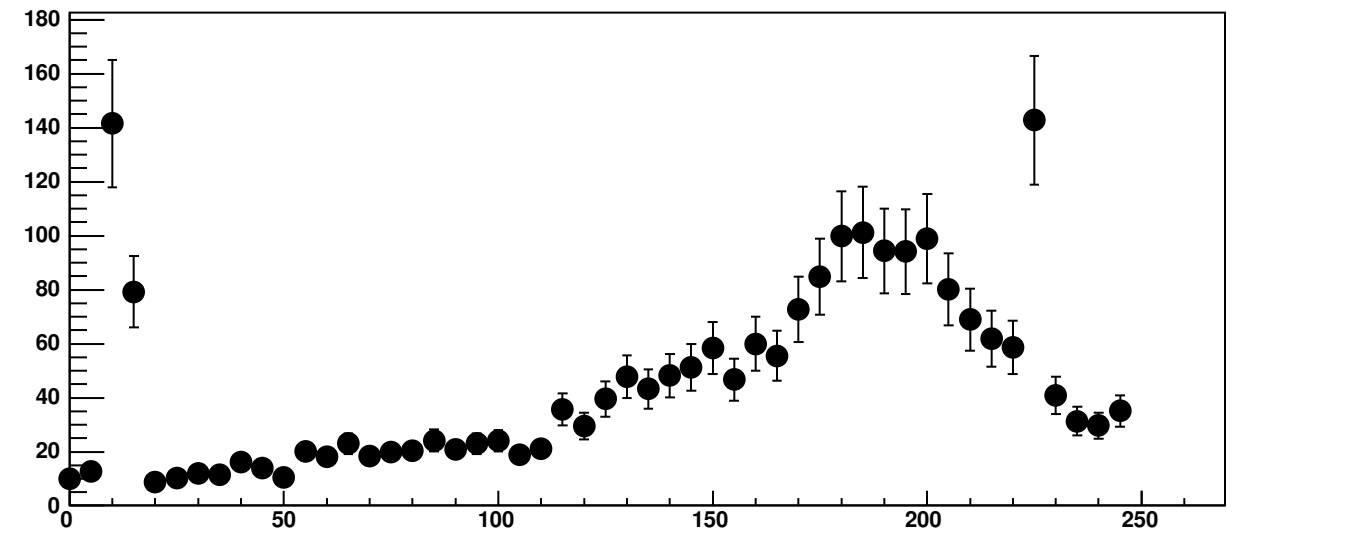
Chip 2, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold



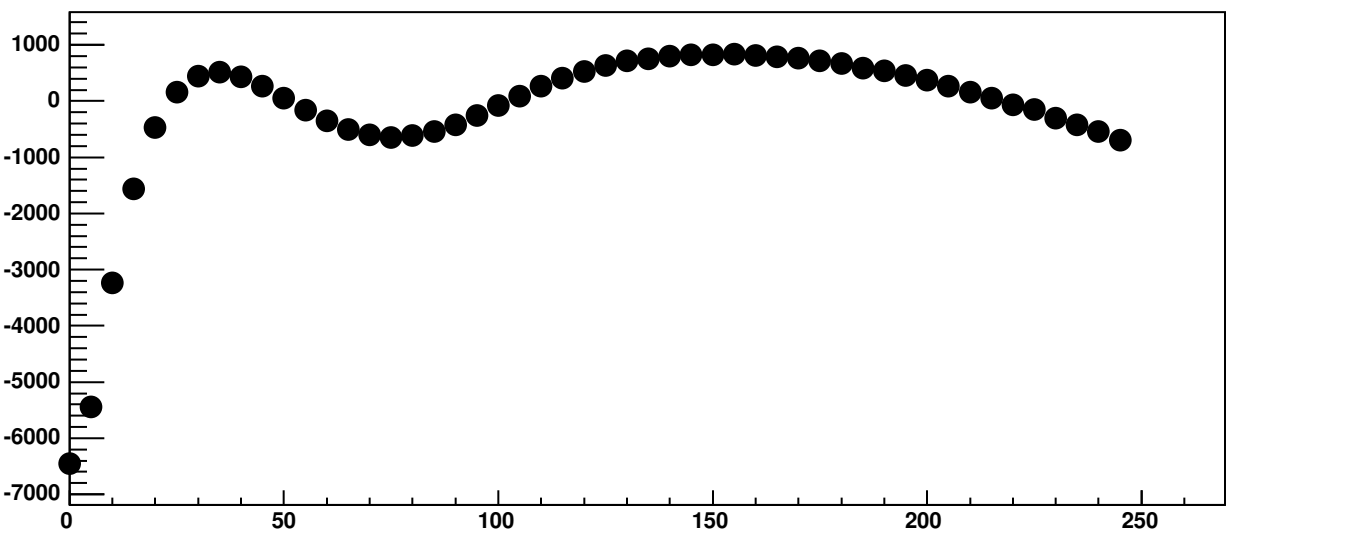
Chip 2, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold

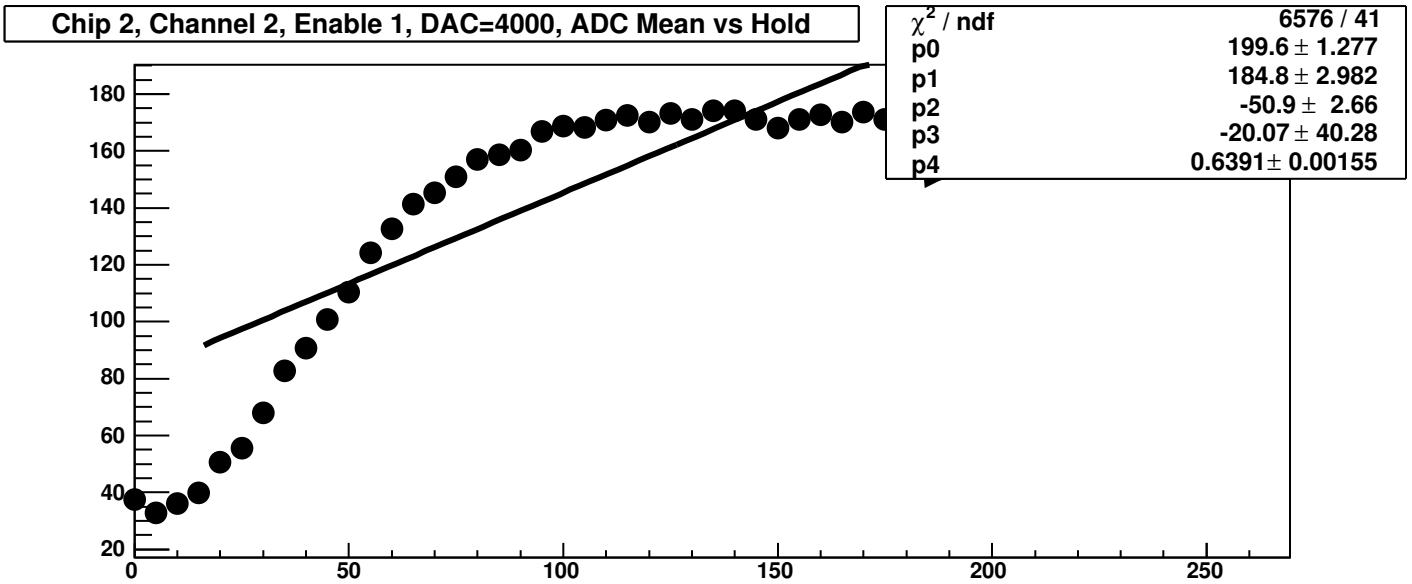


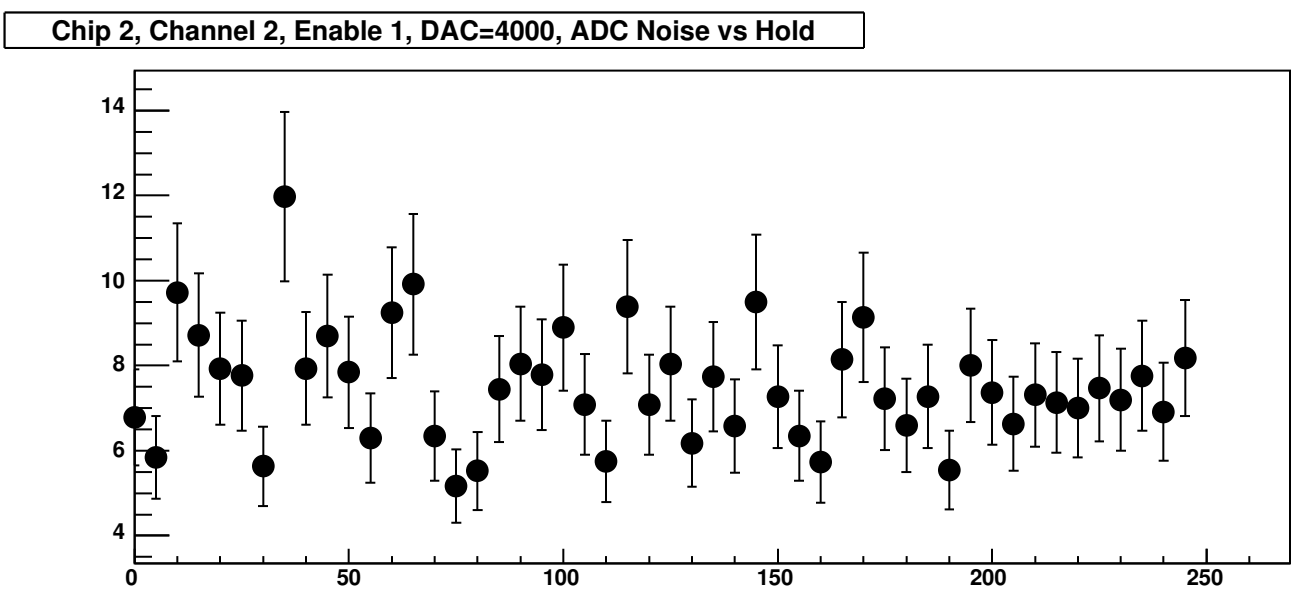
Chip 2, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold

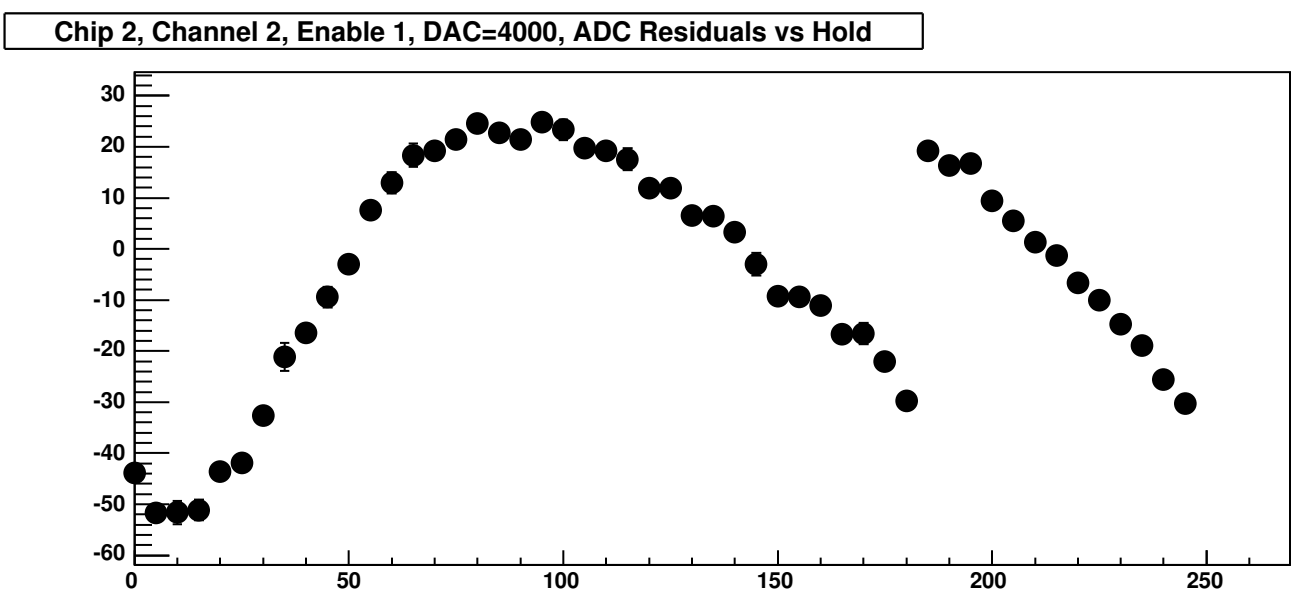


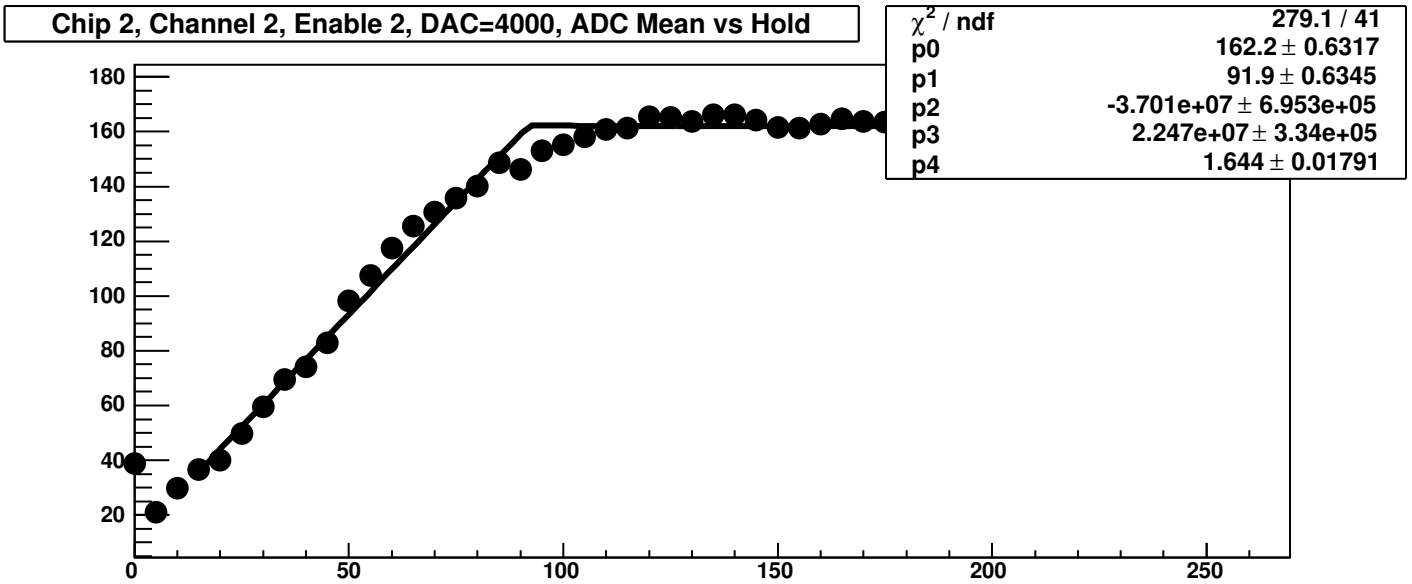
Chip 2, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold

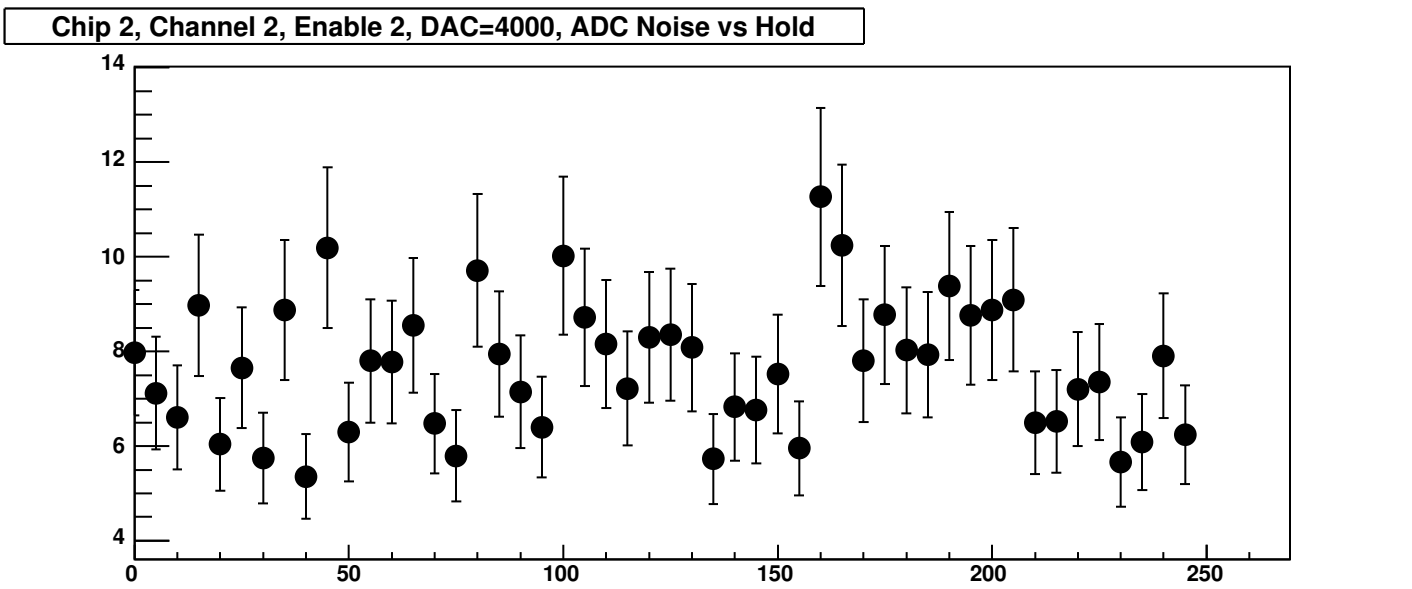


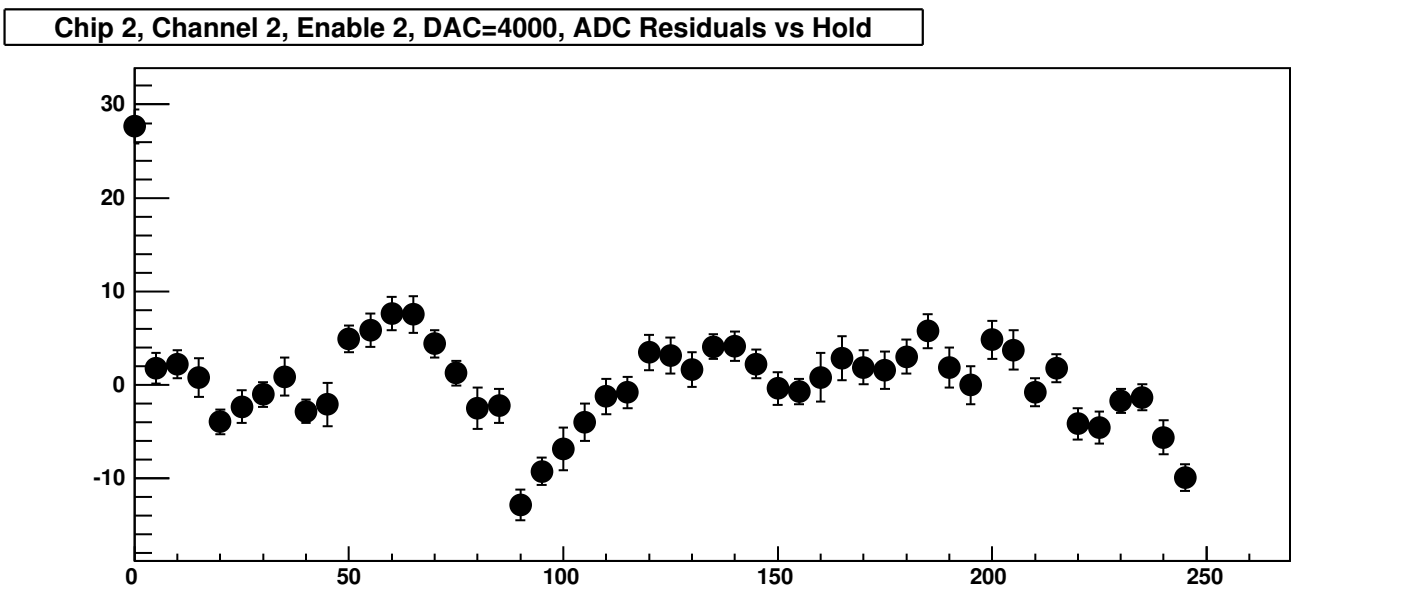

Chip 2, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 6576 / 41
p0 199.6 ± 1.277
p1 184.8 ± 2.982
p2 -50.9 ± 2.66
p3 -20.07 ± 40.28
p4 0.6391 ± 0.00155
180
160
140
120
100
80
60
40
20
0
50
100
150
200
250


Chip 2, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

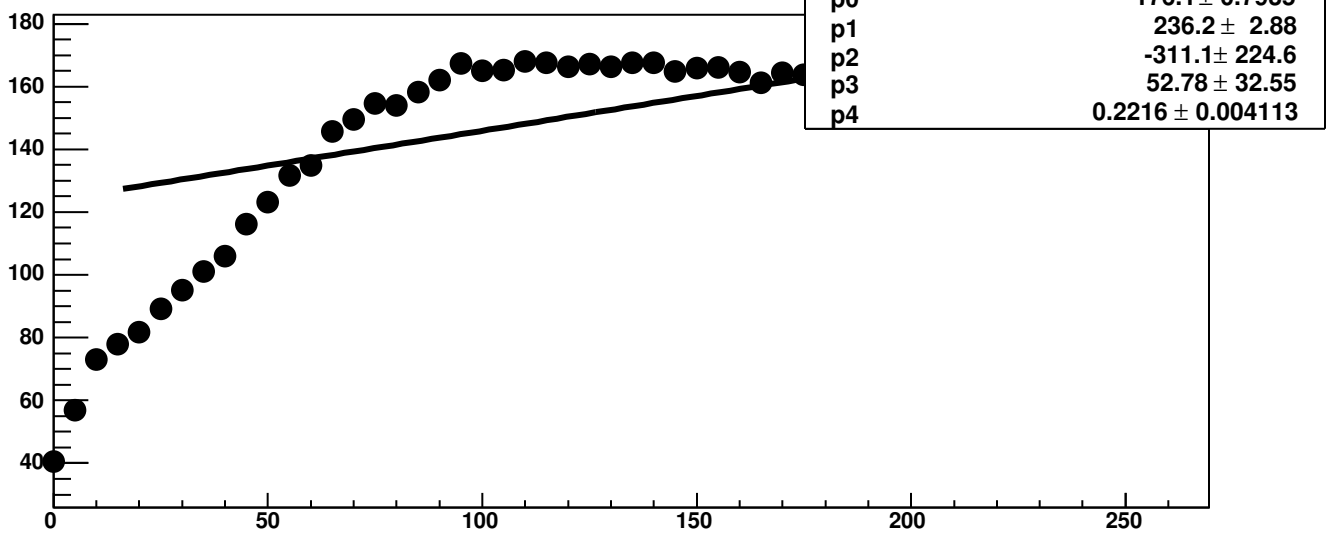

Chip 2, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
-20
-30
-40
-50
-60
0
50
100
150
200
250


Chip 2, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 279.1 / 41
p0 162.2 ± 0.6317
p1 91.9 ± 0.6345
p2 -3.701e+07 ± 6.953e+05
p3 2.247e+07 ± 3.34e+05
p4 1.644 ± 0.01791
180
160
140
120
100
80
60
40
20
0
50
100
150
200
250

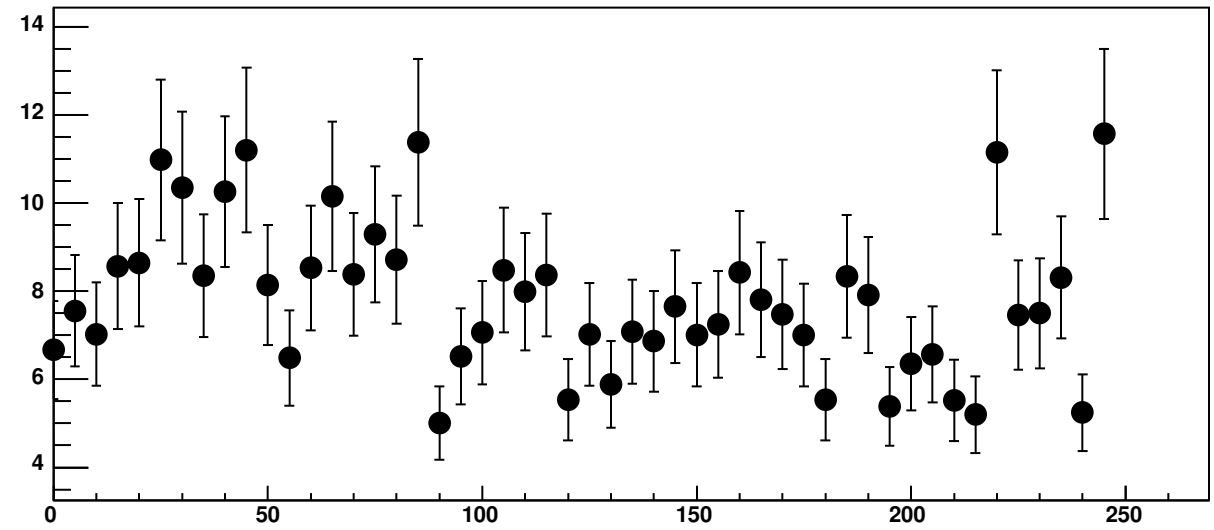

Chip 2, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 2, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
0
50
100
150
200
250

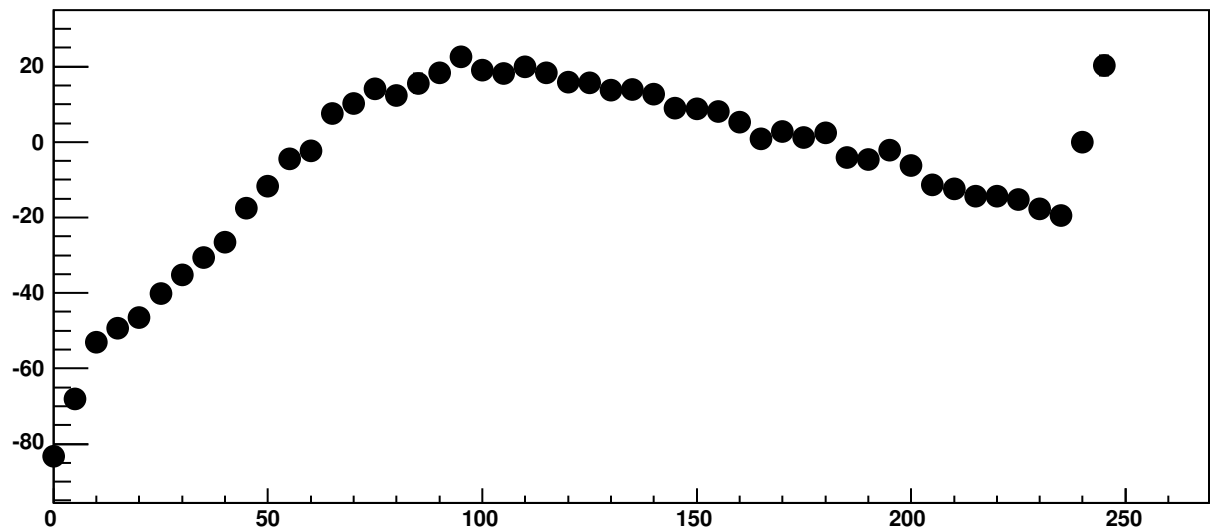
Chip 2, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold



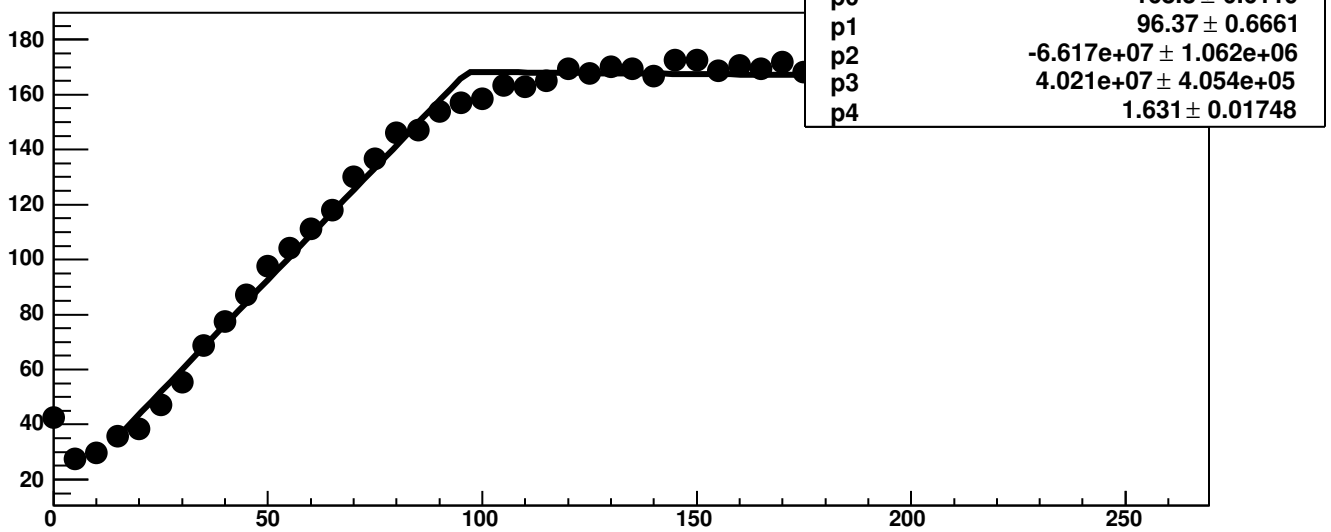
Chip 2, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold



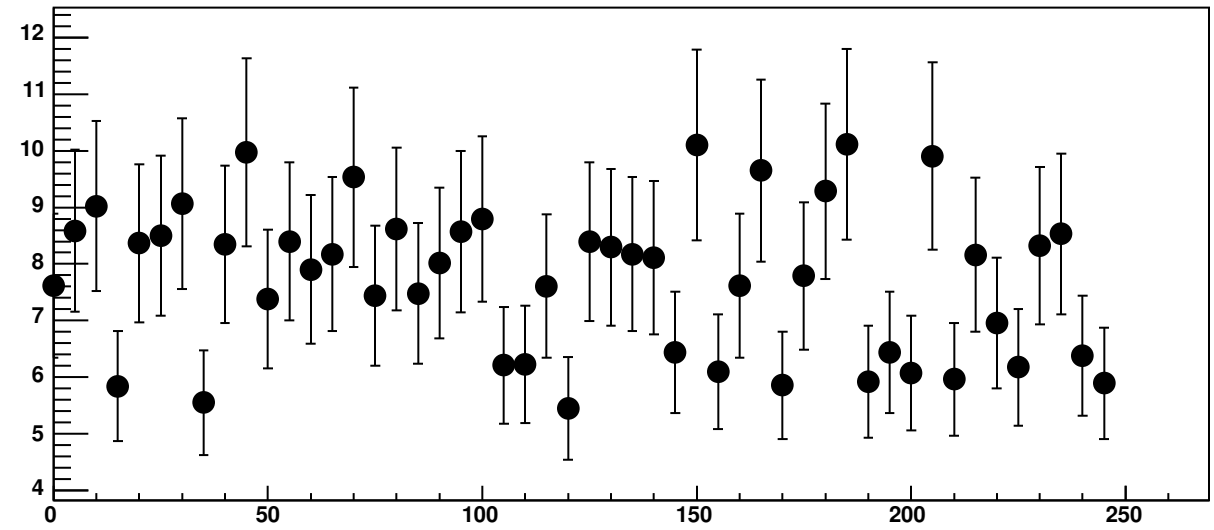
Chip 2, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold



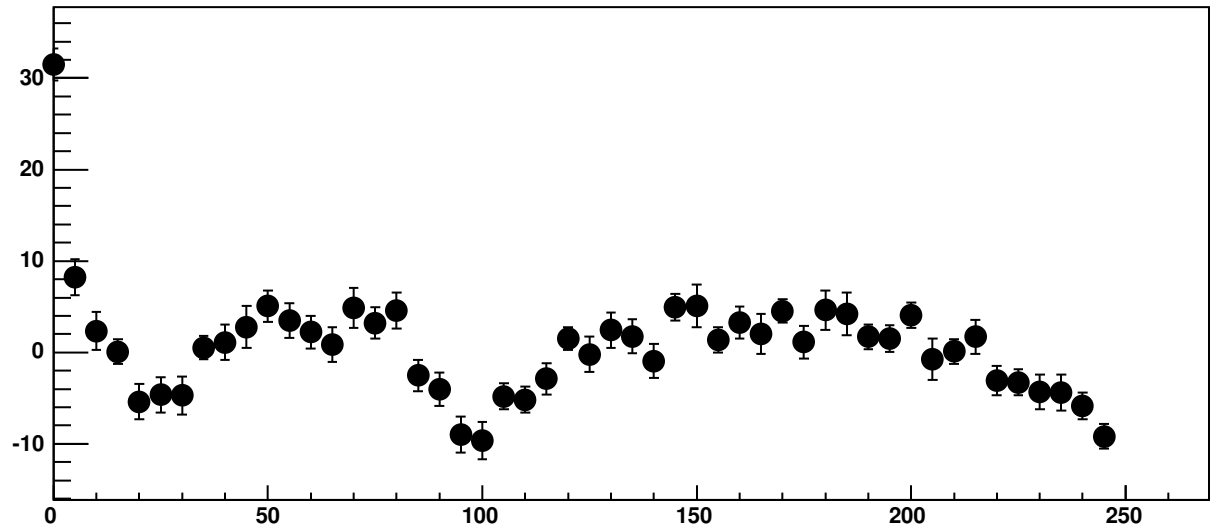
Chip 2, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold



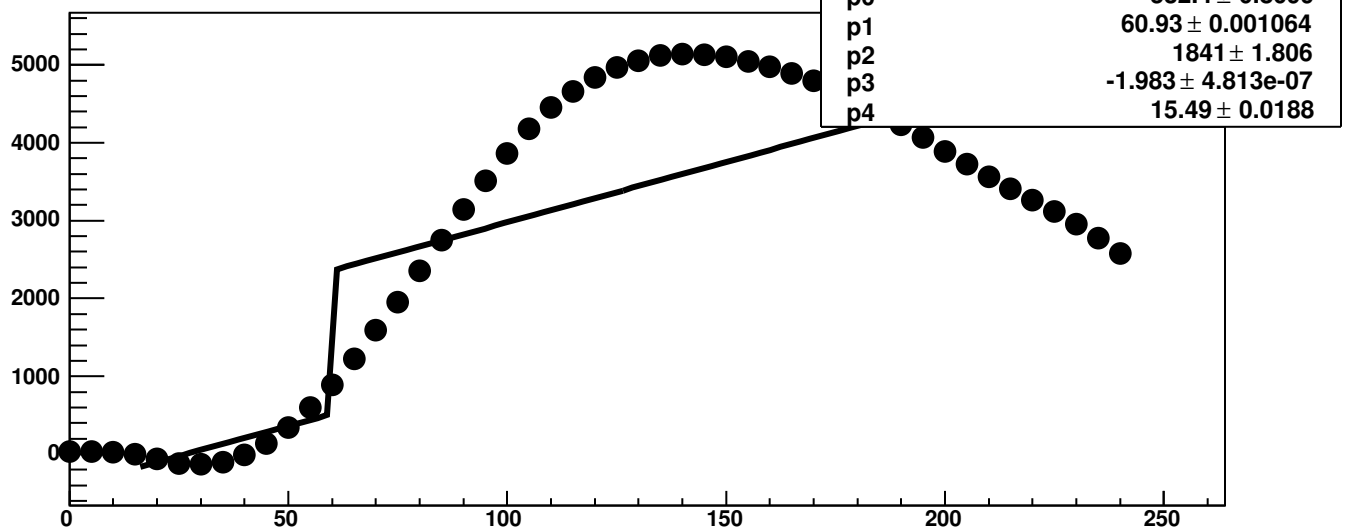
Chip 2, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



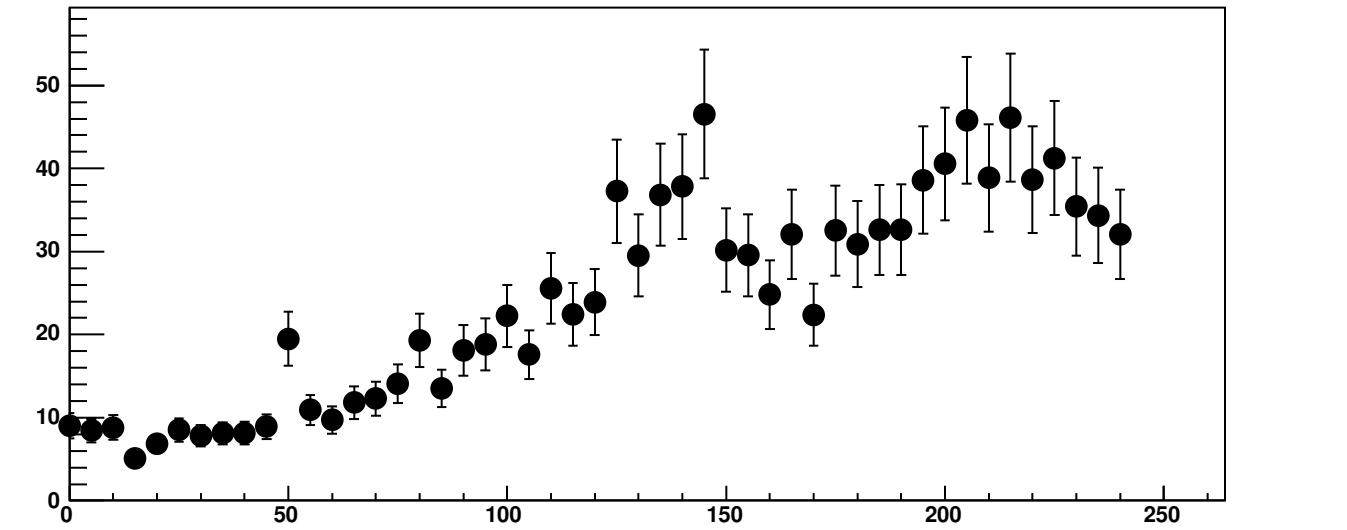
Chip 2, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold



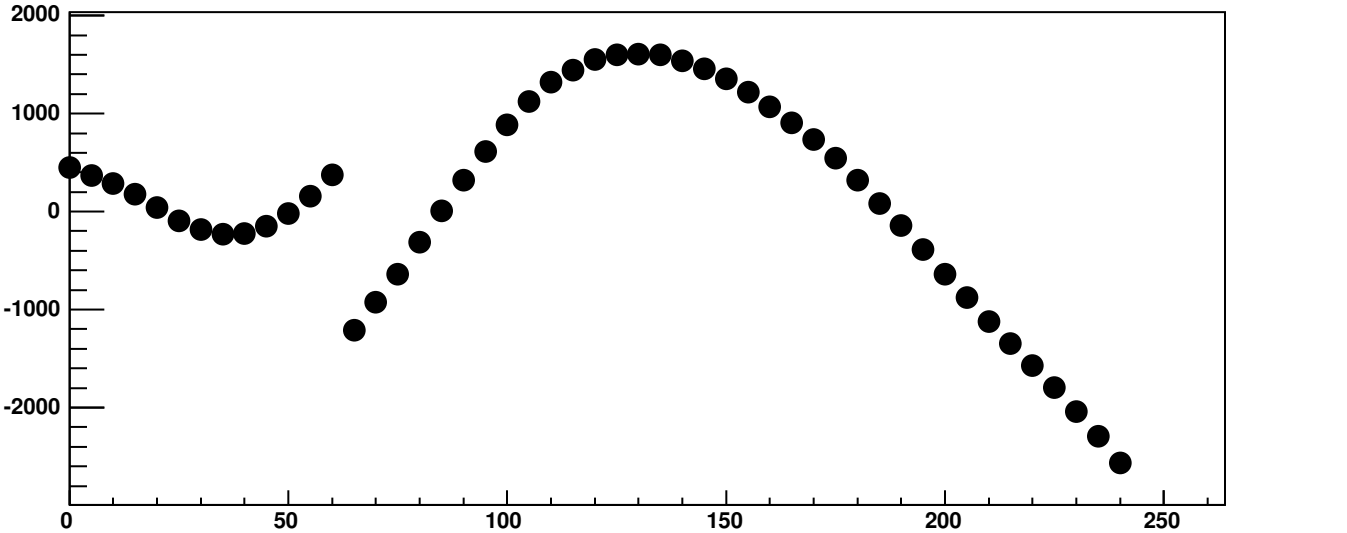
Chip 2, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

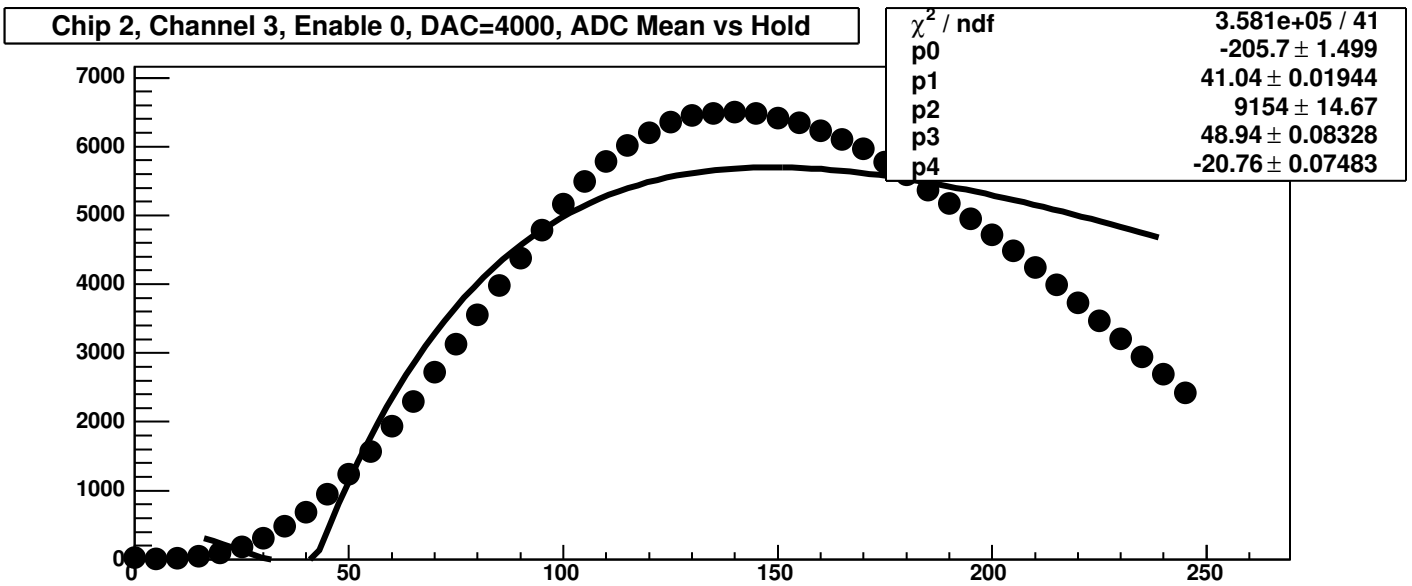


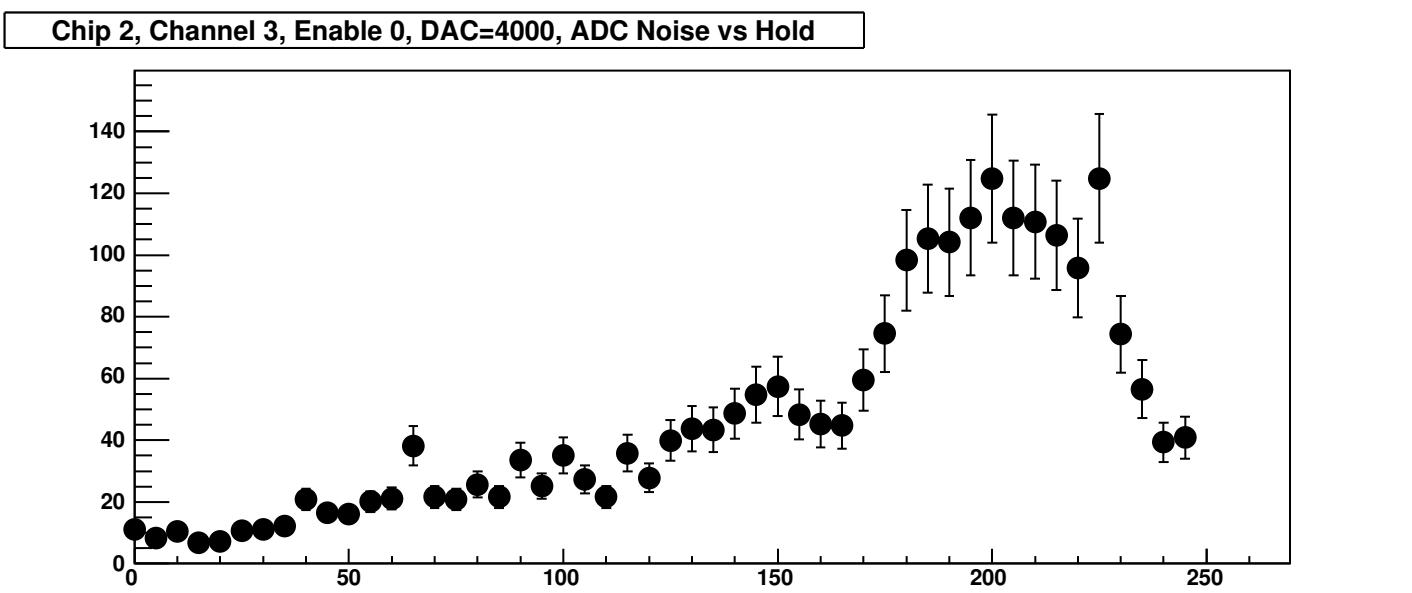
Chip 2, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold

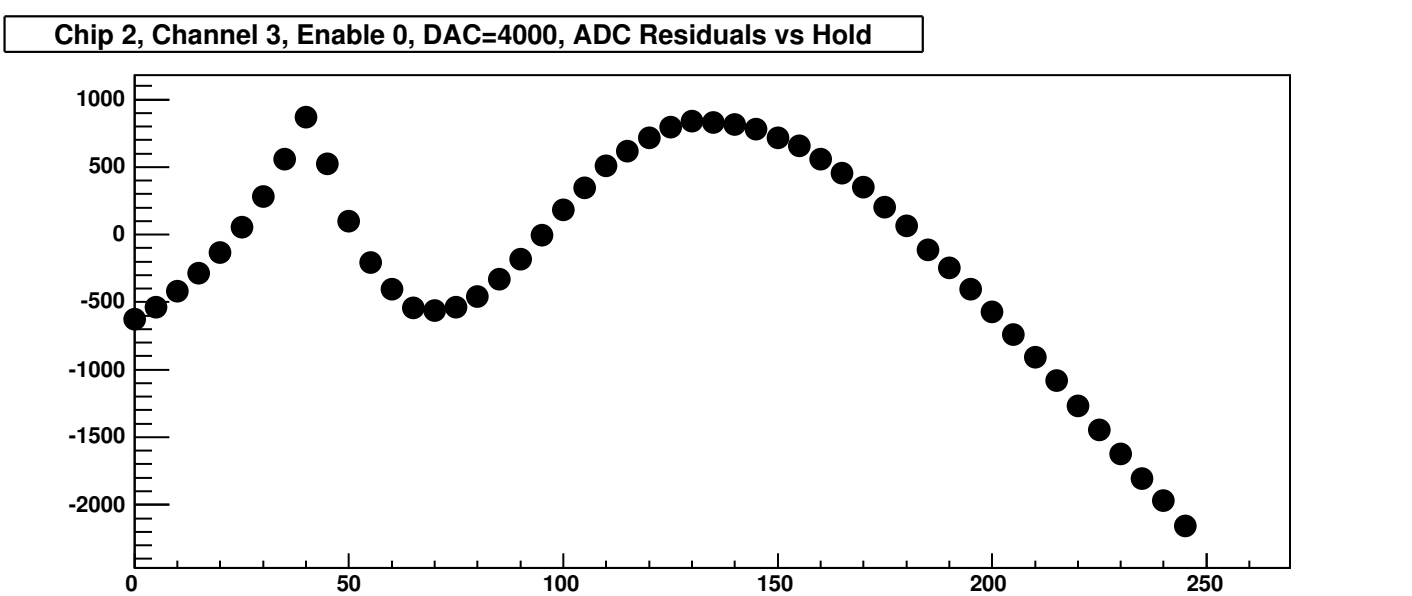


Chip 2, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold

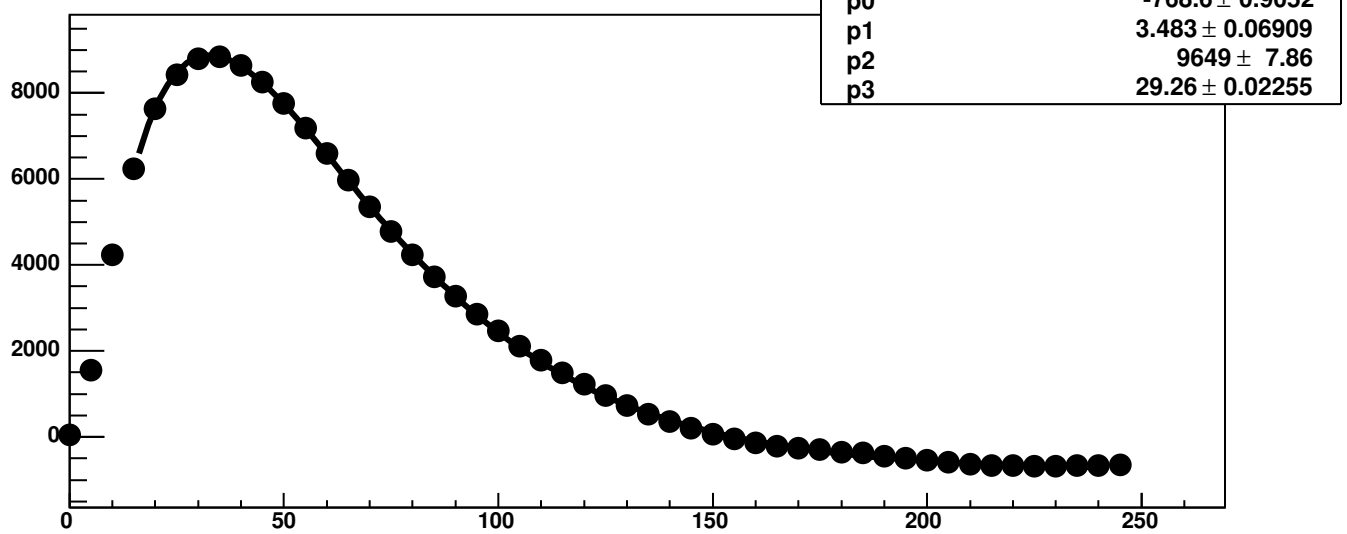



Chip 2, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.581e+05 / 41
p0 -205.7 ± 1.499
p1 41.04 ± 0.01944
p2 9154 ± 14.67
p3 48.94 ± 0.08328
p4 -20.76 ± 0.07483
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

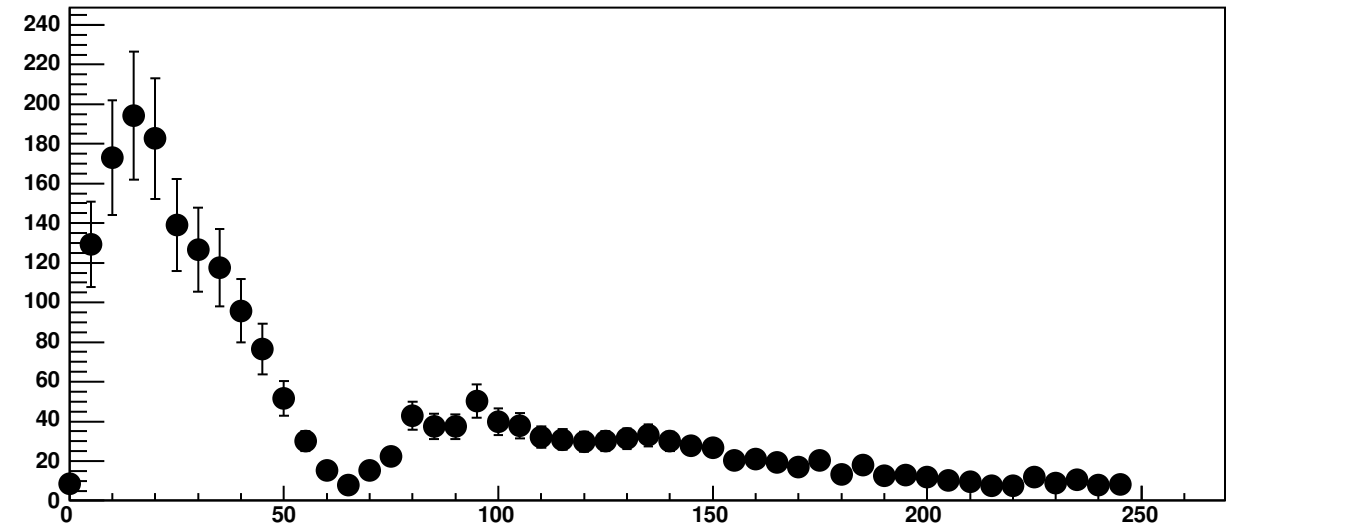

Chip 2, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 2, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

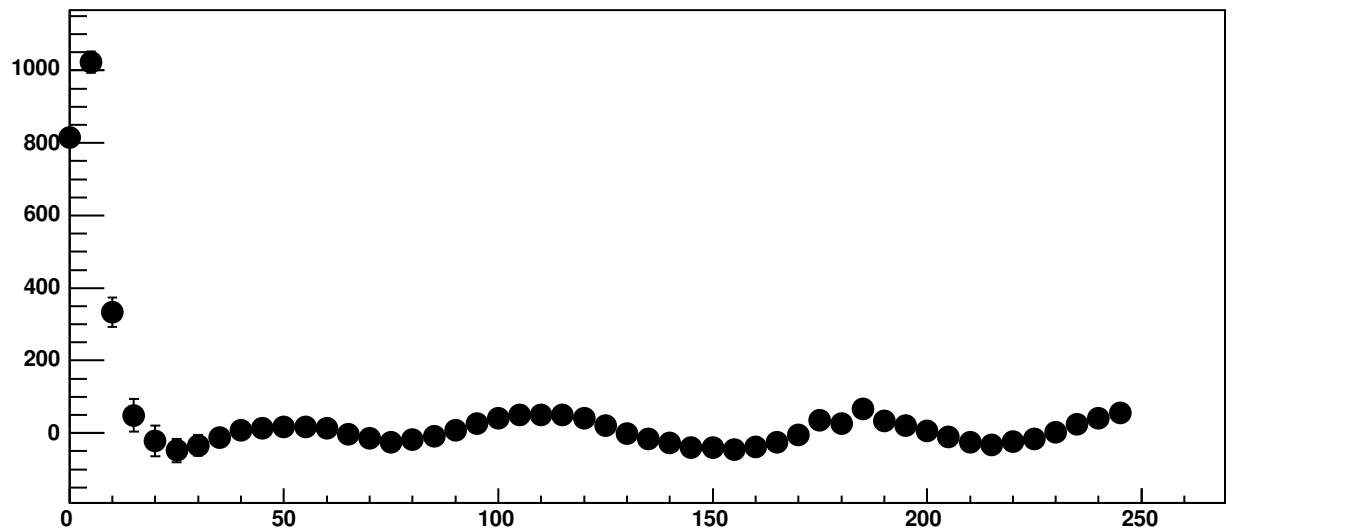
Chip 2, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold



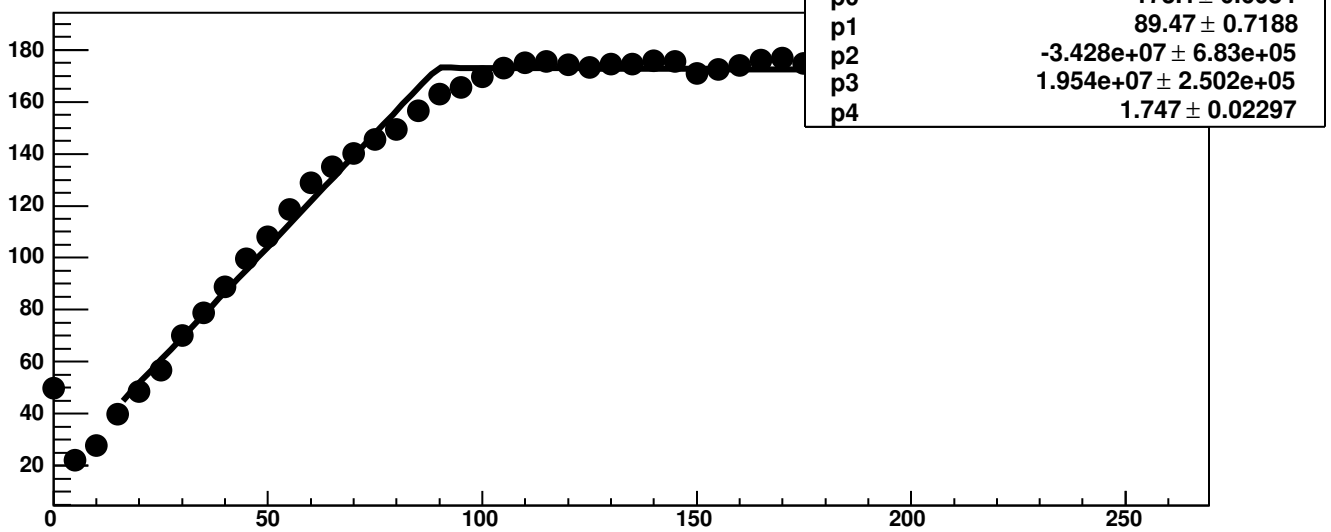
Chip 2, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold



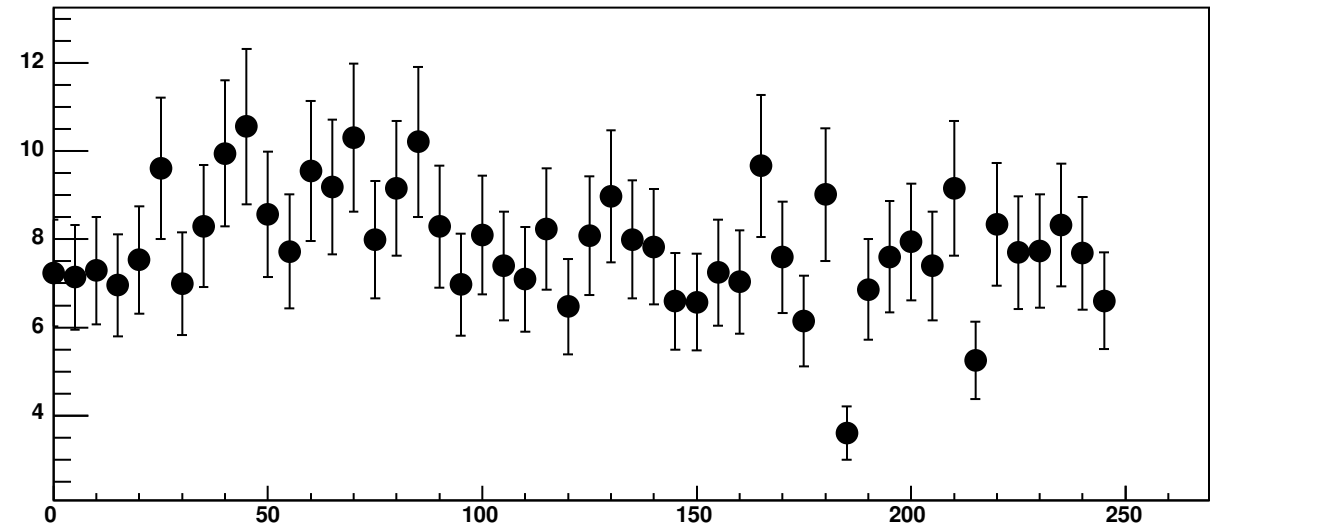
Chip 2, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold



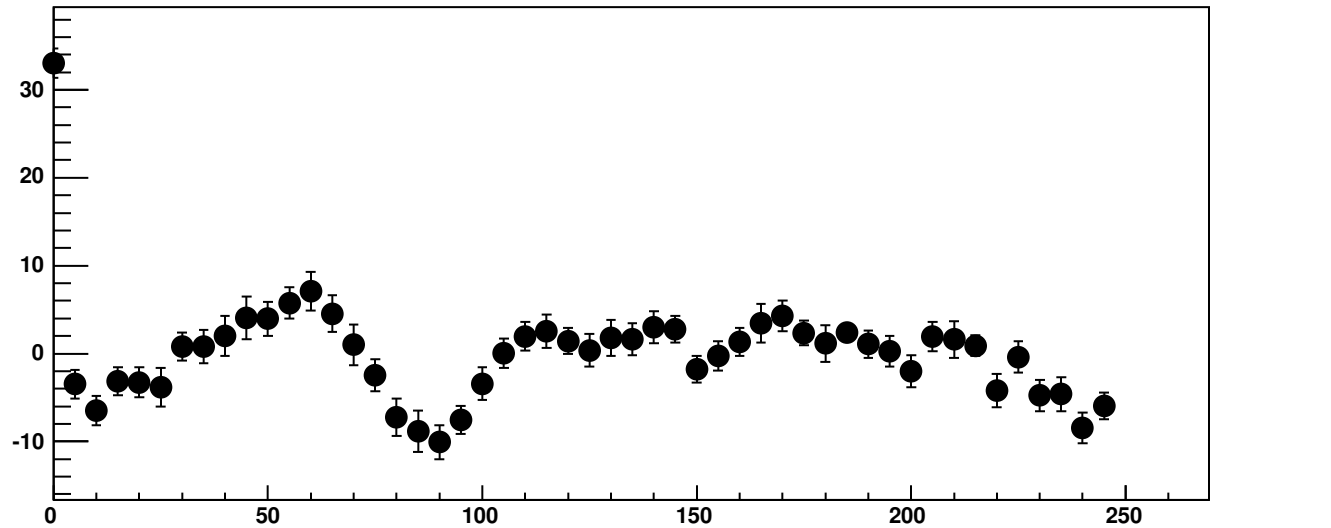
Chip 2, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold



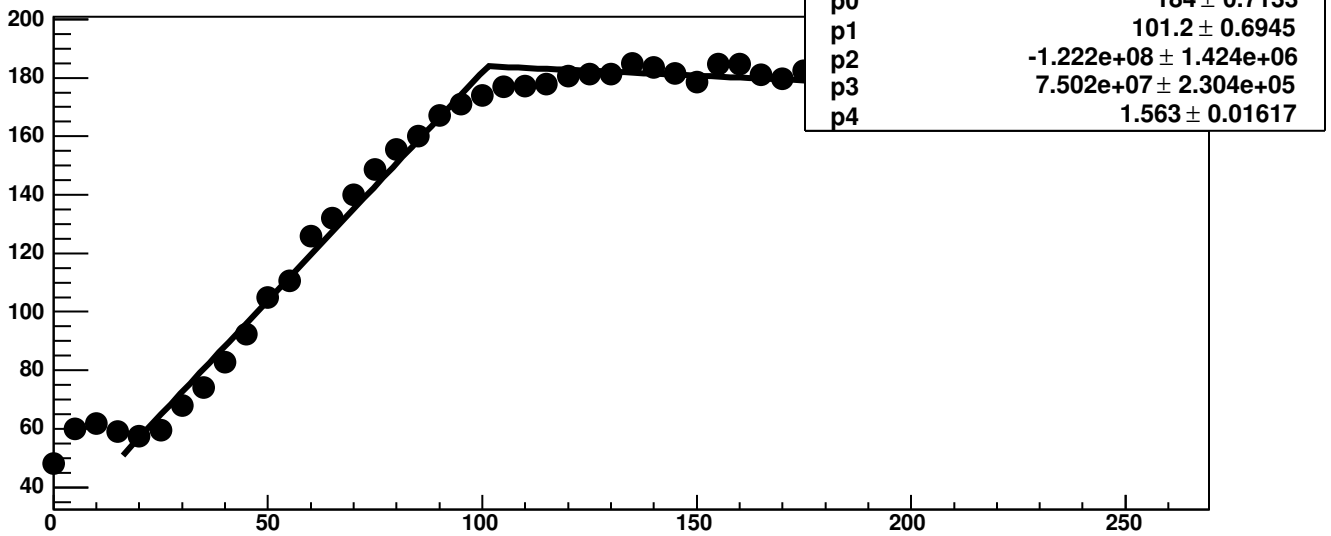
Chip 2, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



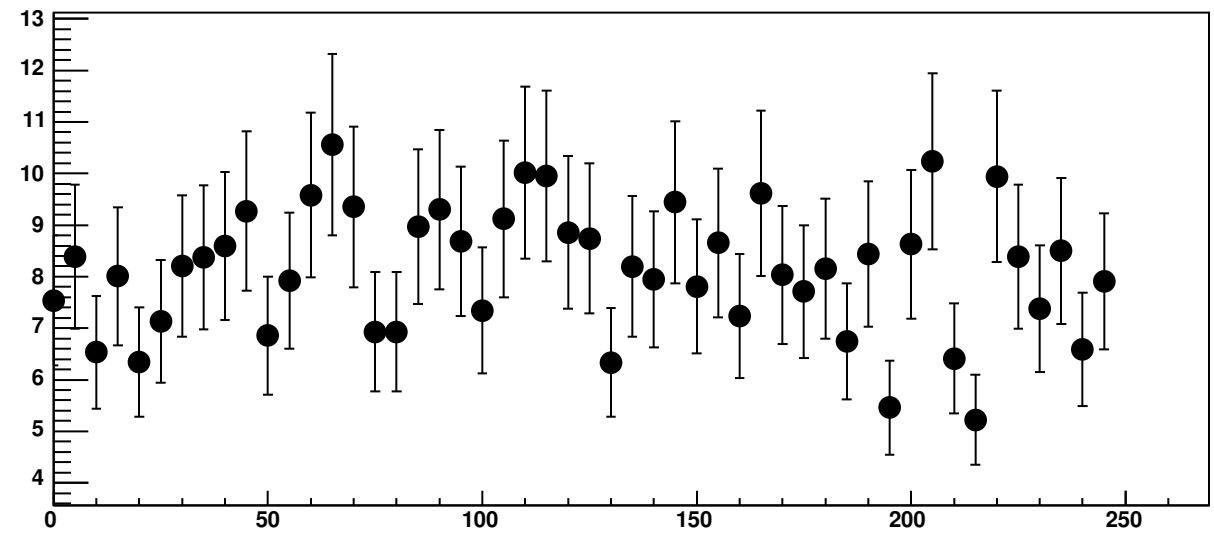
Chip 2, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold



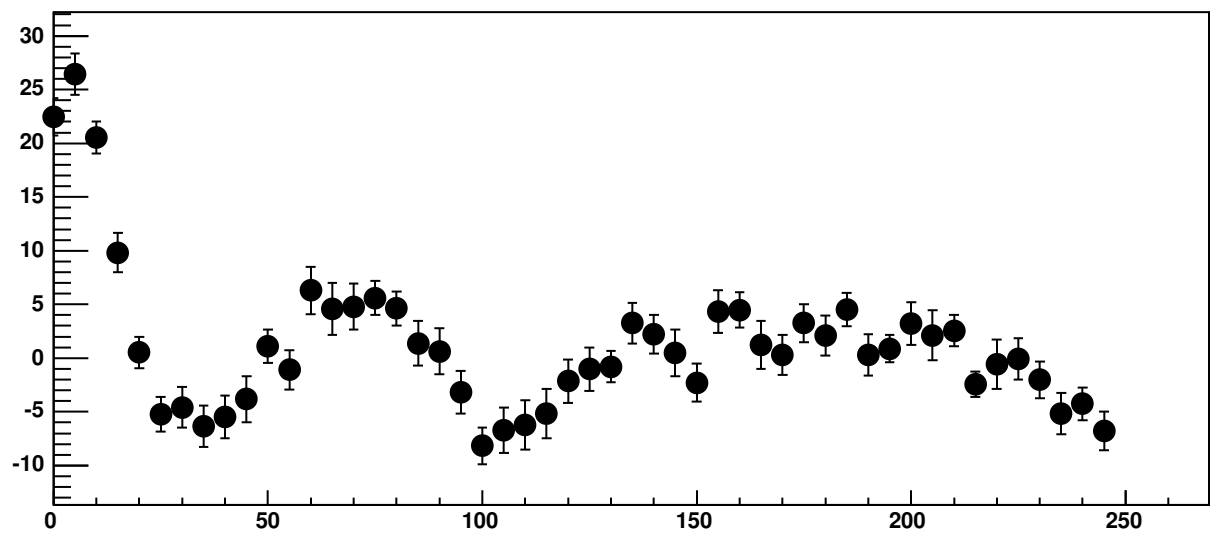
Chip 2, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold



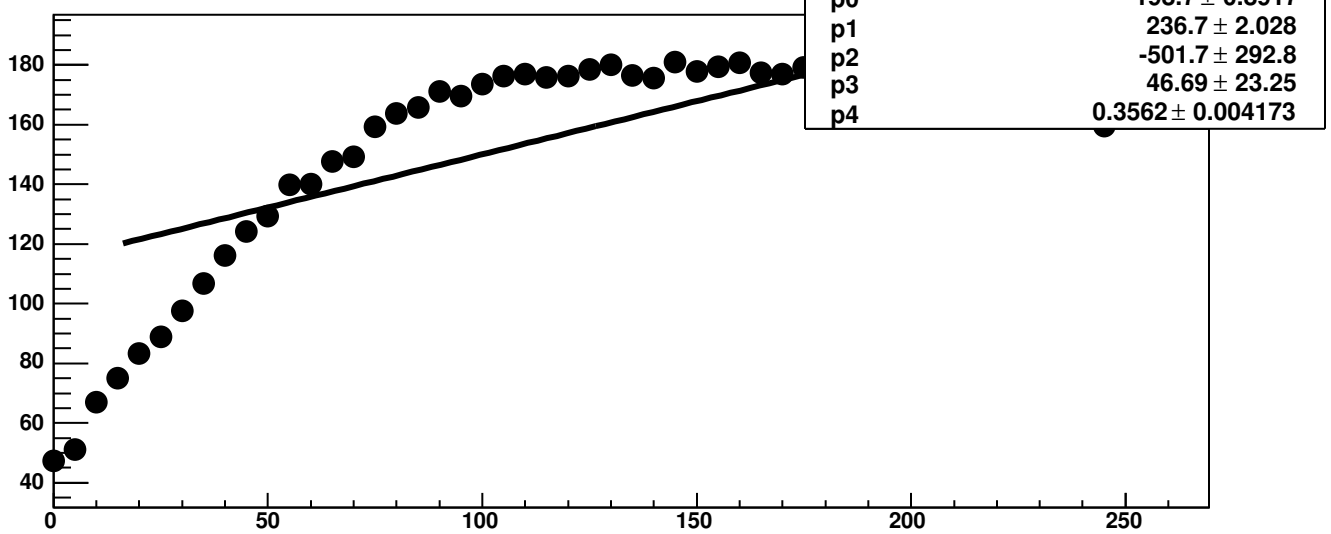
Chip 2, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



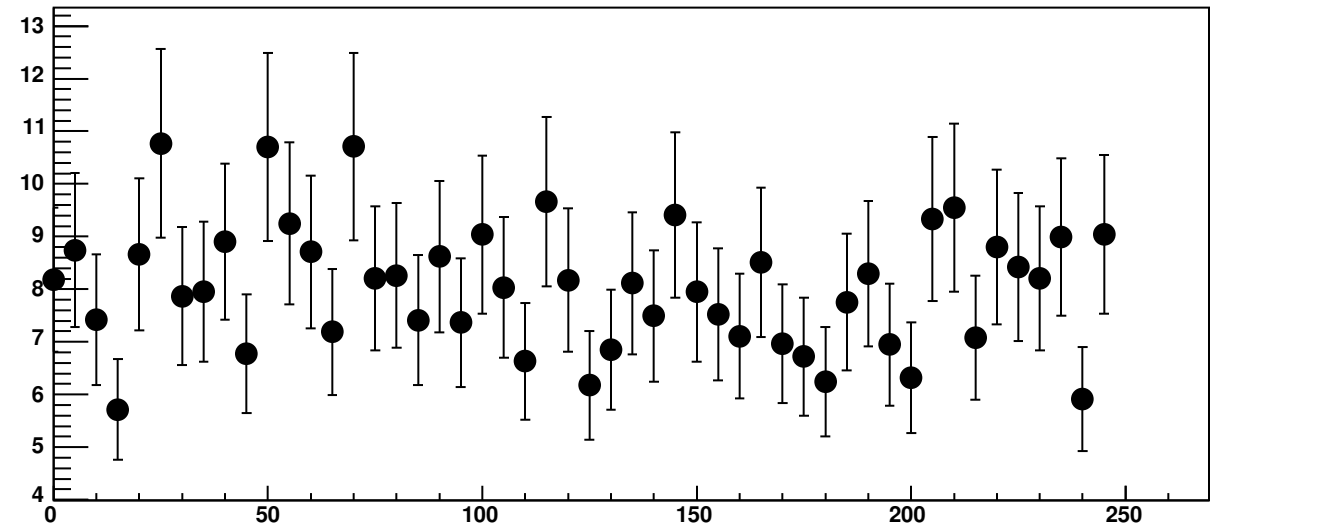
Chip 2, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold



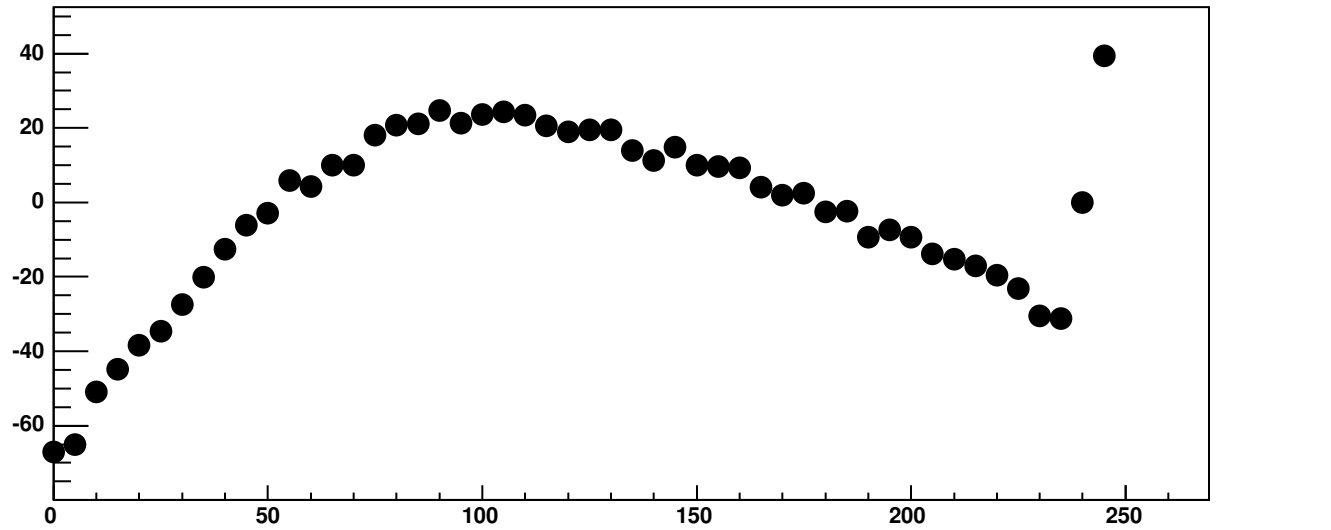
Chip 2, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold



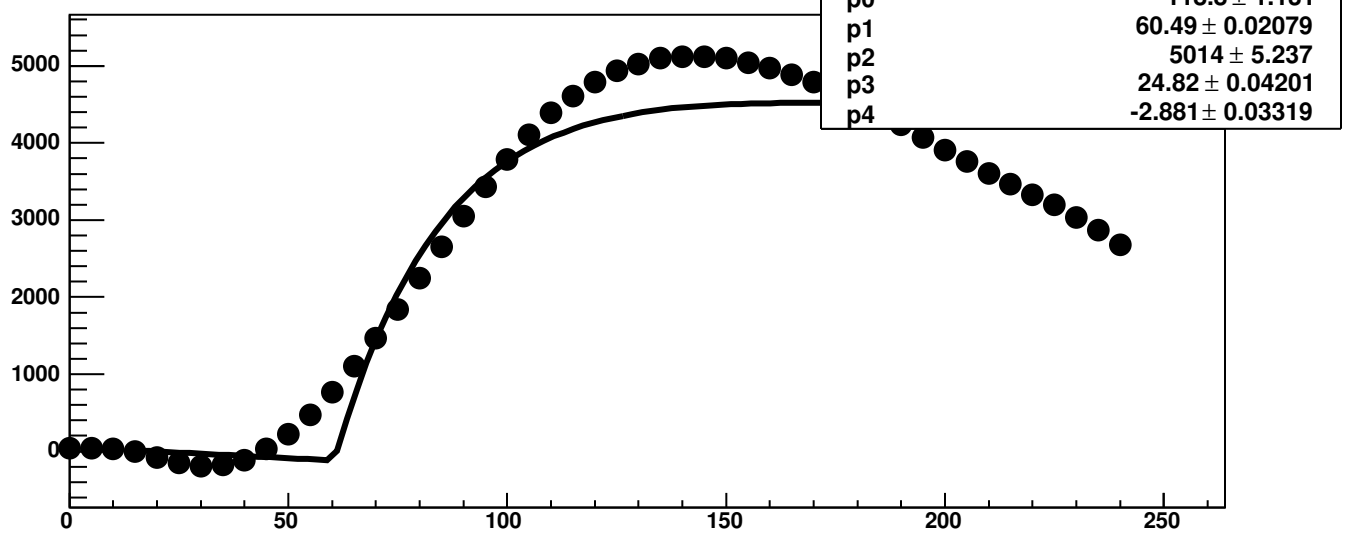
Chip 2, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold



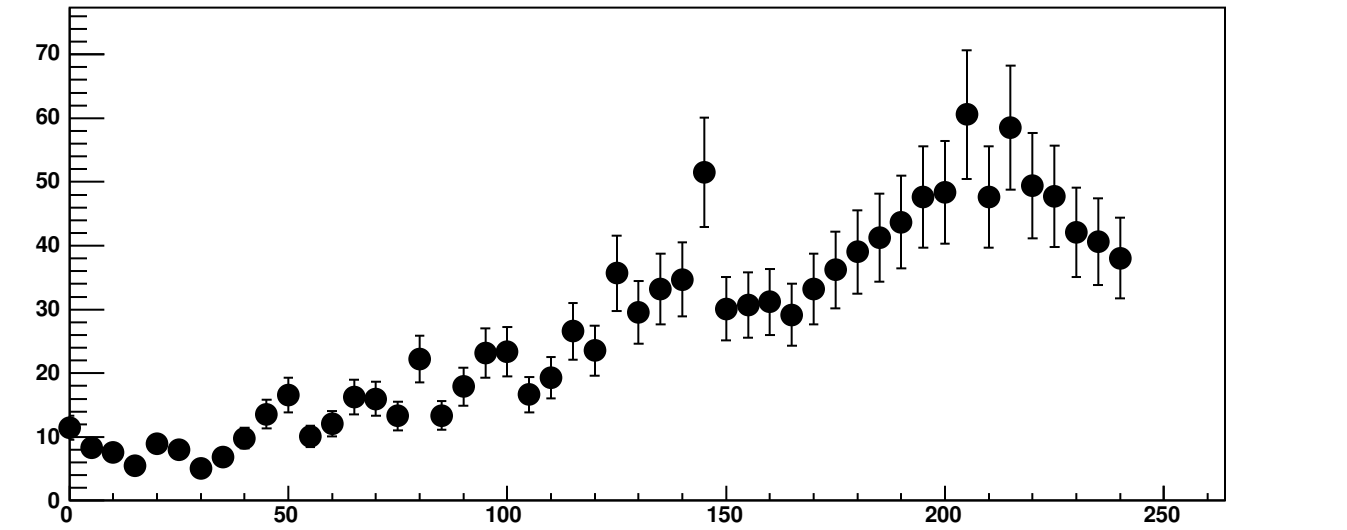
Chip 2, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold



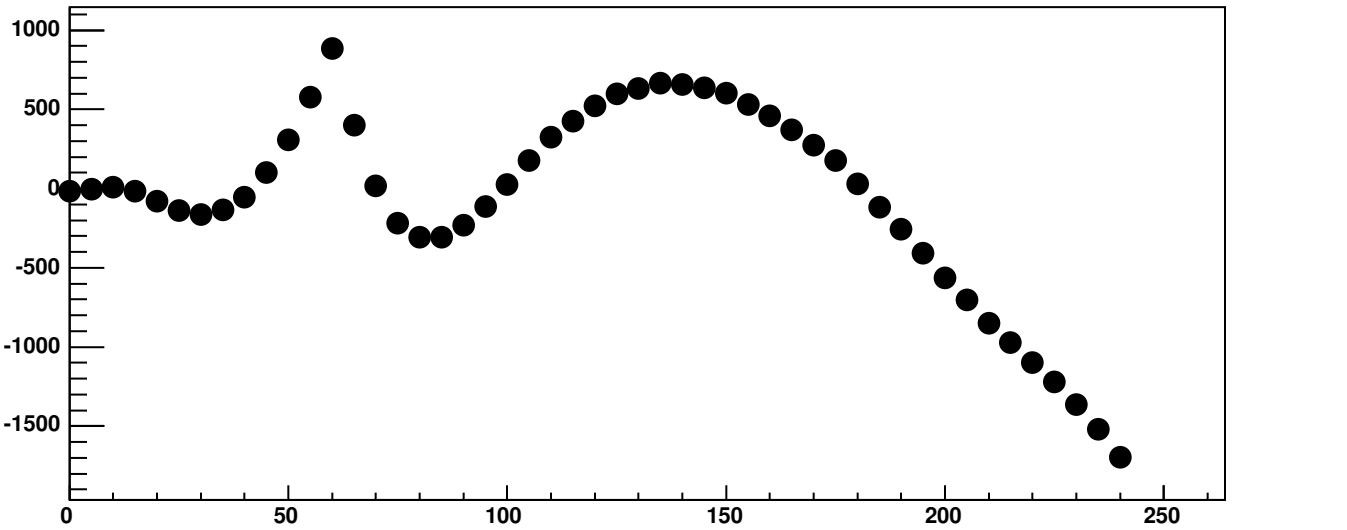
Chip 2, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold

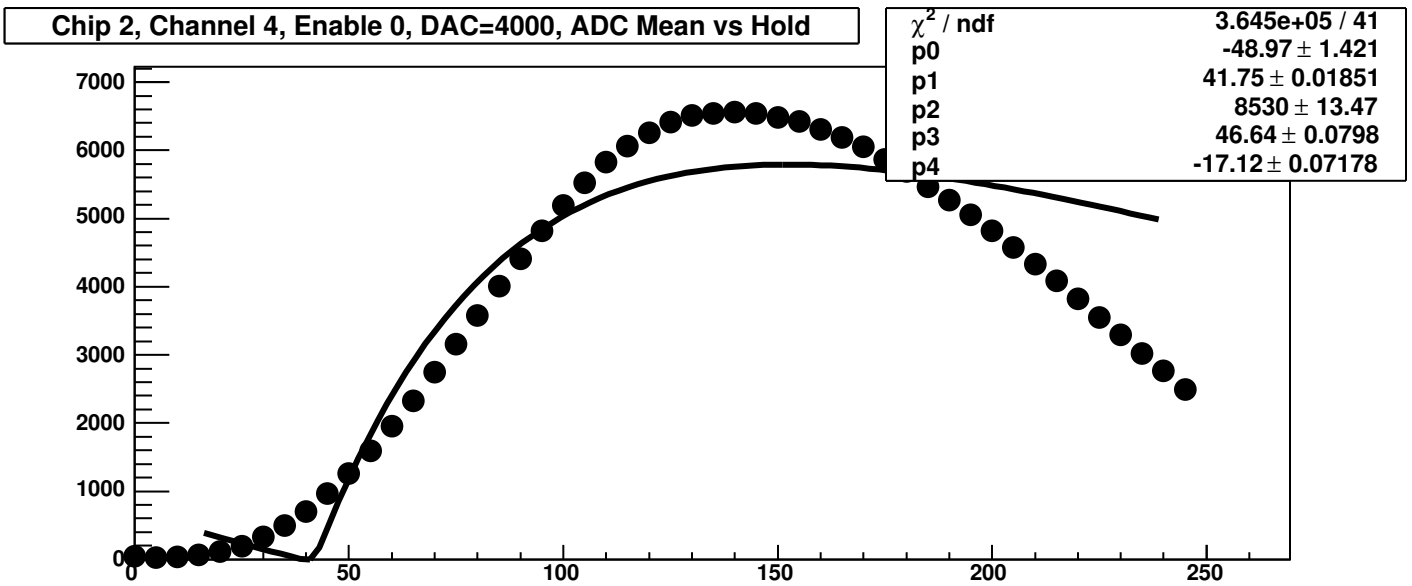


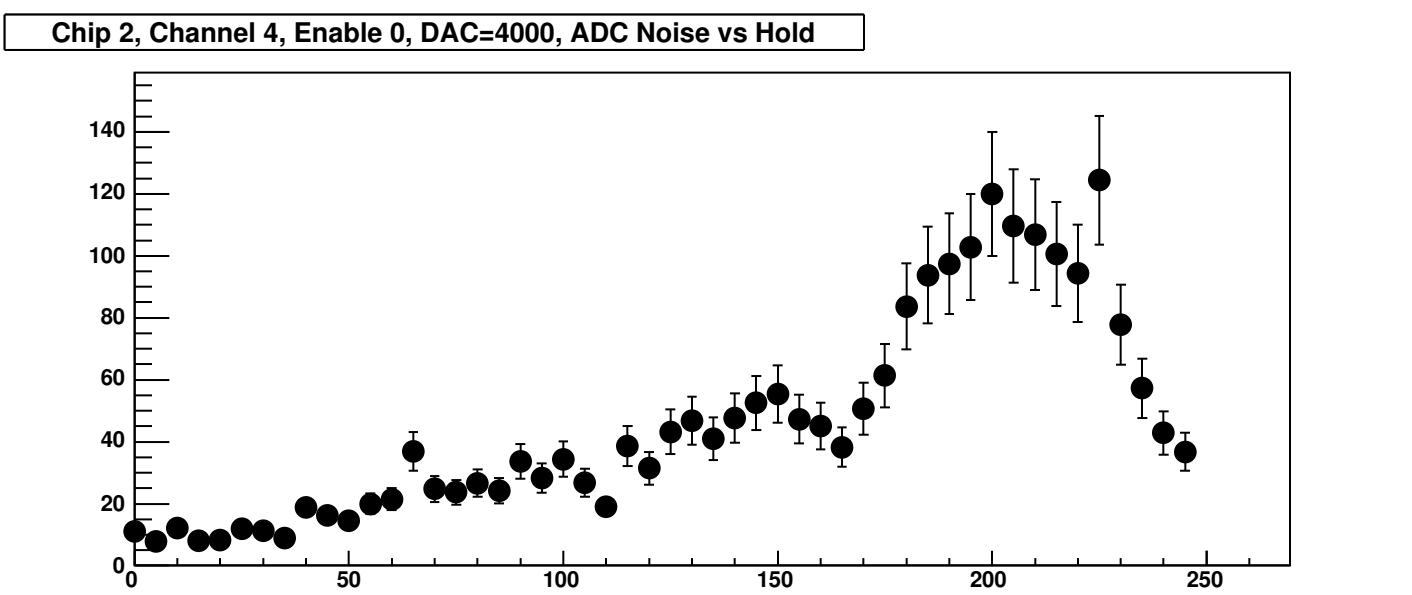
Chip 2, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold

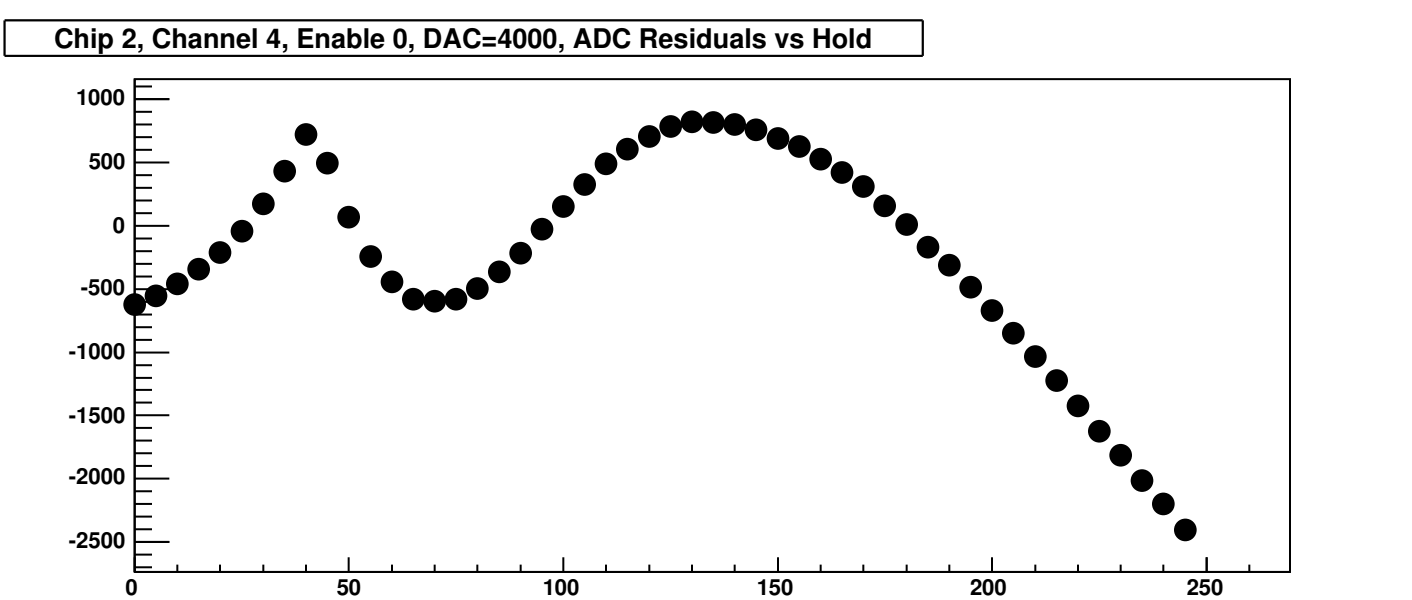


Chip 2, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold

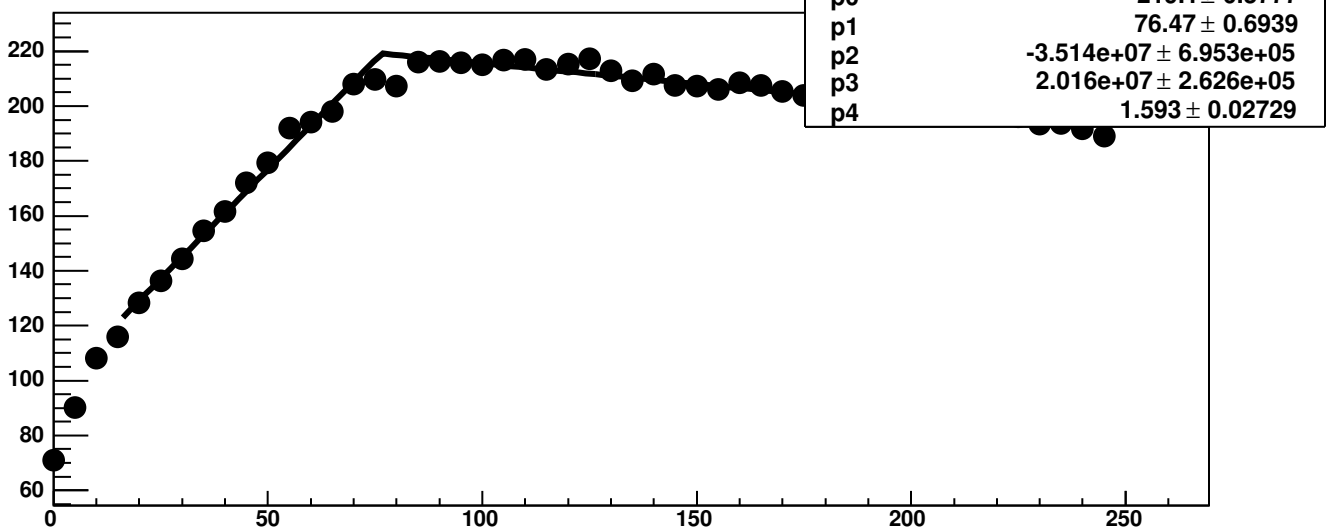



Chip 2, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.645e+05 / 41
p0 -48.97 ± 1.421
p1 41.75 ± 0.01851
p2 8530 ± 13.47
p3 46.64 ± 0.0798
p4 -17.12 ± 0.07178
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

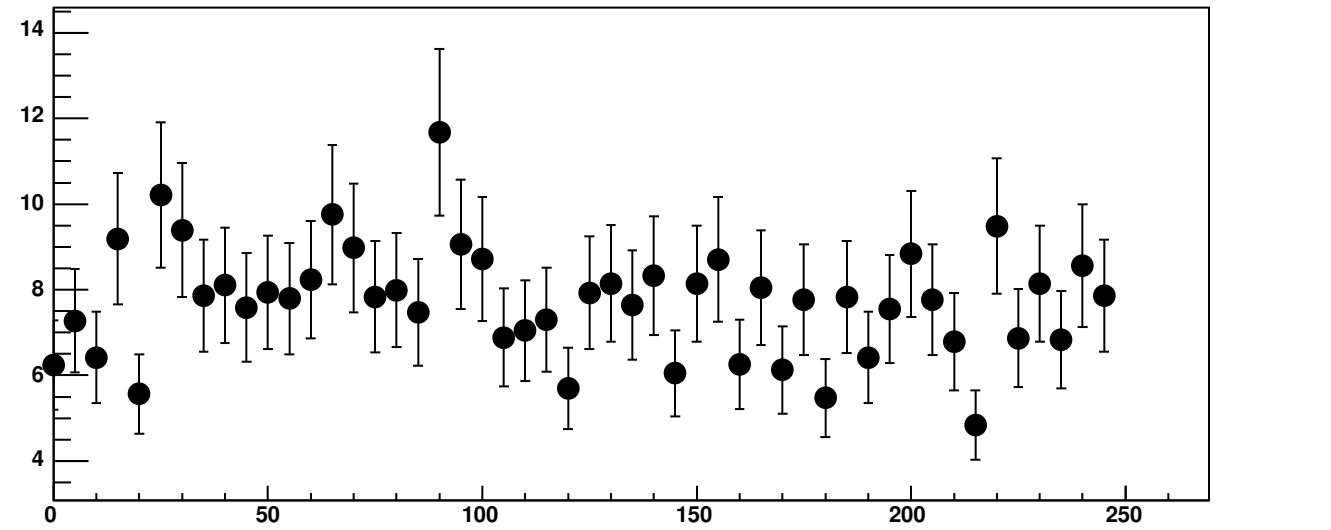

Chip 2, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 2, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
0
50
100
150
200
250

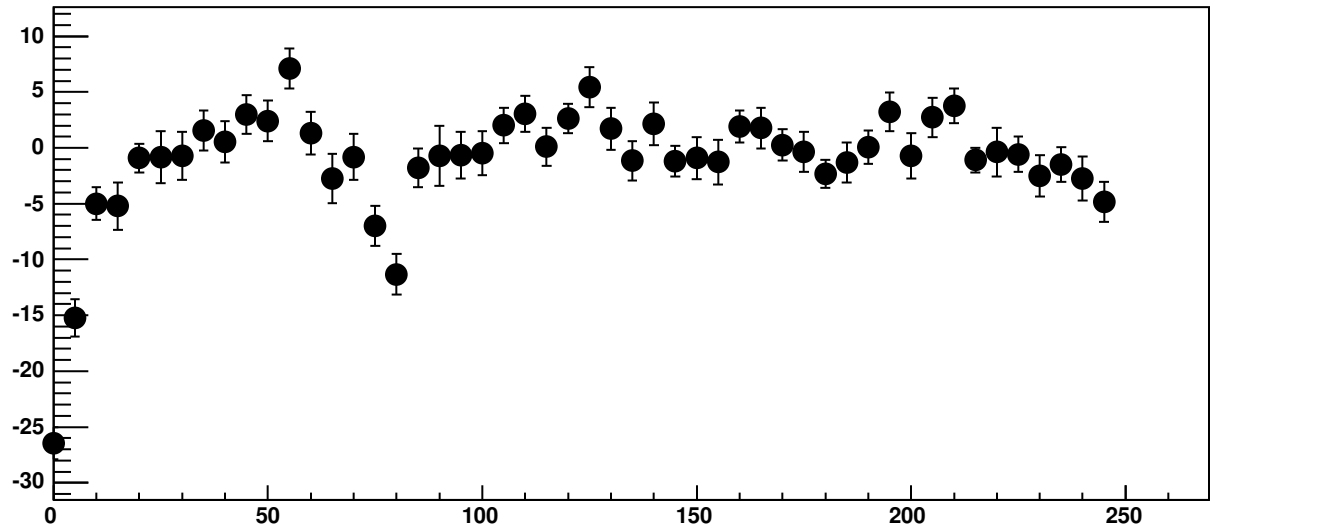
Chip 2, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold



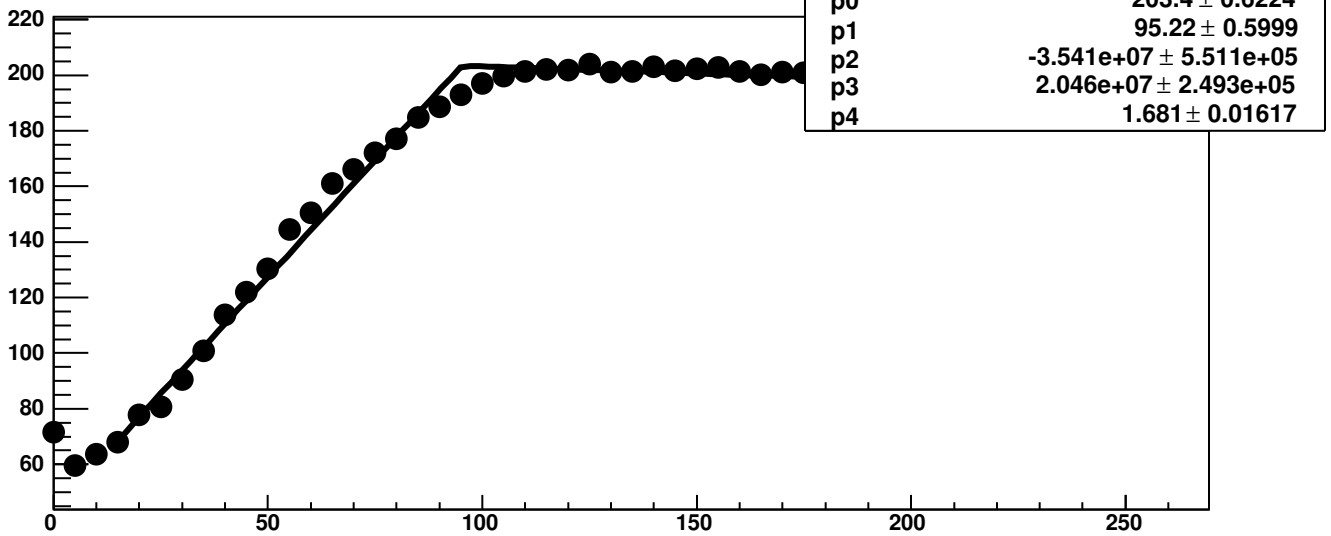
Chip 2, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold



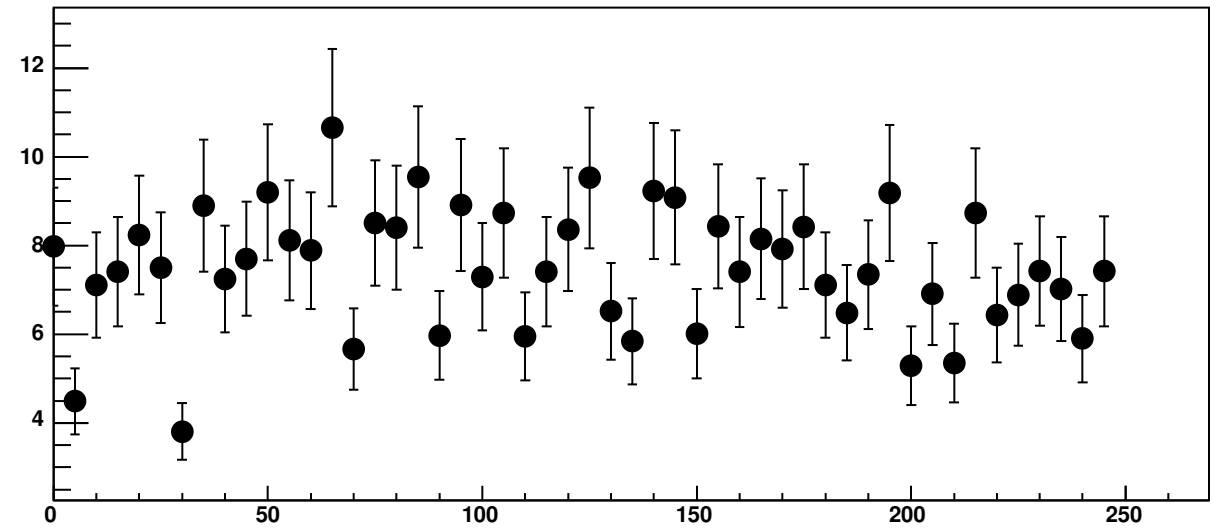
Chip 2, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold



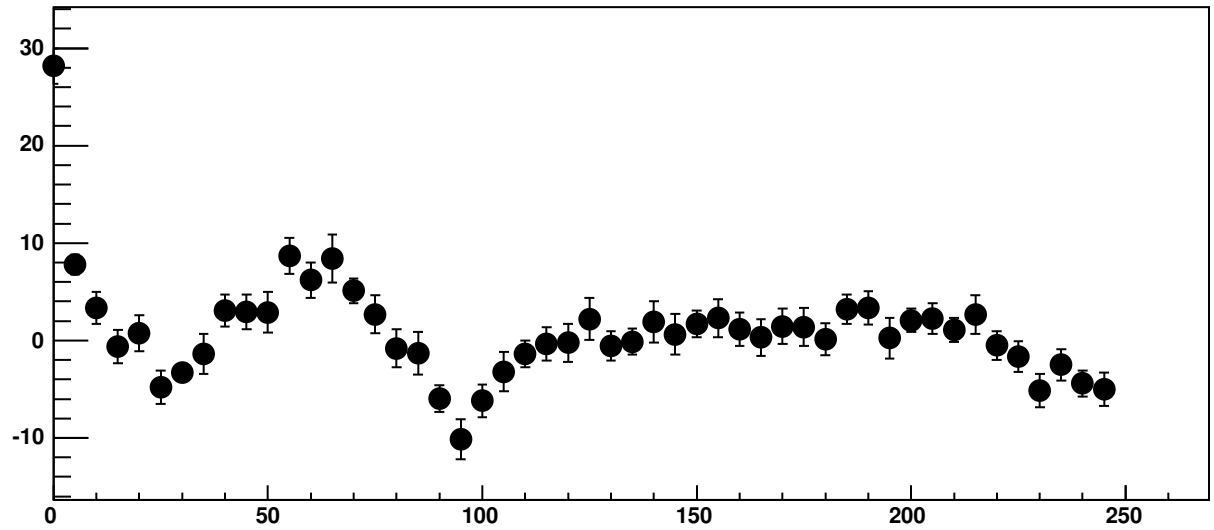
Chip 2, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold



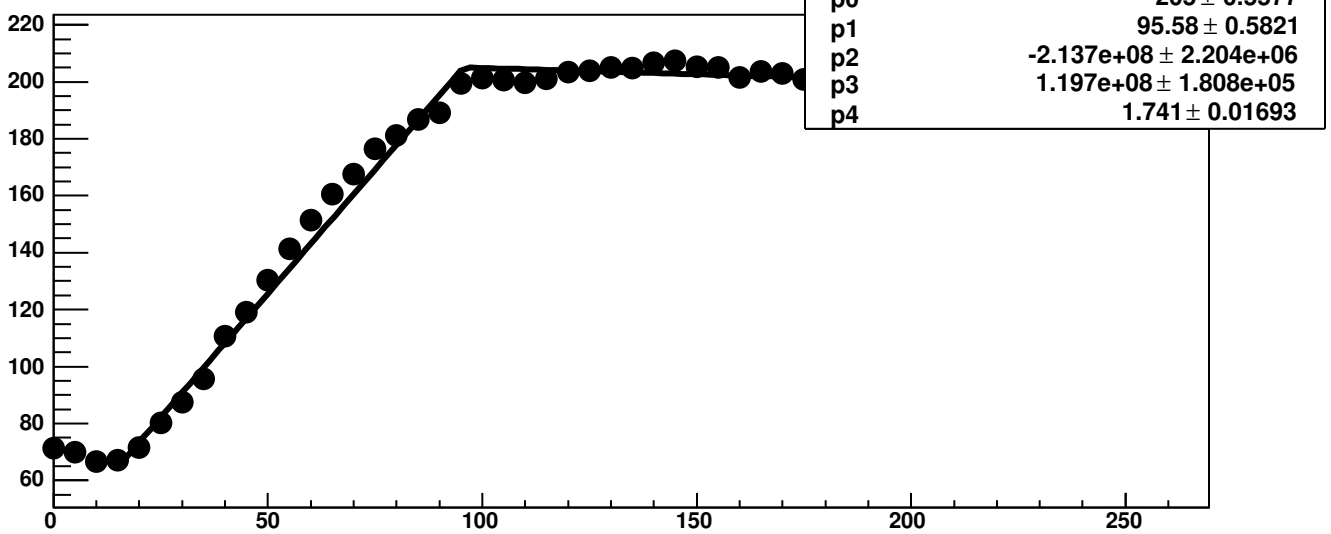
Chip 2, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



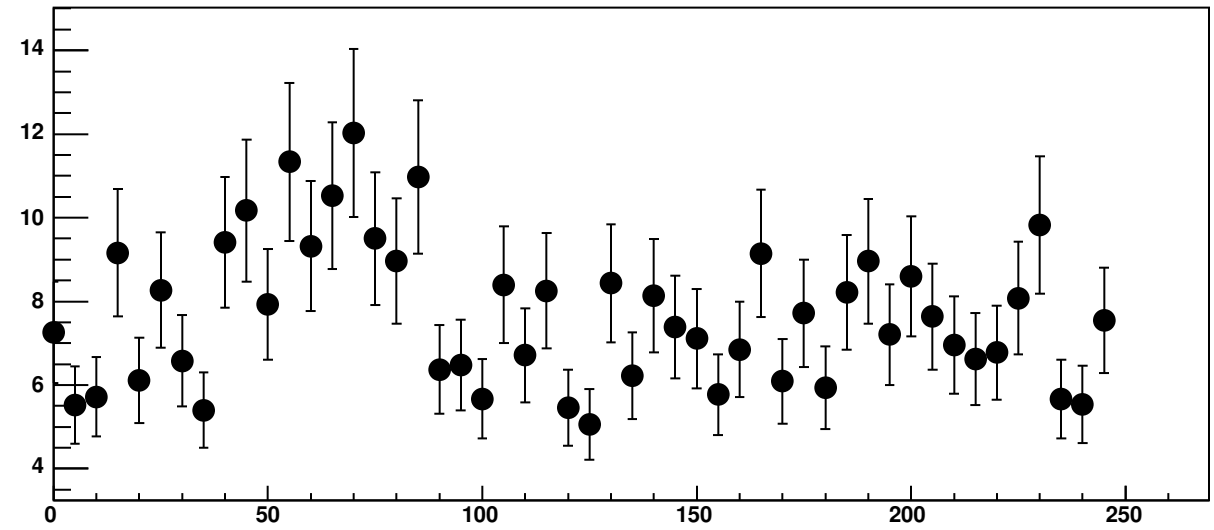
Chip 2, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold



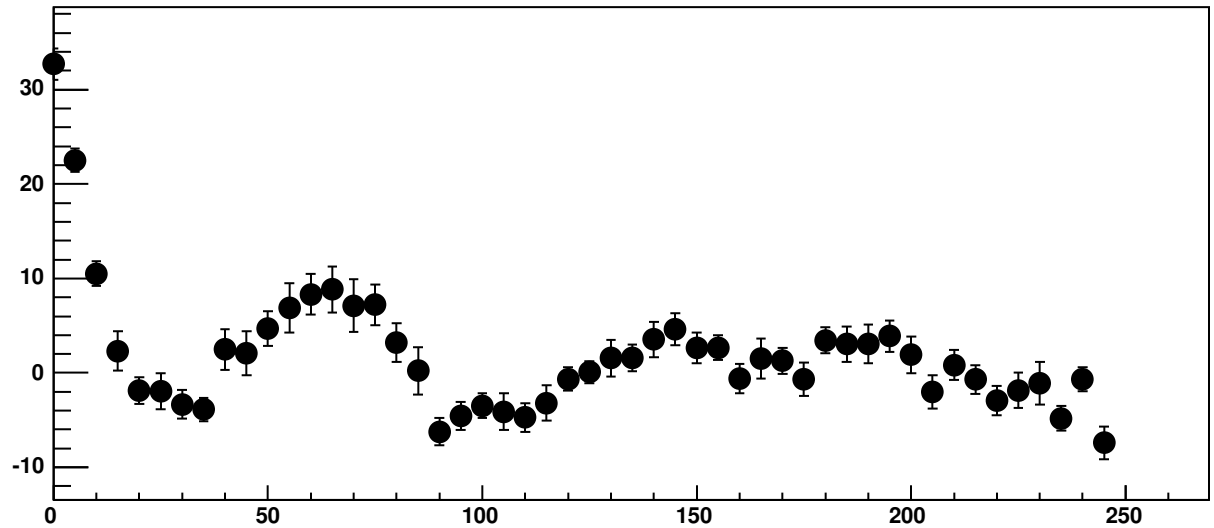
Chip 2, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold



Chip 2, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold

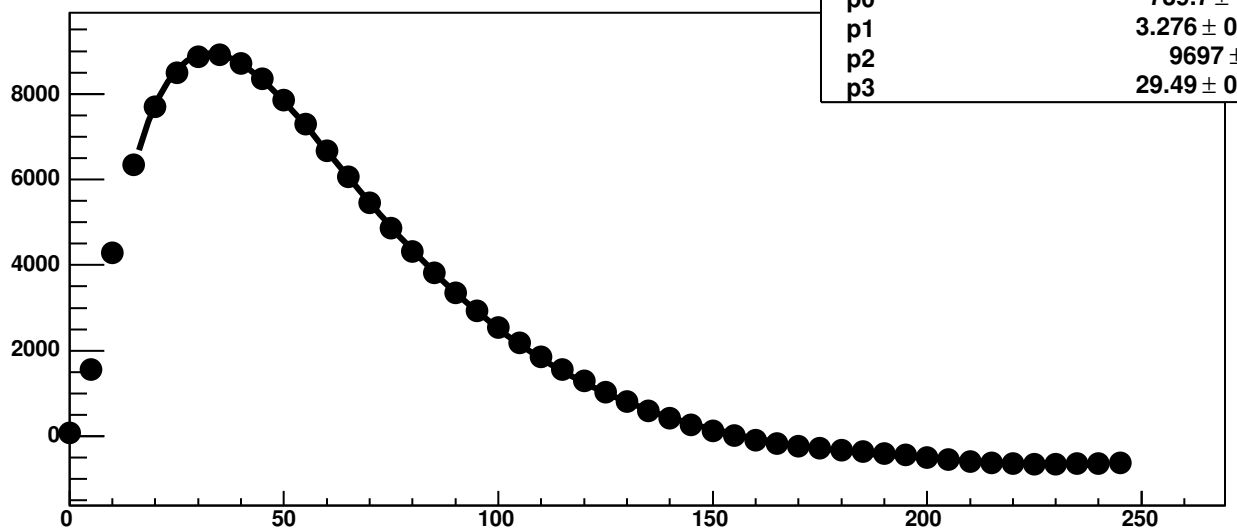


Chip 2, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

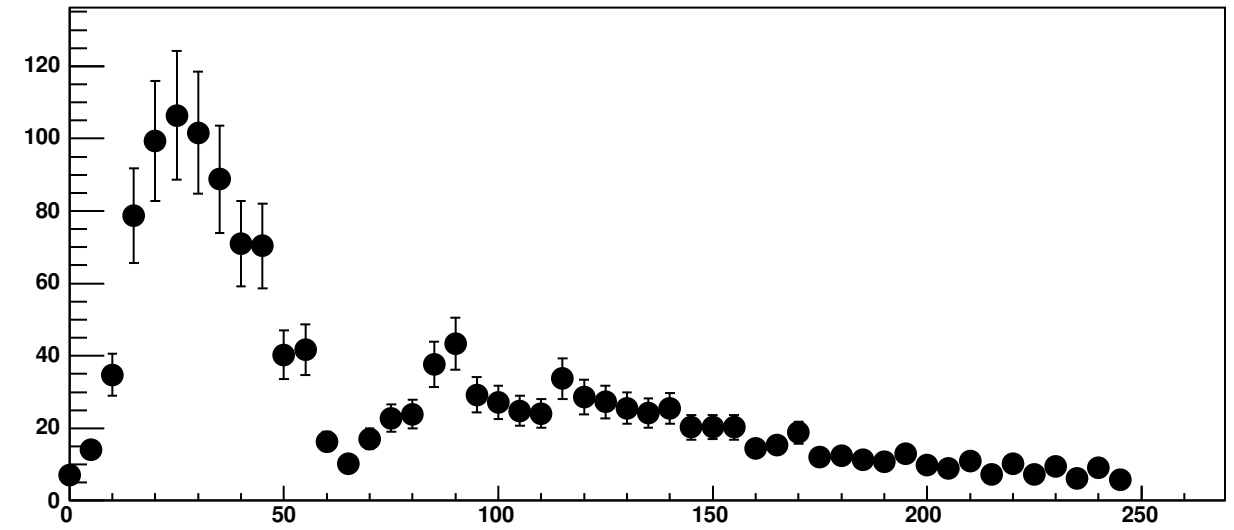


Chip 2, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold

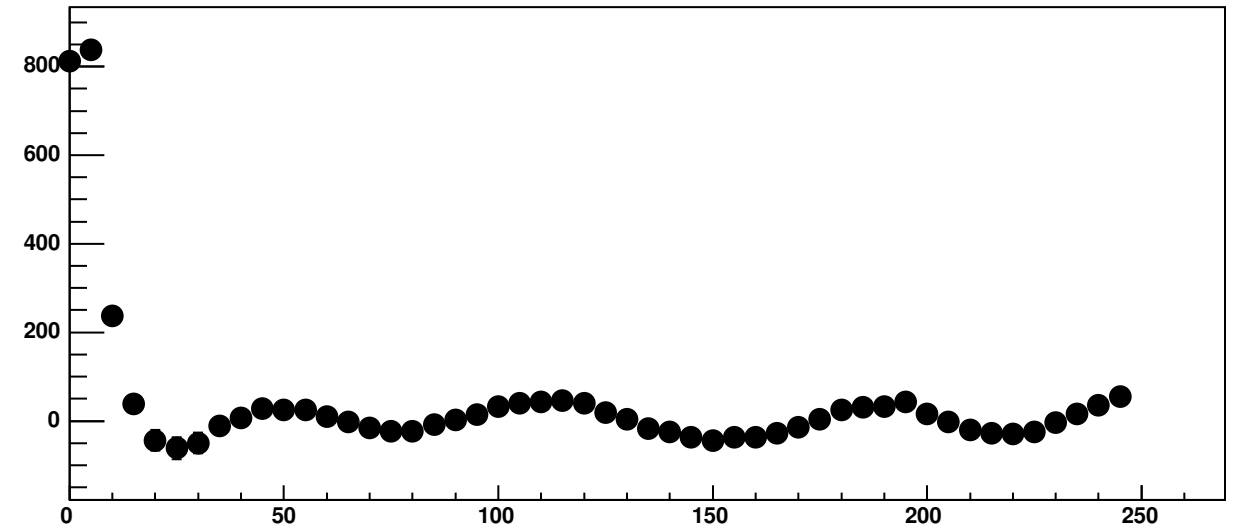
χ^2 / ndf	2520 / 42
p0	-739.7 ± 0.7974
p1	3.276 ± 0.03903
p2	9697 ± 4.856
p3	29.49 ± 0.01726



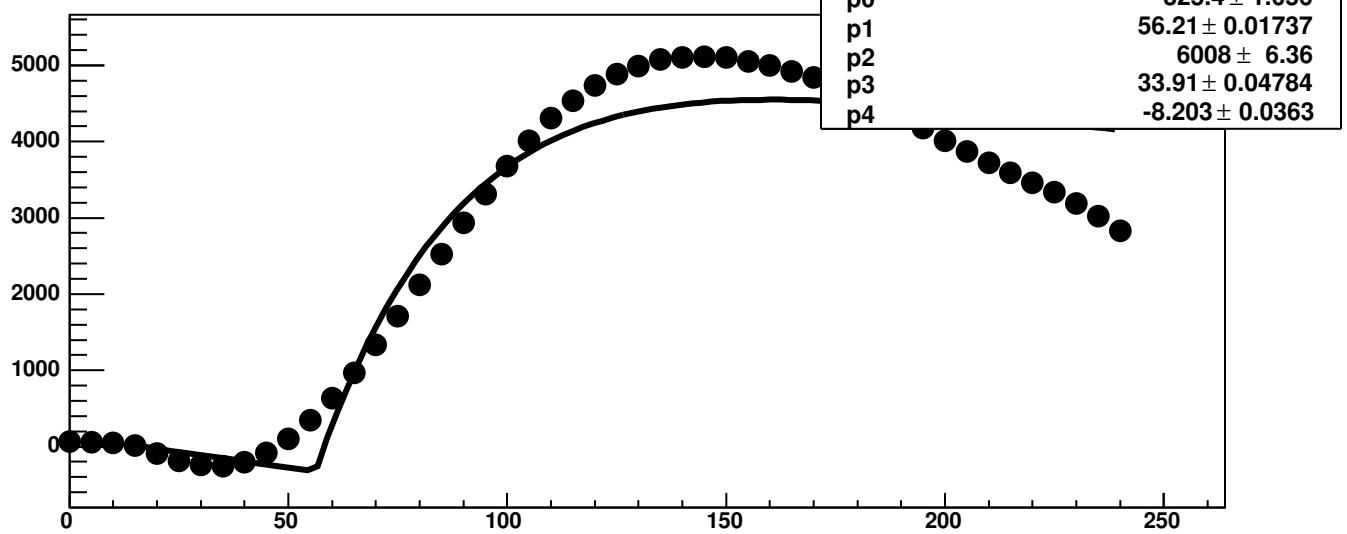
Chip 2, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold



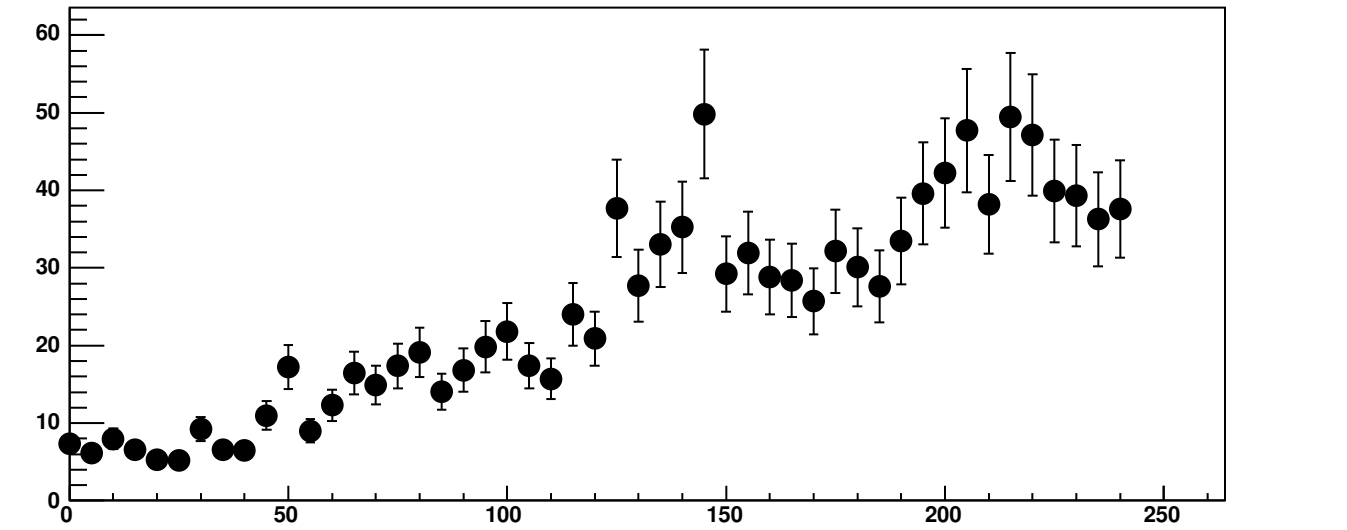
Chip 2, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold



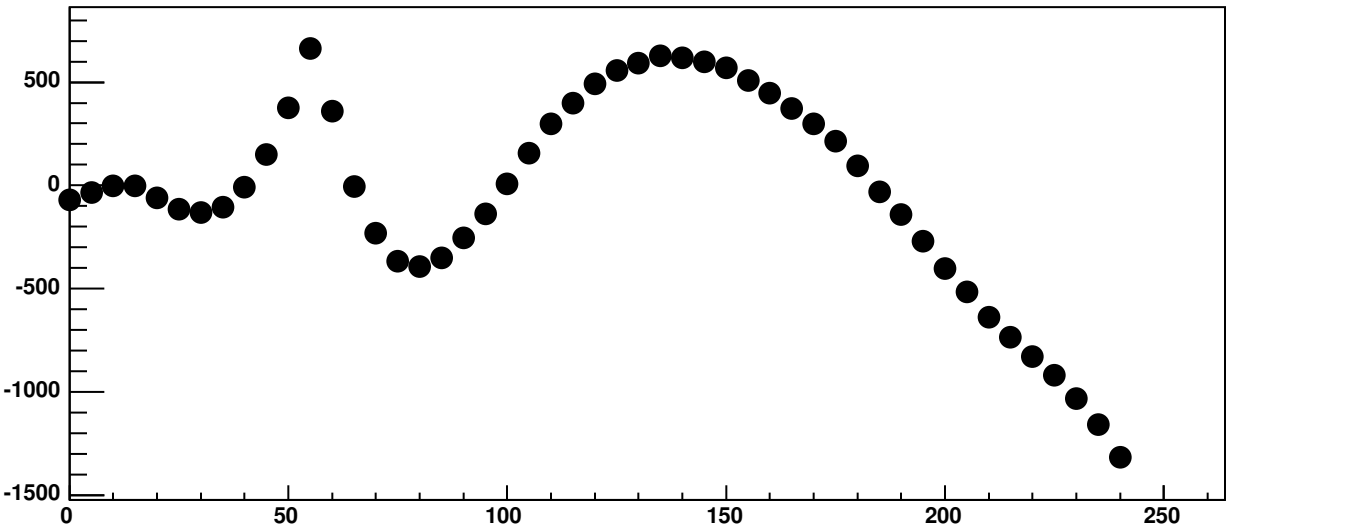
Chip 2, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold



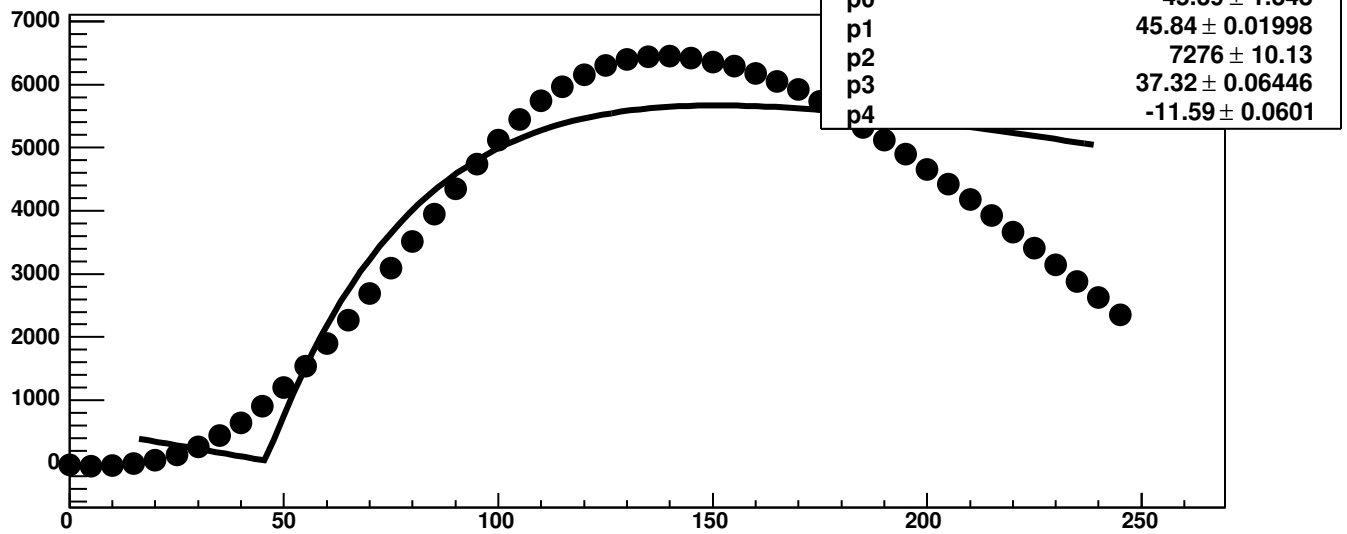
Chip 2, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold



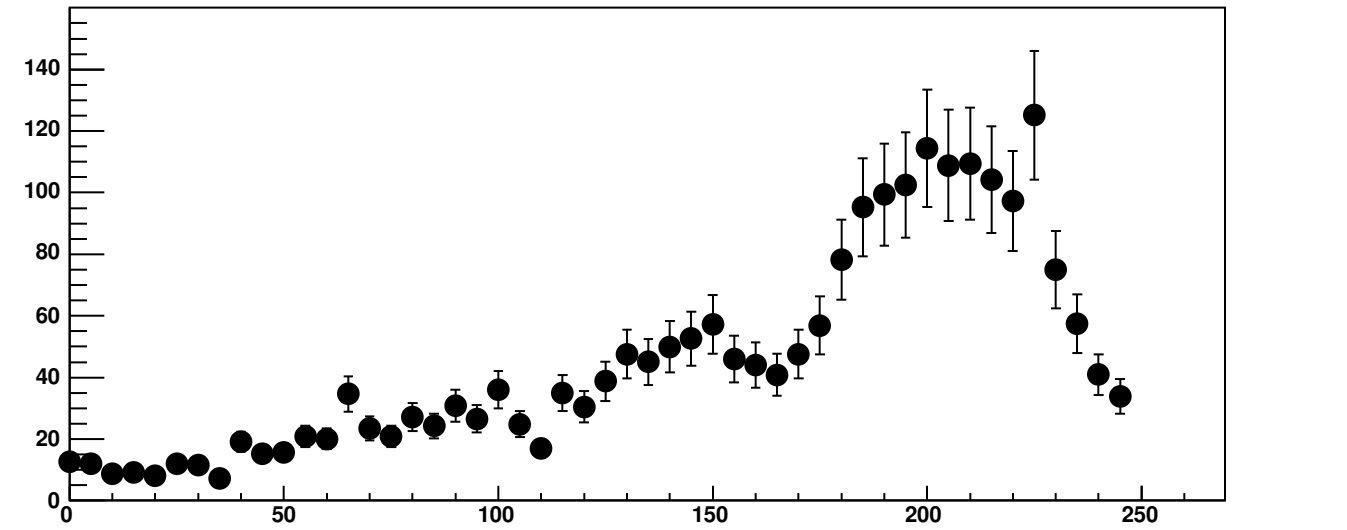
Chip 2, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold



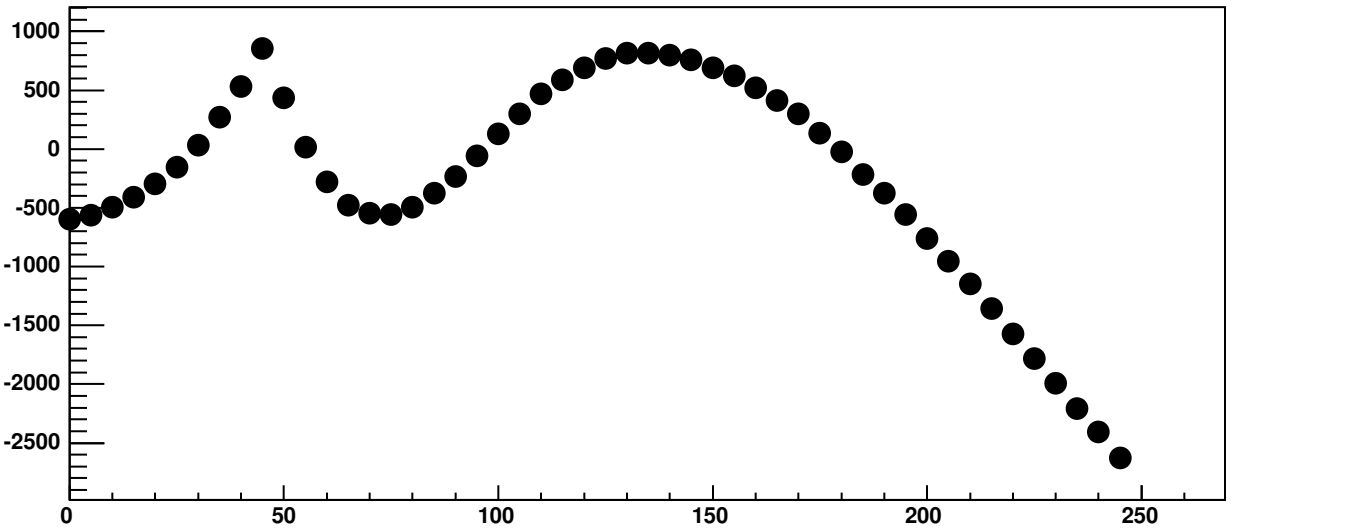
Chip 2, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold



Chip 2, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold

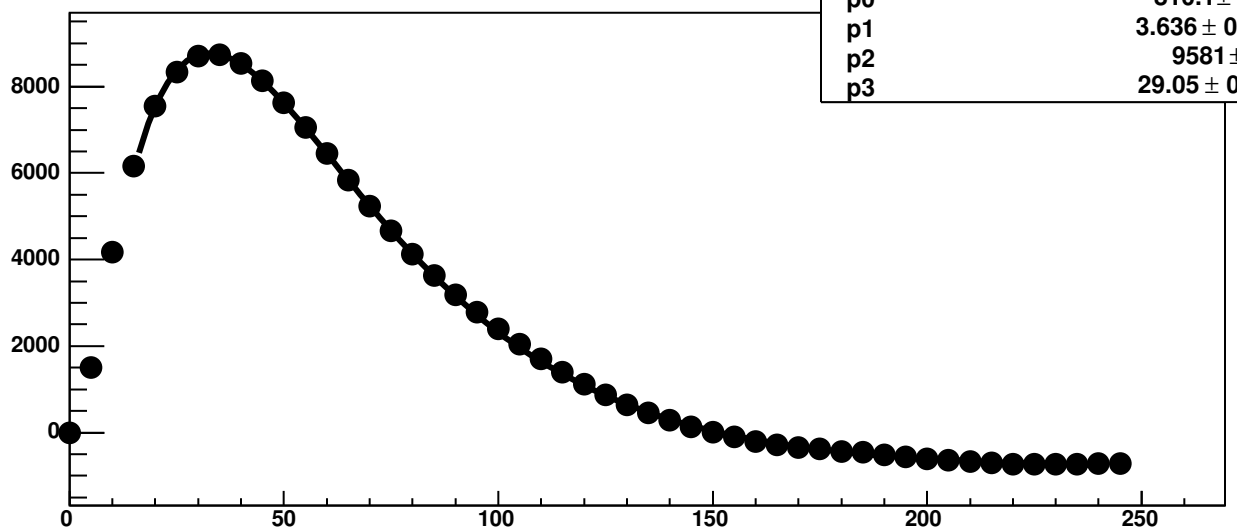


Chip 2, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold

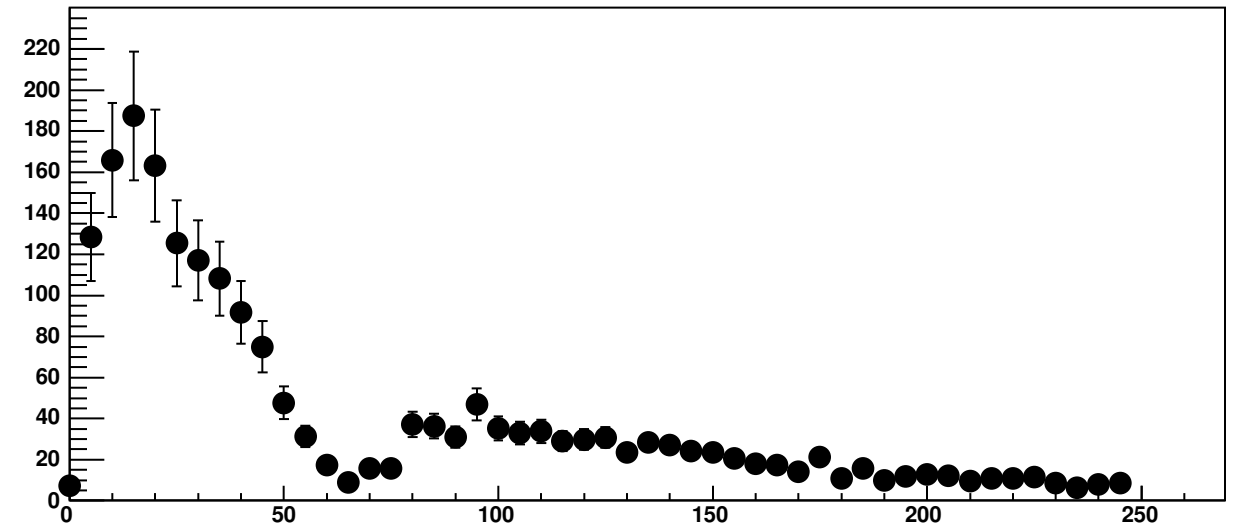


Chip 2, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold

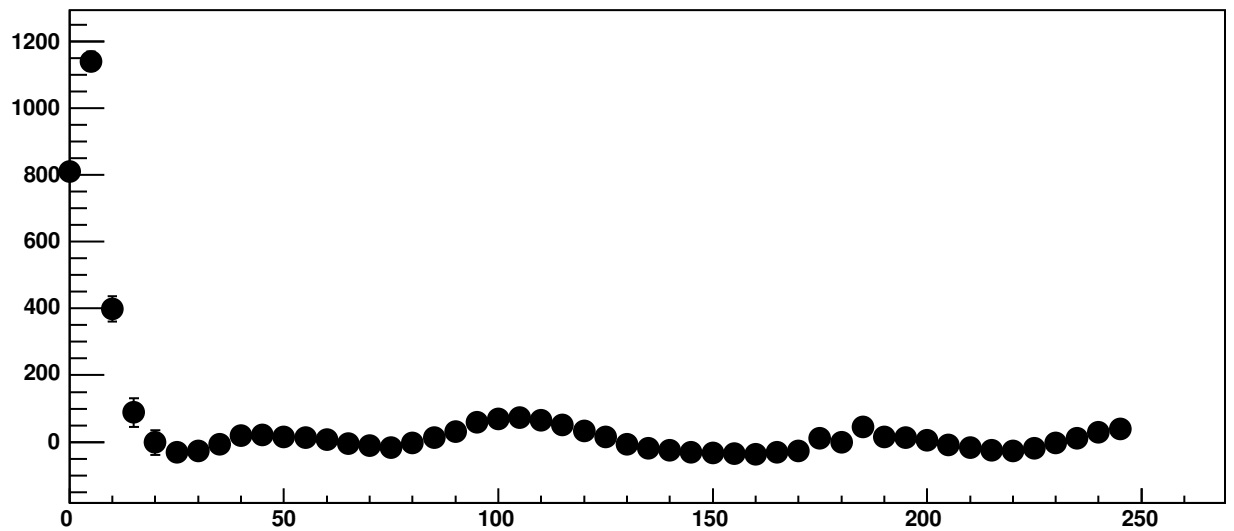
χ^2 / ndf	1668 / 42
p0	-810.1 ± 0.8602
p1	3.636 ± 0.06422
p2	9581 ± 7.379
p3	29.05 ± 0.02091



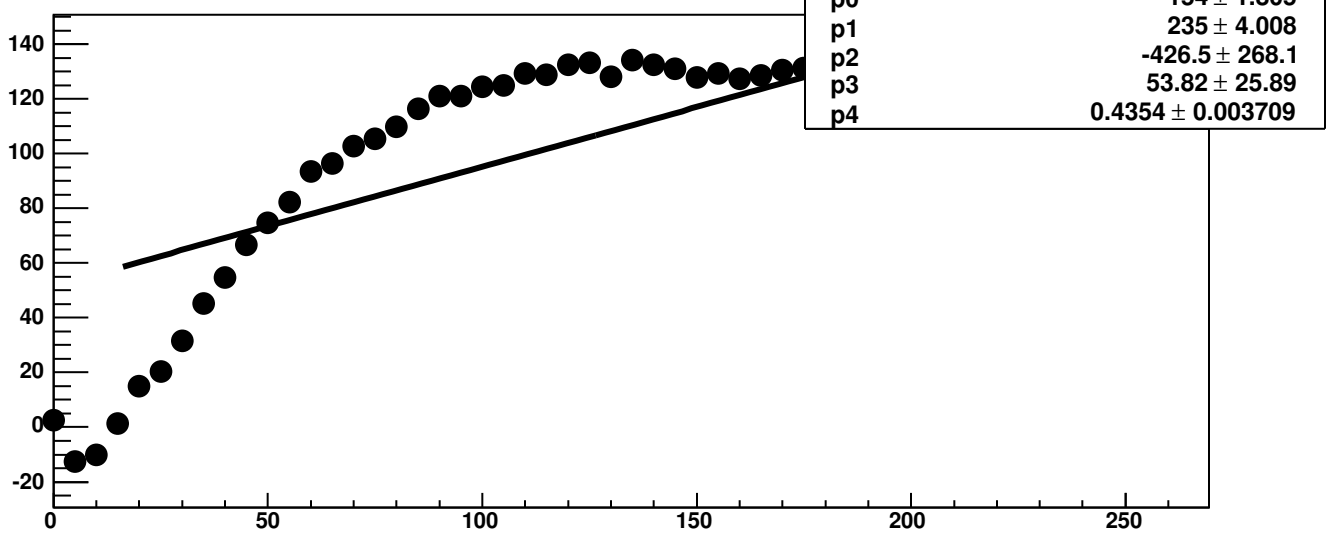
Chip 2, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold



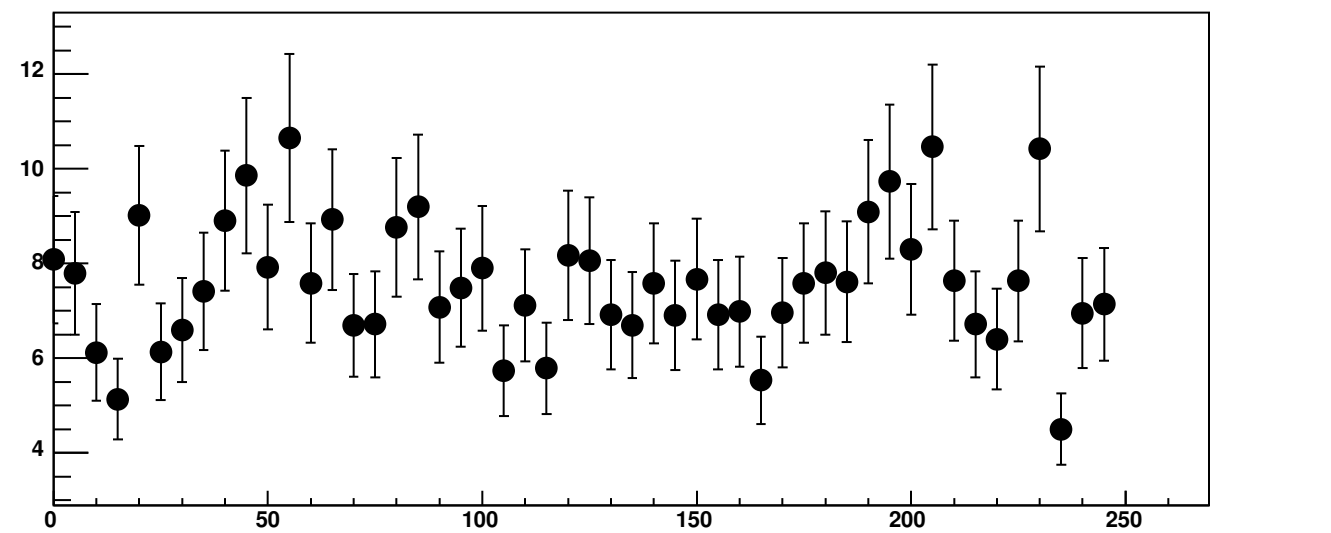
Chip 2, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold



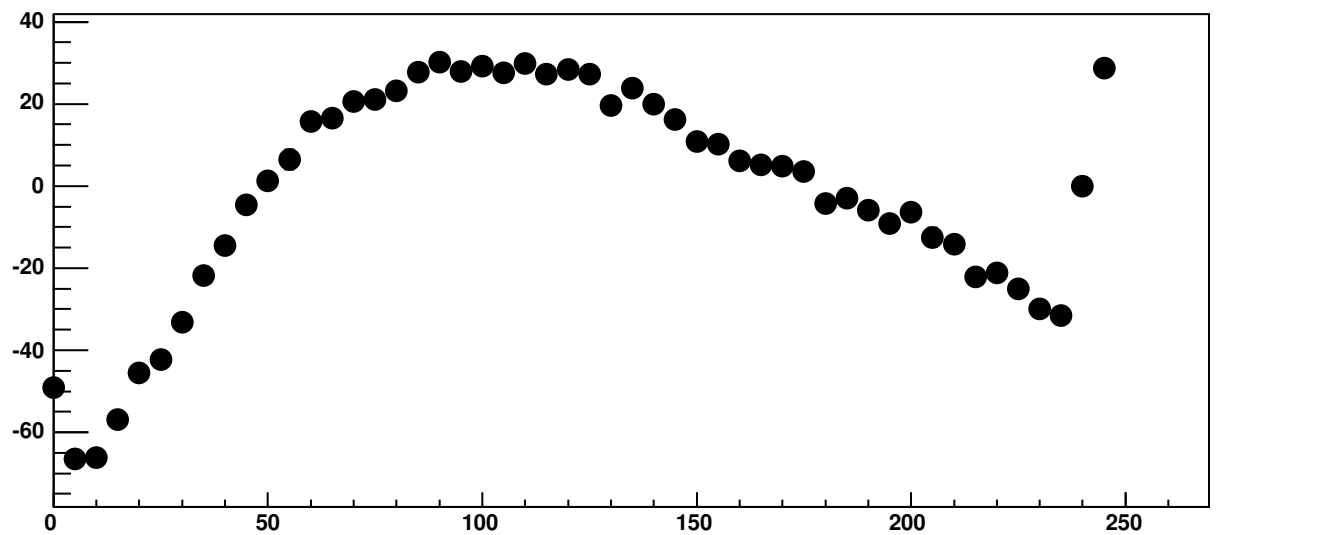
Chip 2, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

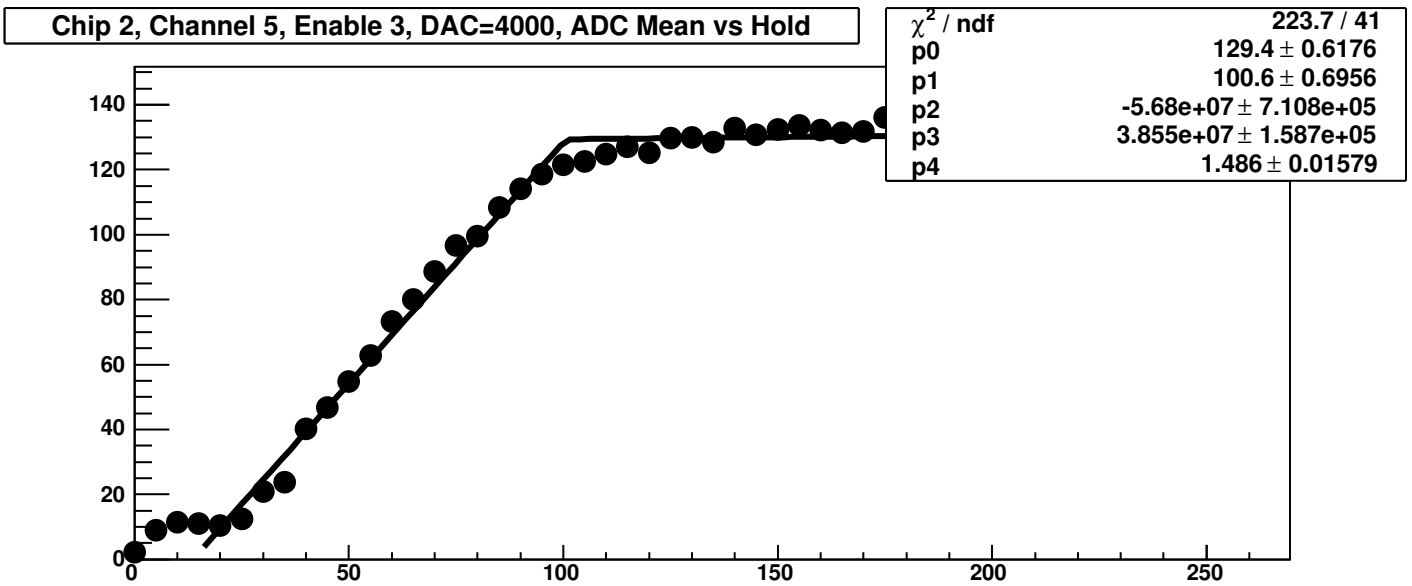


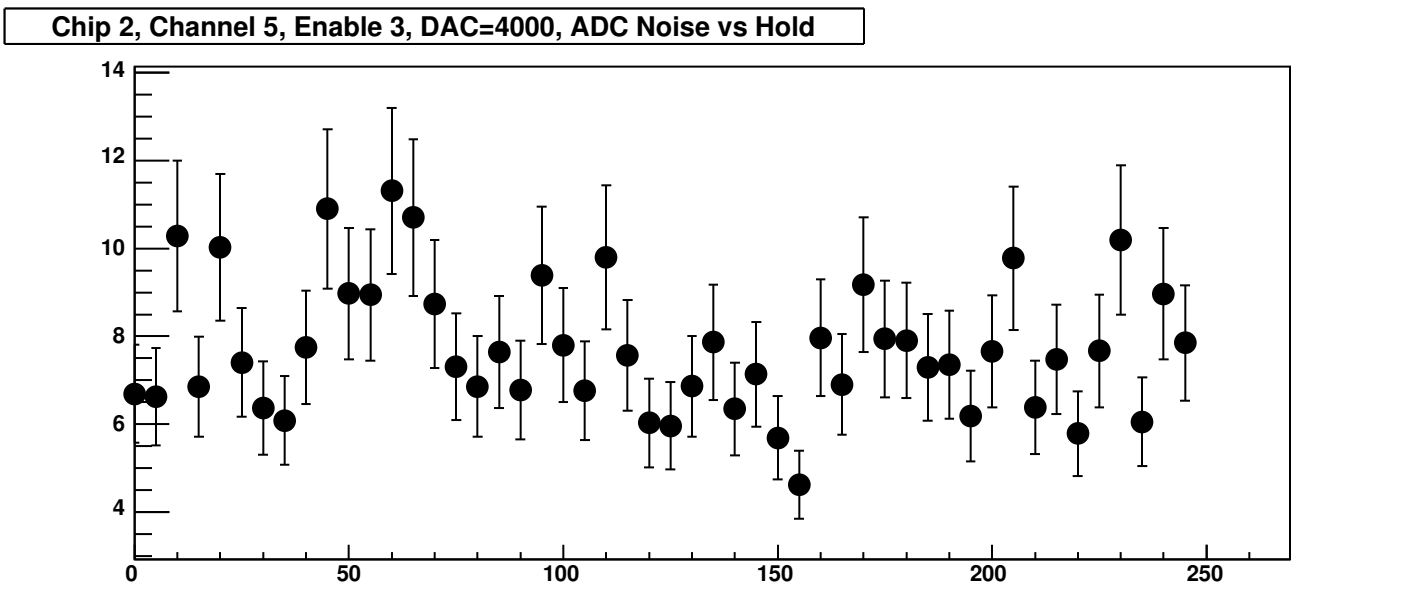
Chip 2, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold

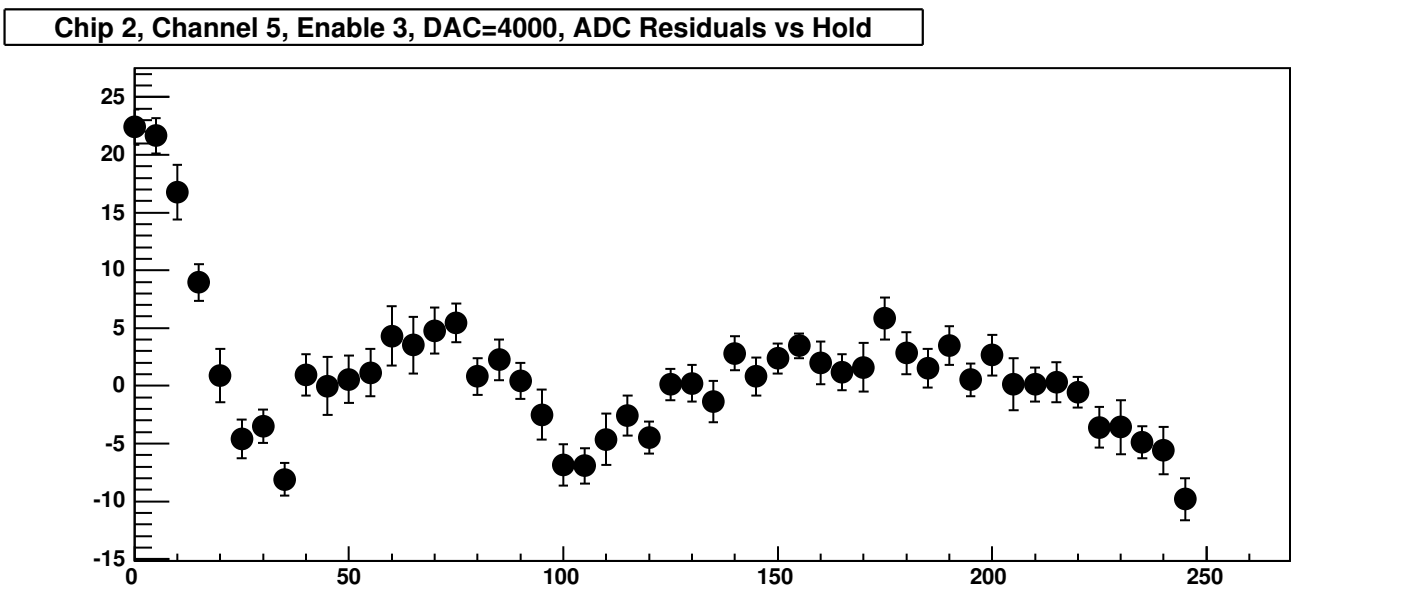


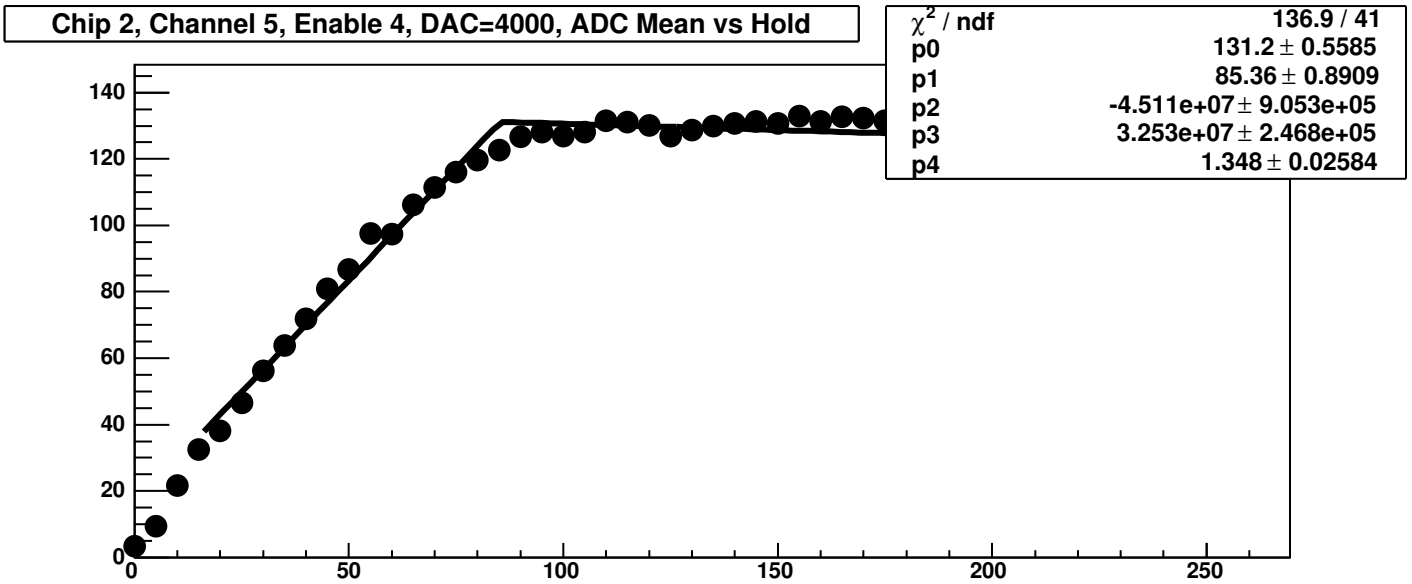
Chip 2, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

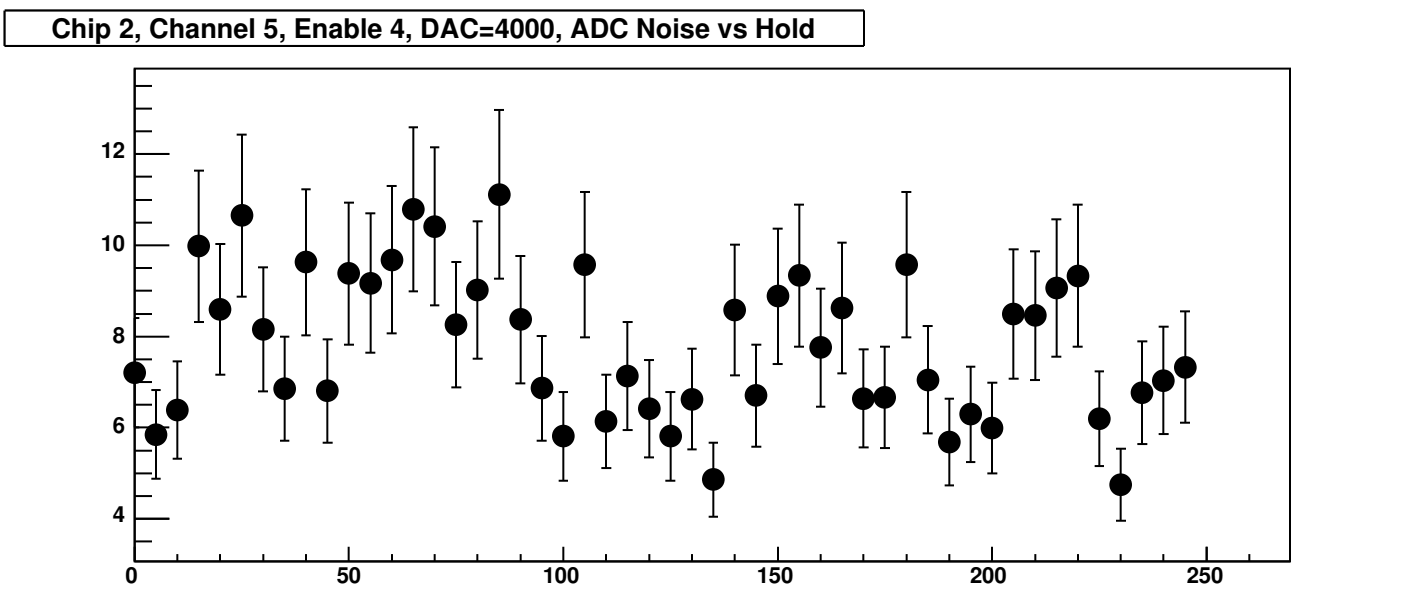


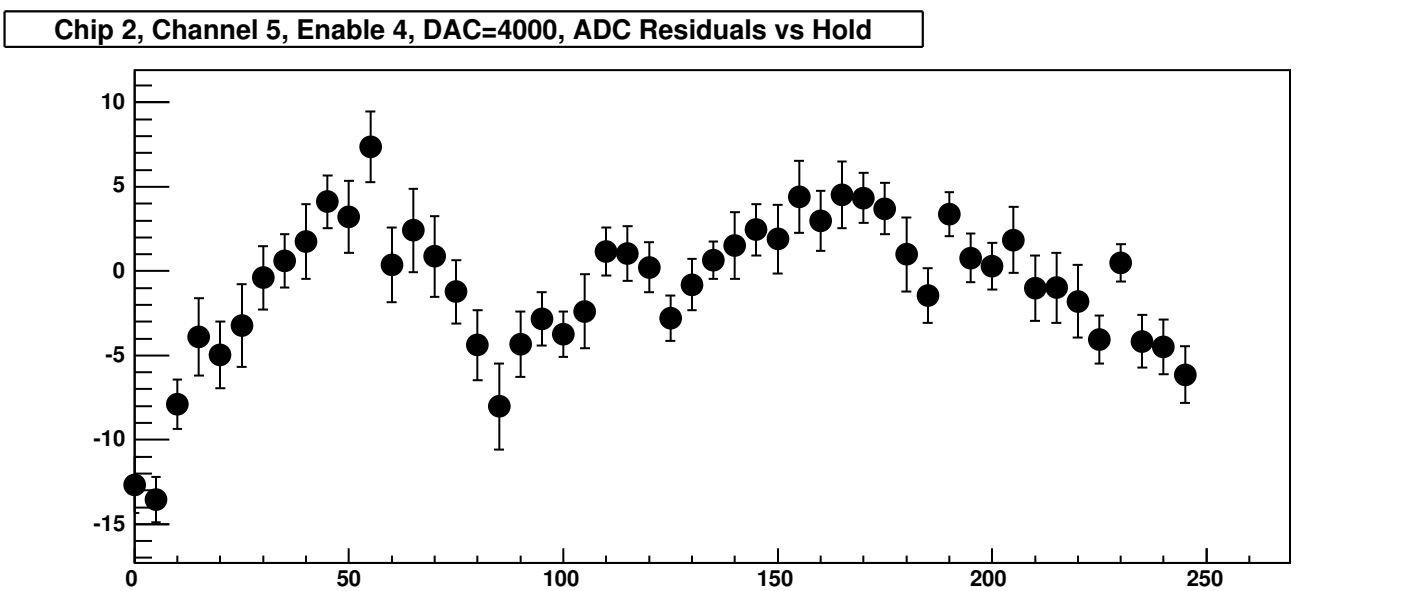

Chip 2, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 223.7 / 41
p0 129.4 ± 0.6176
p1 100.6 ± 0.6956
p2 -5.68e+07 ± 7.108e+05
p3 3.855e+07 ± 1.587e+05
p4 1.486 ± 0.01579
0 20 40 60 80 100 120 140
0 50 100 150 200 250


Chip 2, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold
4 6 8 10 12 14
0 50 100 150 200 250

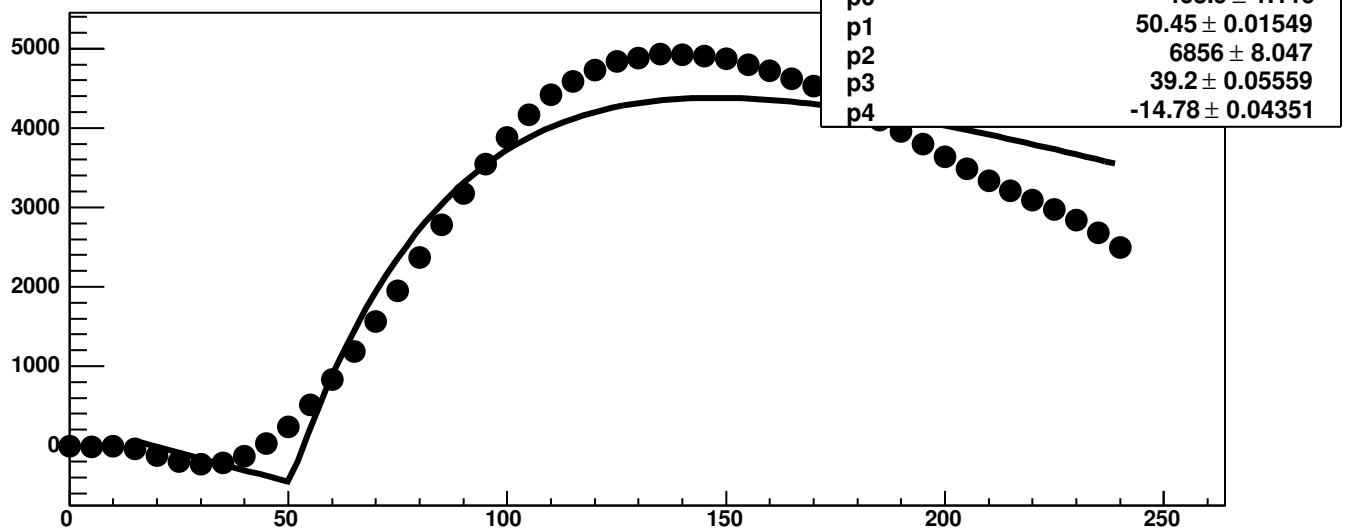

Chip 2, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold
-15 -10 -5 0 5 10 15 20 25
0 50 100 150 200 250


Chip 2, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 136.9 / 41
p0 131.2 ± 0.5585
p1 85.36 ± 0.8909
p2 -4.511e+07 ± 9.053e+05
p3 3.253e+07 ± 2.468e+05
p4 1.348 ± 0.02584
0 20 40 60 80 100 120 140
0 50 100 150 200 250

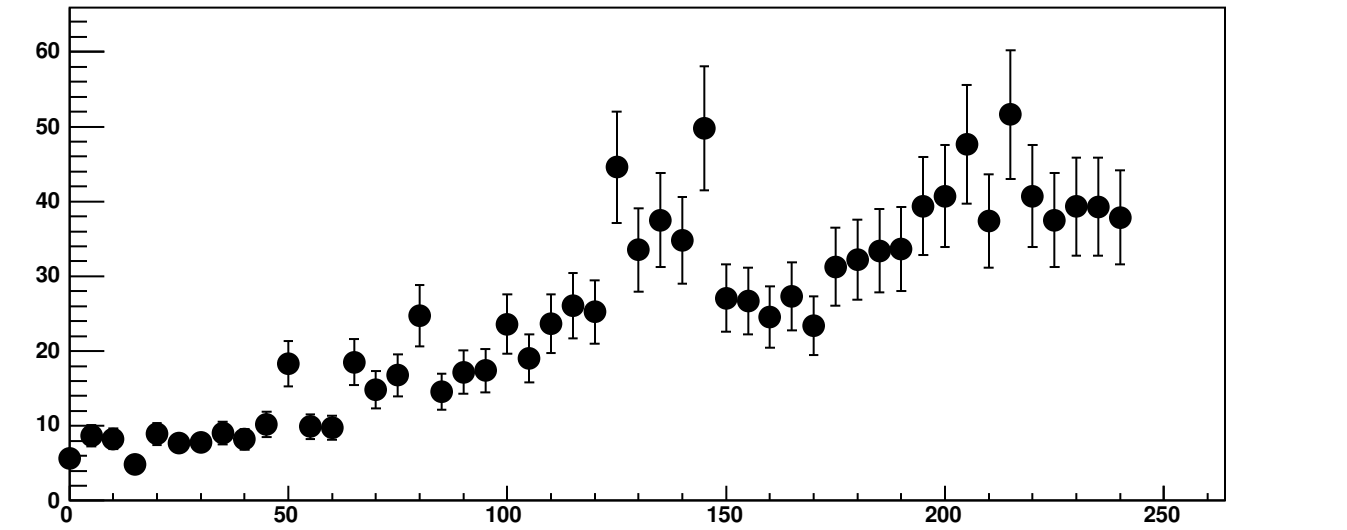

Chip 2, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold
4 6 8 10 12
0 50 100 150 200 250


Chip 2, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold
-15 -10 -5 0 5 10
0 50 100 150 200 250

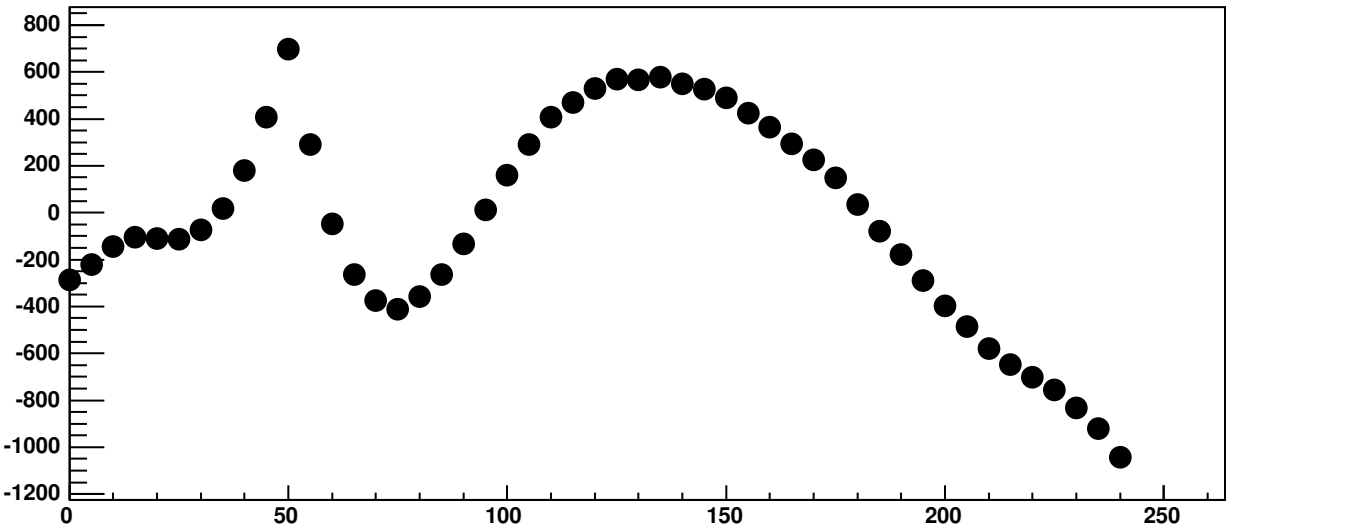
Chip 2, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

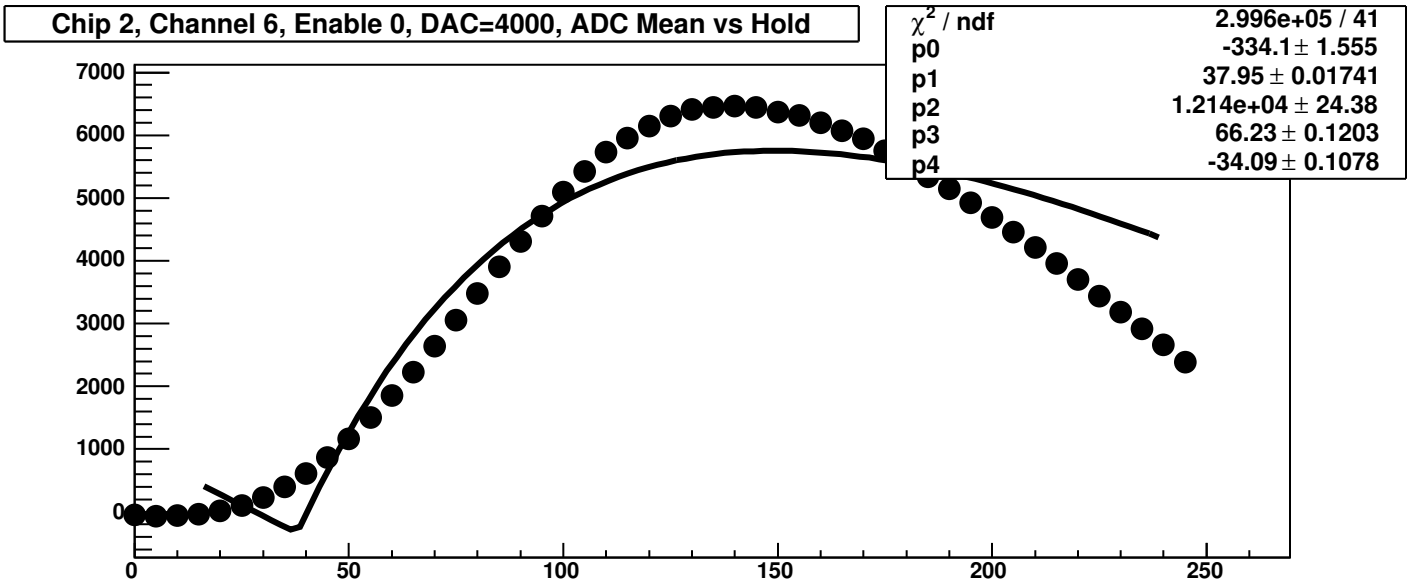


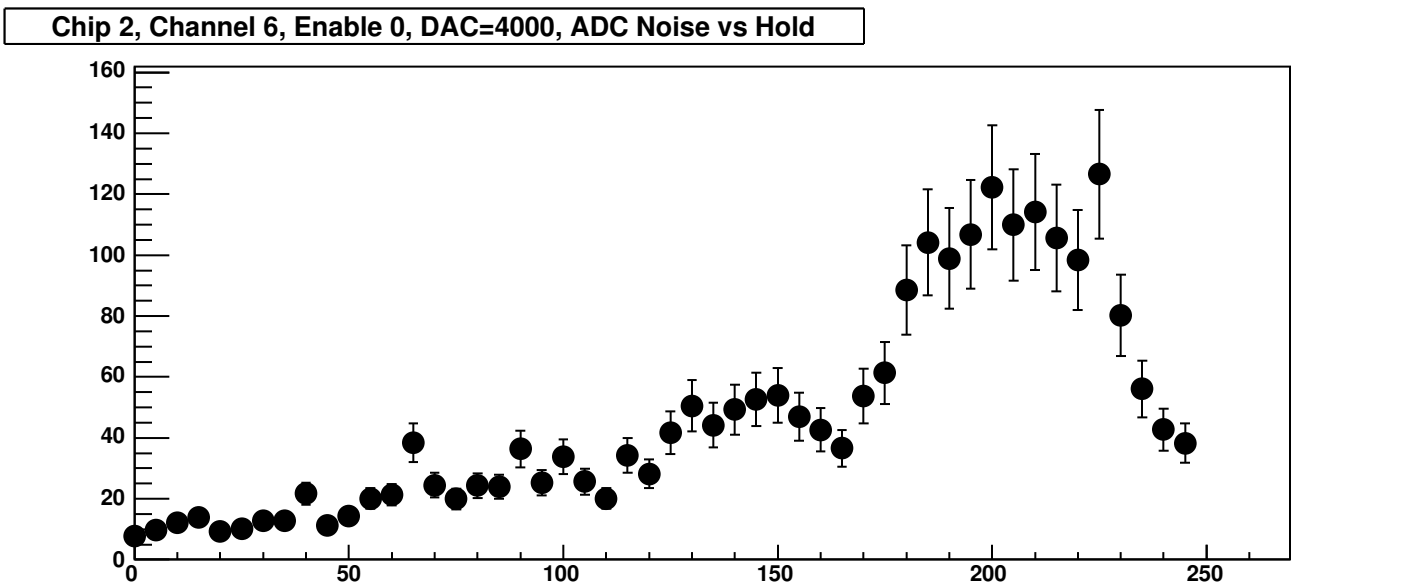
Chip 2, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold

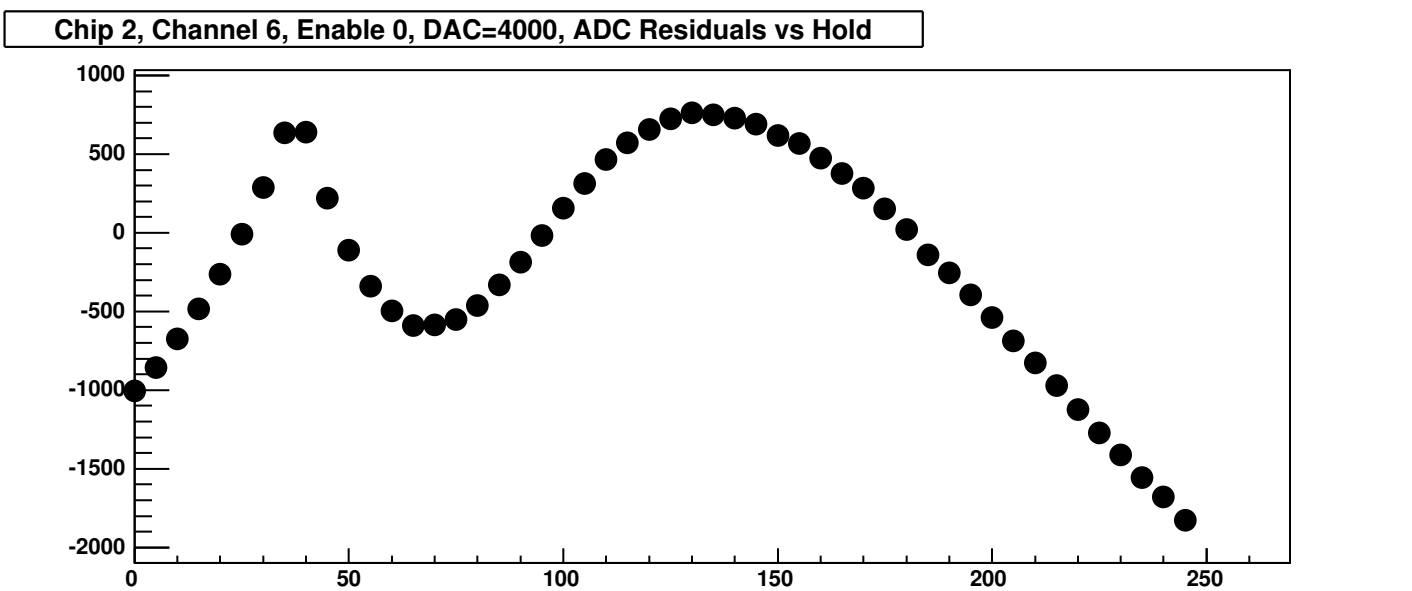


Chip 2, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

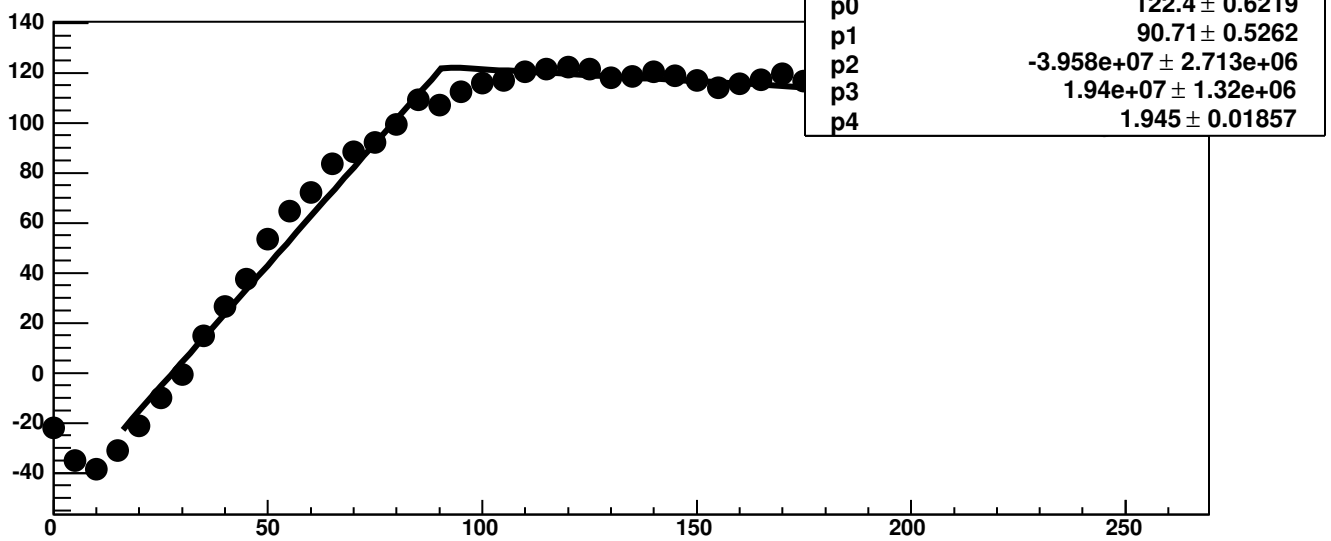



Chip 2, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.996e+05 / 41
p0 -334.1 ± 1.555
p1 37.95 ± 0.01741
p2 1.214e+04 ± 24.38
p3 66.23 ± 0.1203
p4 -34.09 ± 0.1078
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

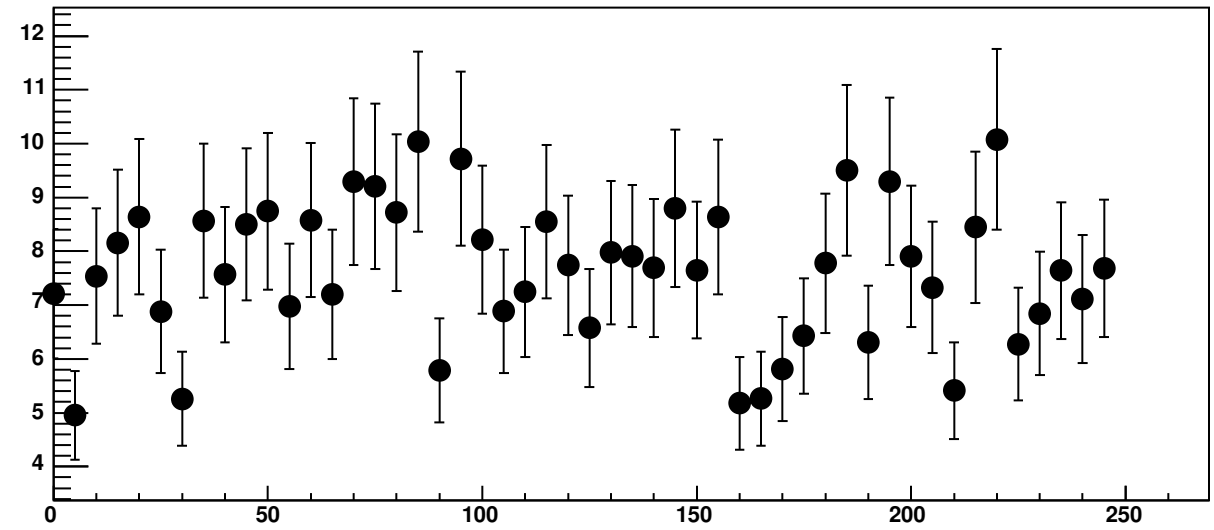

Chip 2, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 2, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

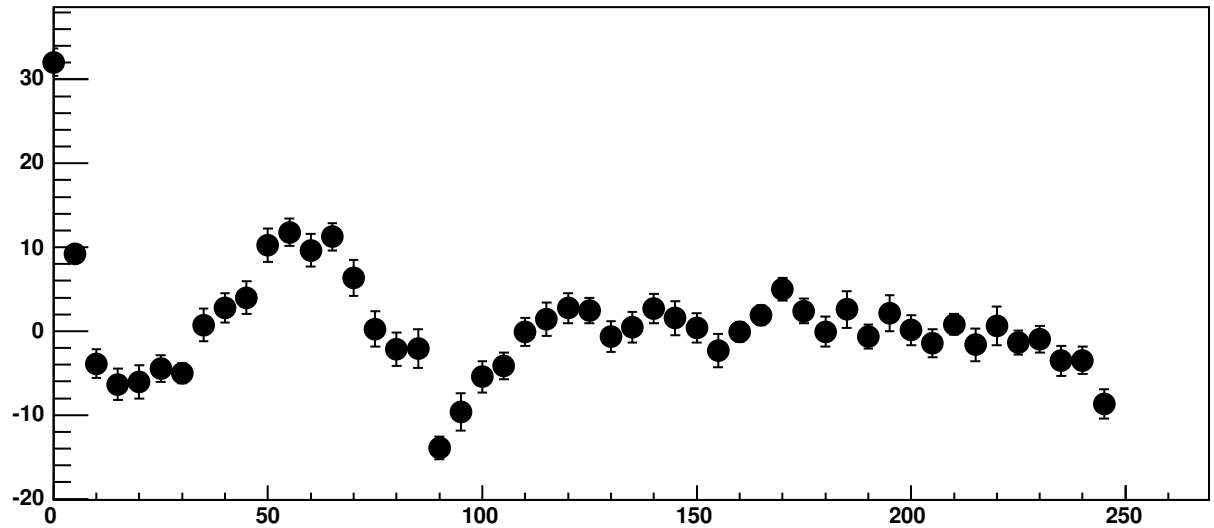
Chip 2, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold



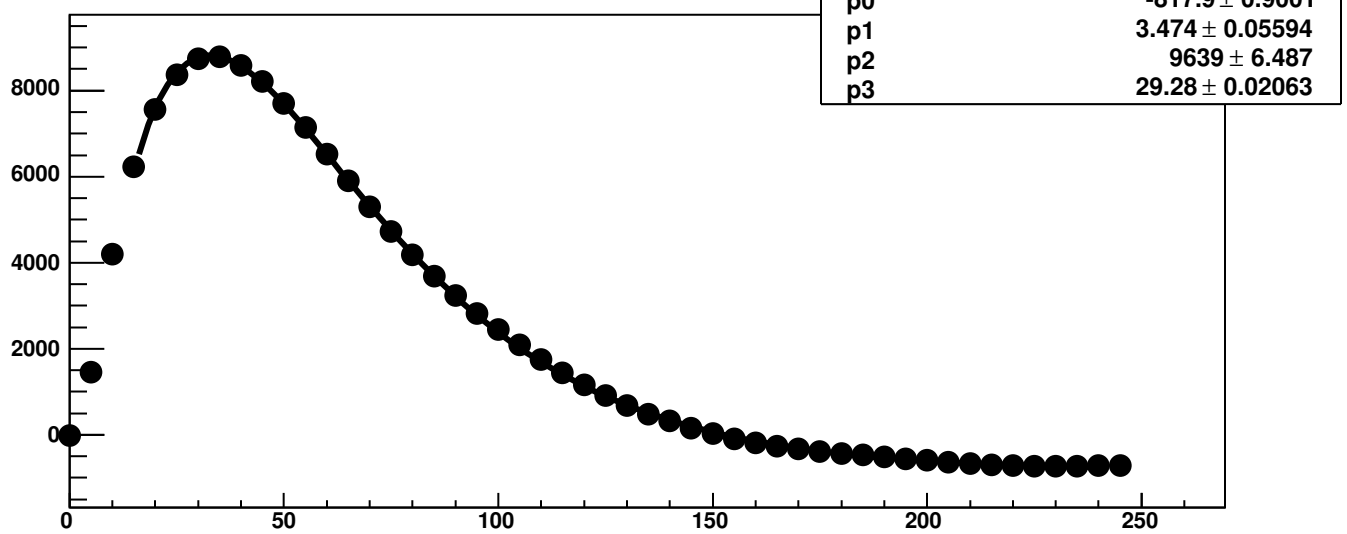
Chip 2, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



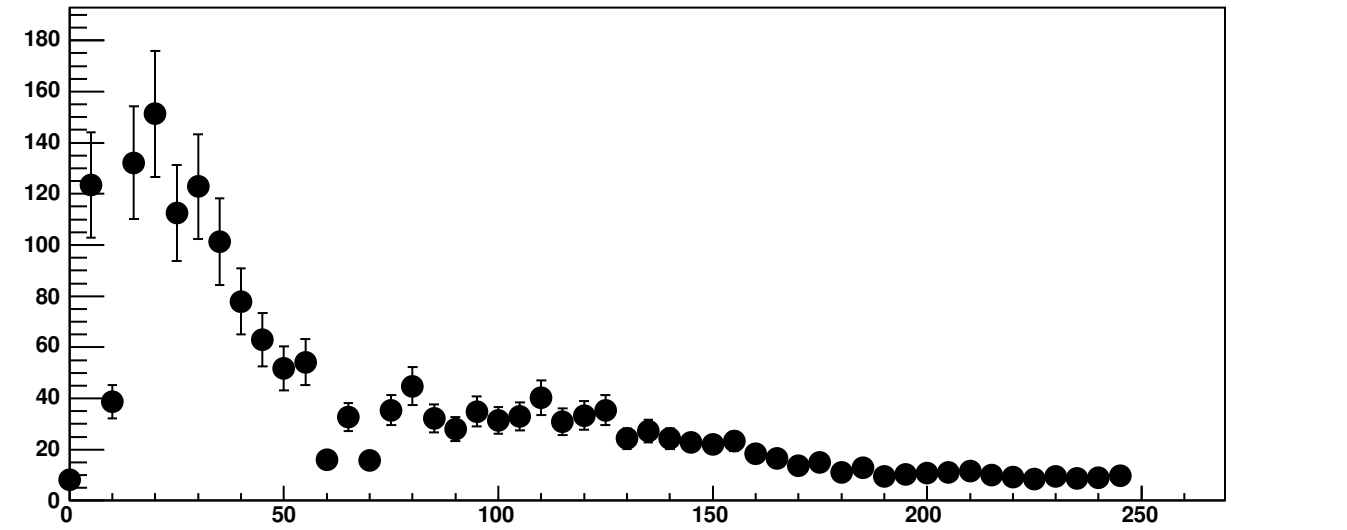
Chip 2, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold



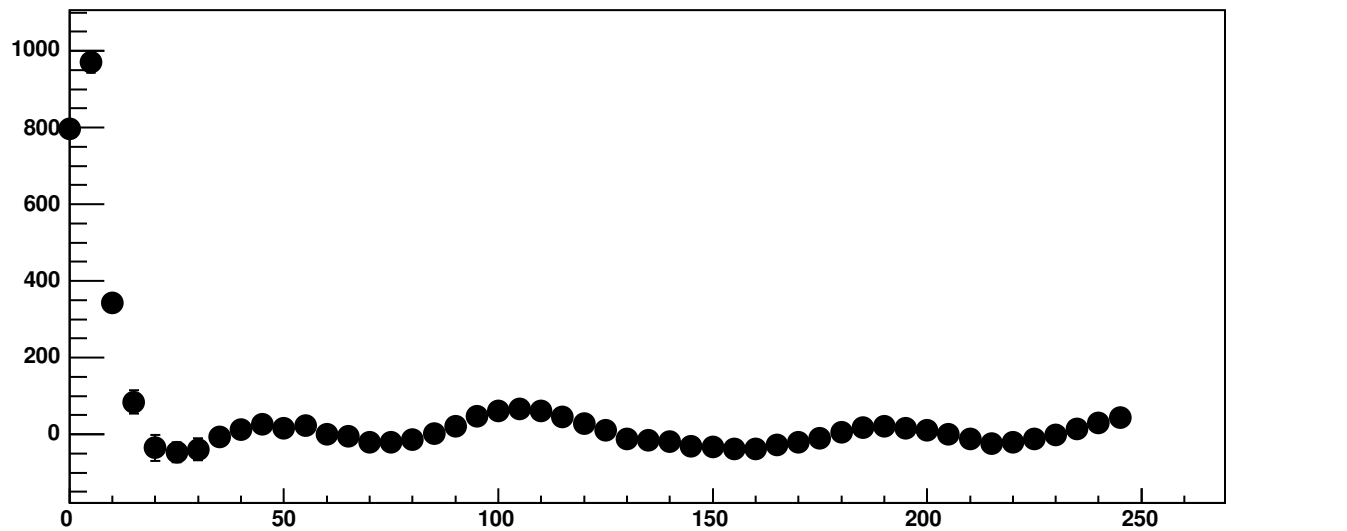
Chip 2, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold



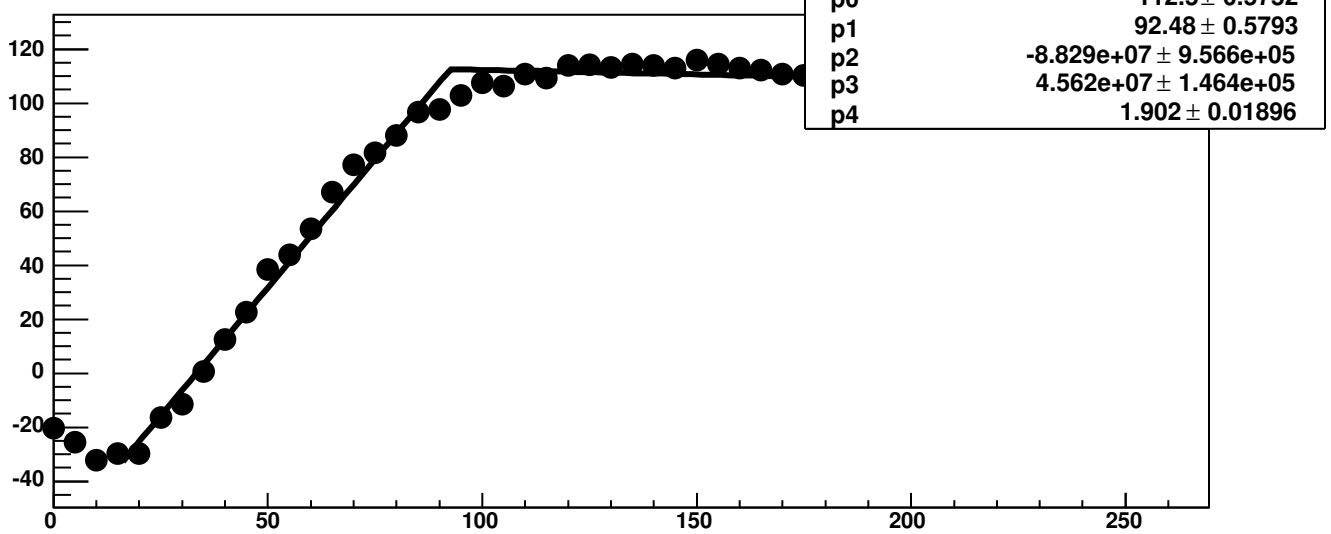
Chip 2, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold



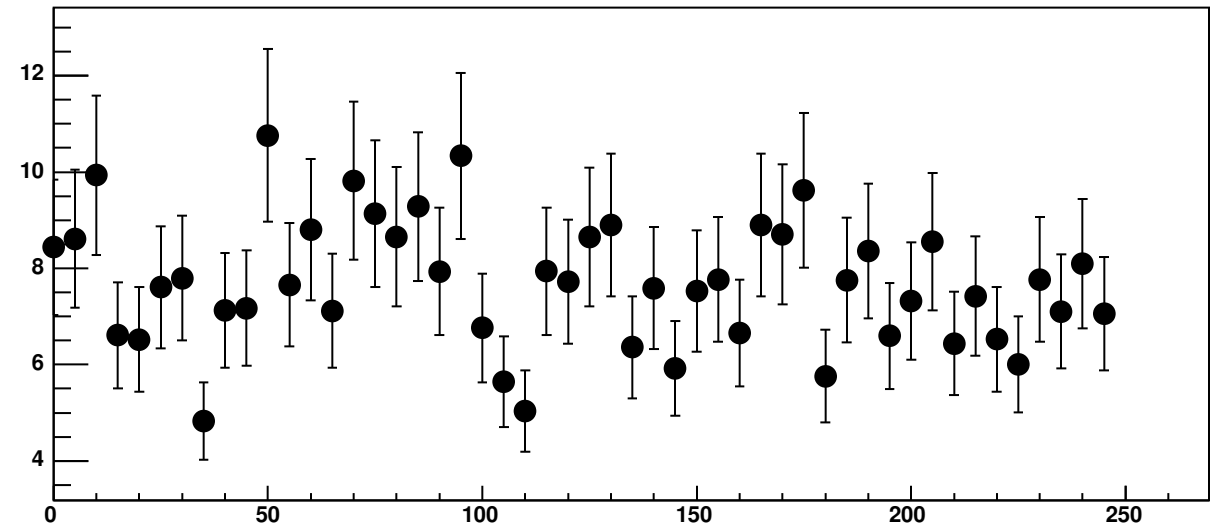
Chip 2, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold



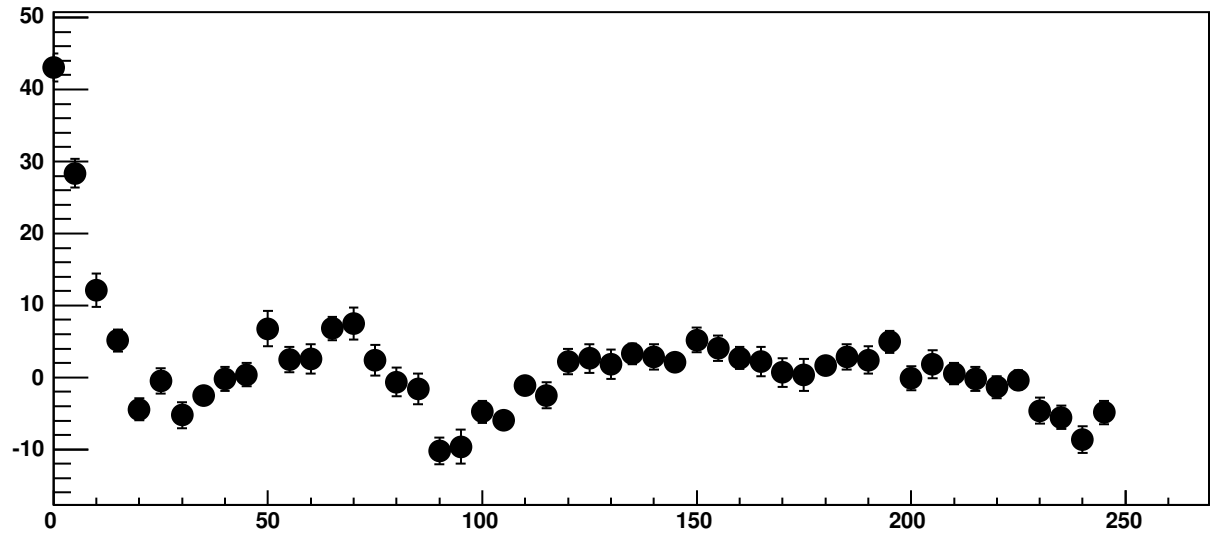
Chip 2, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

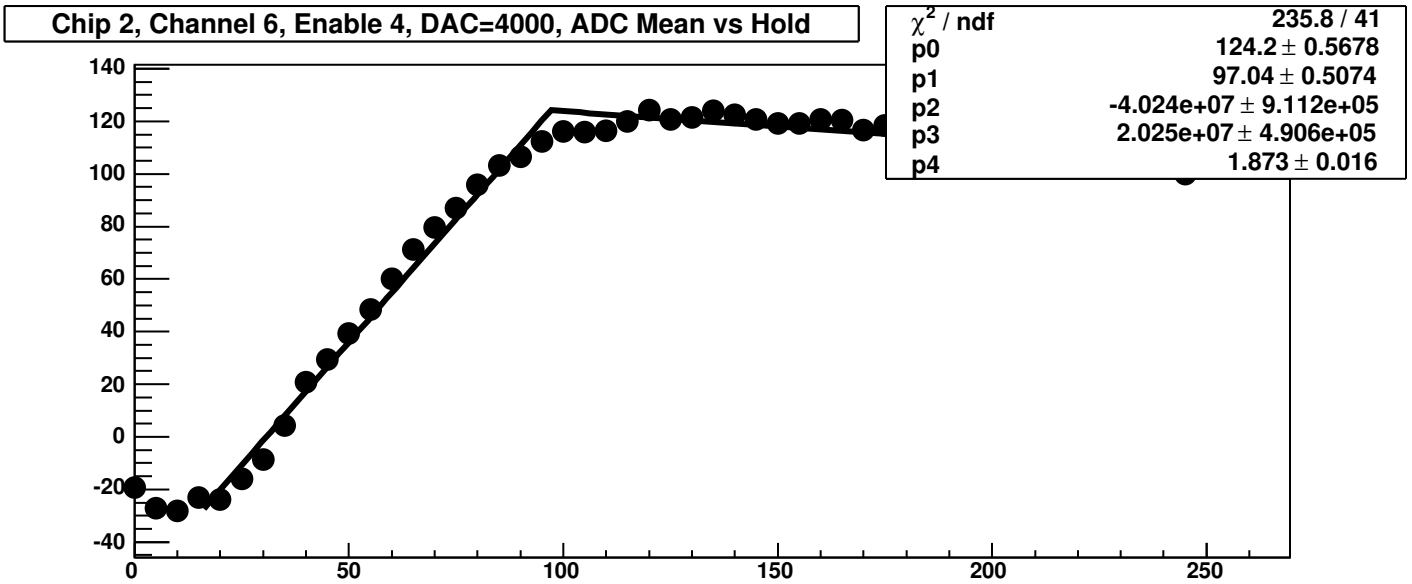


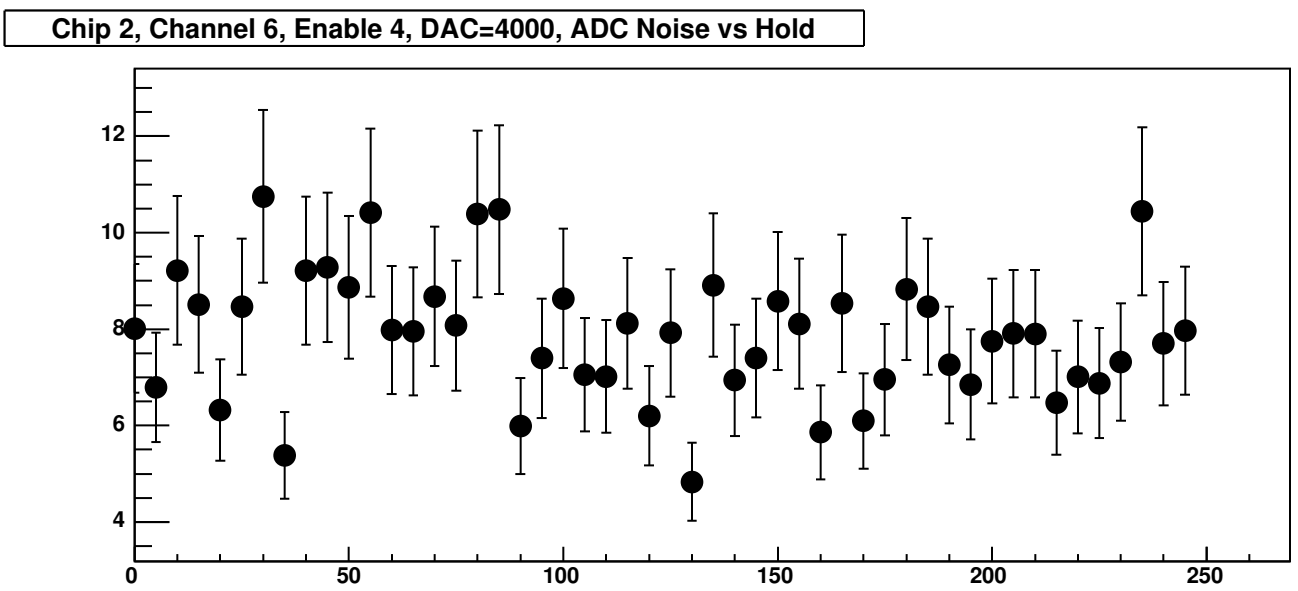
Chip 2, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold

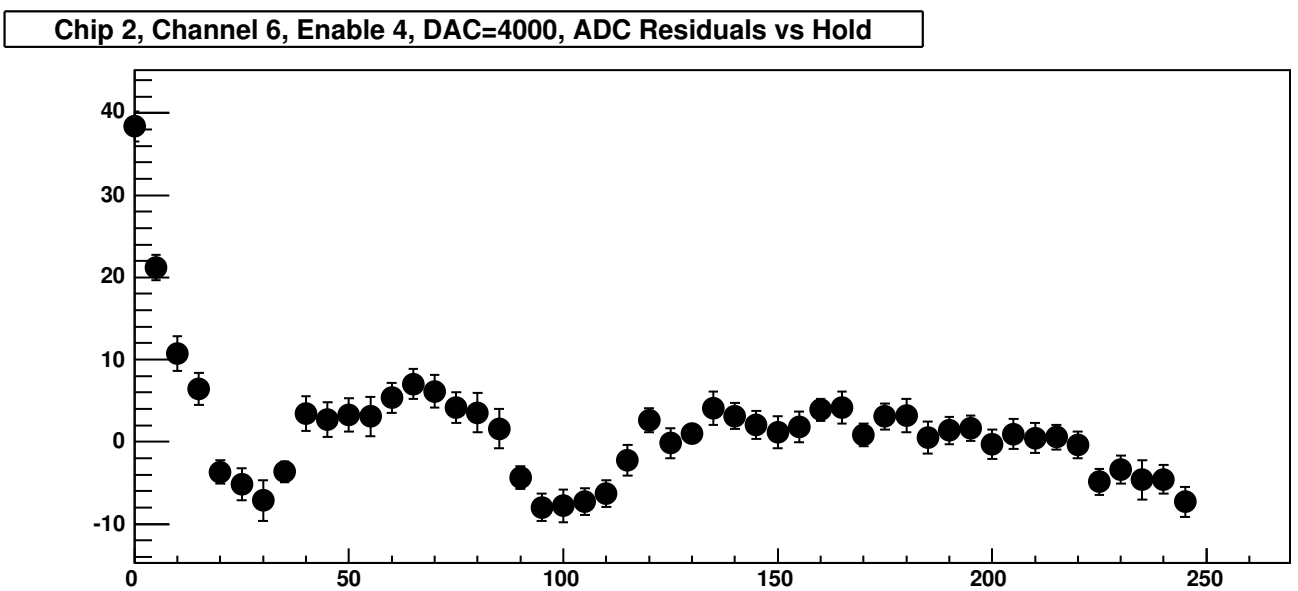


Chip 2, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

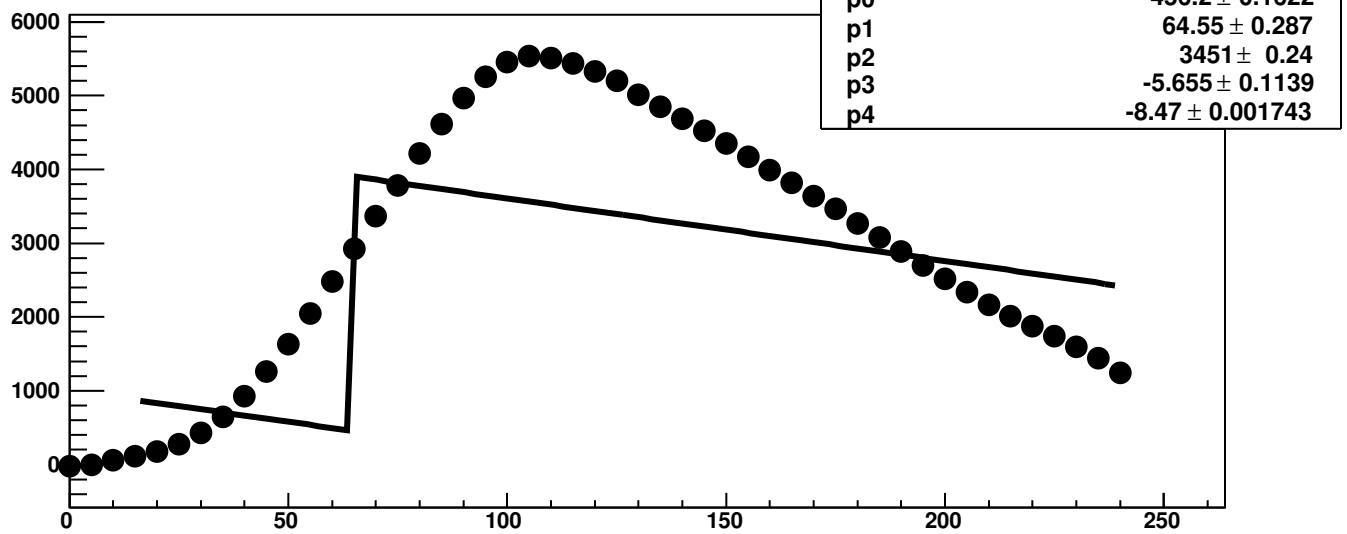



Chip 2, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 235.8 / 41
p0 124.2 ± 0.5678
p1 97.04 ± 0.5074
p2 -4.024e+07 ± 9.112e+05
p3 2.025e+07 ± 4.906e+05
p4 1.873 ± 0.016

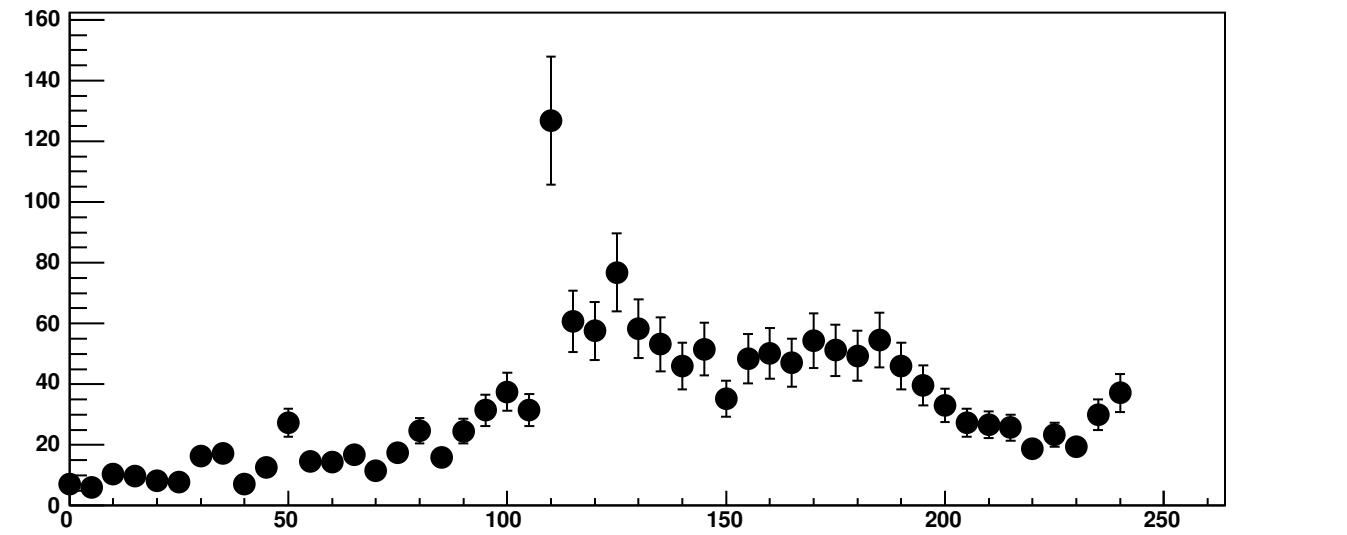

Chip 2, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold


Chip 2, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

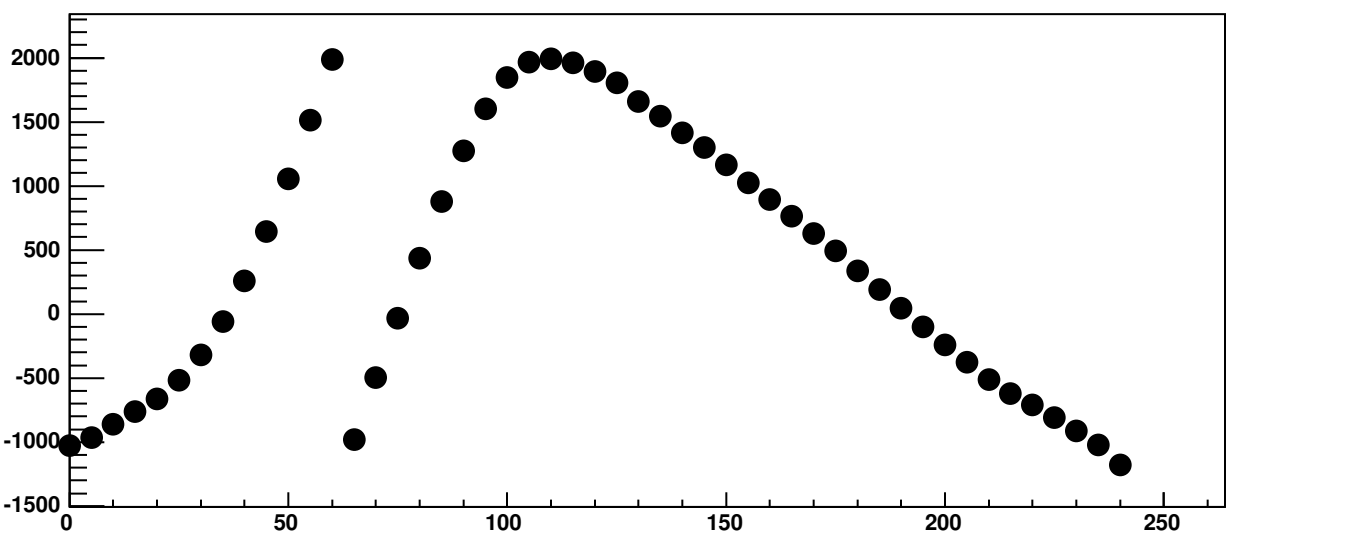
Chip 2, Channel 6, Enable 5, DAC=4000, ADC Mean vs Hold

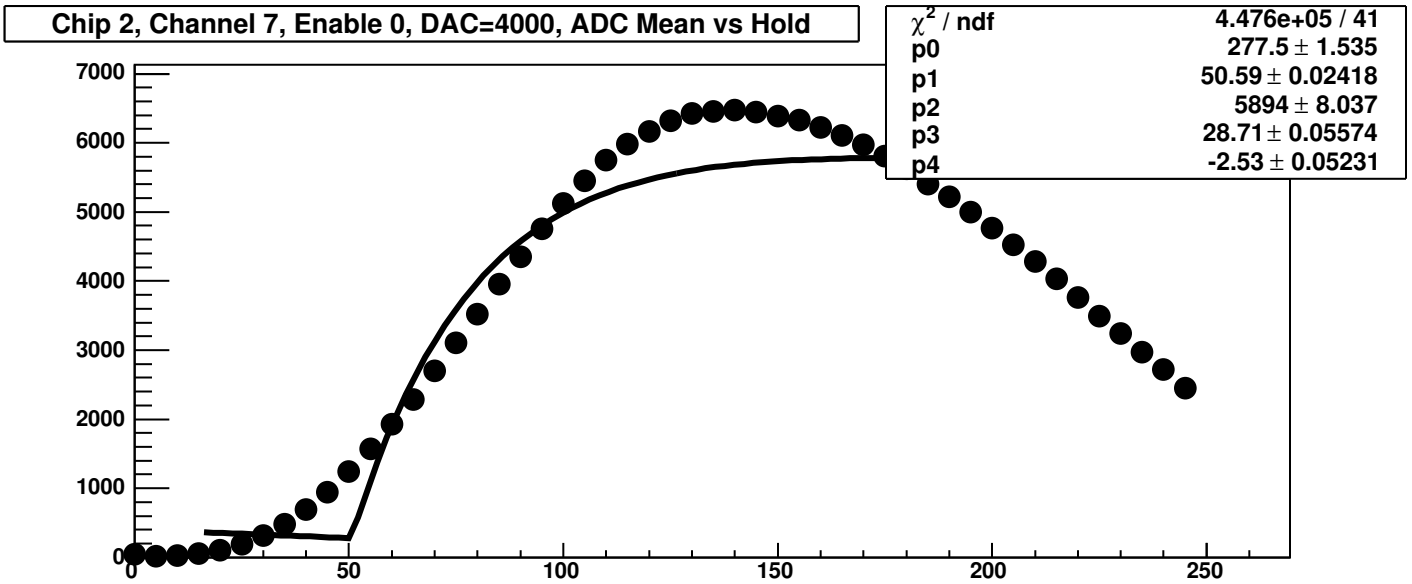


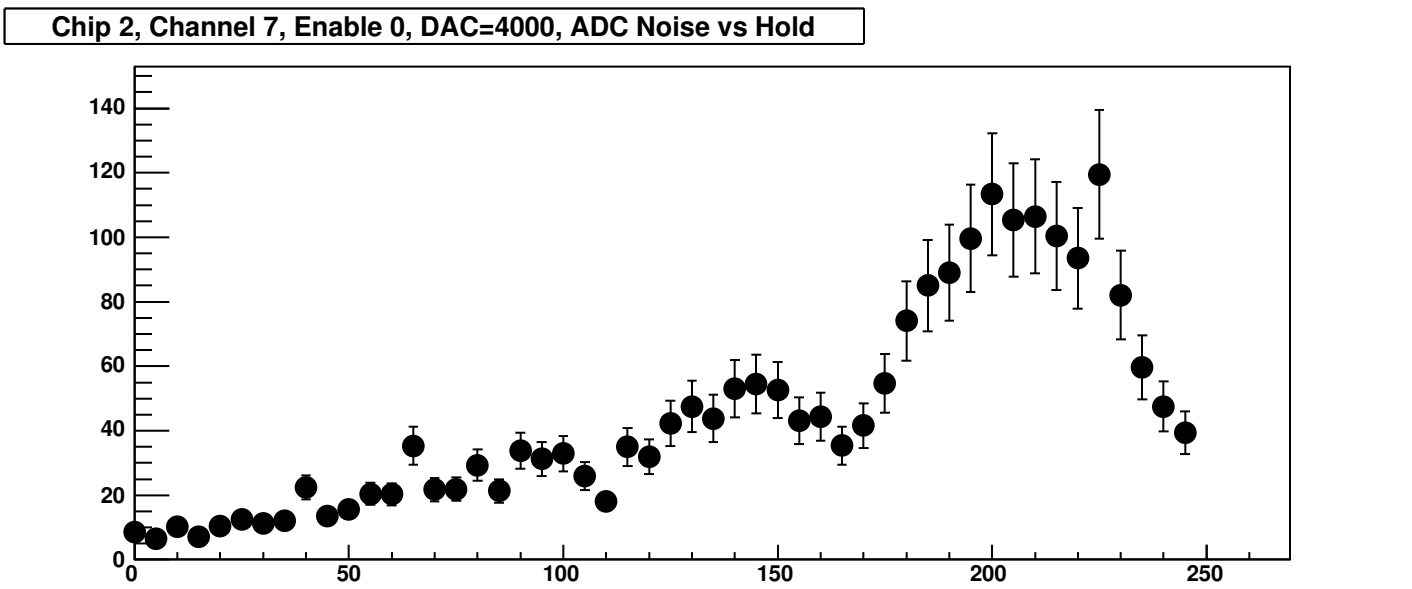
Chip 2, Channel 6, Enable 5, DAC=4000, ADC Noise vs Hold

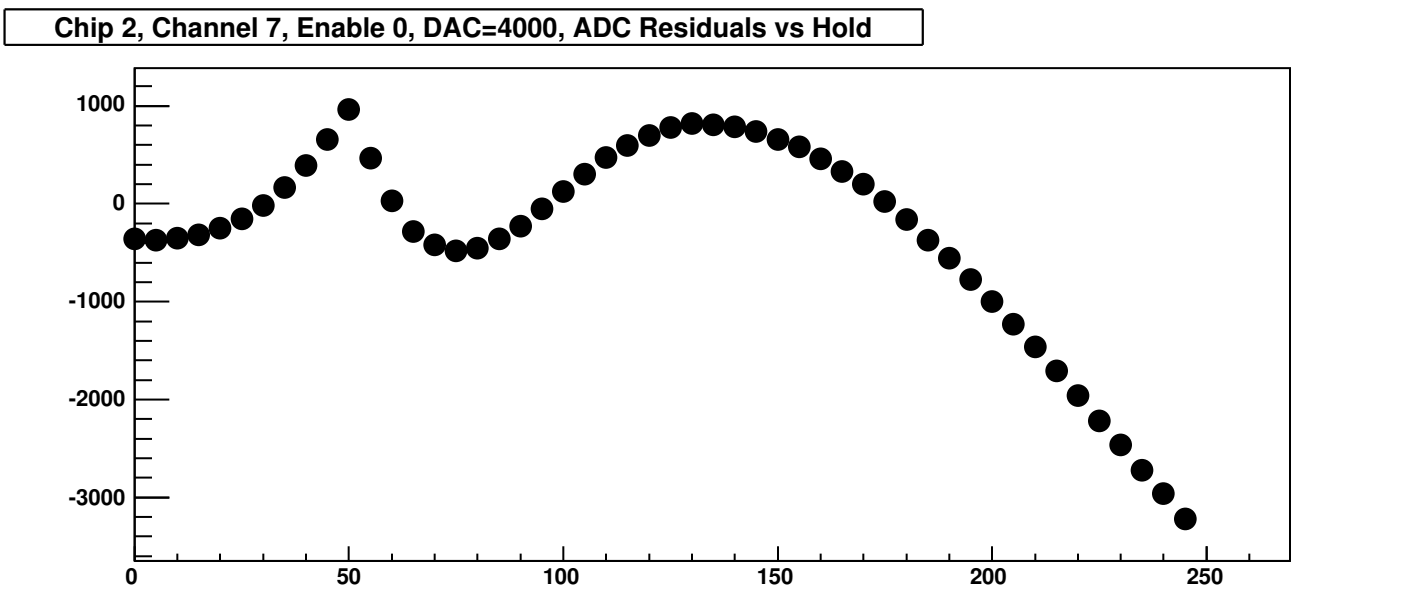


Chip 2, Channel 6, Enable 5, DAC=4000, ADC Residuals vs Hold

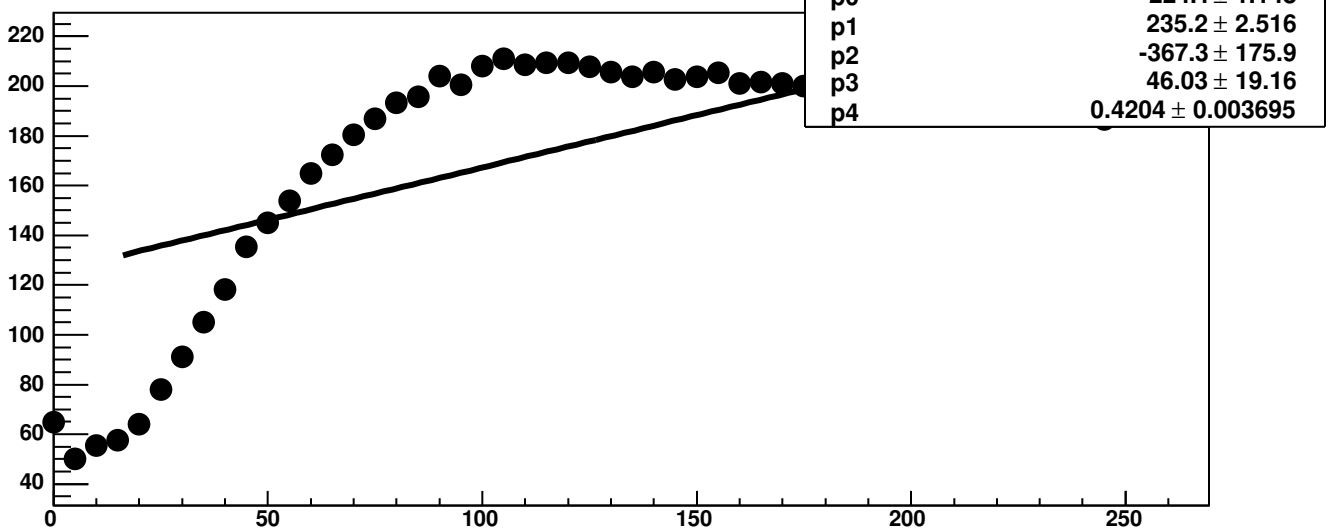



Chip 2, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.476e+05 / 41
p0 277.5 ± 1.535
p1 50.59 ± 0.02418
p2 5894 ± 8.037
p3 28.71 ± 0.05574
p4 -2.53 ± 0.05231
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

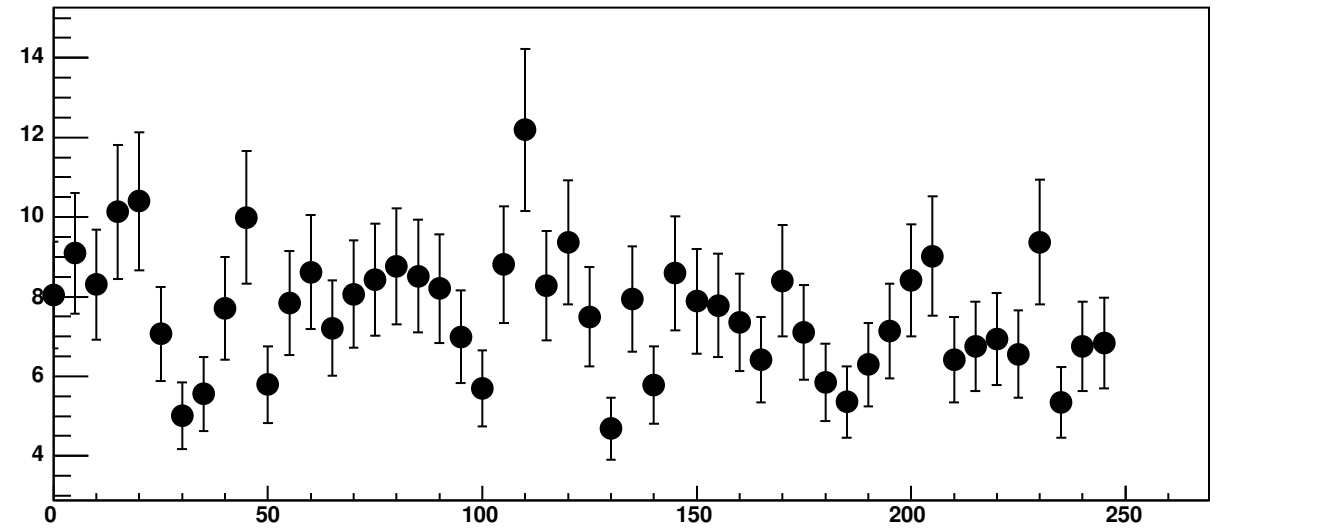

Chip 2, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 2, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

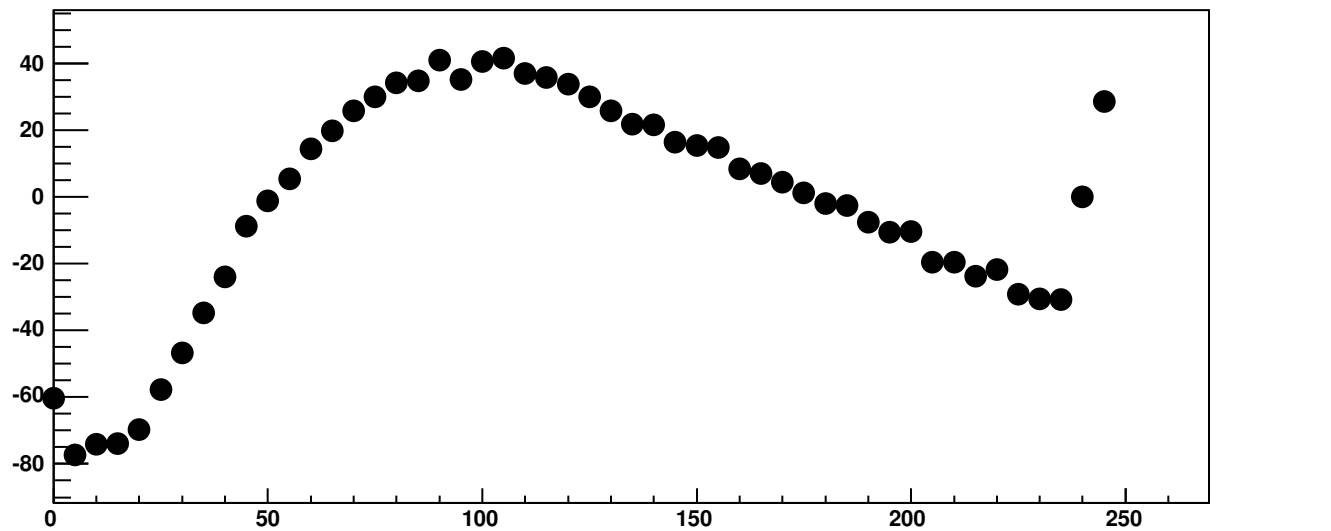
Chip 2, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold



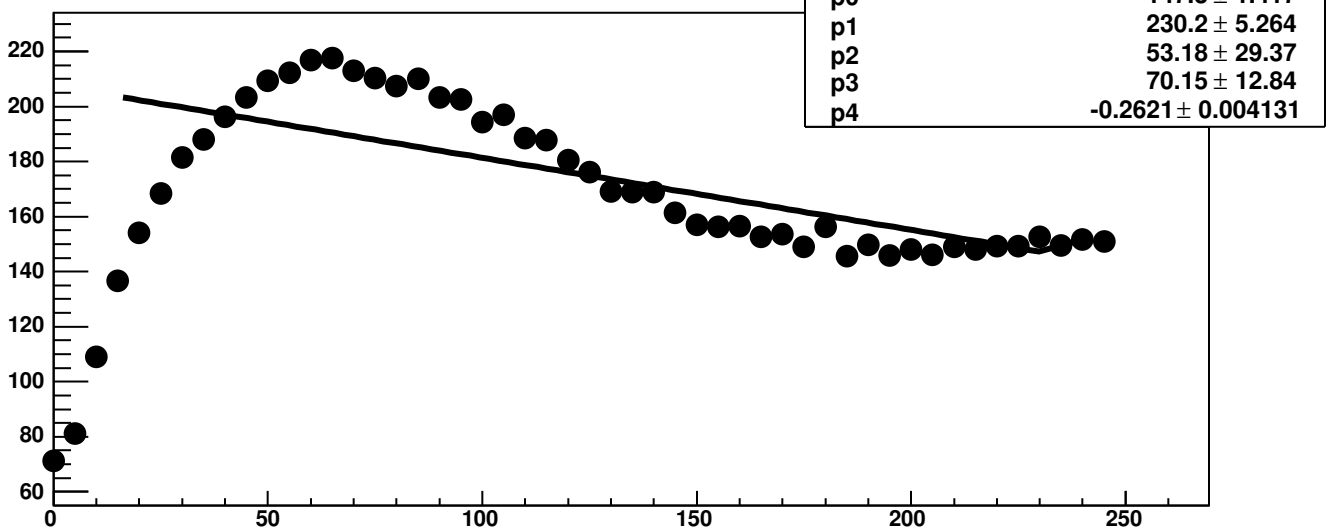
Chip 2, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



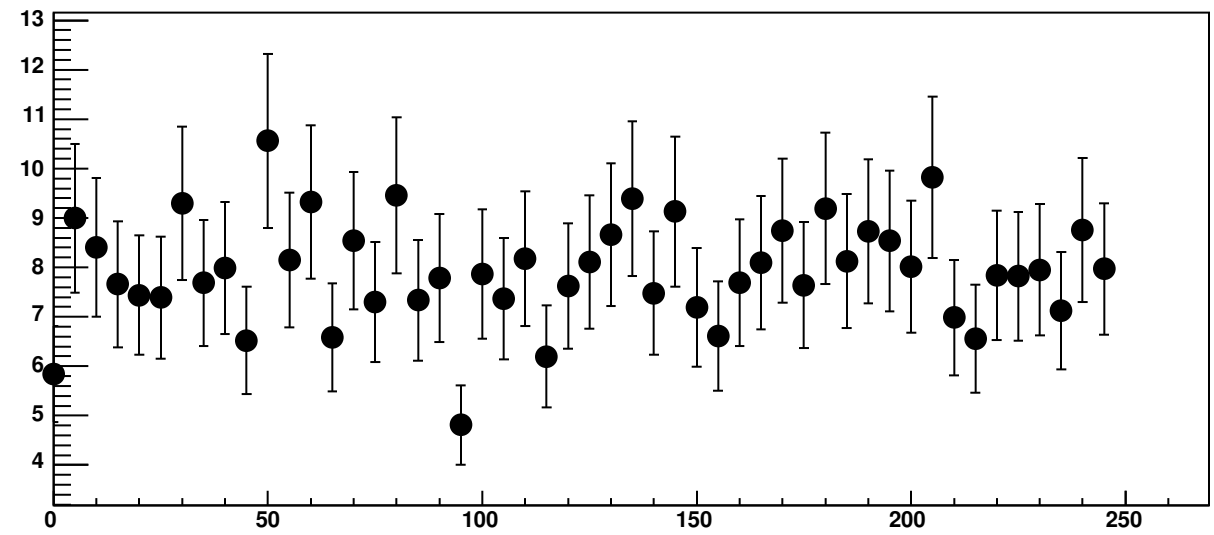
Chip 2, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold



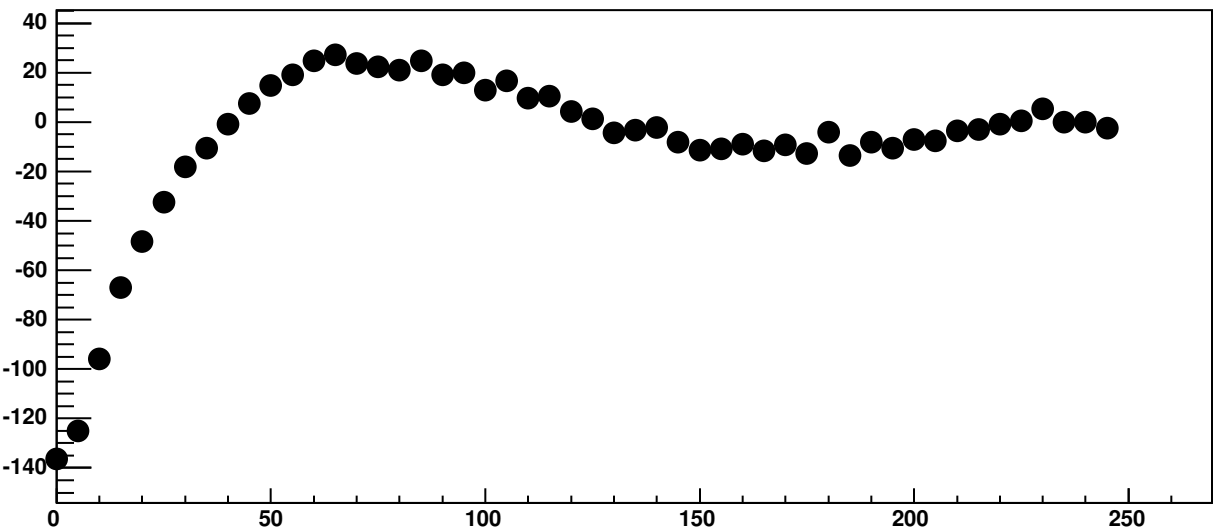
Chip 2, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold



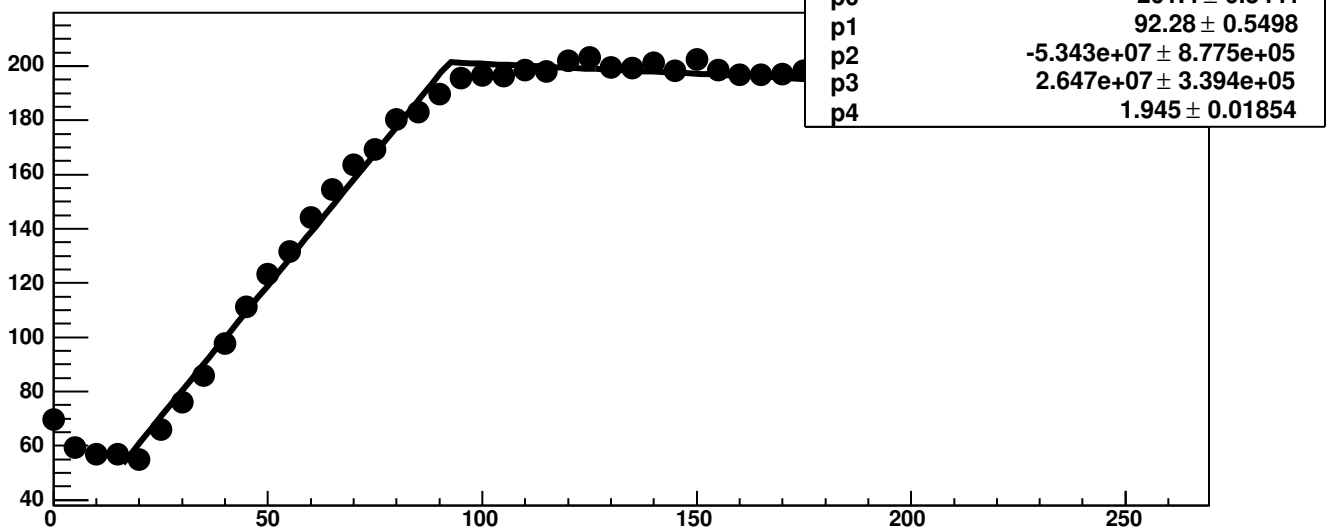
Chip 2, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold



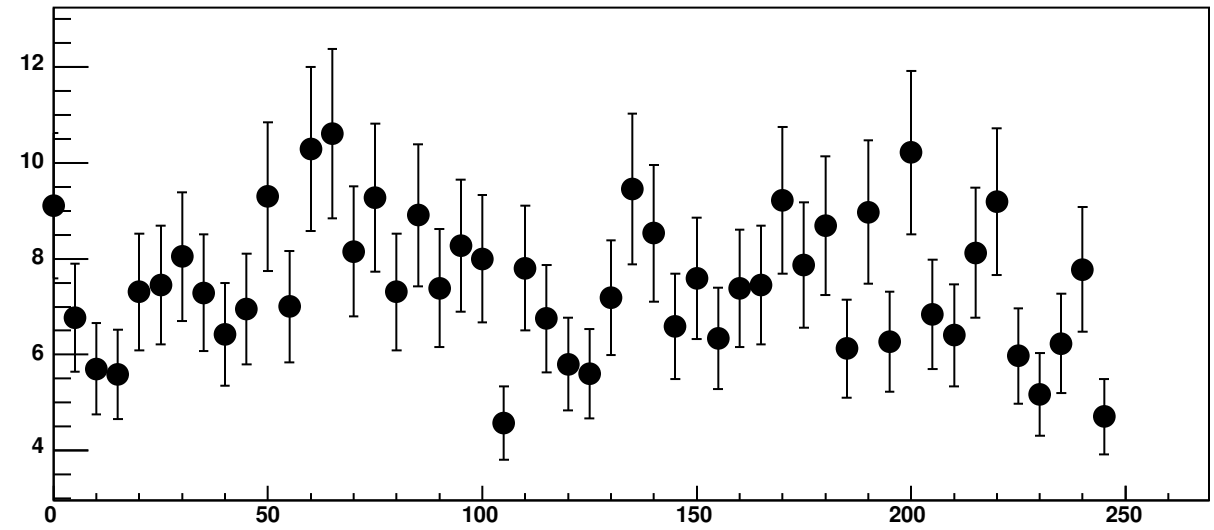
Chip 2, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold



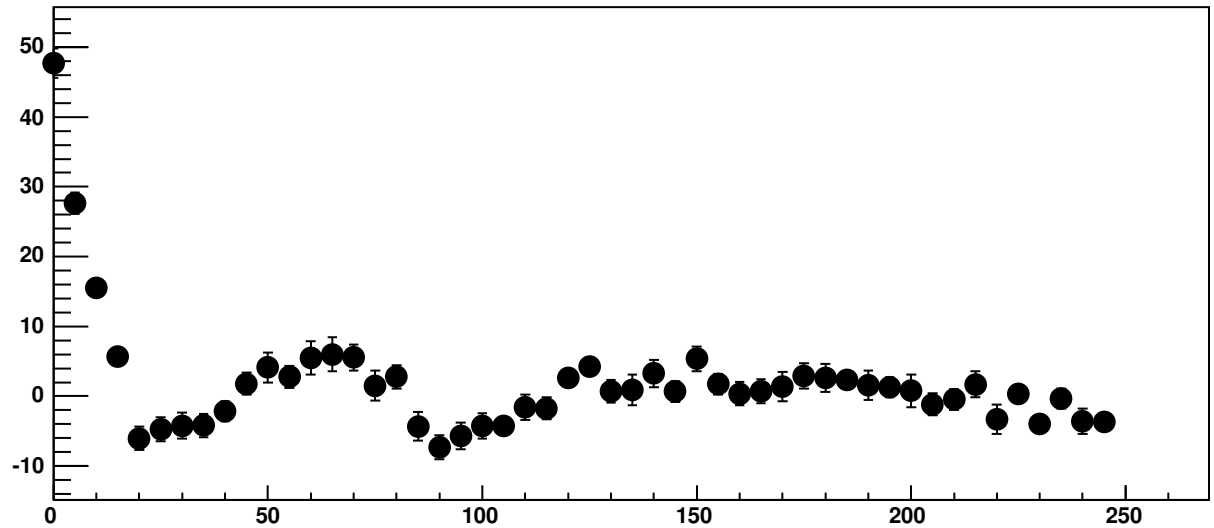
Chip 2, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold



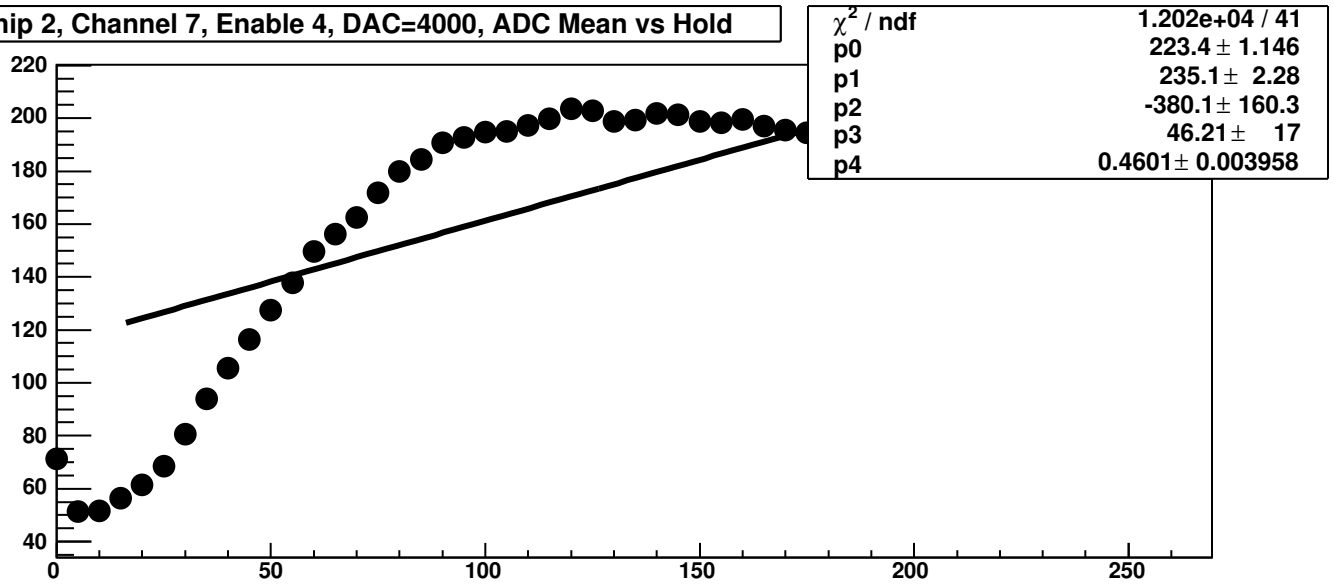
Chip 2, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



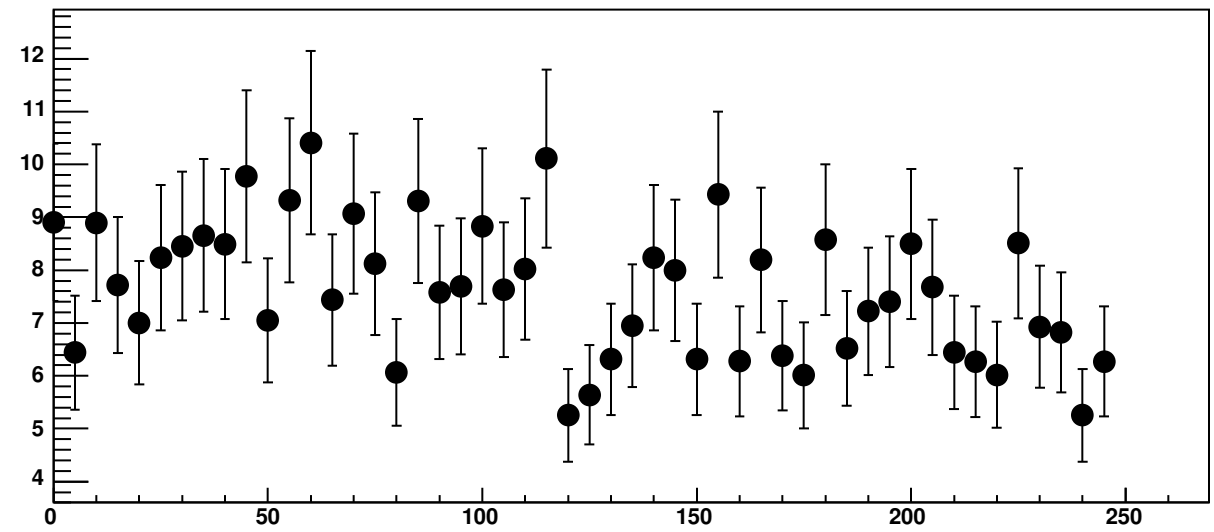
Chip 2, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold



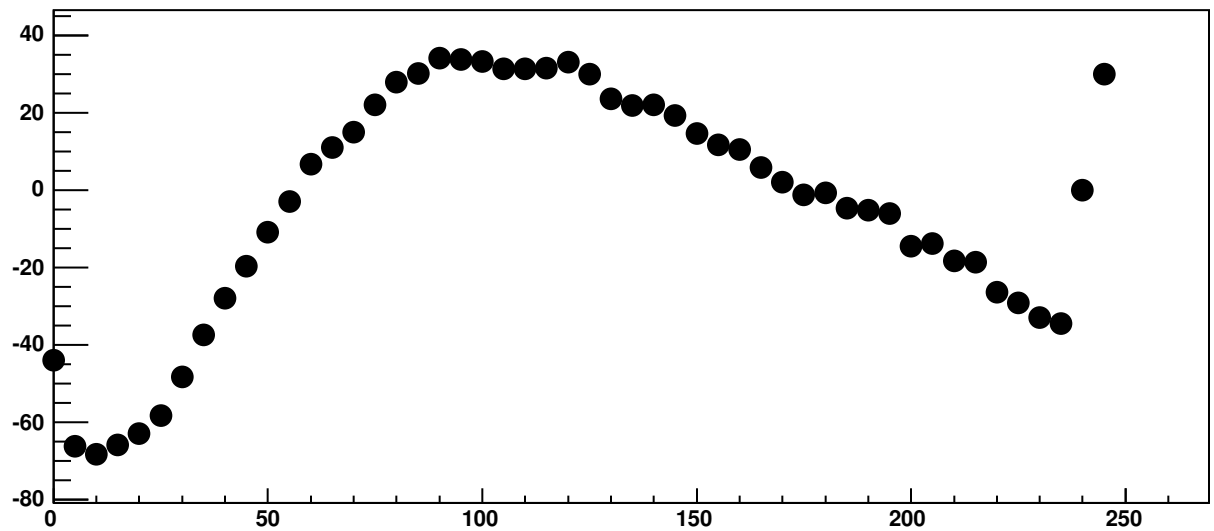
Chip 2, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold



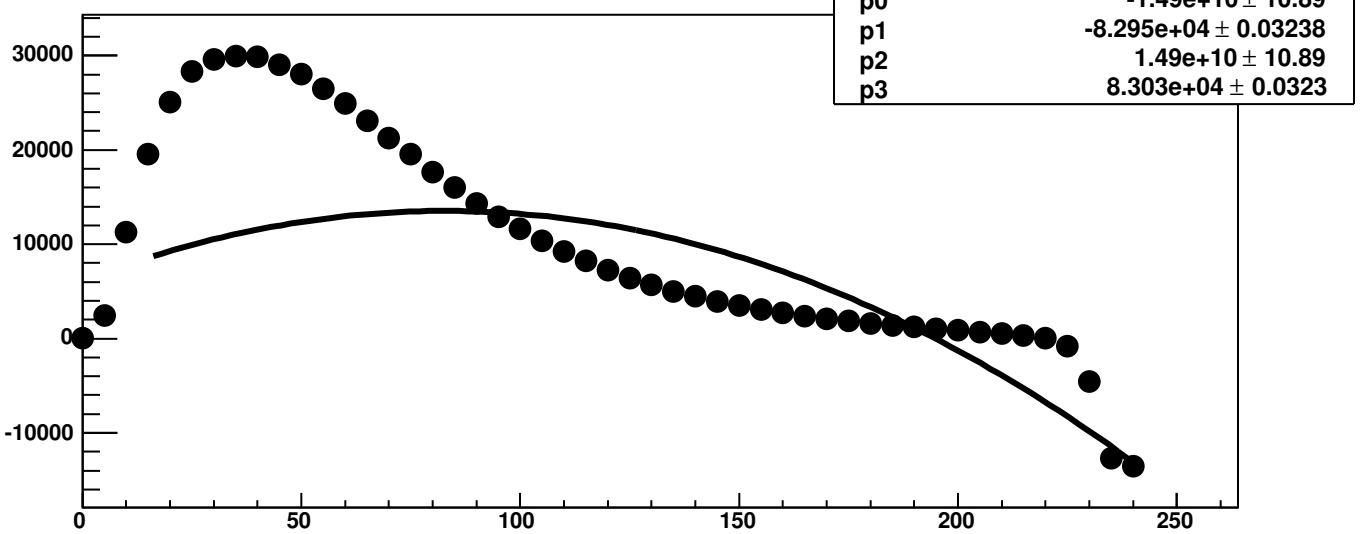
Chip 2, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



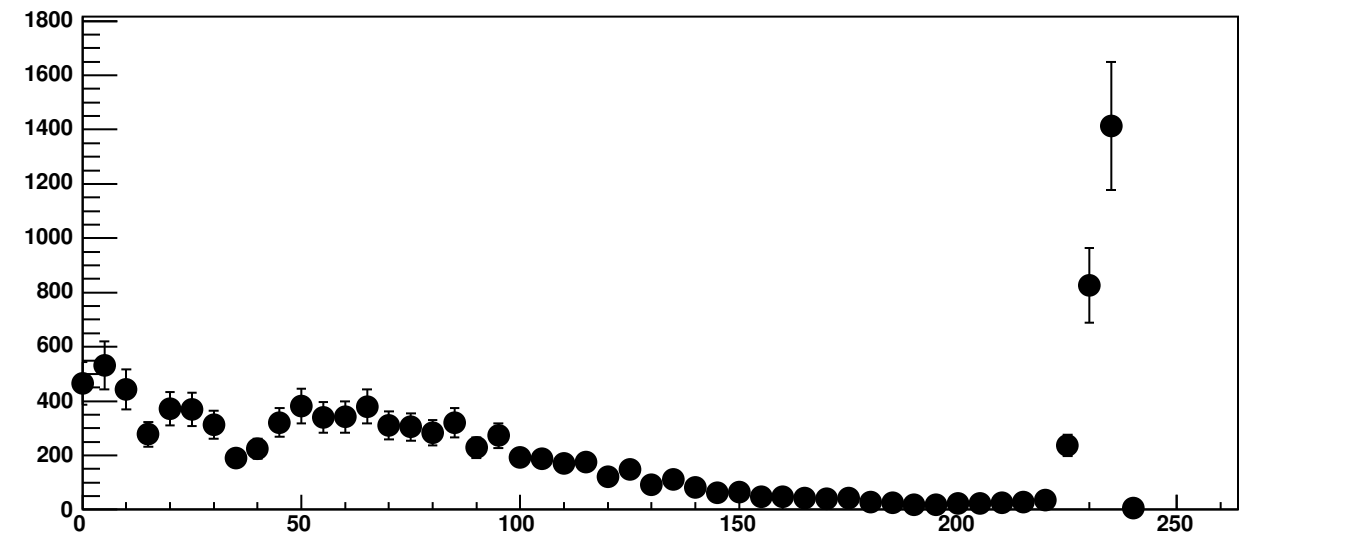
Chip 2, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold



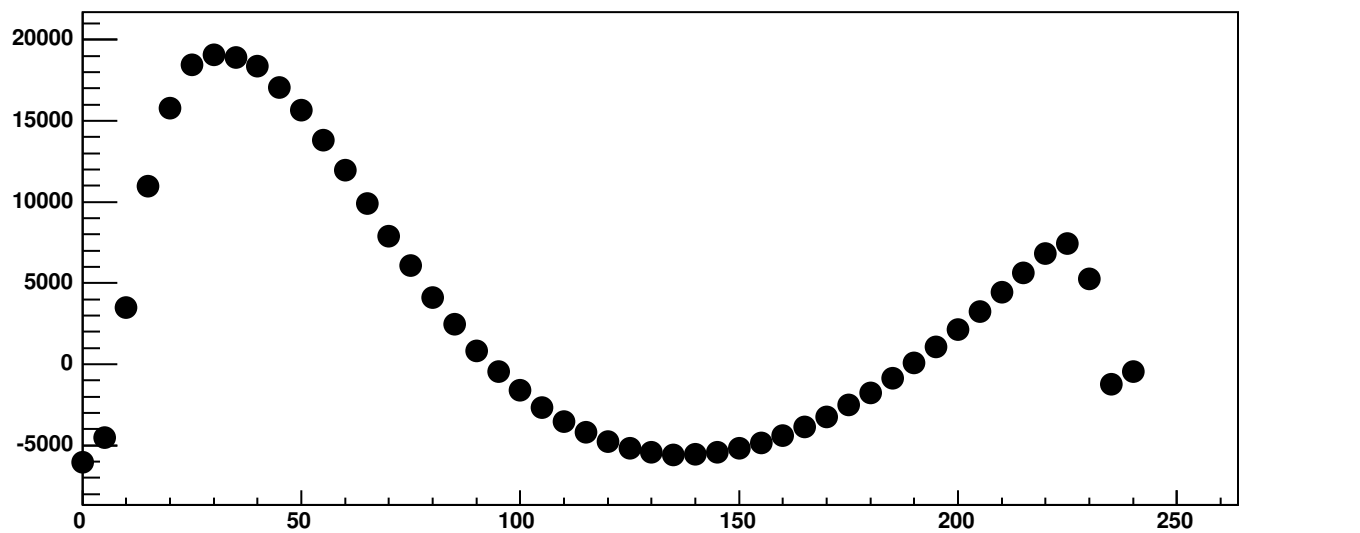
Chip 2, Channel 7, Enable 5!, DAC=4000, ADC Mean vs Hold



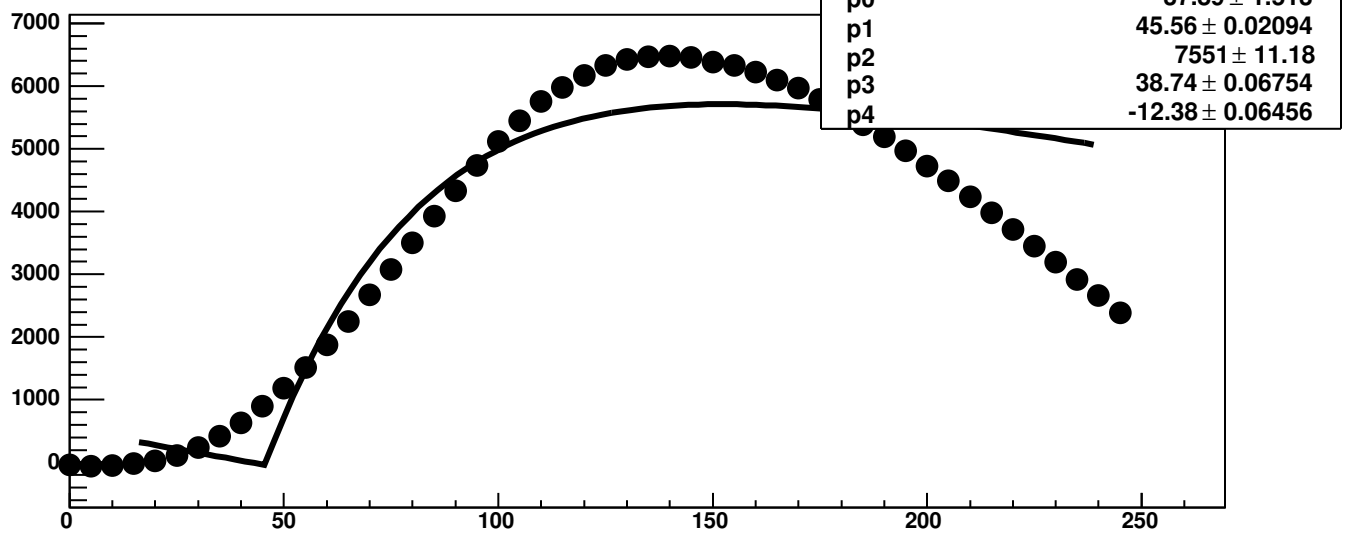
Chip 2, Channel 7, Enable 5!, DAC=4000, ADC Noise vs Hold



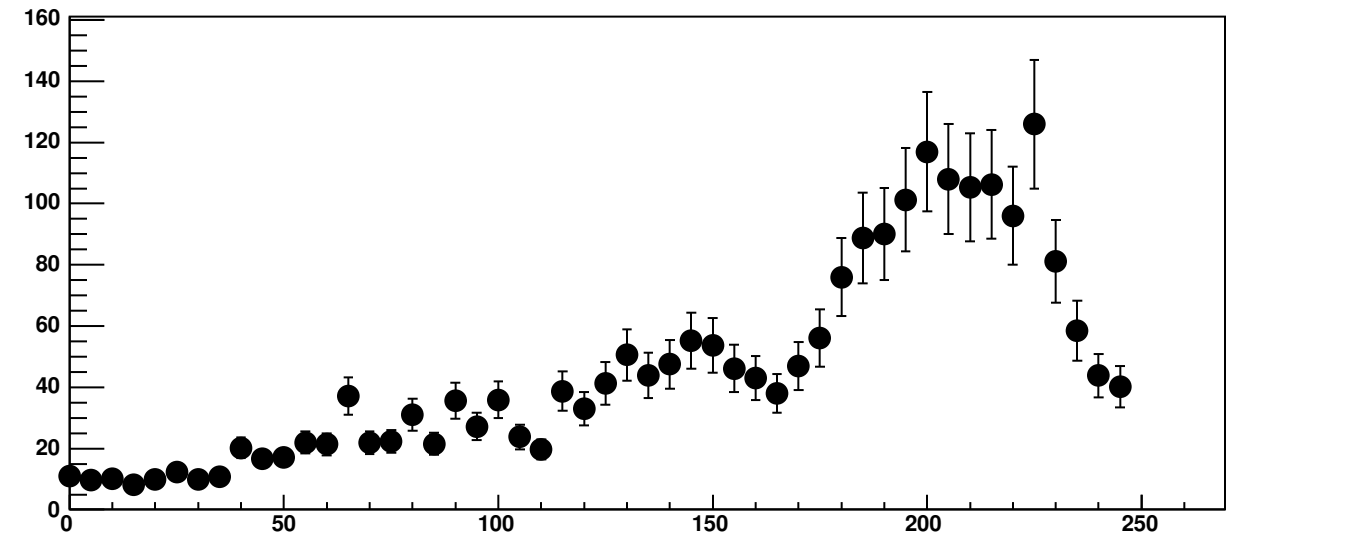
Chip 2, Channel 7, Enable 5!, DAC=4000, ADC Residuals vs Hold



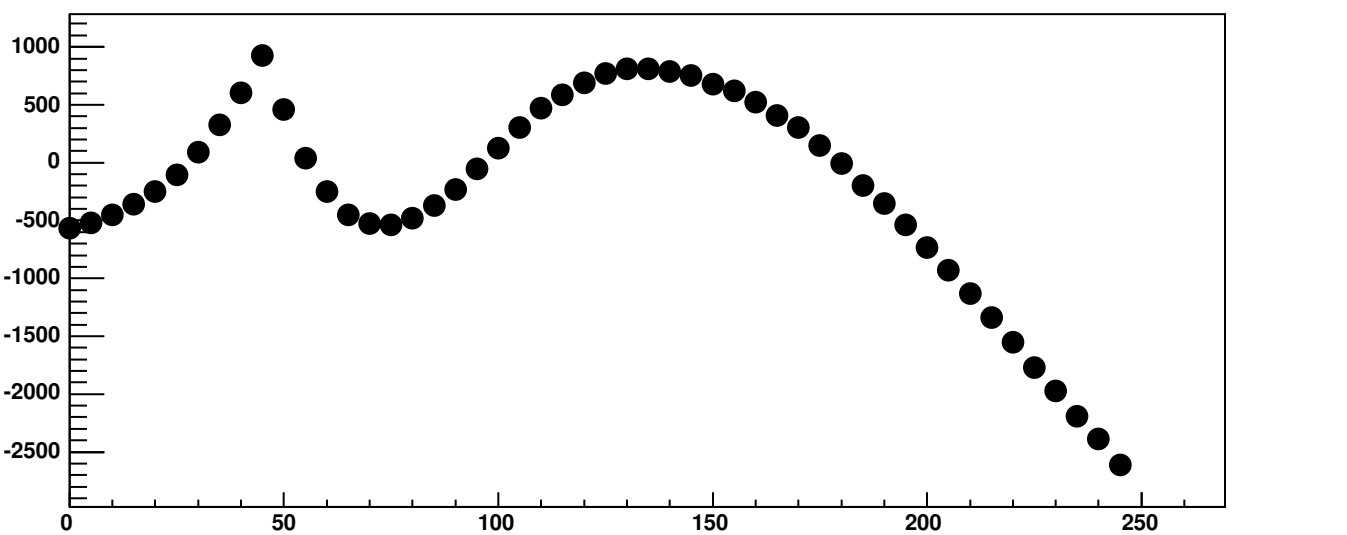
Chip 2, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold



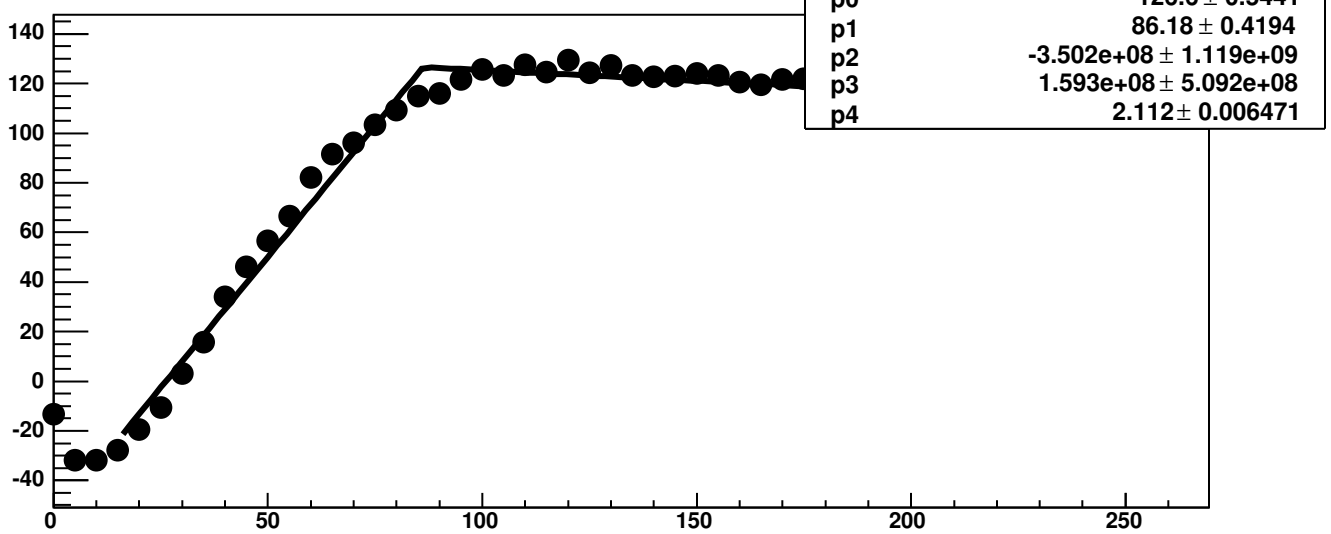
Chip 2, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



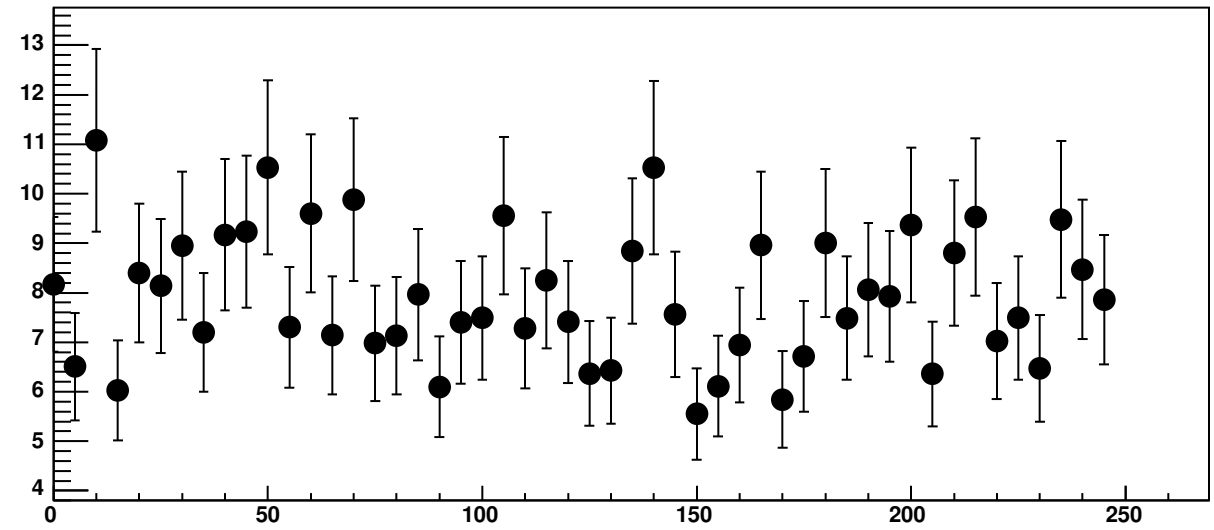
Chip 2, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold



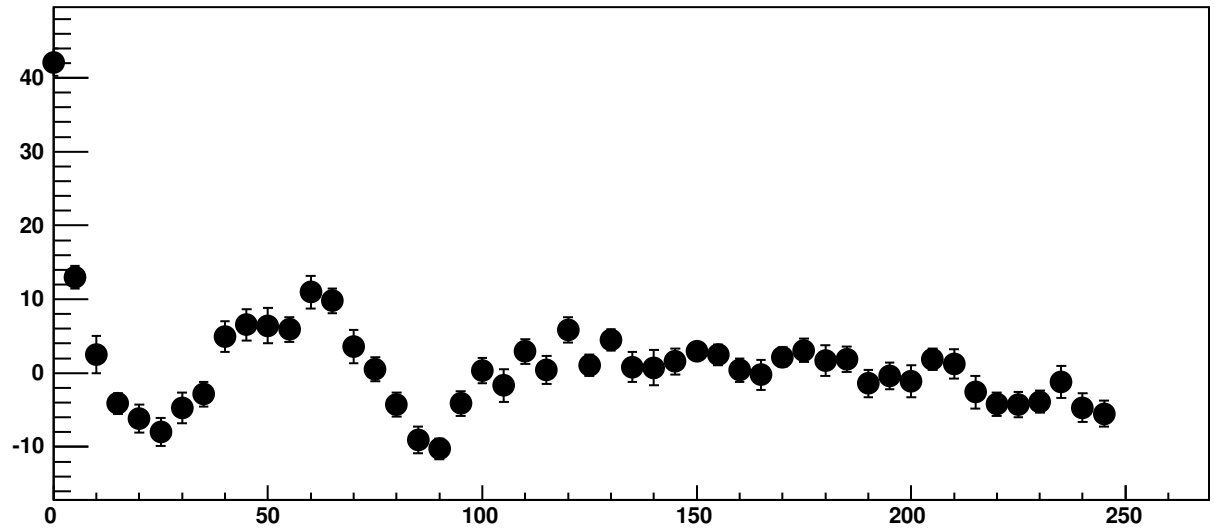
Chip 2, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold



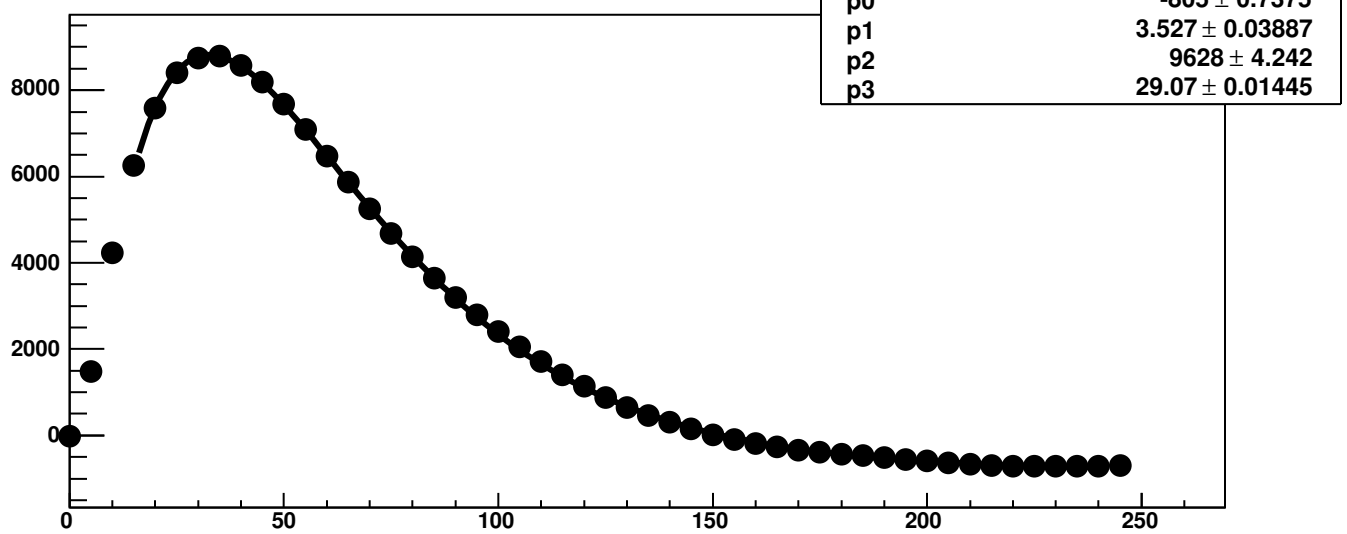
Chip 2, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



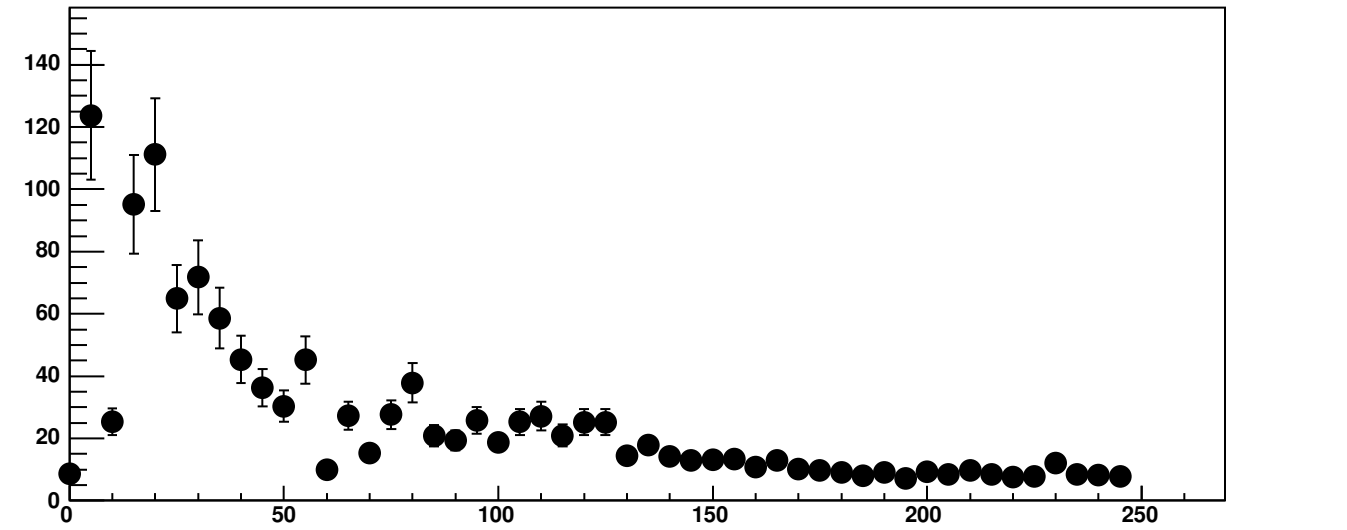
Chip 2, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold



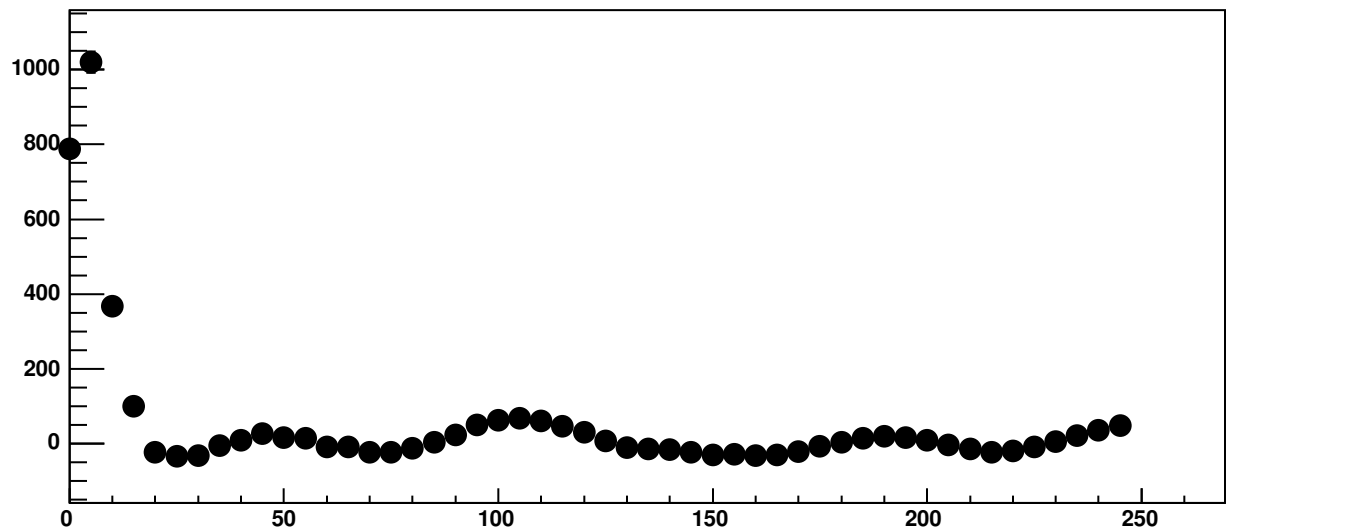
Chip 2, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold



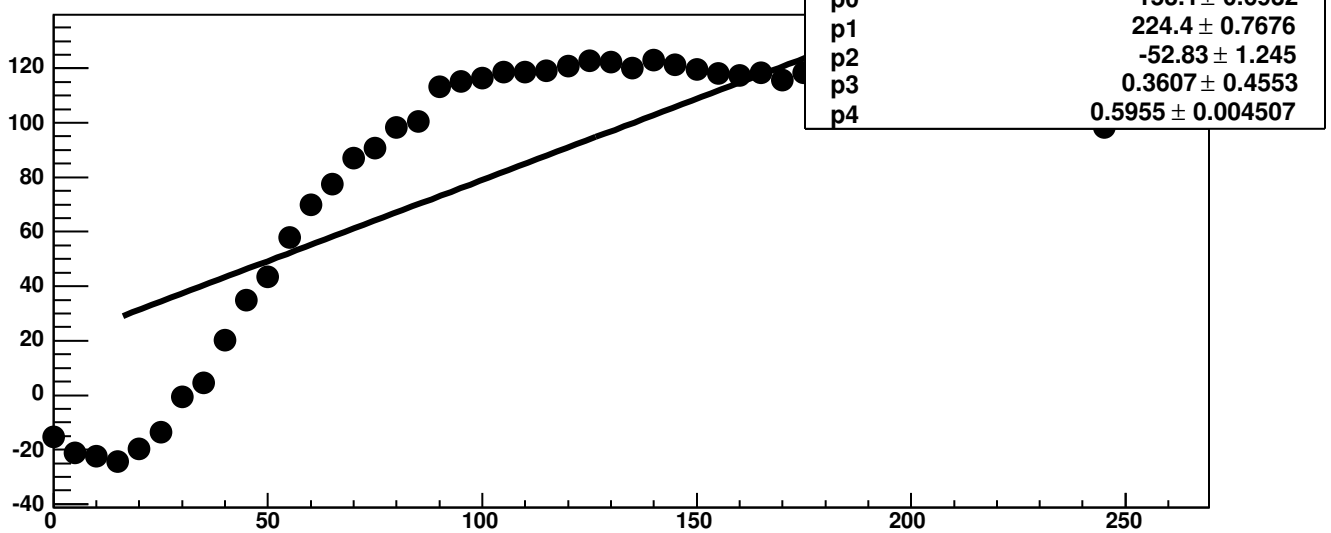
Chip 2, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold



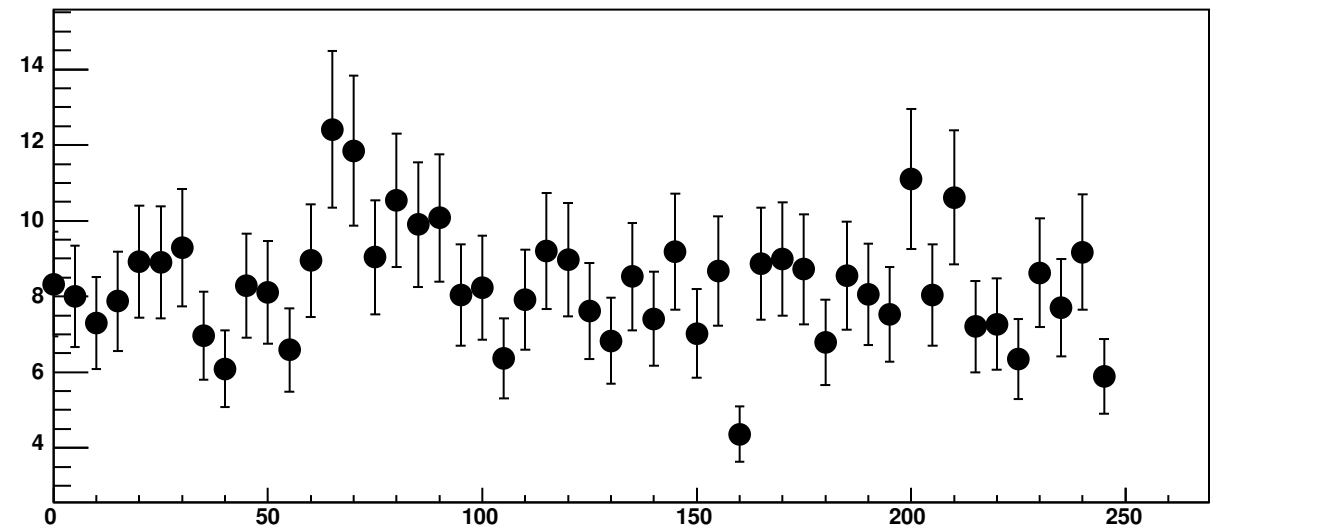
Chip 2, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold



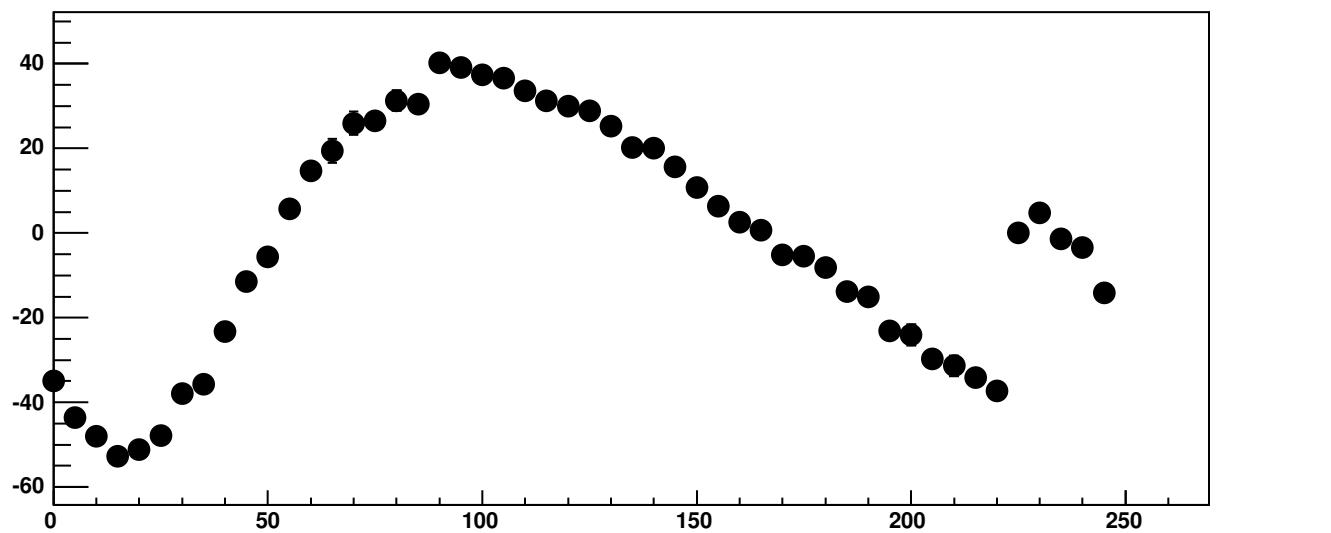
Chip 2, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold



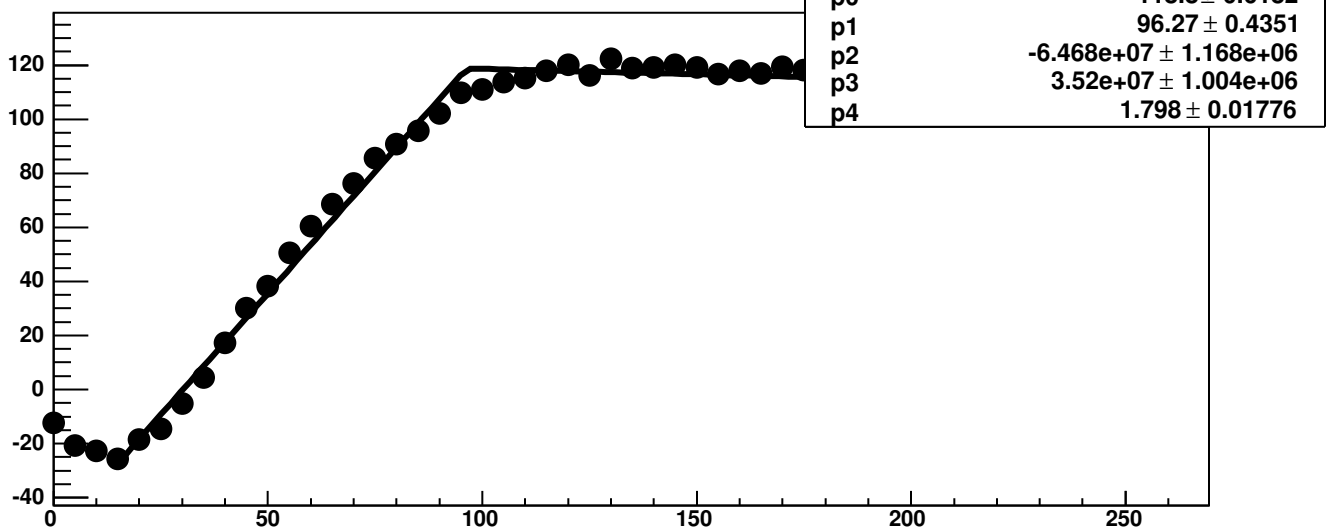
Chip 2, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



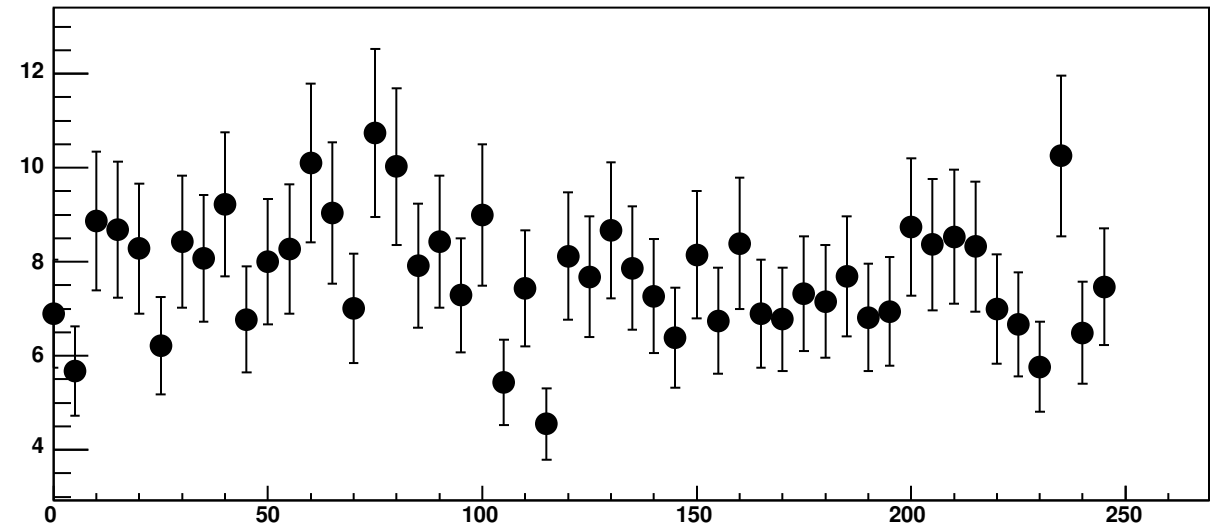
Chip 2, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold



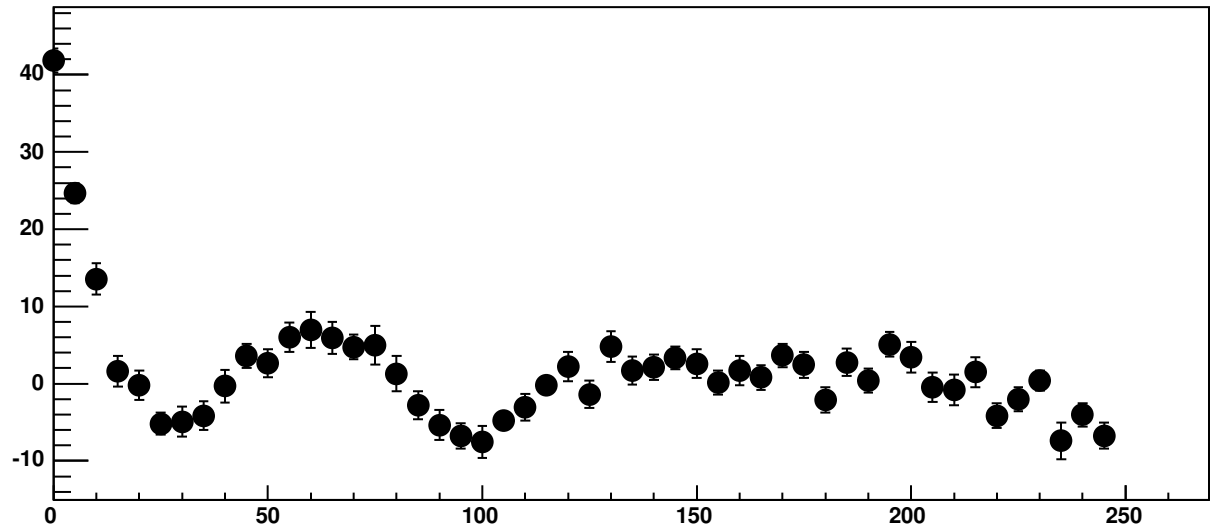
Chip 2, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold



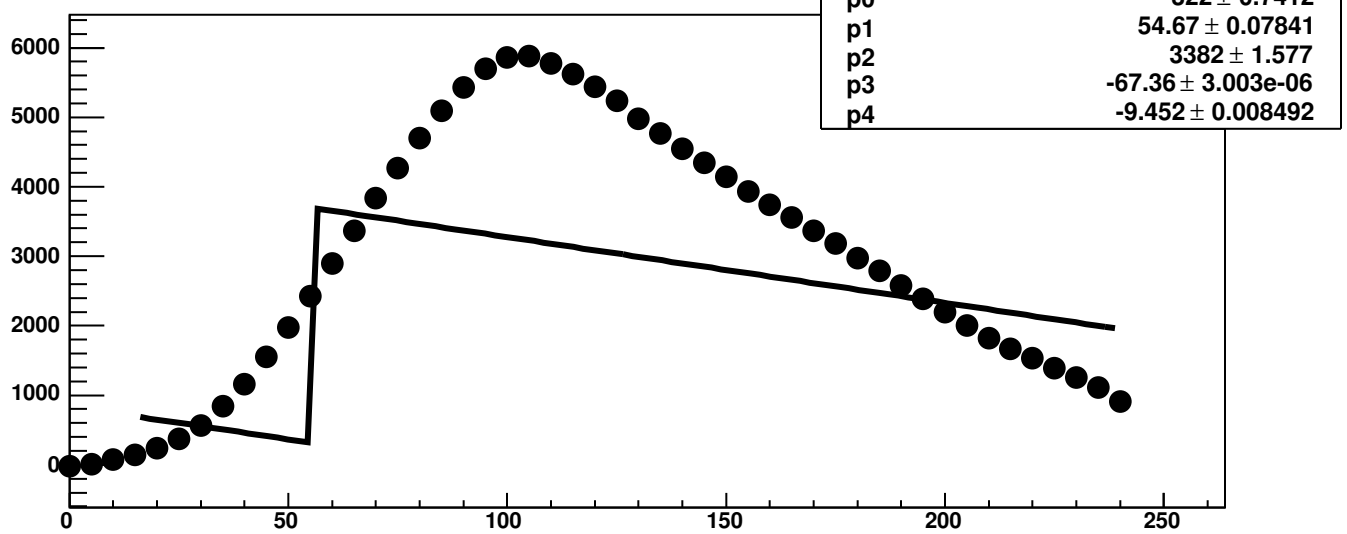
Chip 2, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



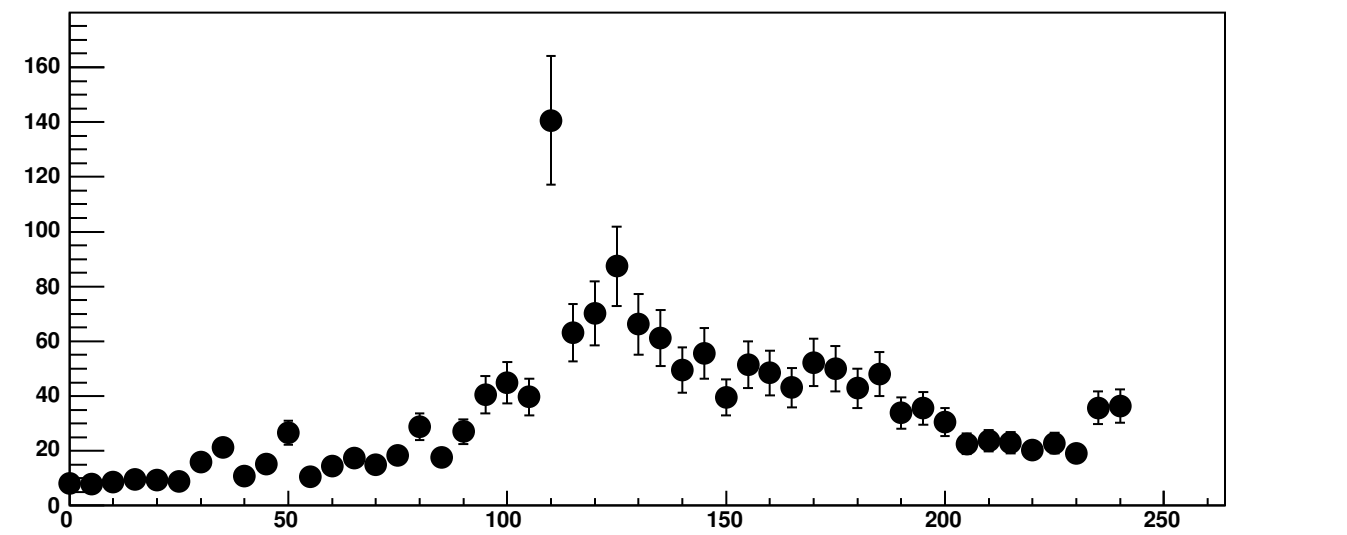
Chip 2, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold



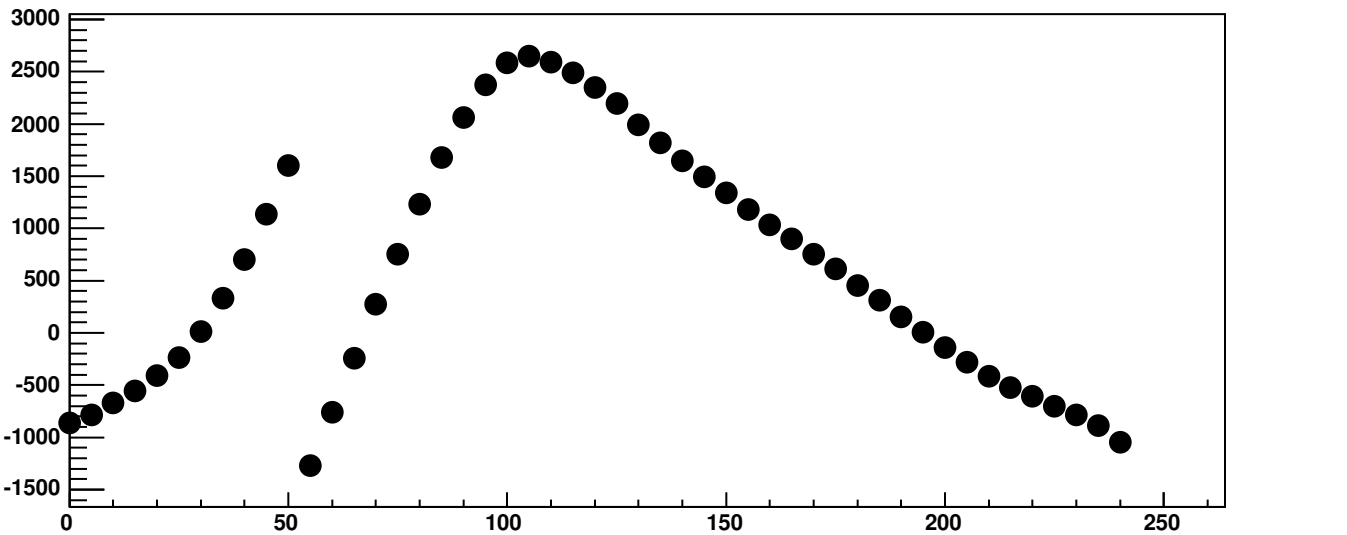
Chip 2, Channel 8, Enable 5, DAC=4000, ADC Mean vs Hold



Chip 2, Channel 8, Enable 5, DAC=4000, ADC Noise vs Hold

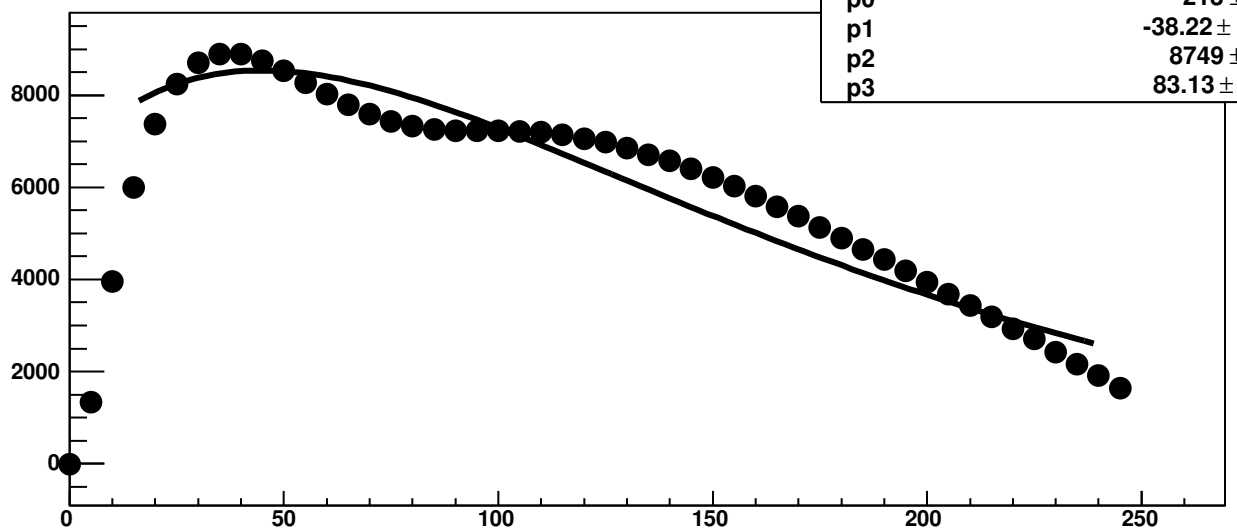


Chip 2, Channel 8, Enable 5, DAC=4000, ADC Residuals vs Hold

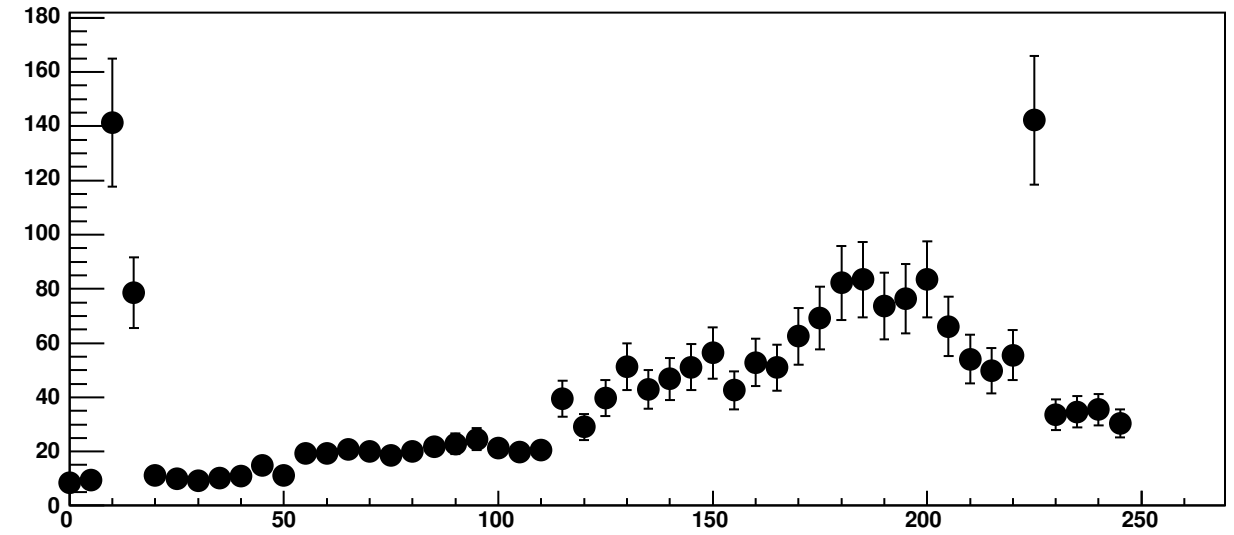


Chip 2, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold

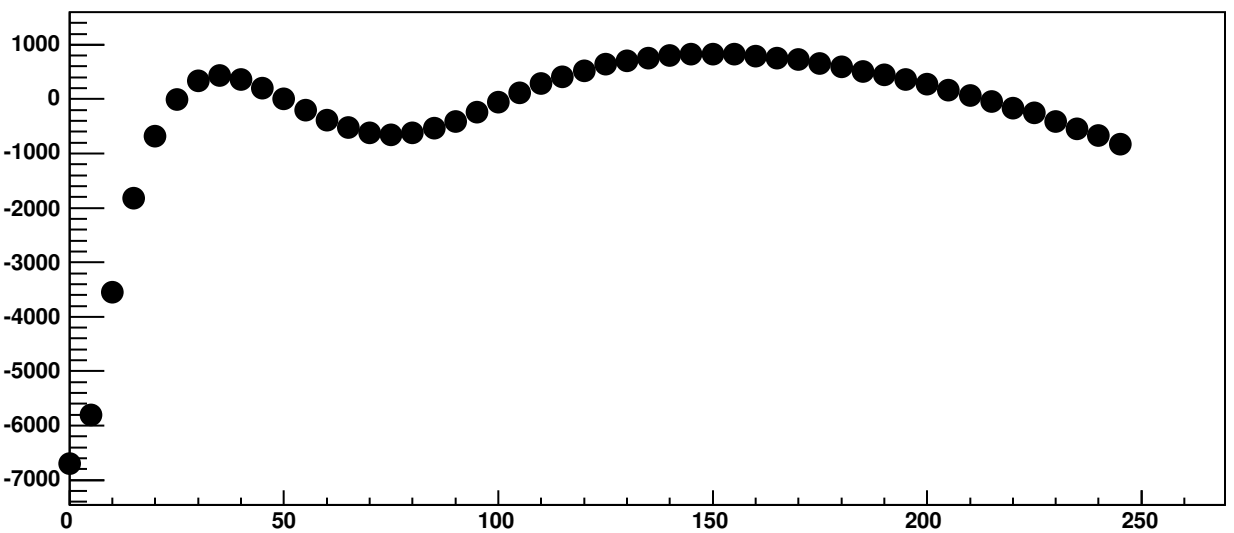
χ^2 / ndf	3.443e+05 / 42
p0	-213 ± 23.19
p1	-38.22 ± 0.2437
p2	8749 ± 22.39
p3	83.13 ± 0.2311



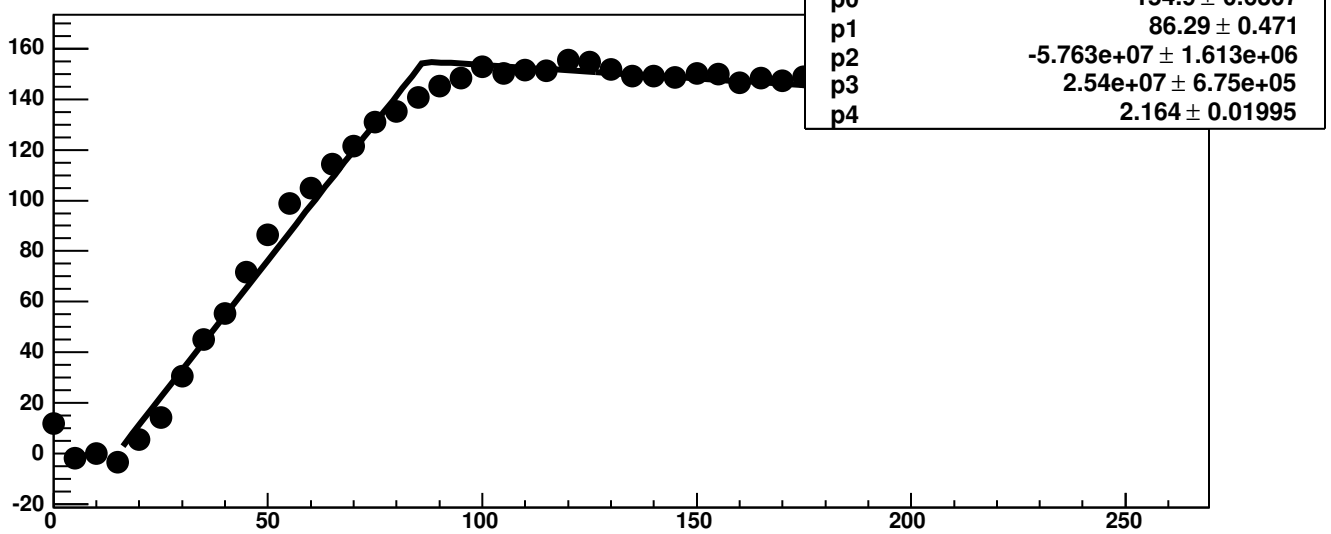
Chip 2, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold



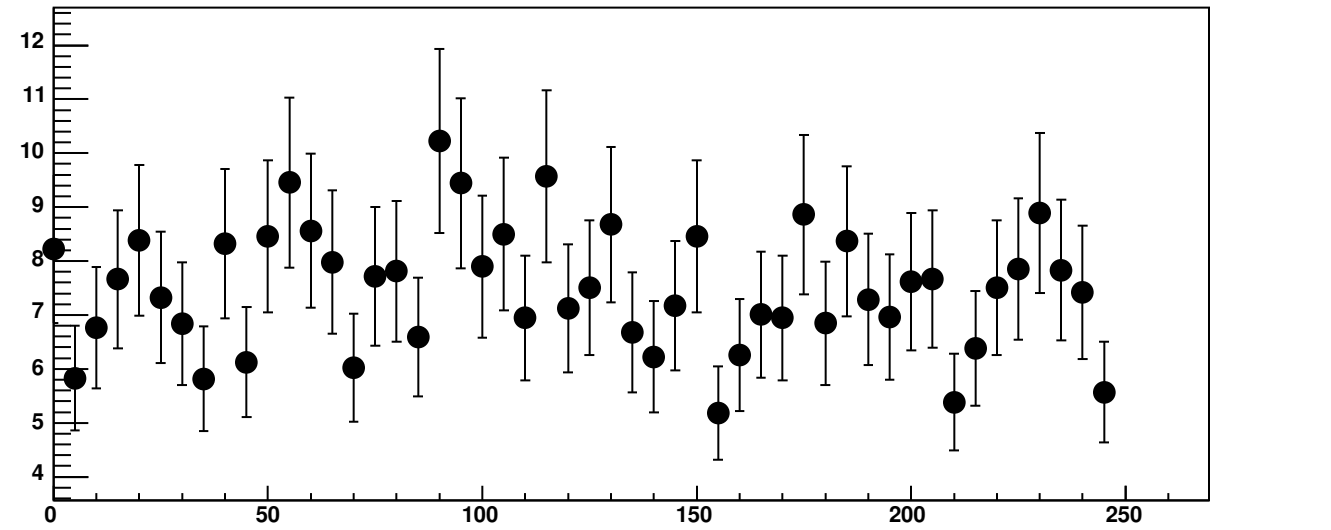
Chip 2, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold



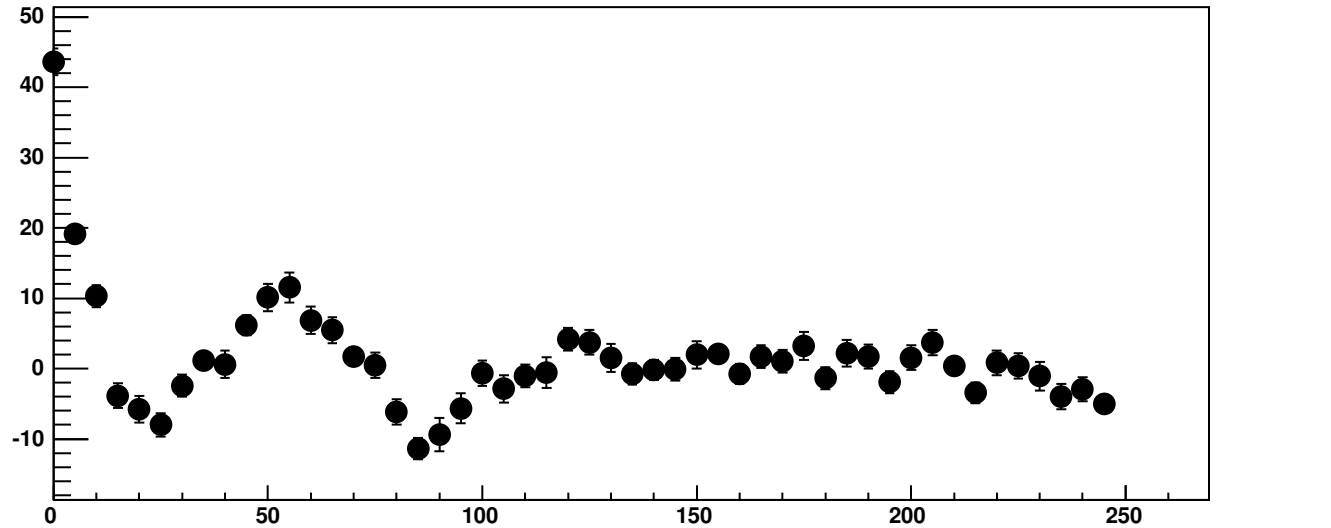
Chip 2, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold



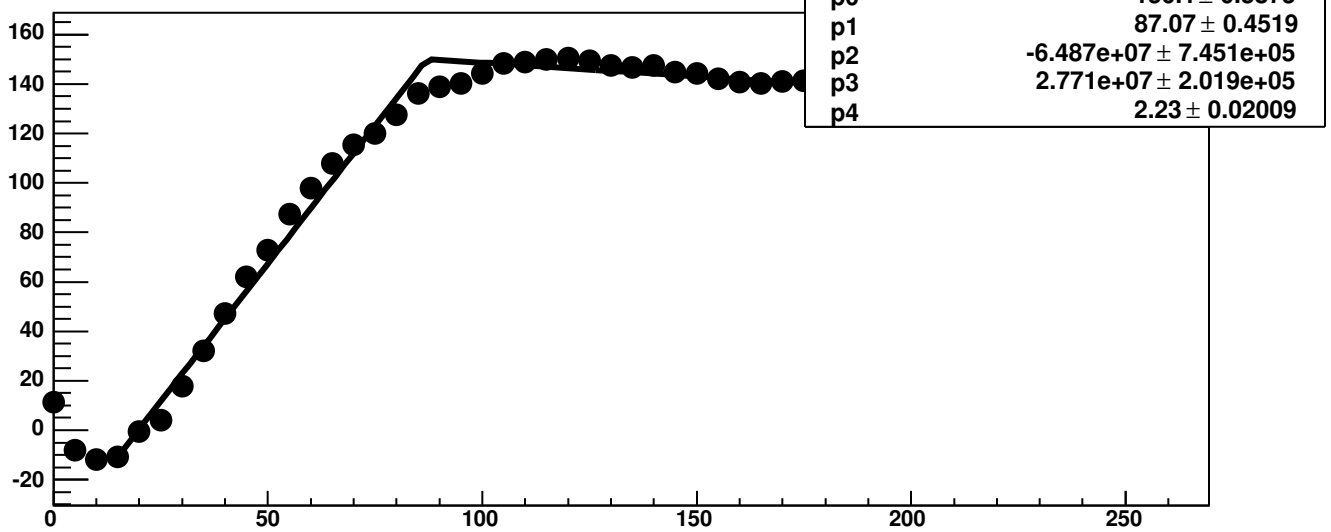
Chip 2, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



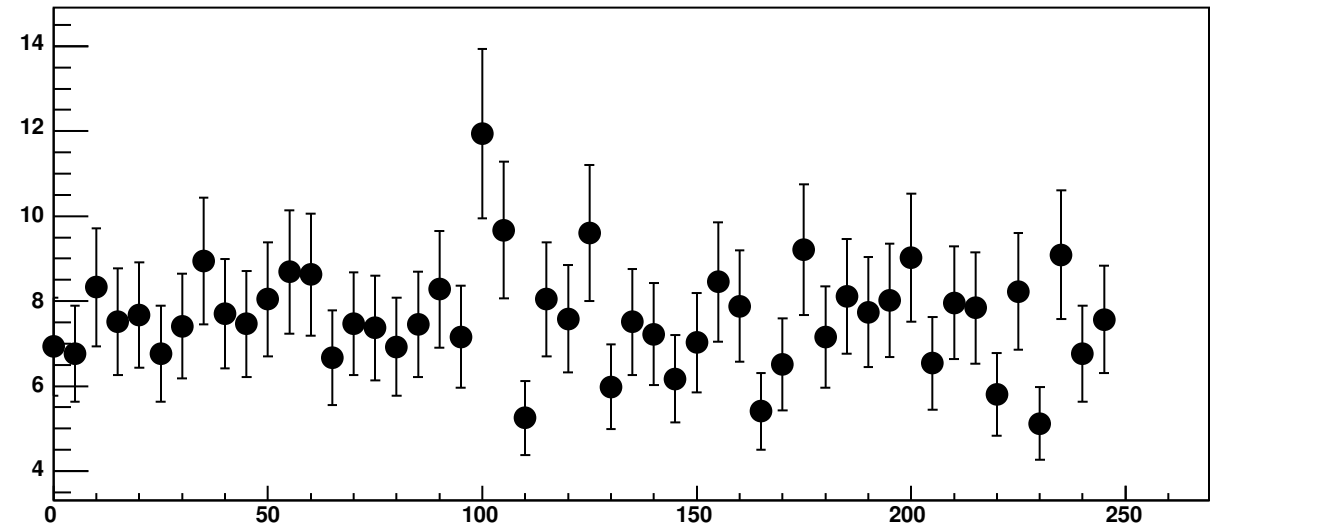
Chip 2, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold



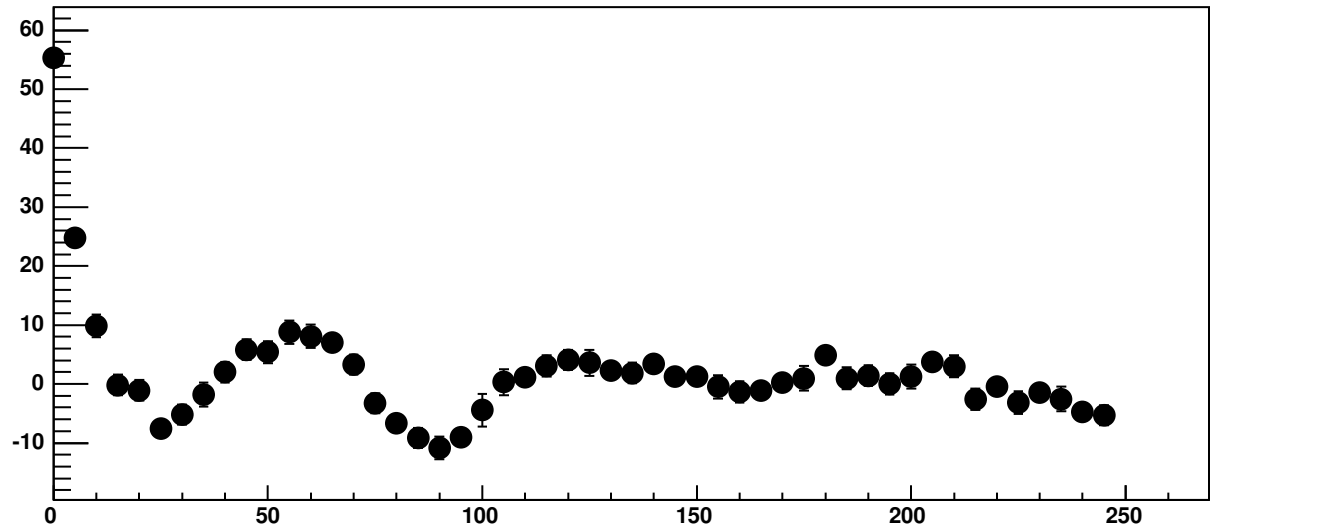
Chip 2, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold



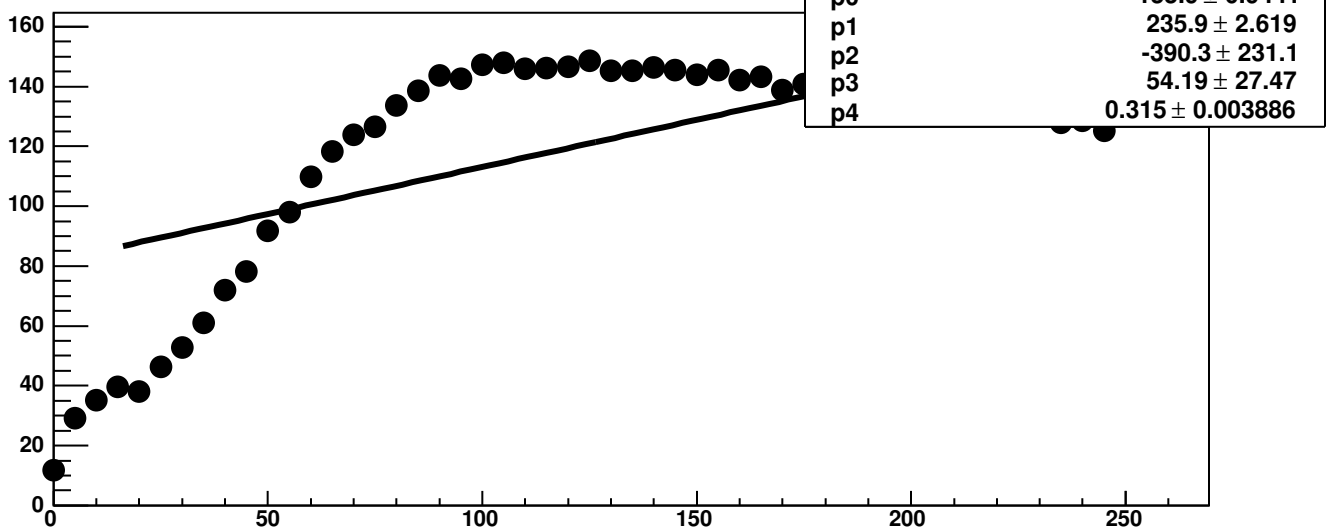
Chip 2, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



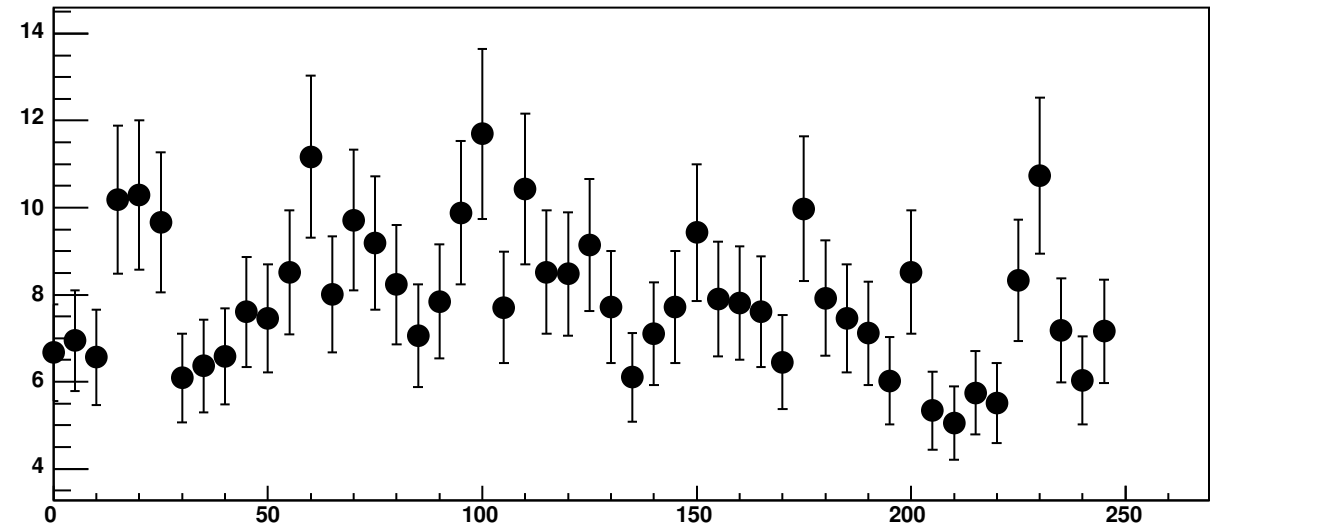
Chip 2, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold



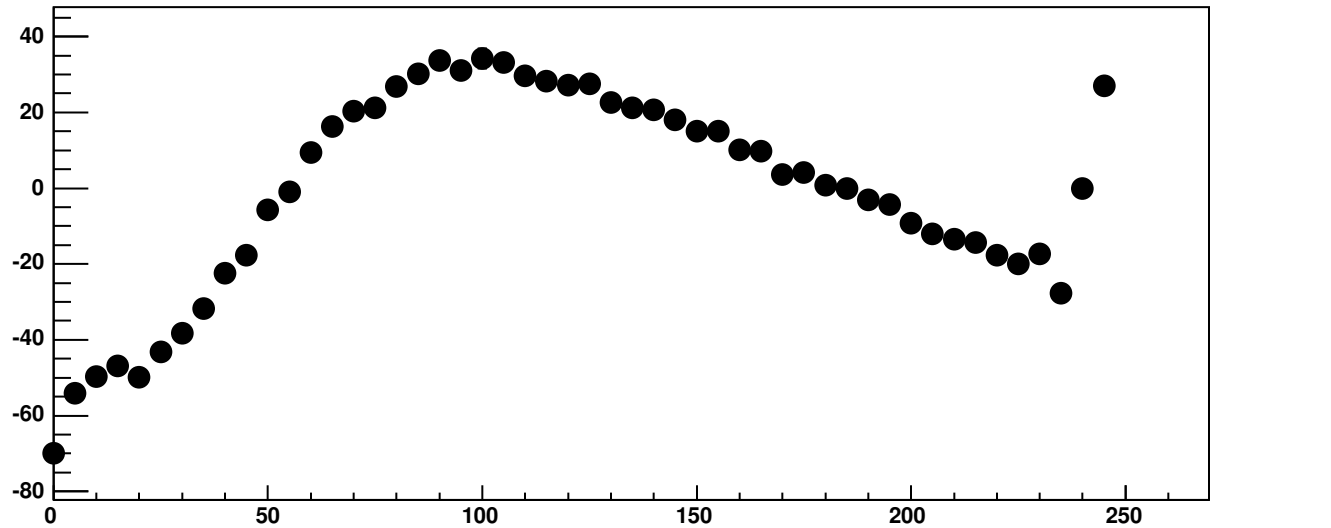
Chip 2, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold



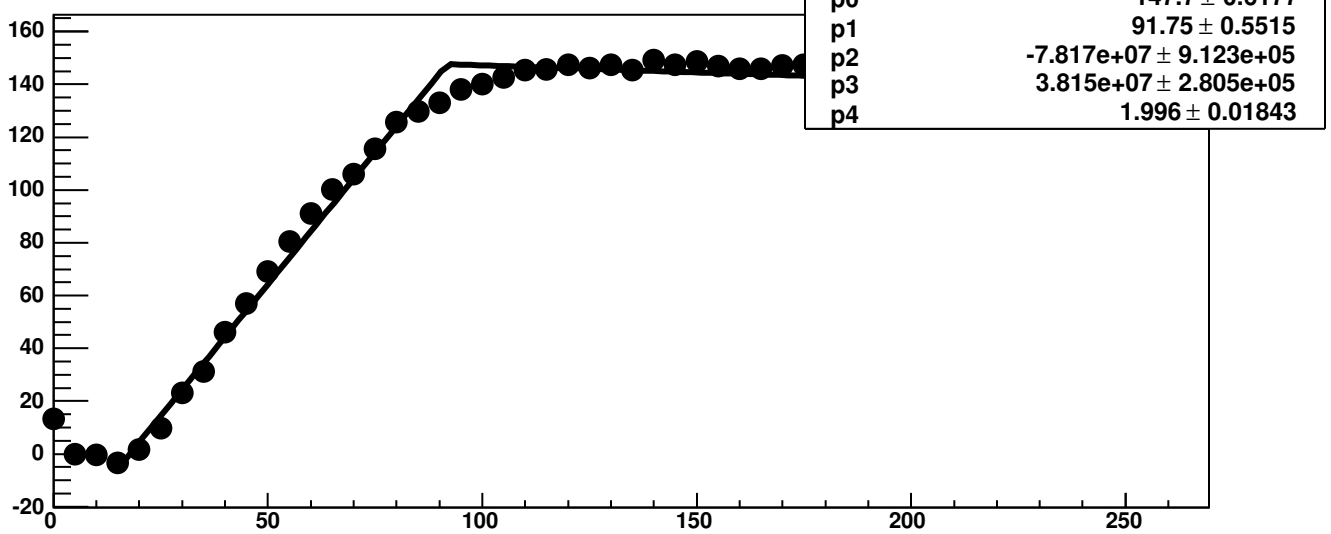
Chip 2, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold



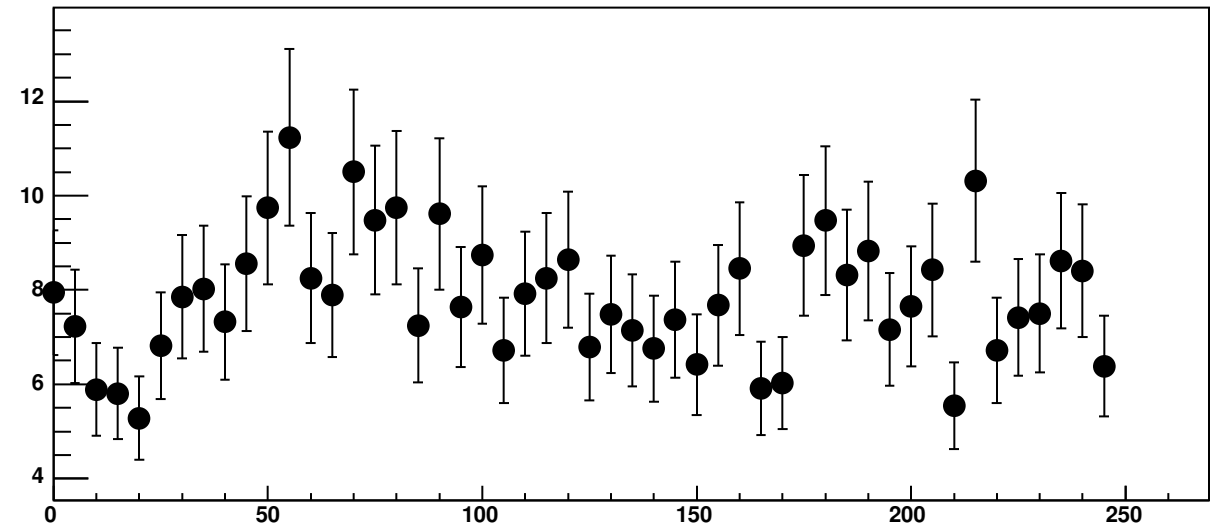
Chip 2, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold



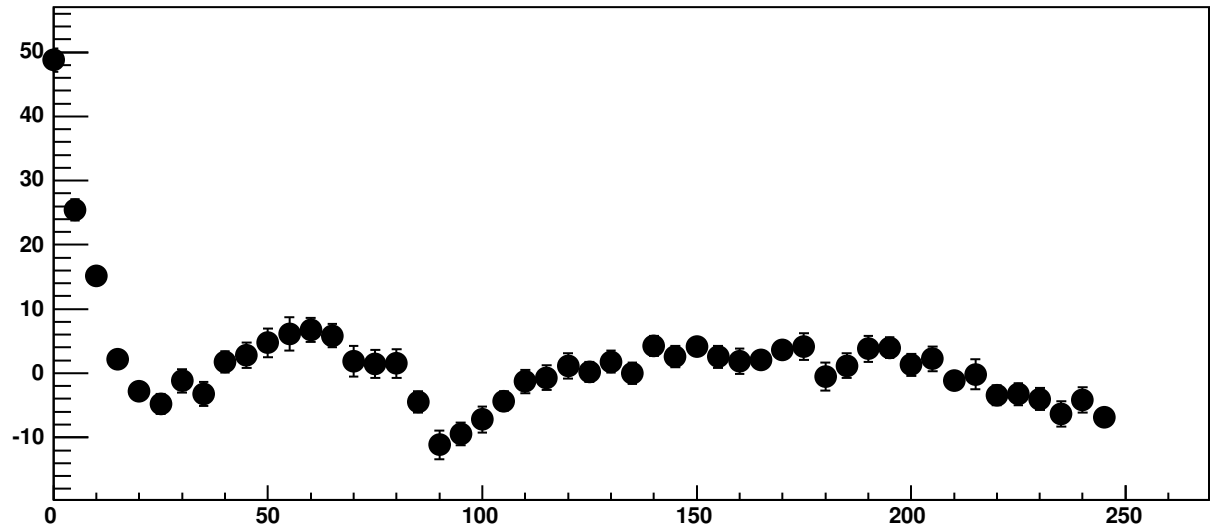
Chip 2, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

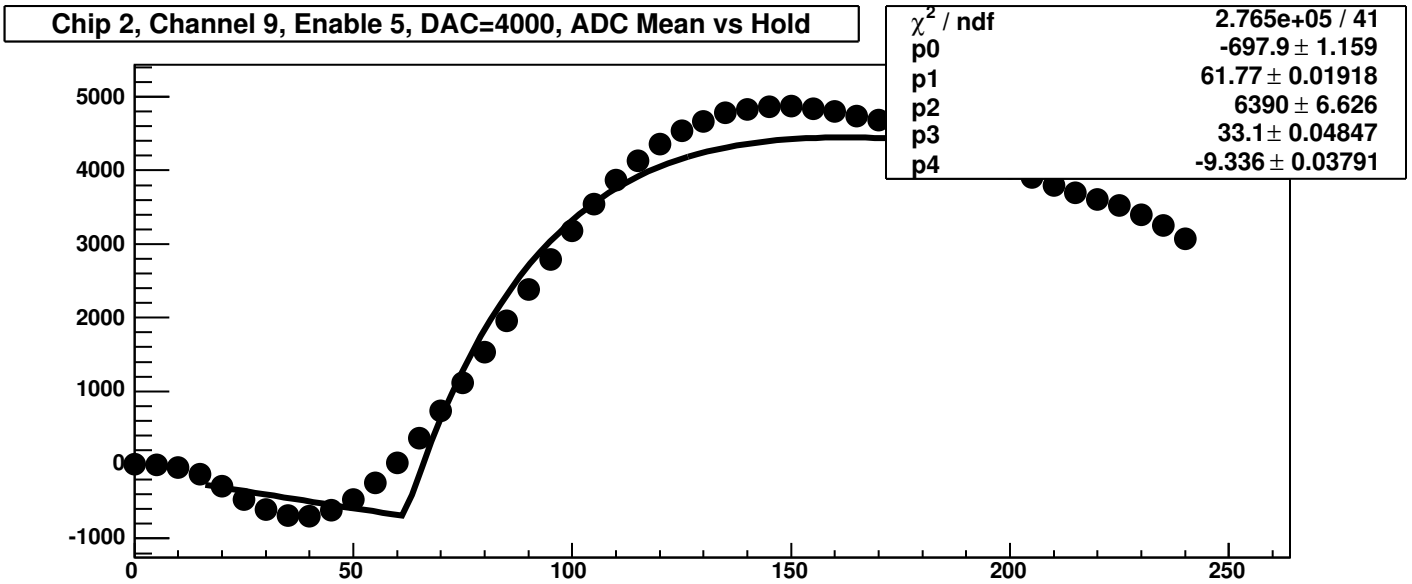


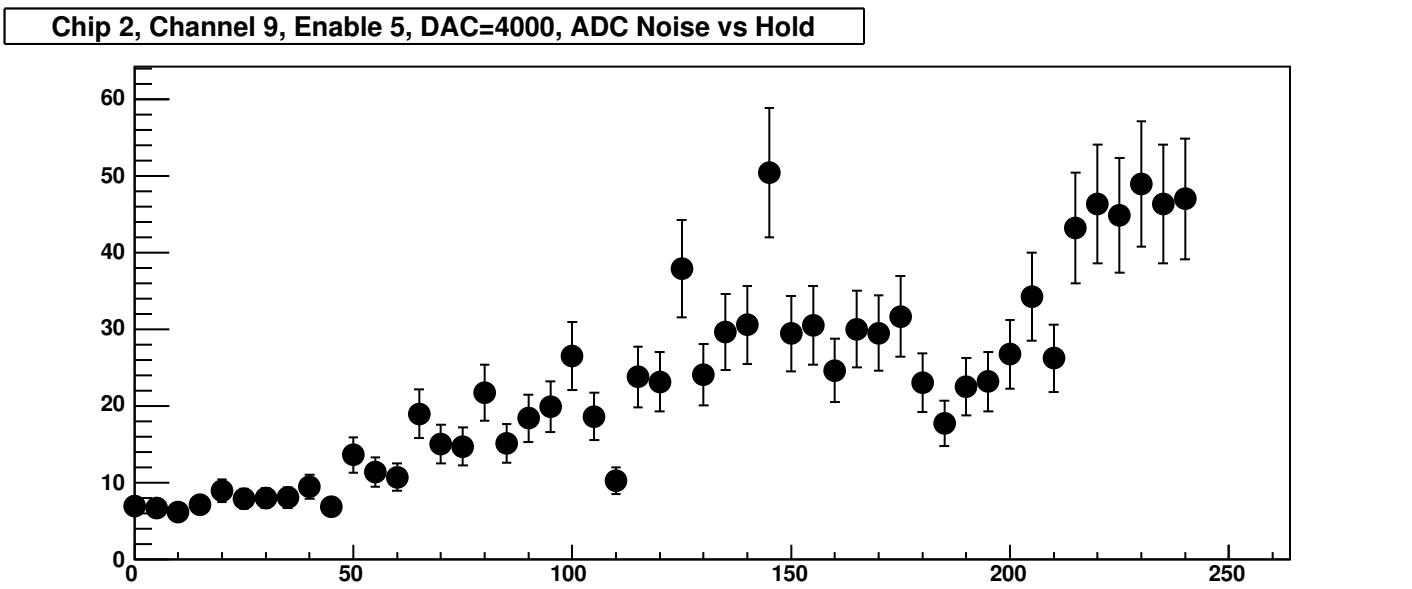
Chip 2, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold

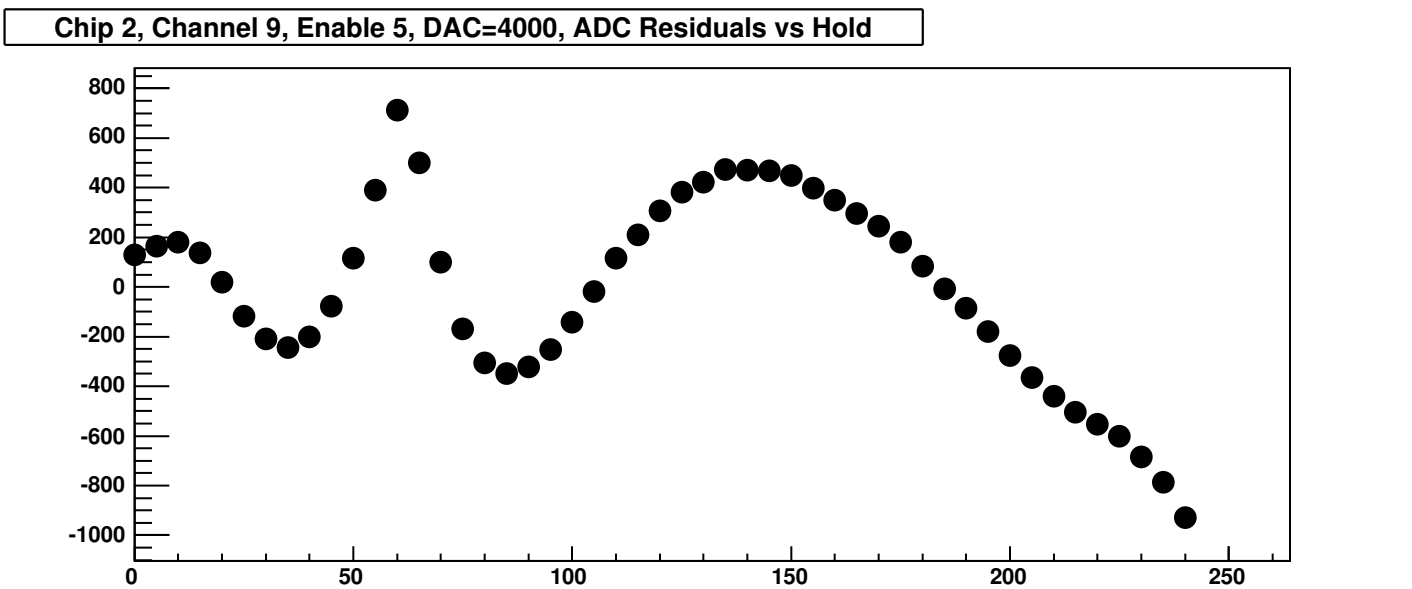


Chip 2, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

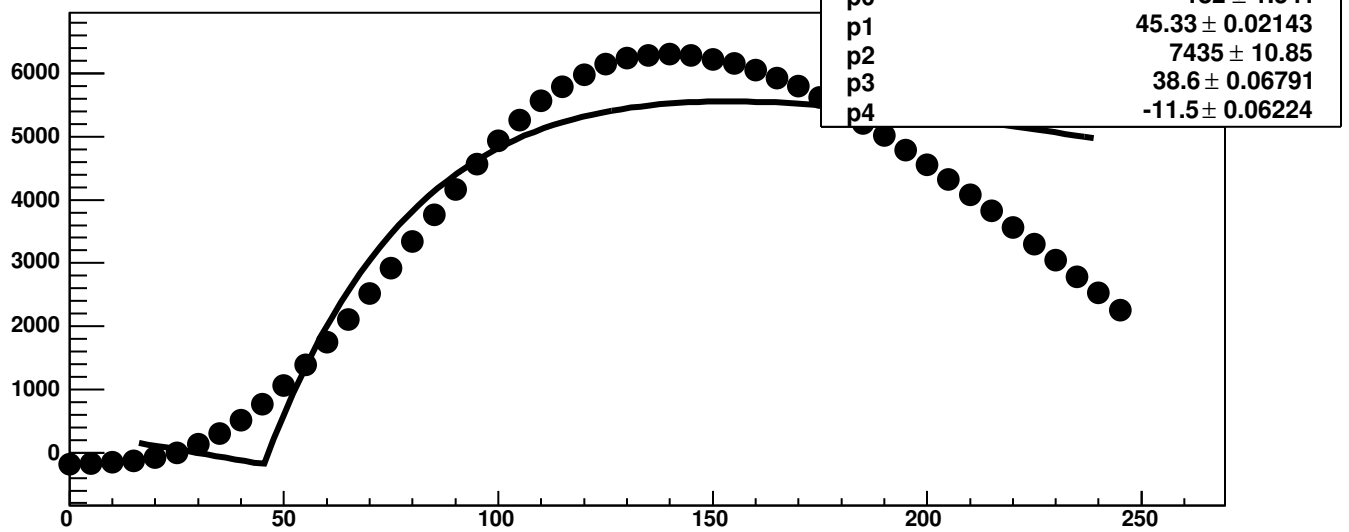



Chip 2, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.765e+05 / 41
p0 -697.9 ± 1.159
p1 61.77 ± 0.01918
p2 6390 ± 6.626
p3 33.1 ± 0.04847
p4 -9.336 ± 0.03791
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

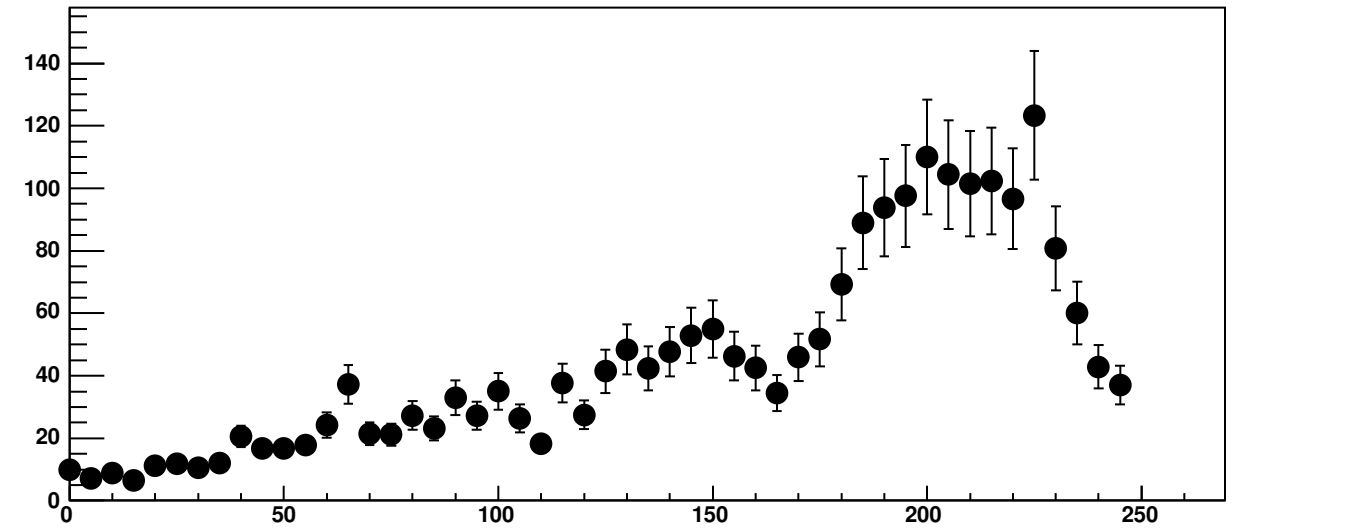

Chip 2, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold
60
50
40
30
20
10
0
0
50
100
150
200
250


Chip 2, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold
800
600
400
200
0
-200
-400
-600
-800
-1000
0
50
100
150
200
250

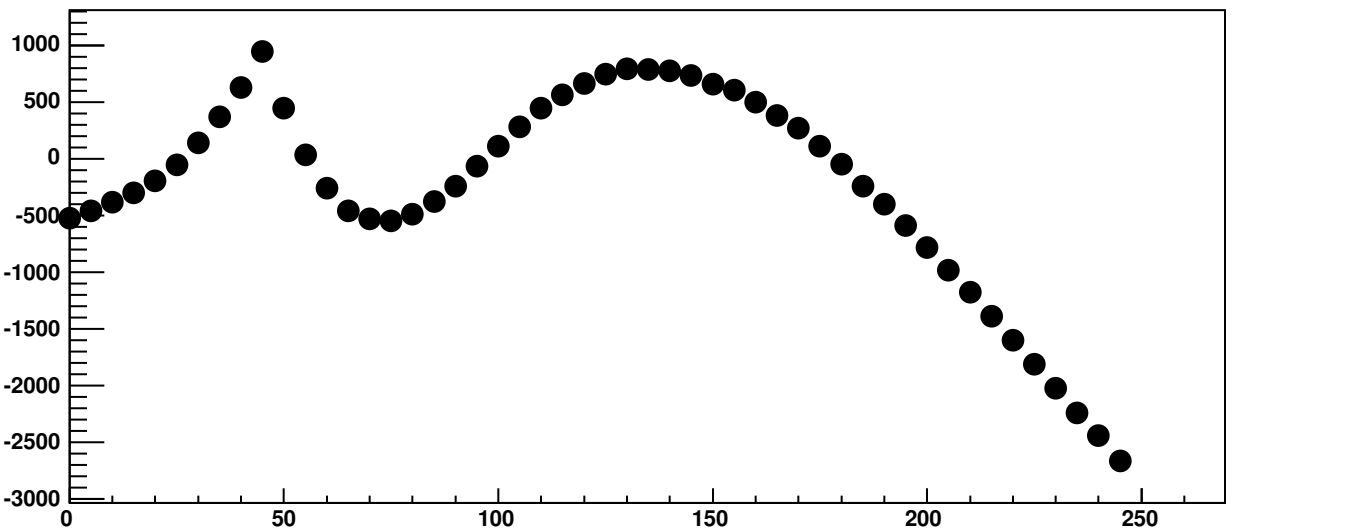
Chip 2, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold



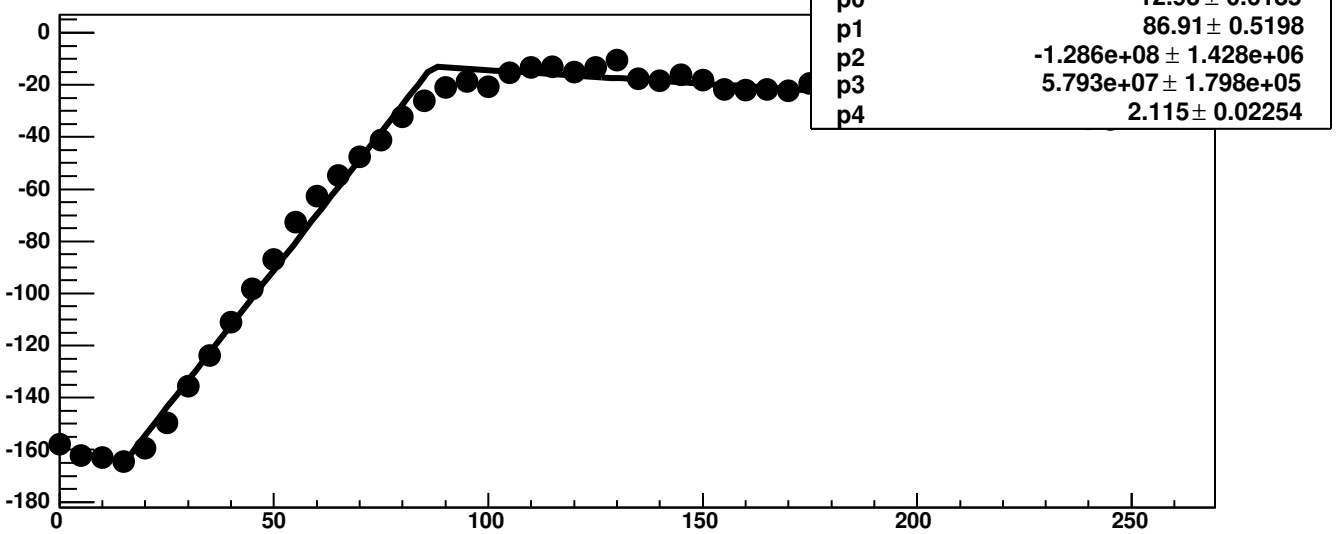
Chip 2, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold



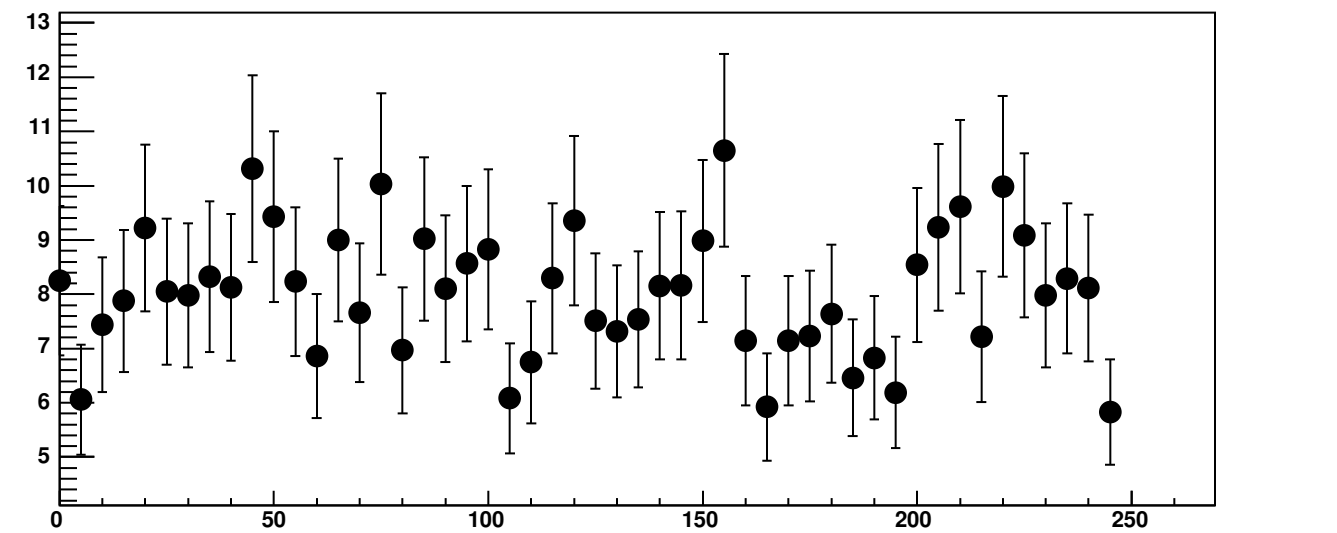
Chip 2, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold



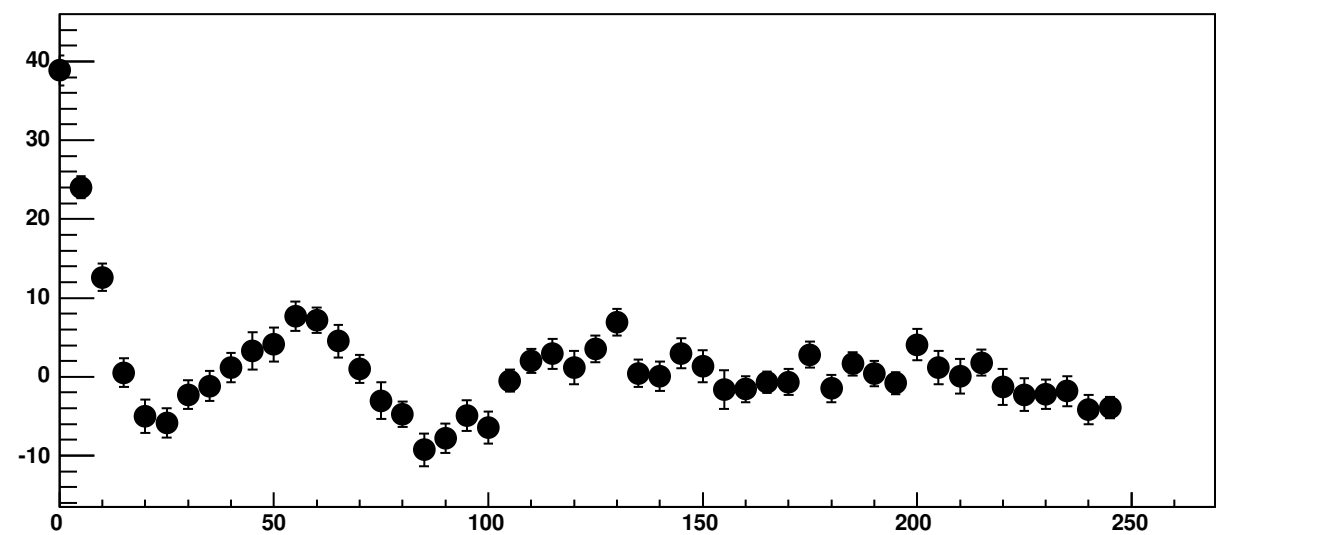
Chip 2, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold



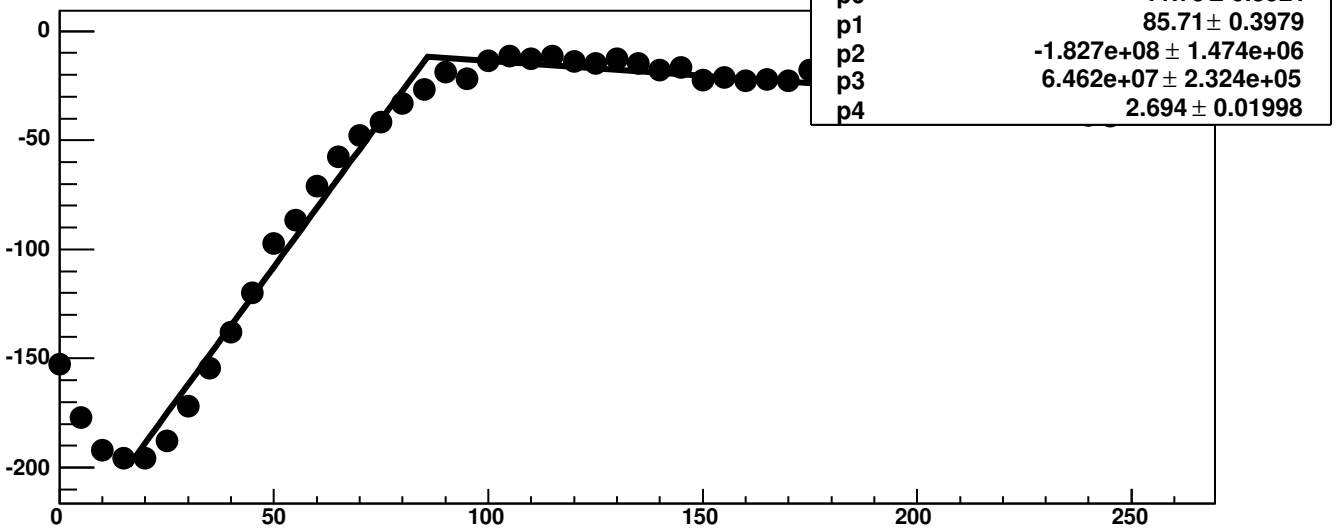
Chip 2, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



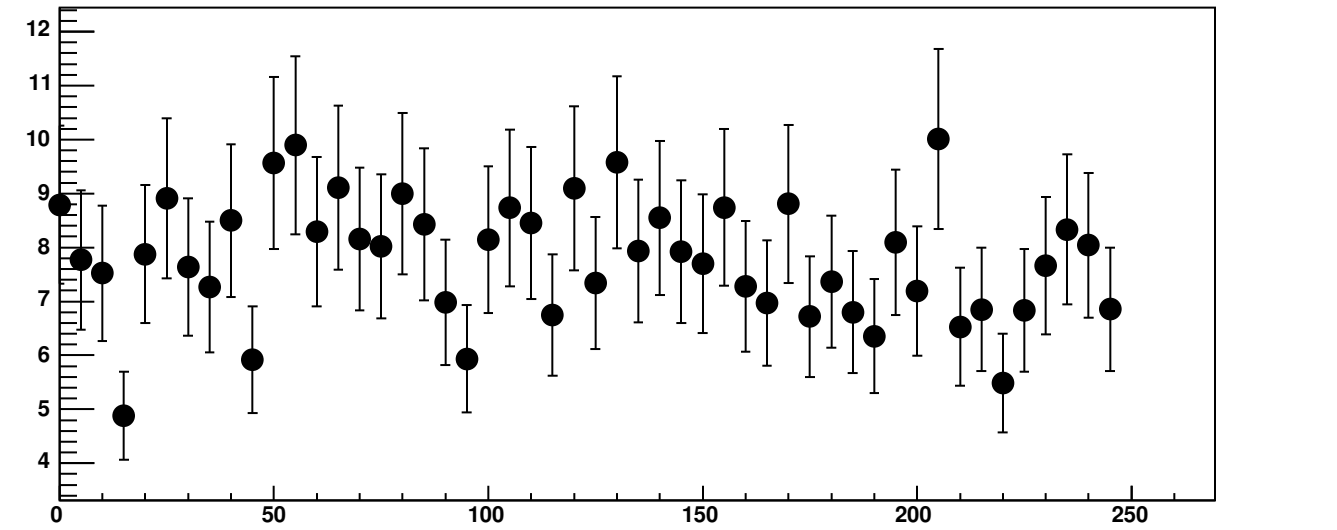
Chip 2, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold



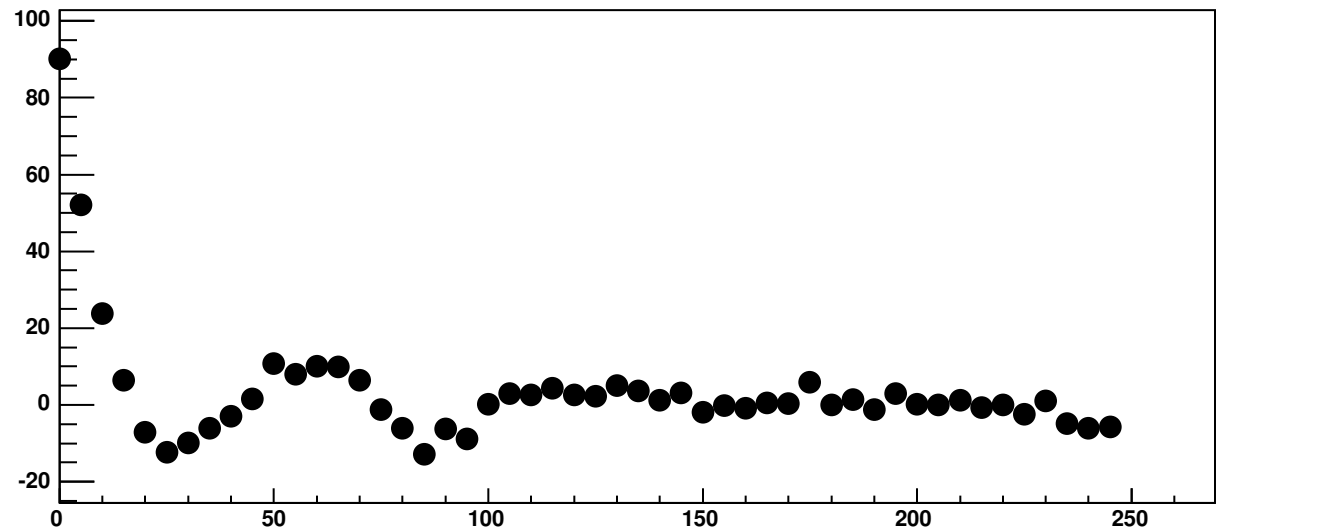
Chip 2, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold



Chip 2, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold

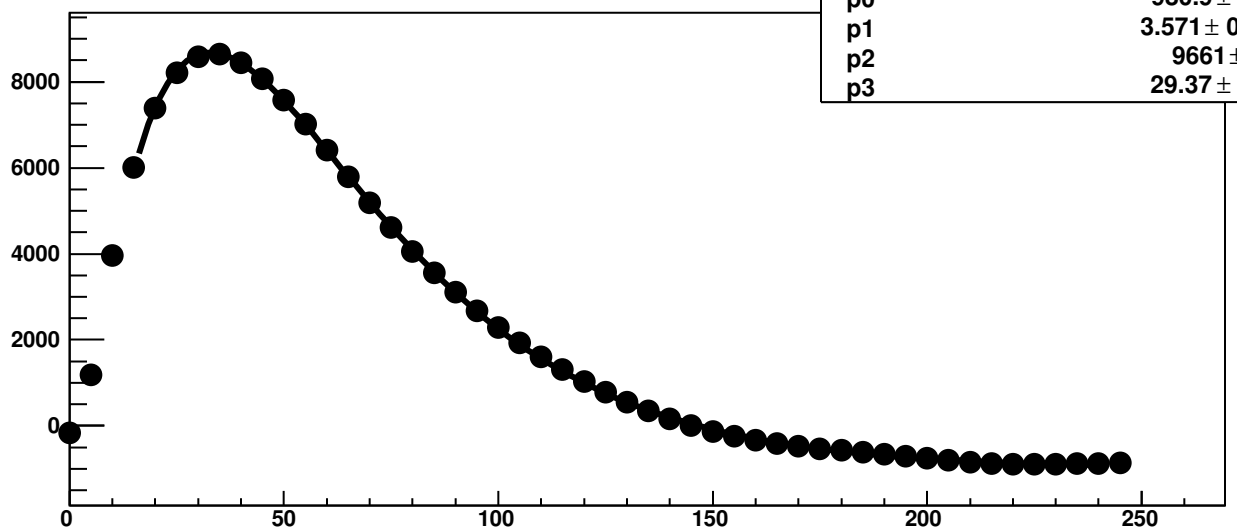


Chip 2, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

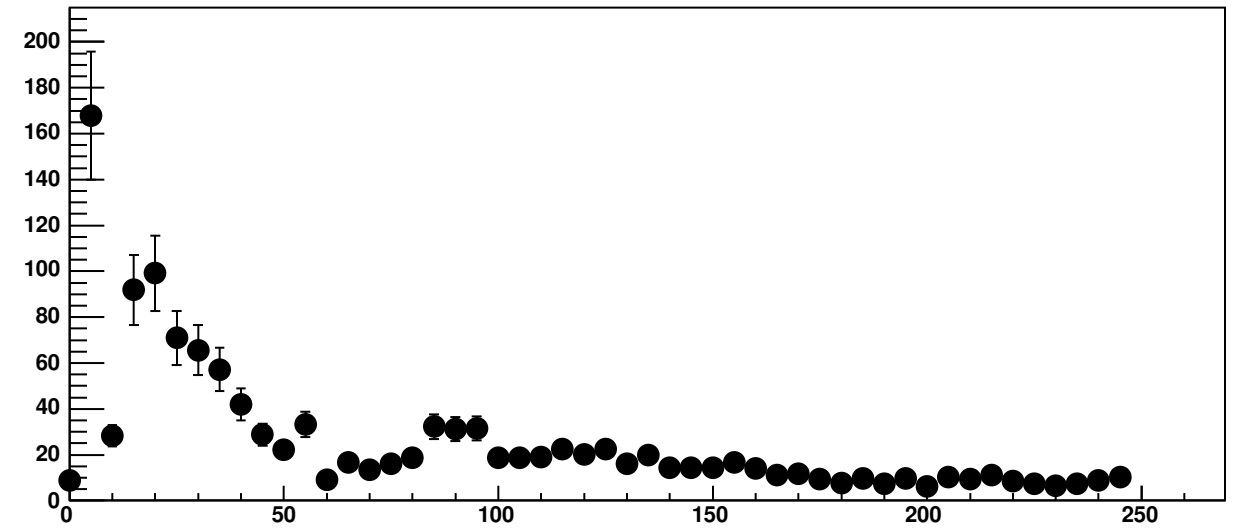


Chip 2, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold

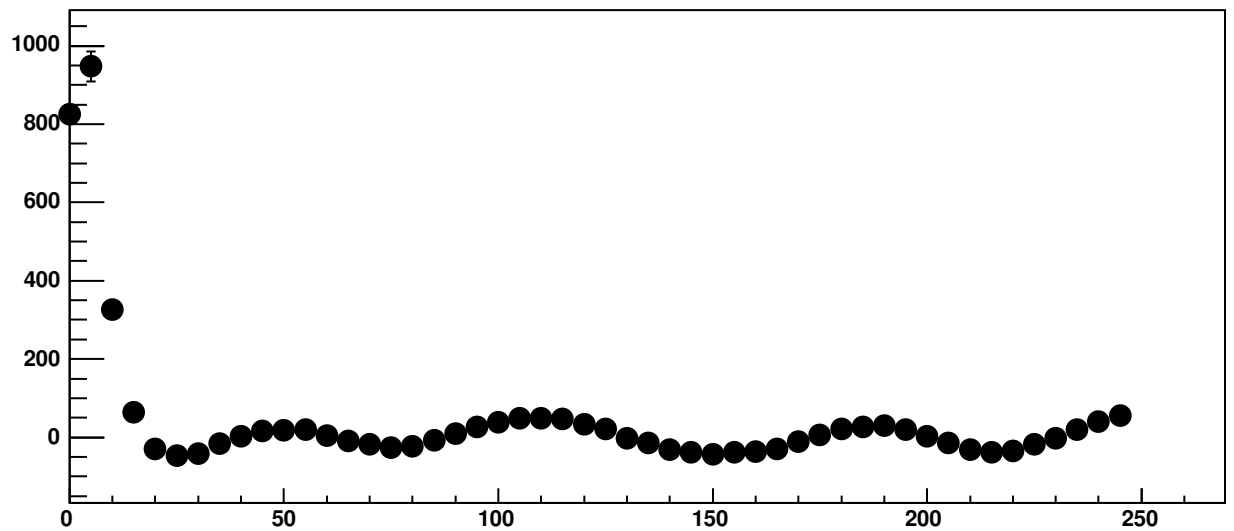
χ^2 / ndf	3589 / 42
p0	-980.9 ± 0.7059
p1	3.571 ± 0.03771
p2	9661 ± 3.956
p3	29.37 ± 0.0139



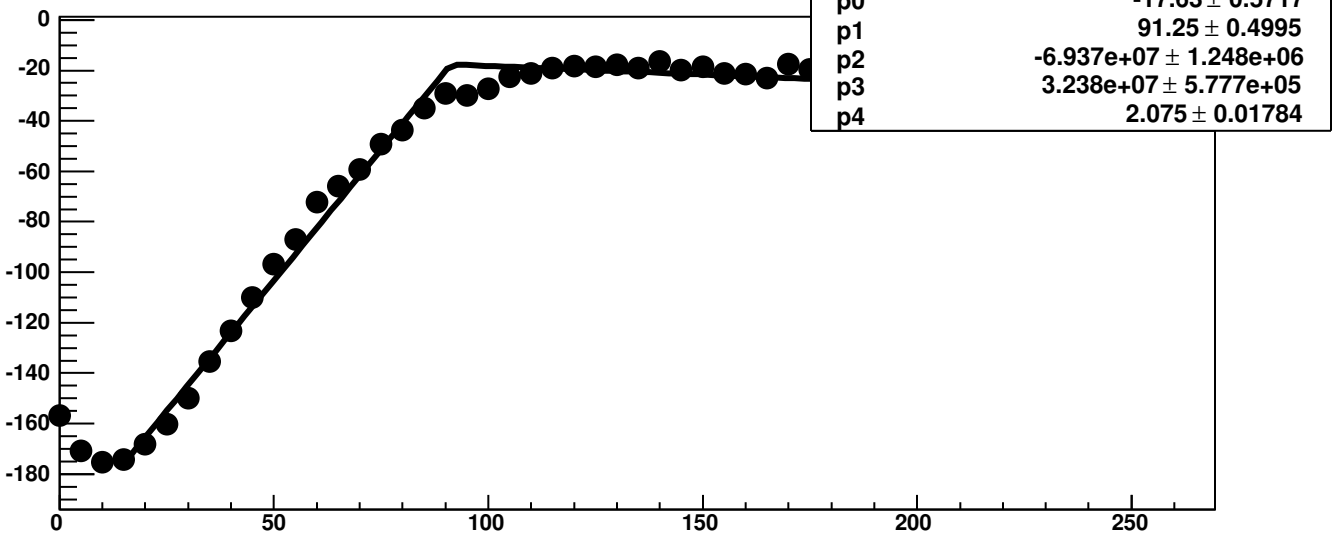
Chip 2, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold



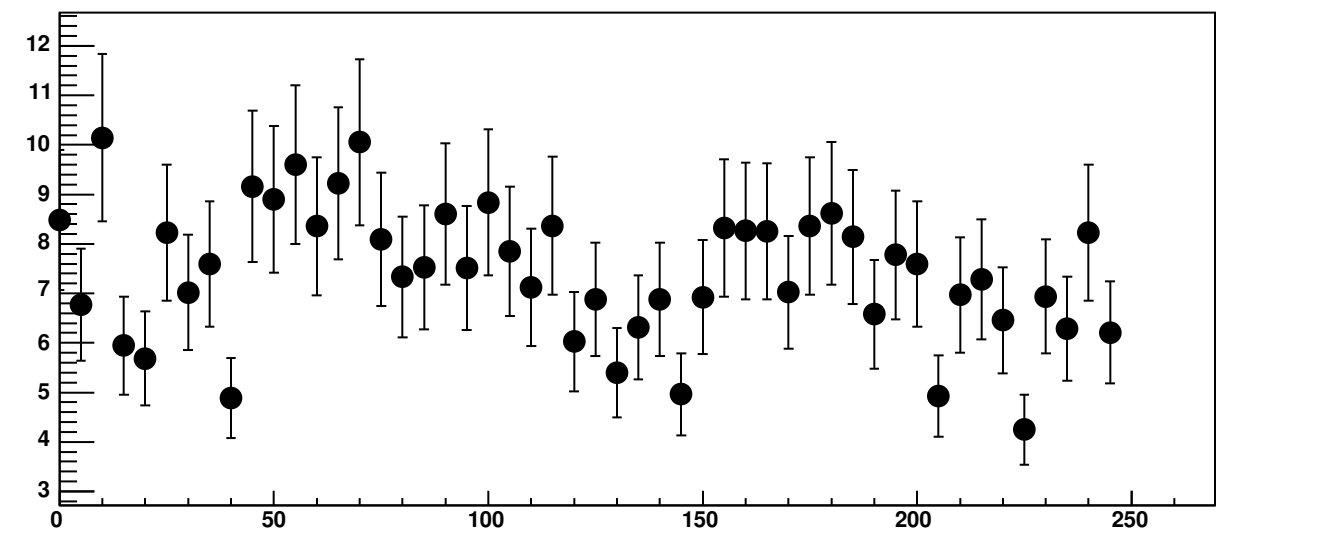
Chip 2, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold



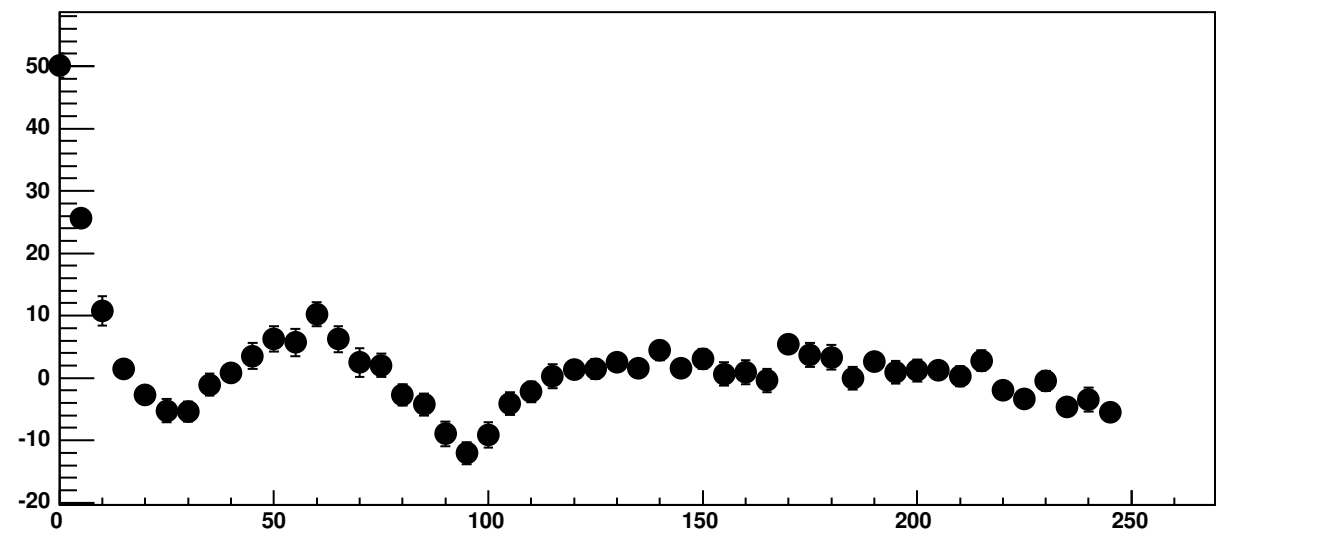
Chip 2, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold



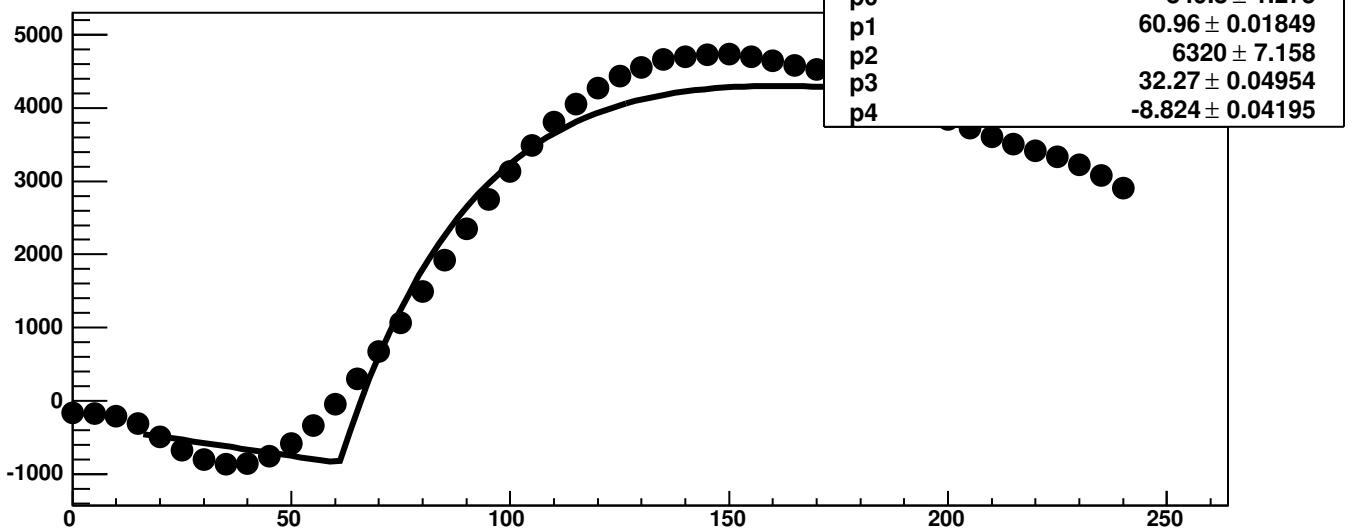
Chip 2, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



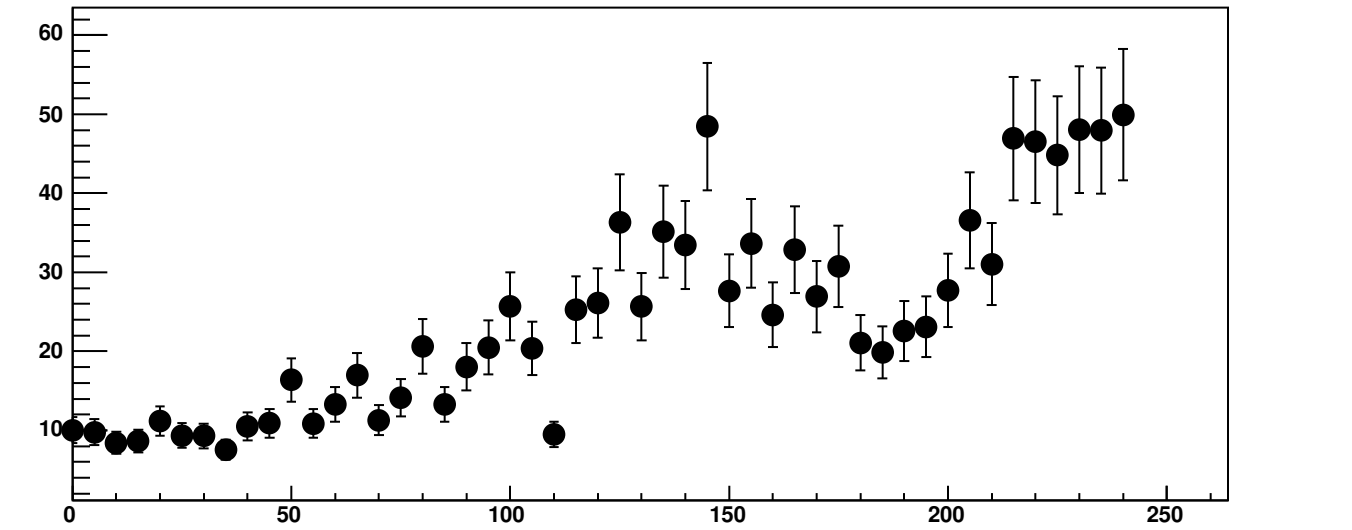
Chip 2, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold



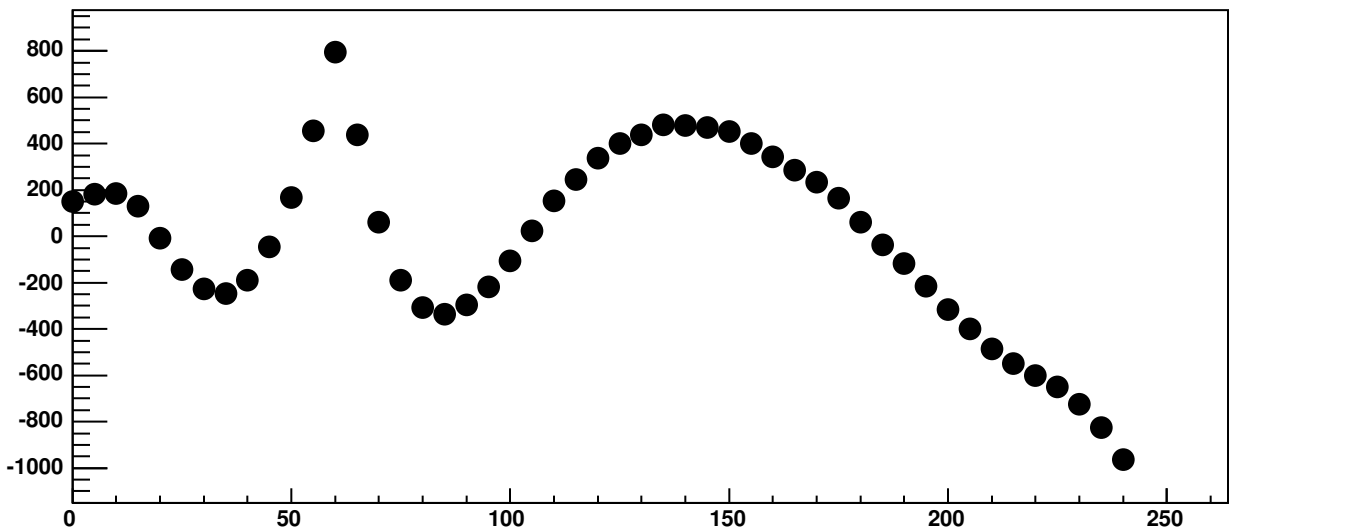
Chip 2, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold

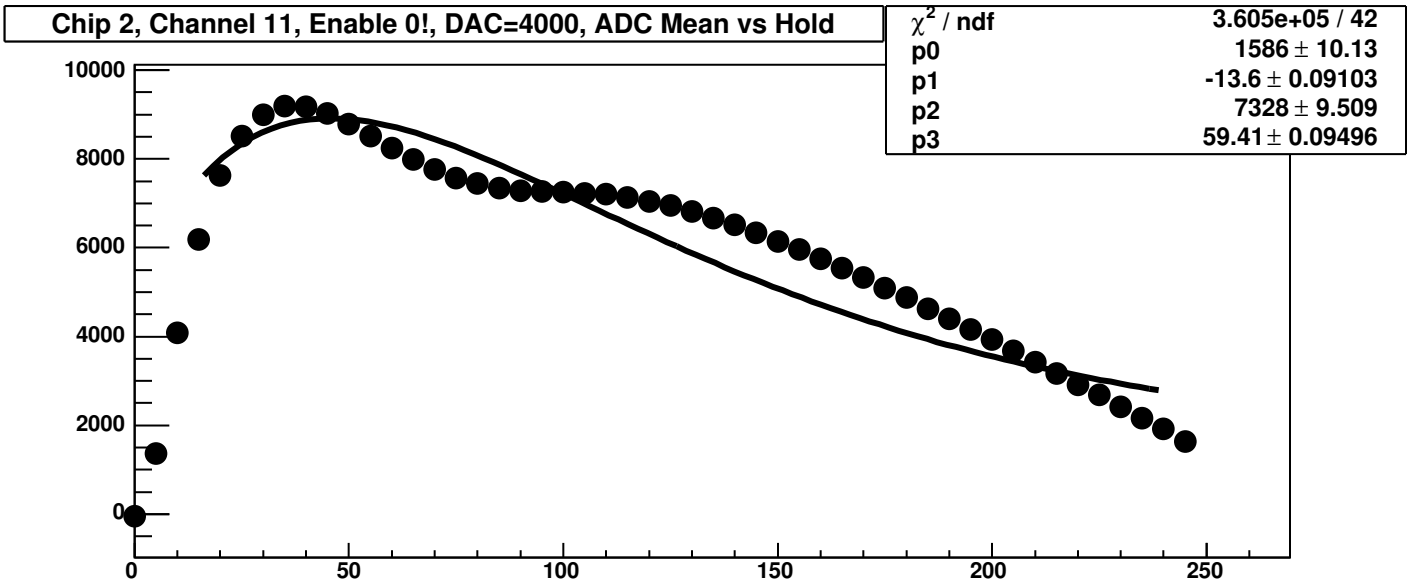


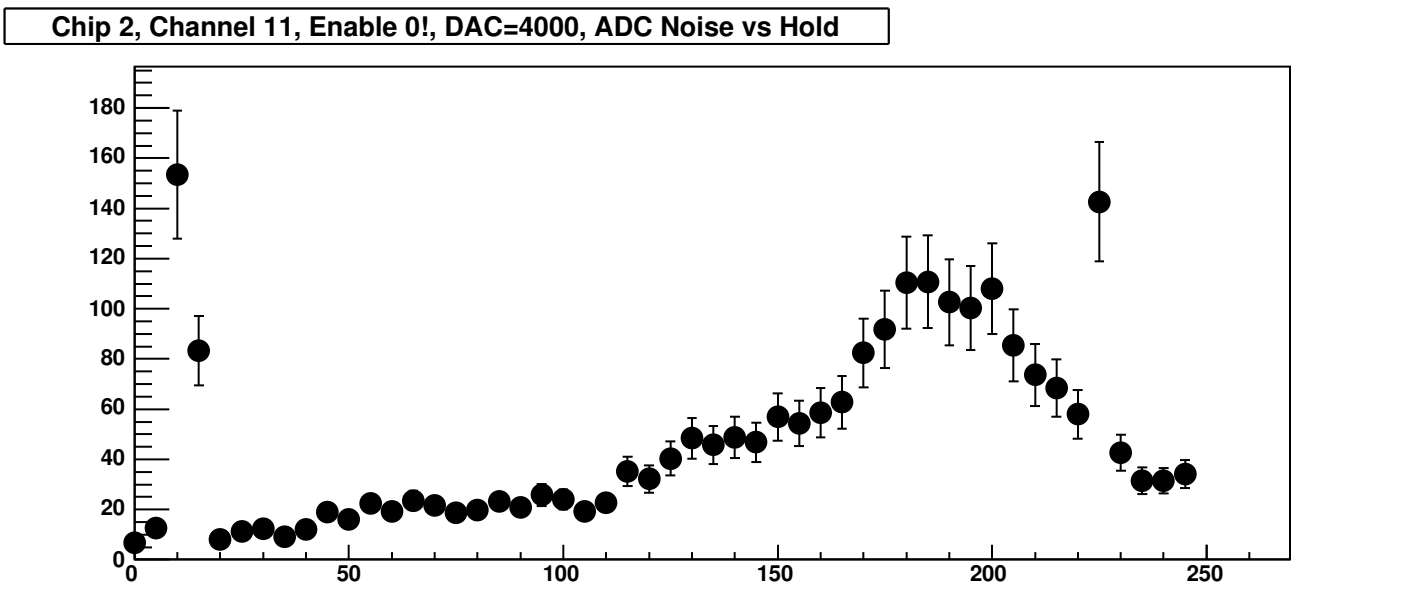
Chip 2, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold

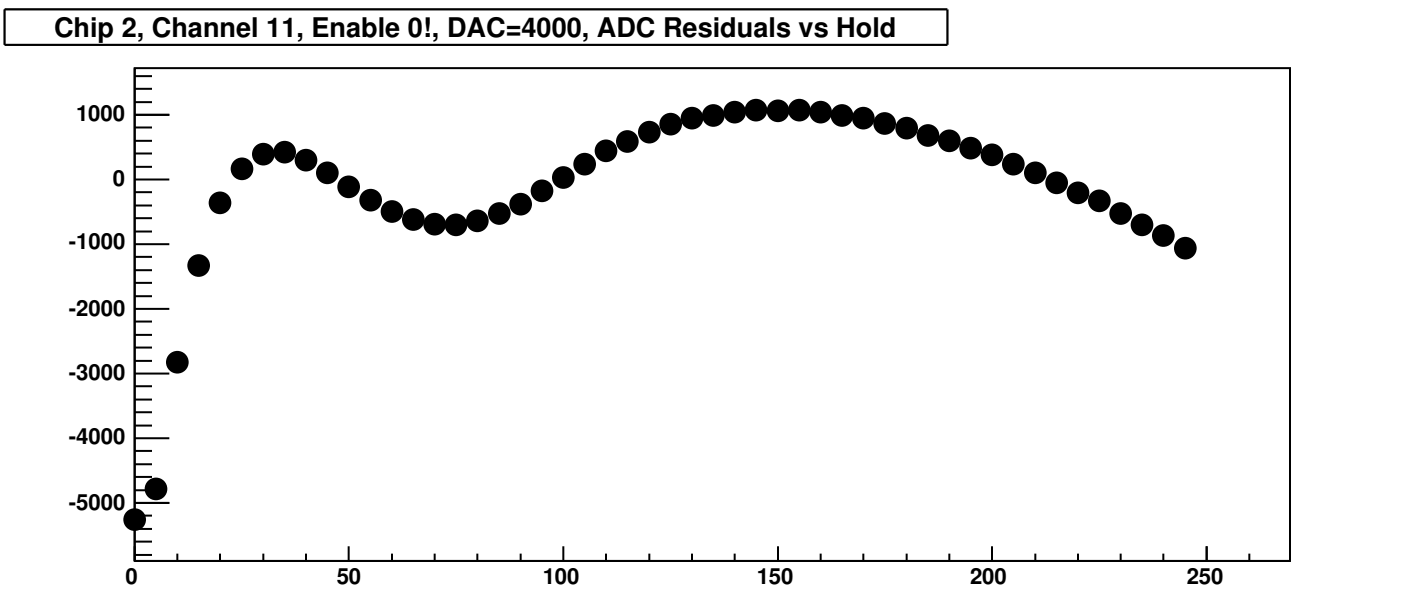


Chip 2, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

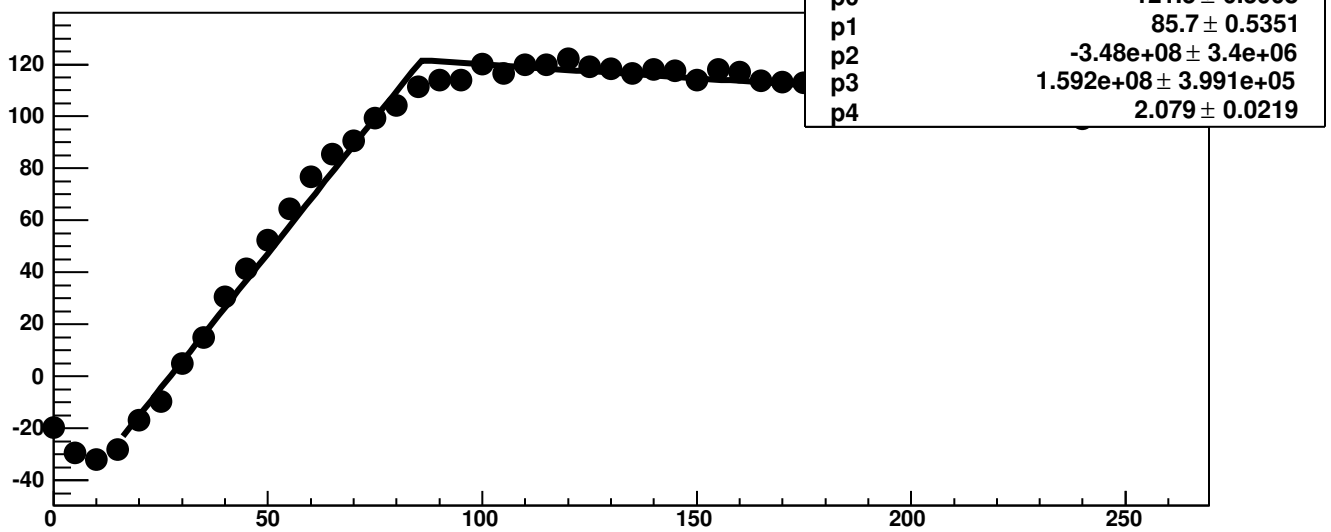



Chip 2, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.605e+05 / 42
p0 1586 ± 10.13
p1 -13.6 ± 0.09103
p2 7328 ± 9.509
p3 59.41 ± 0.09496
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

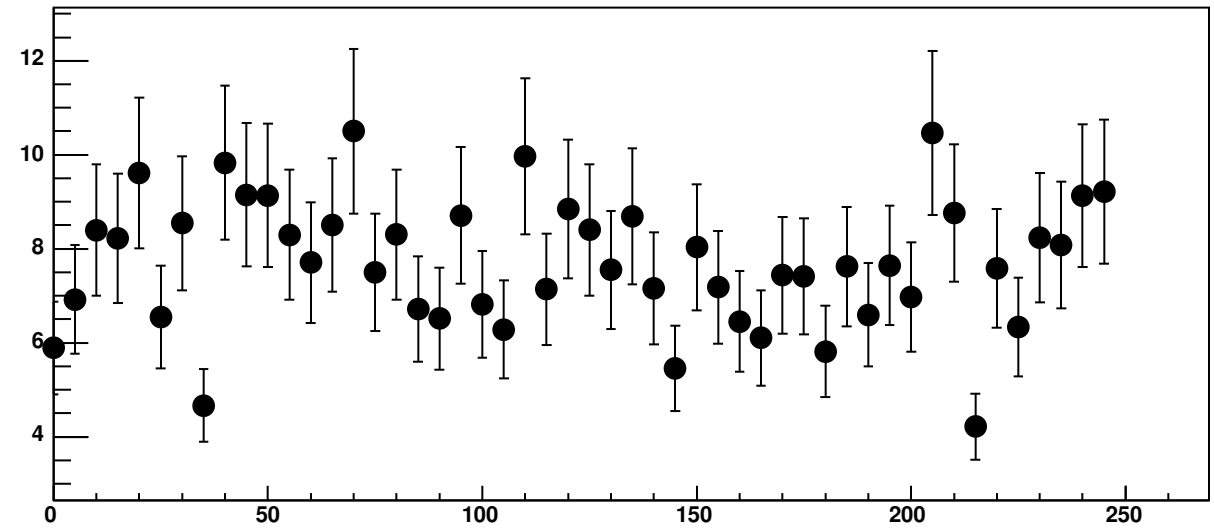

Chip 2, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 2, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

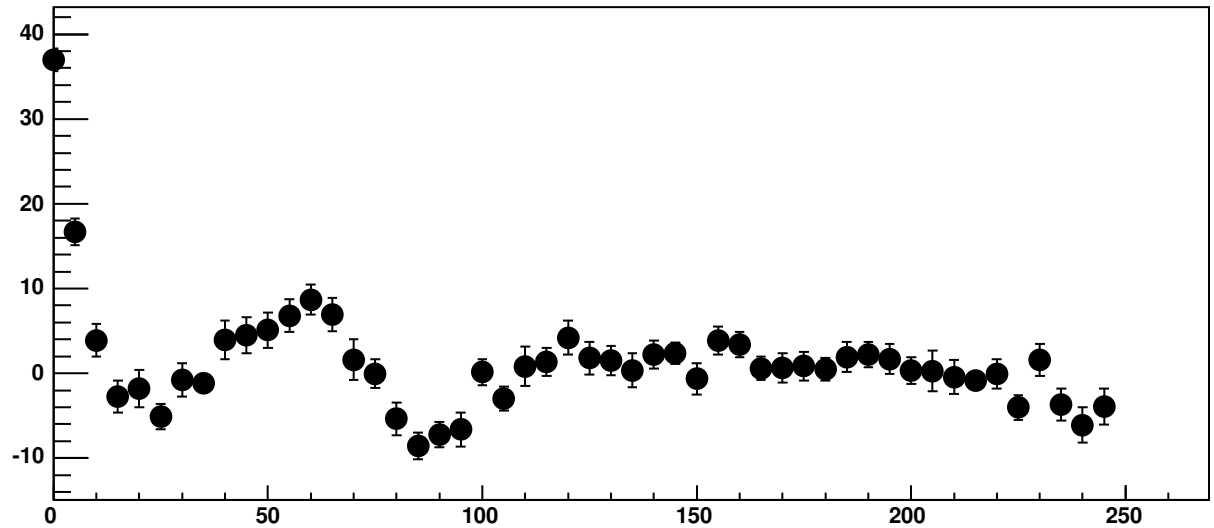
Chip 2, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold



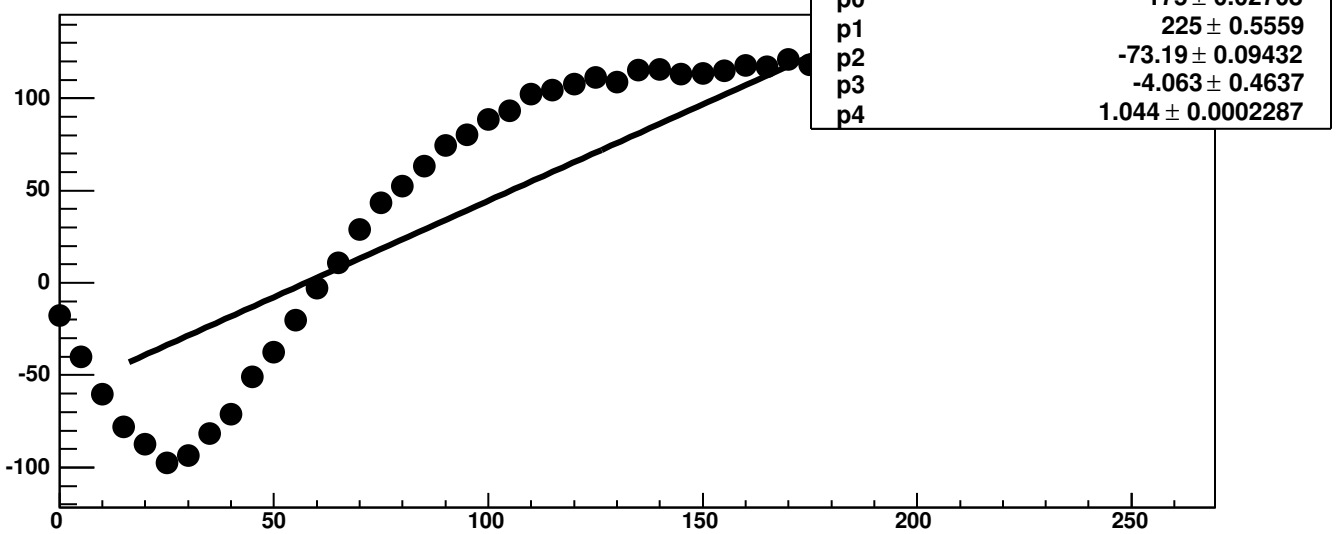
Chip 2, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



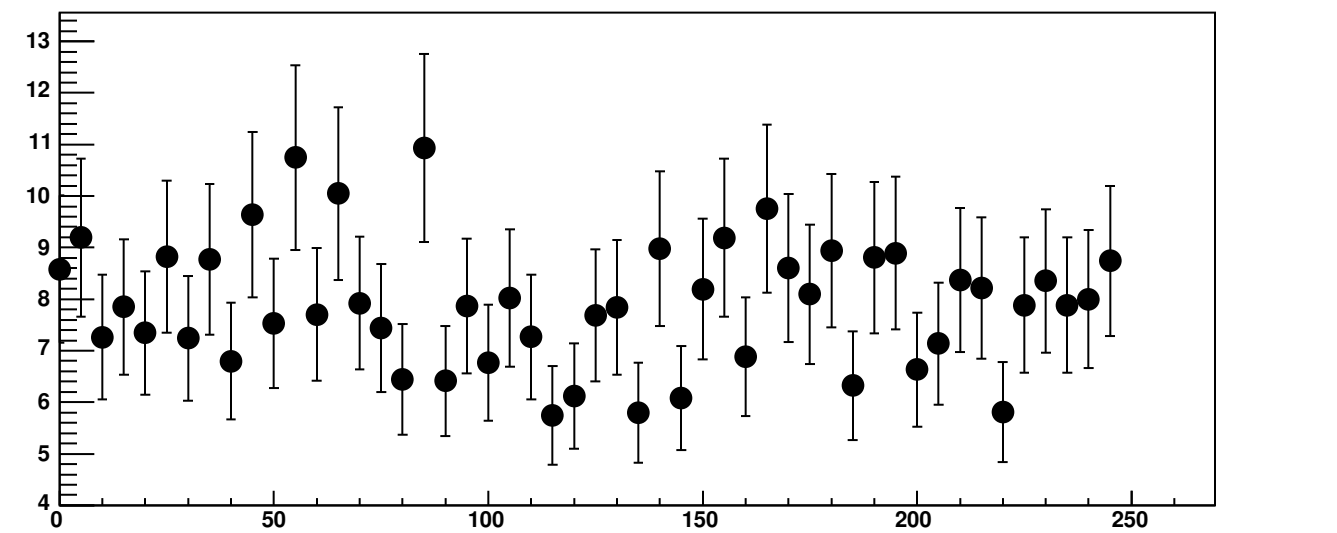
Chip 2, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold



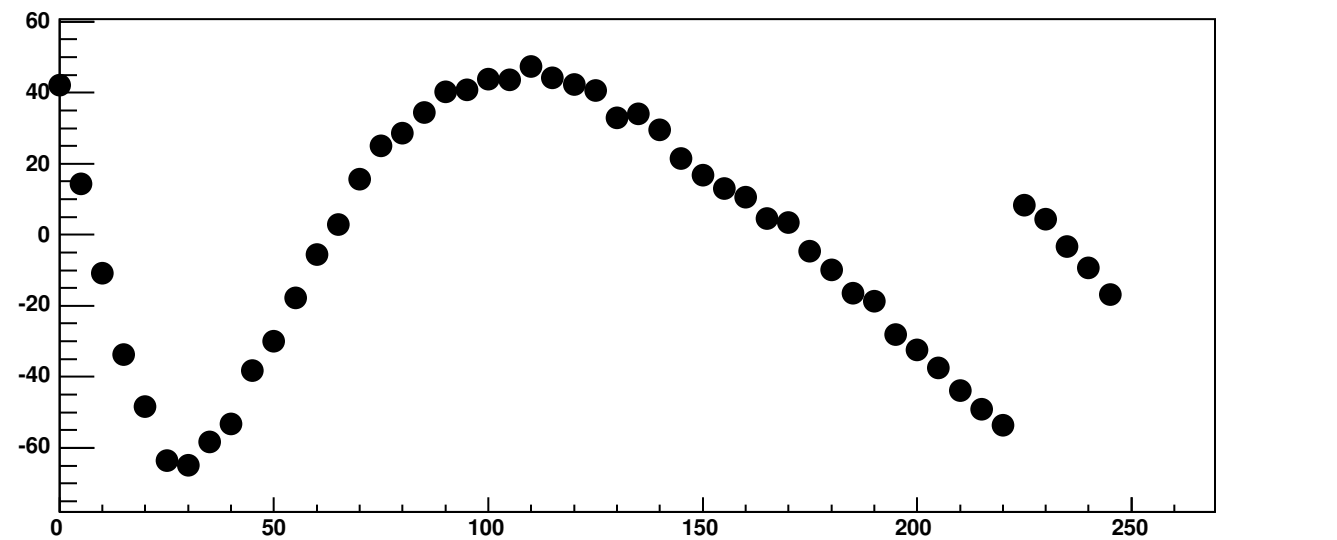
Chip 2, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold



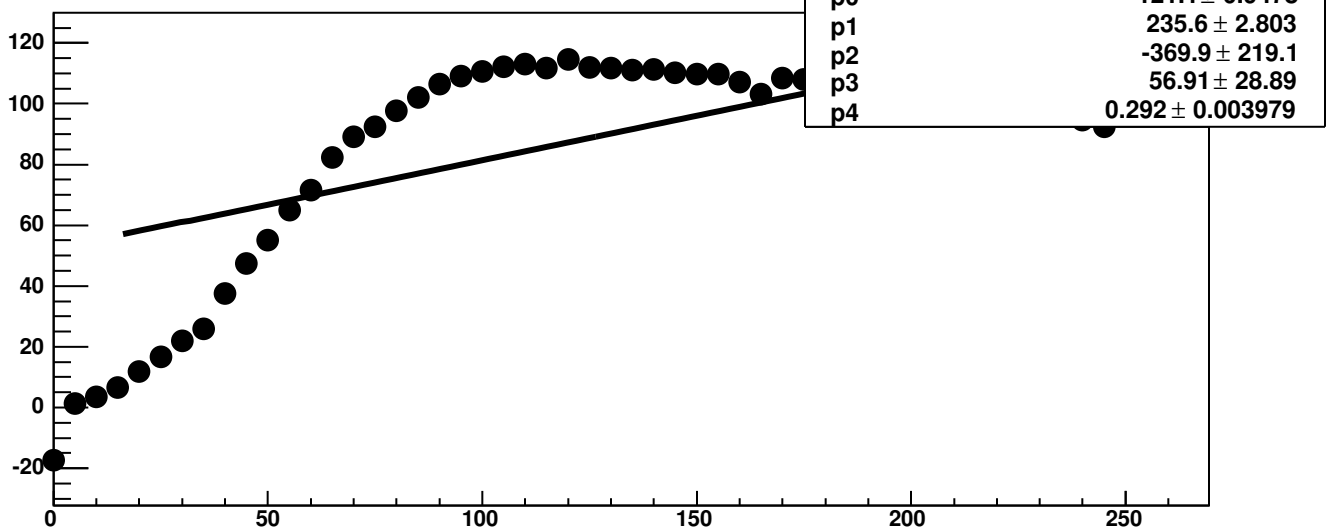
Chip 2, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



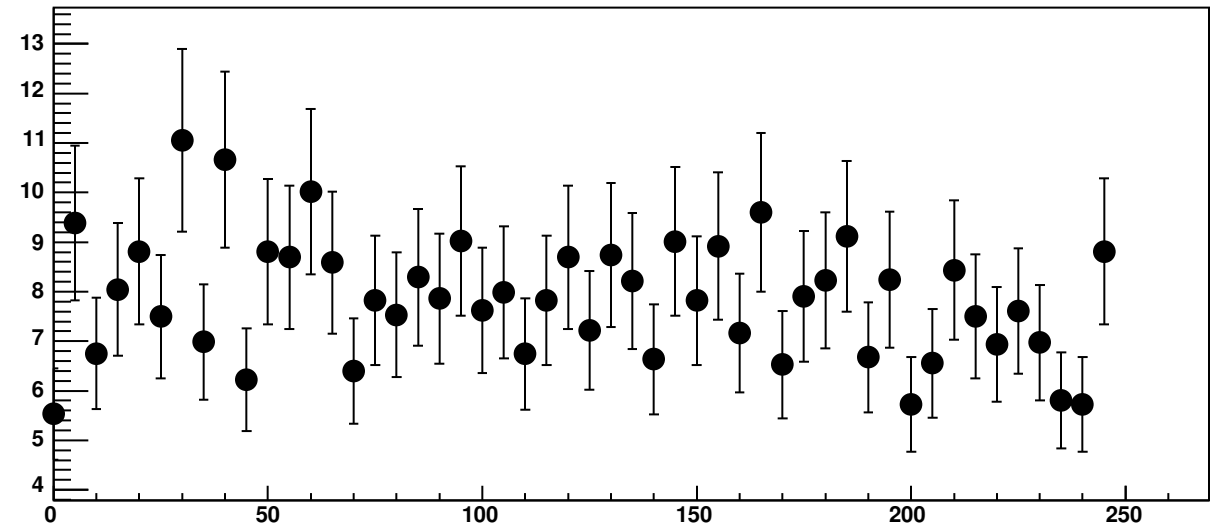
Chip 2, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold



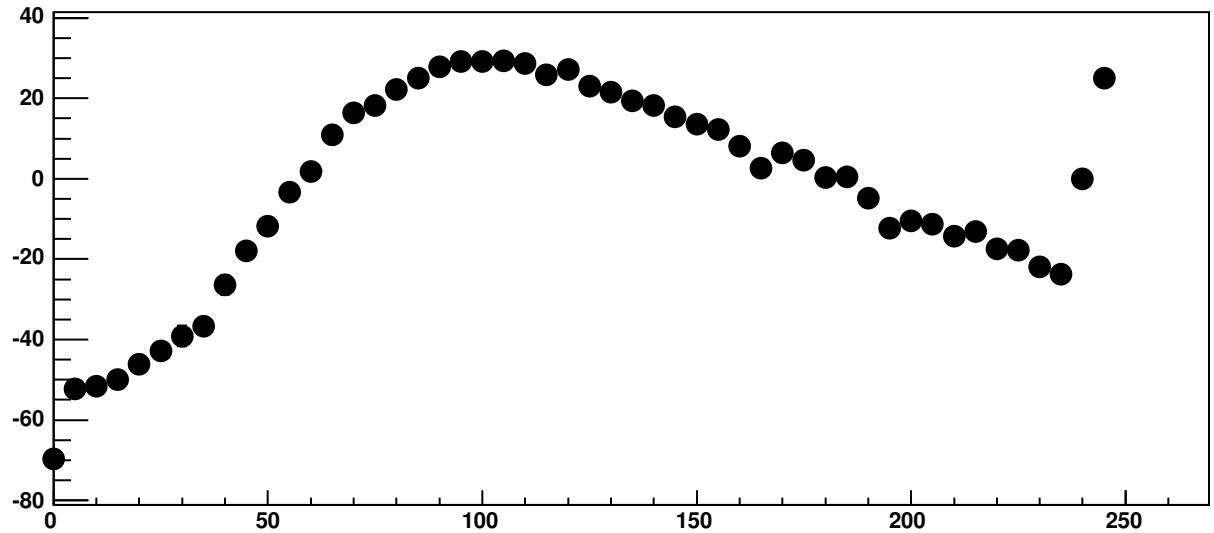
Chip 2, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold



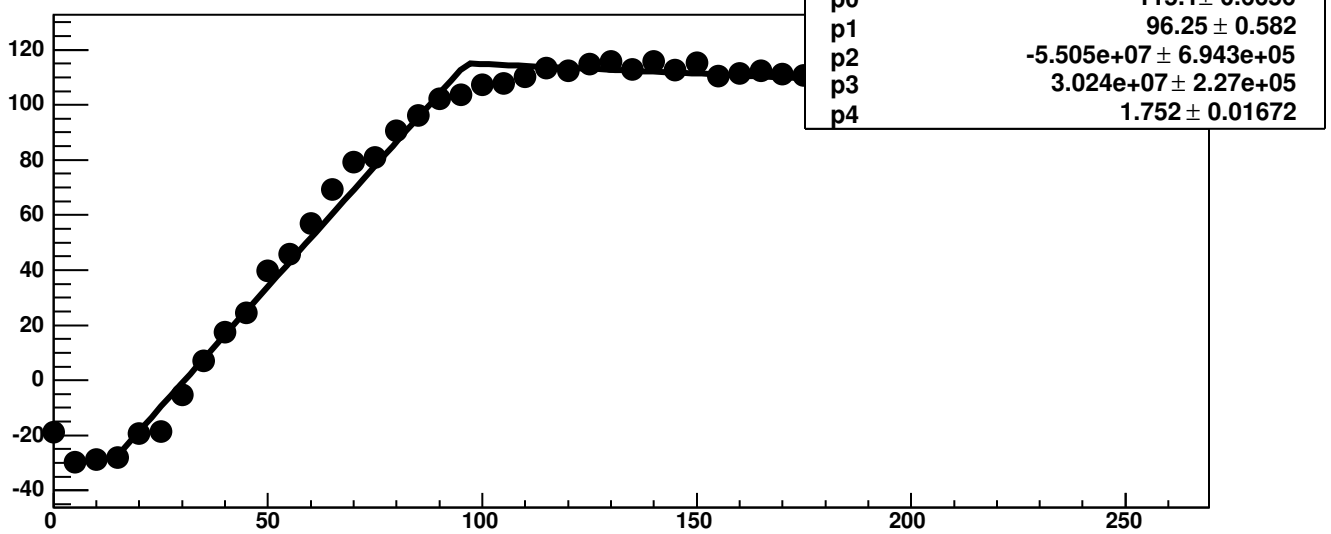
Chip 2, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold



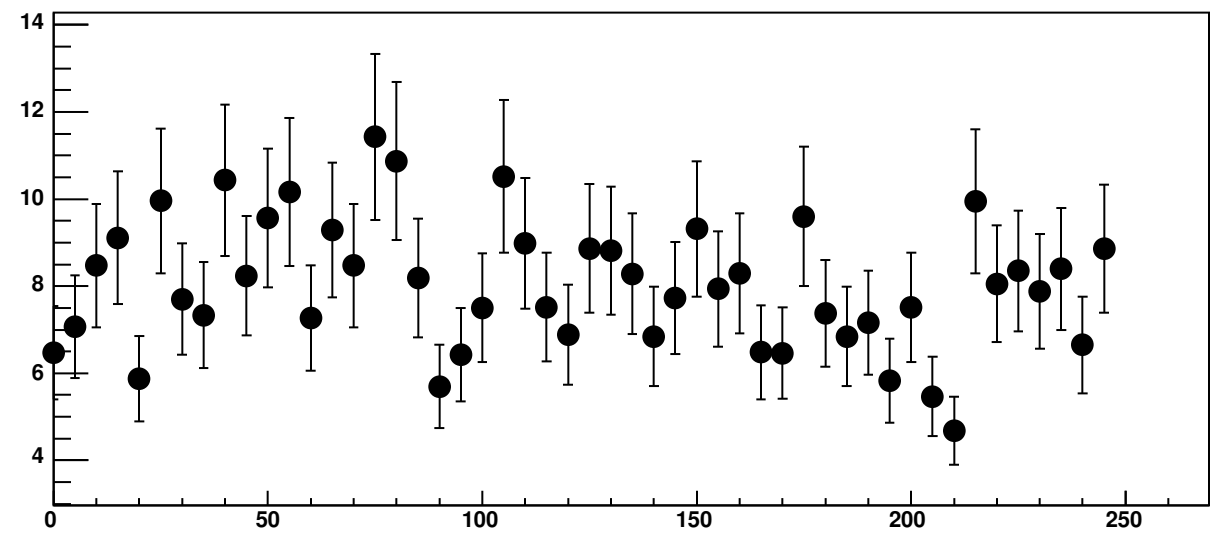
Chip 2, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold



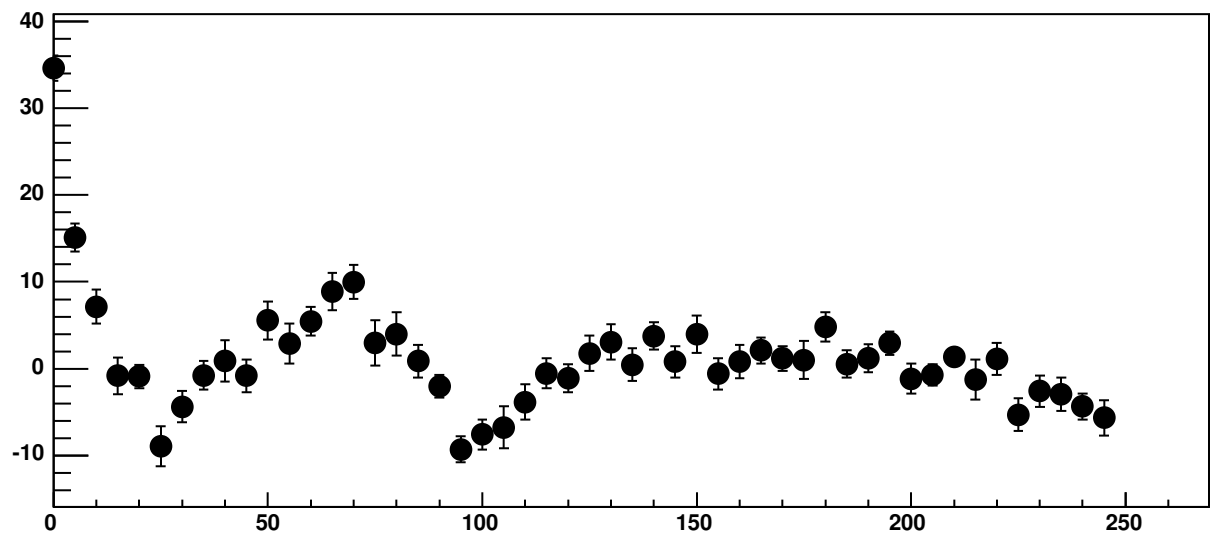
Chip 2, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold



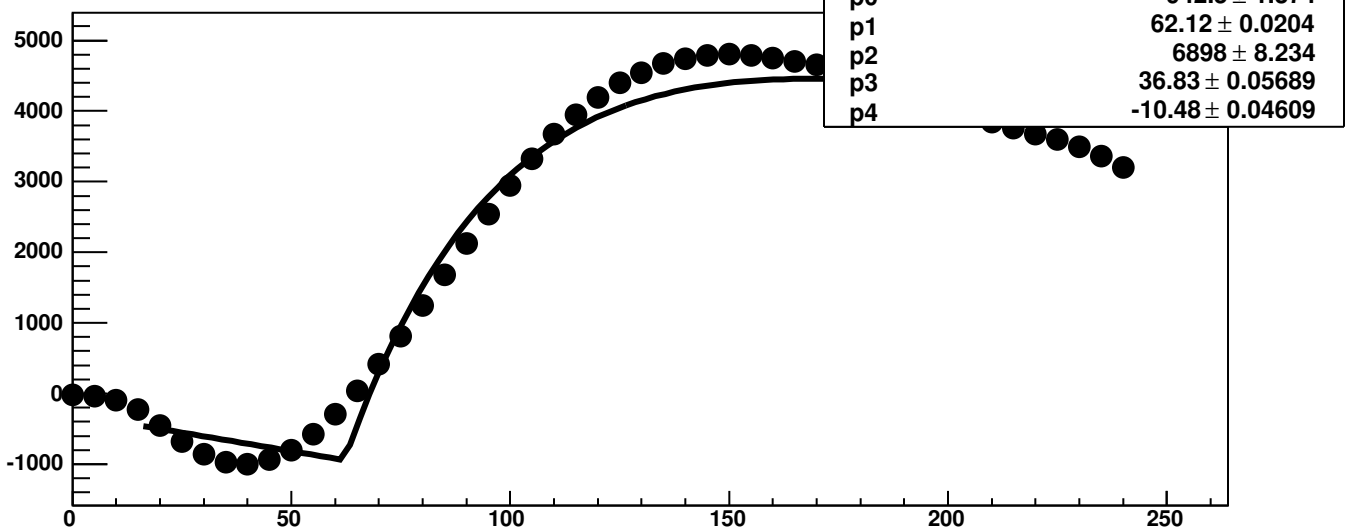
Chip 2, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



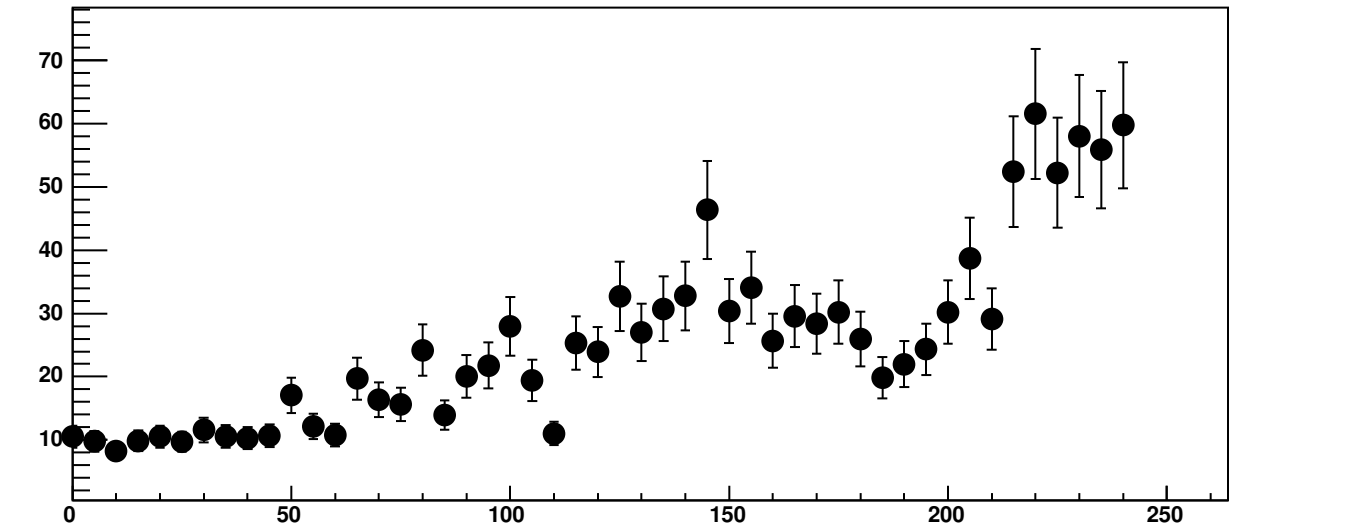
Chip 2, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold



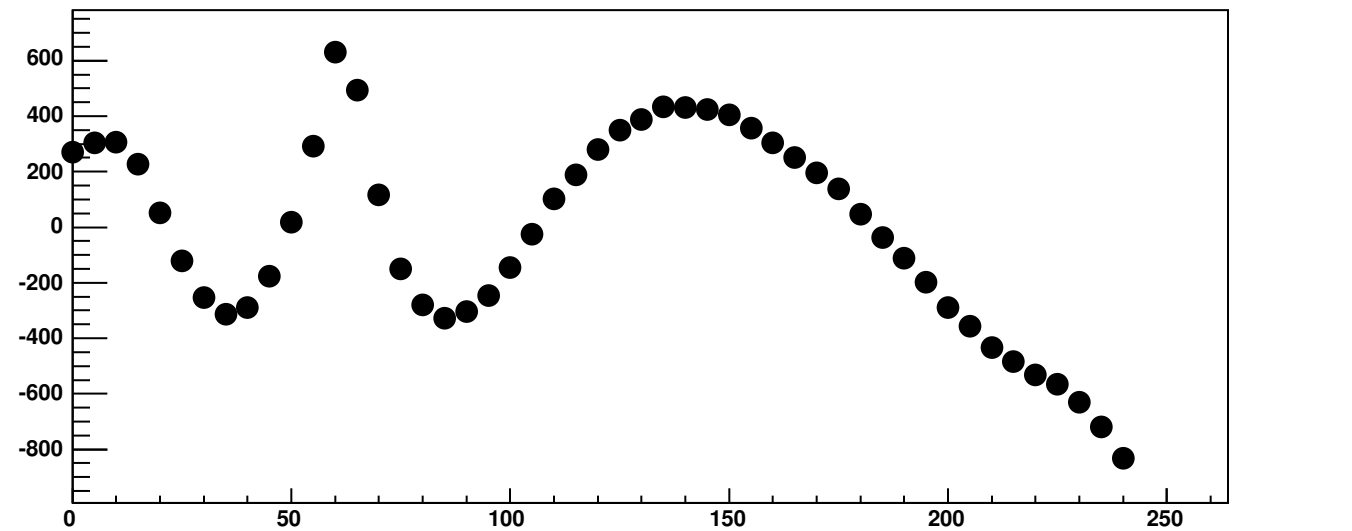
Chip 2, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold



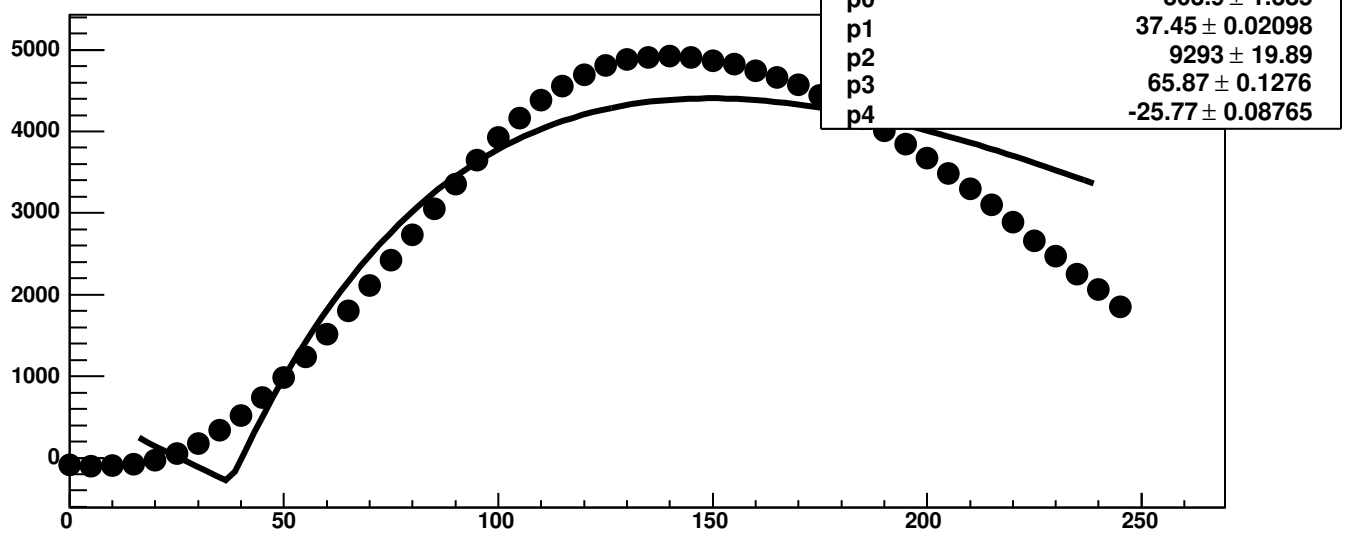
Chip 2, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



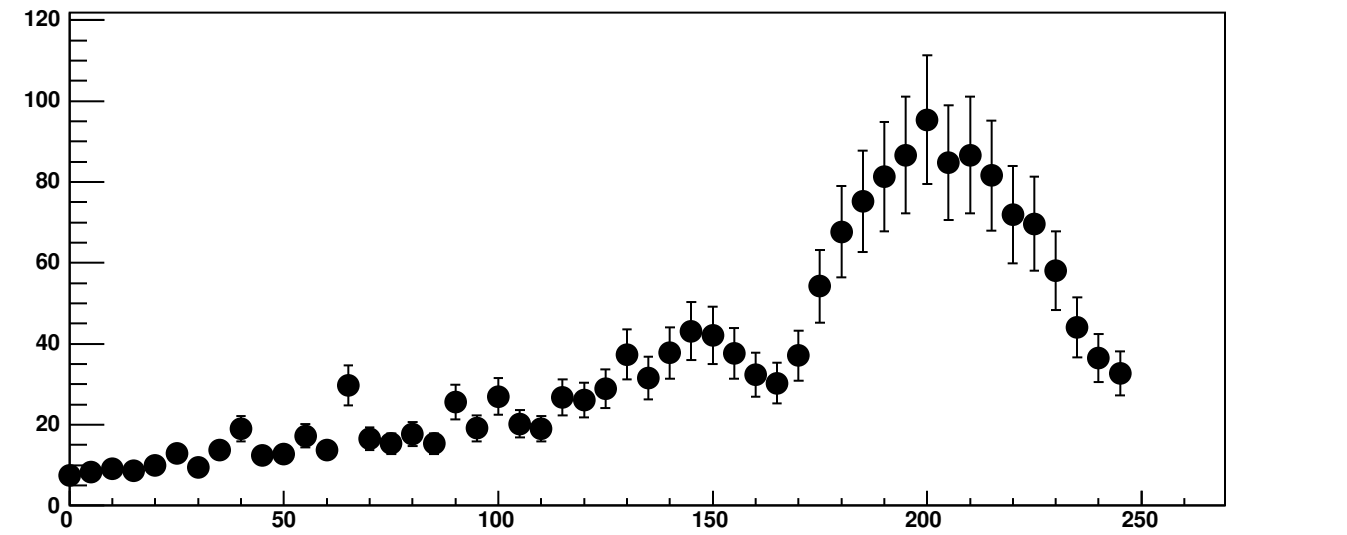
Chip 2, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold



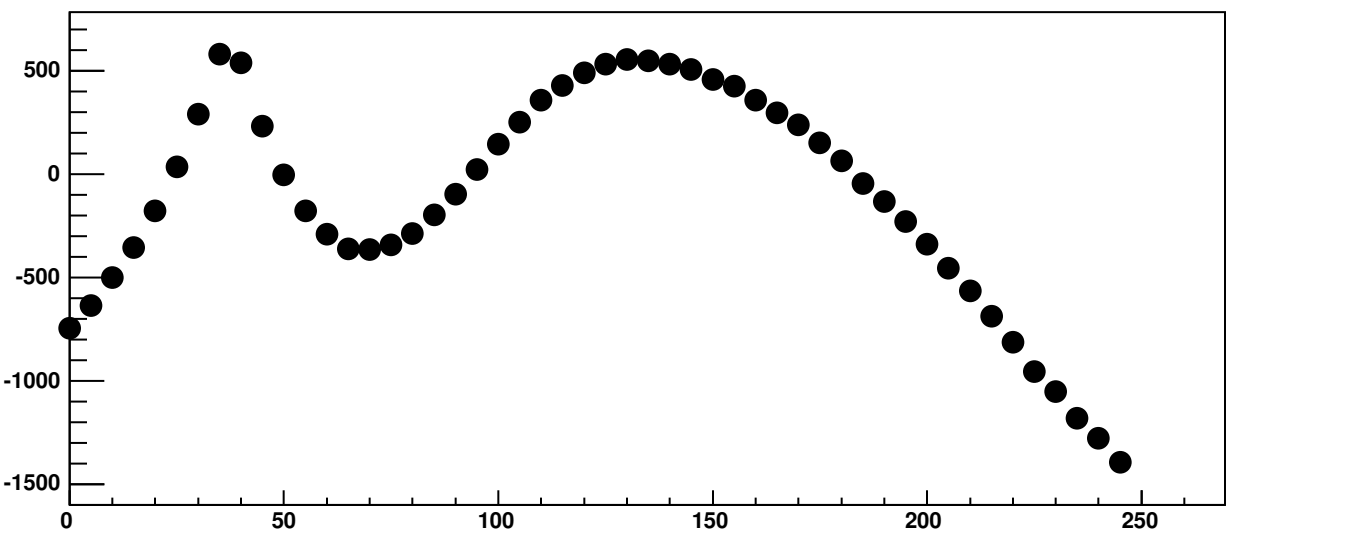
Chip 2, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

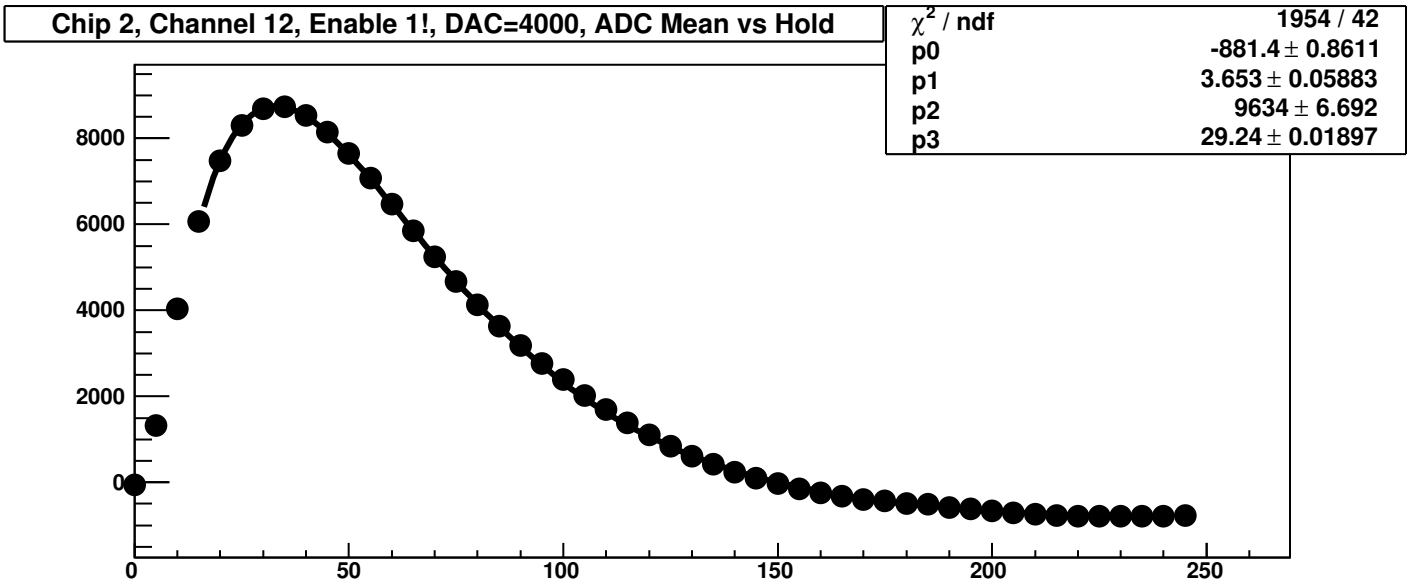


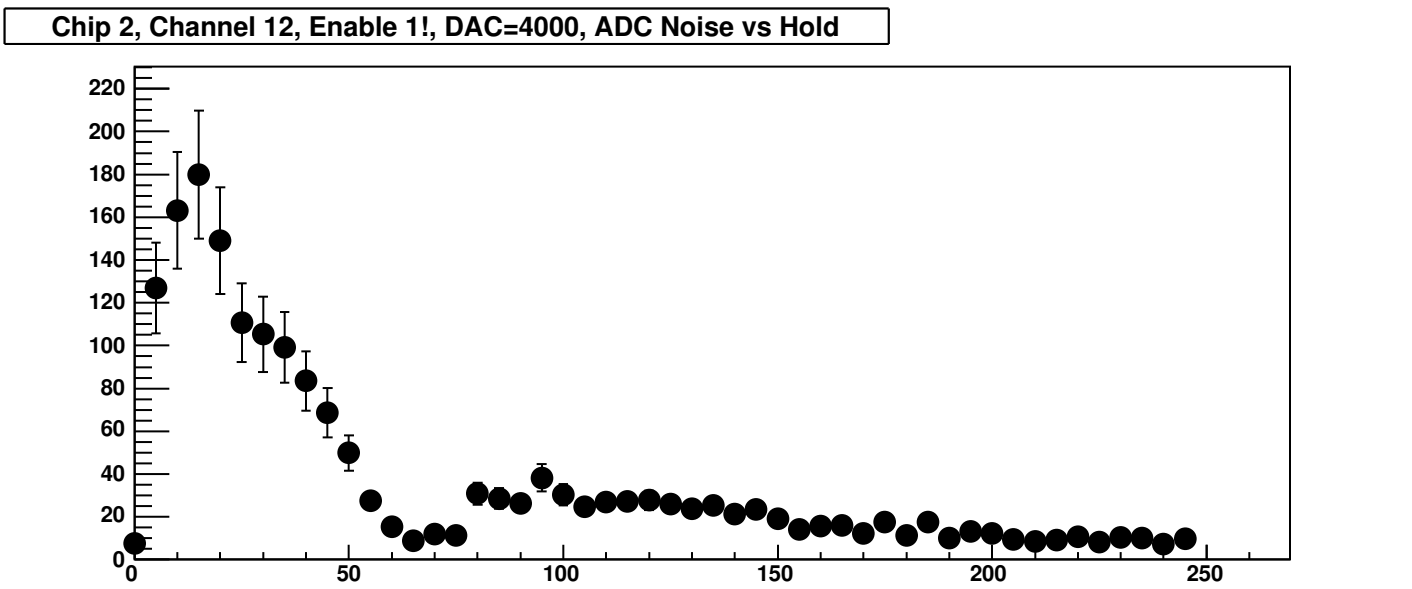
Chip 2, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold

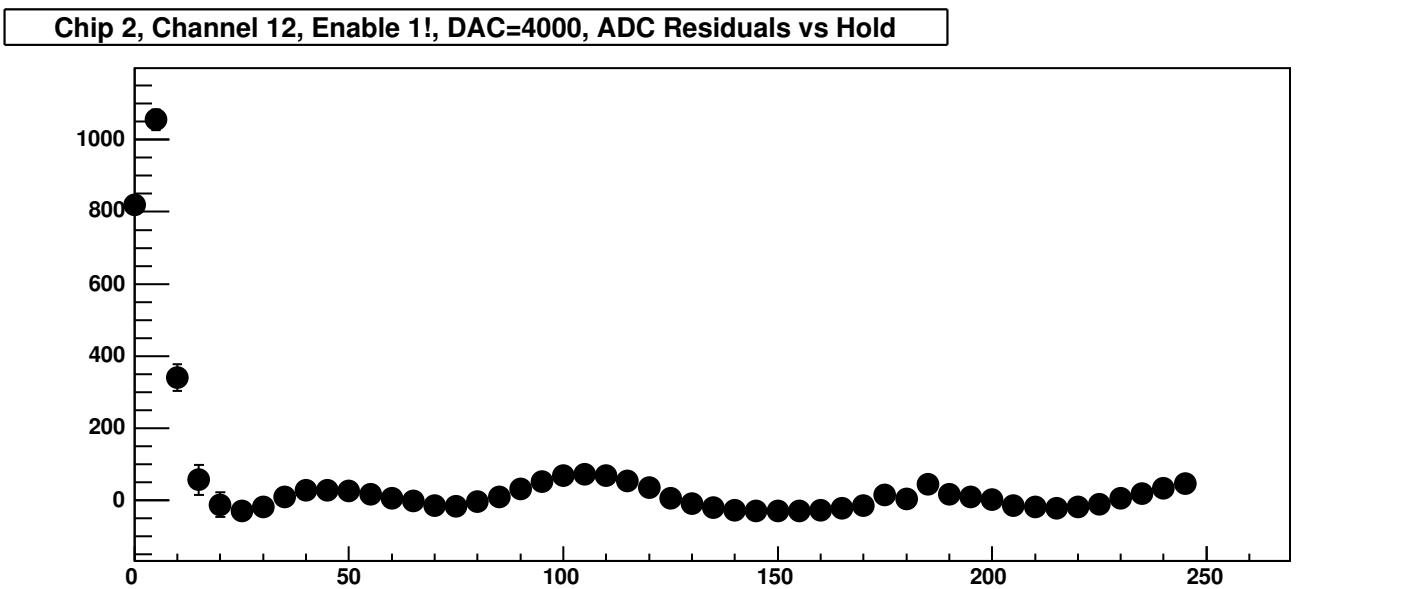


Chip 2, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

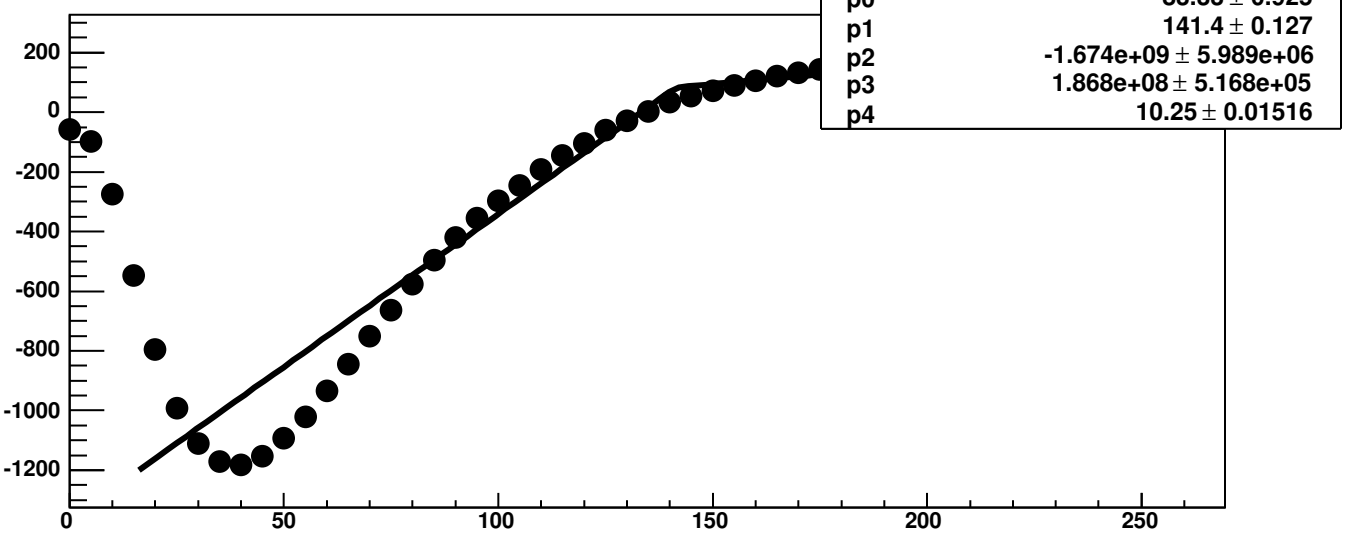



Chip 2, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1954 / 42
p0 -881.4 ± 0.8611
p1 3.653 ± 0.05883
p2 9634 ± 6.692
p3 29.24 ± 0.01897
8000
6000
4000
2000
0
0
50
100
150
200
250

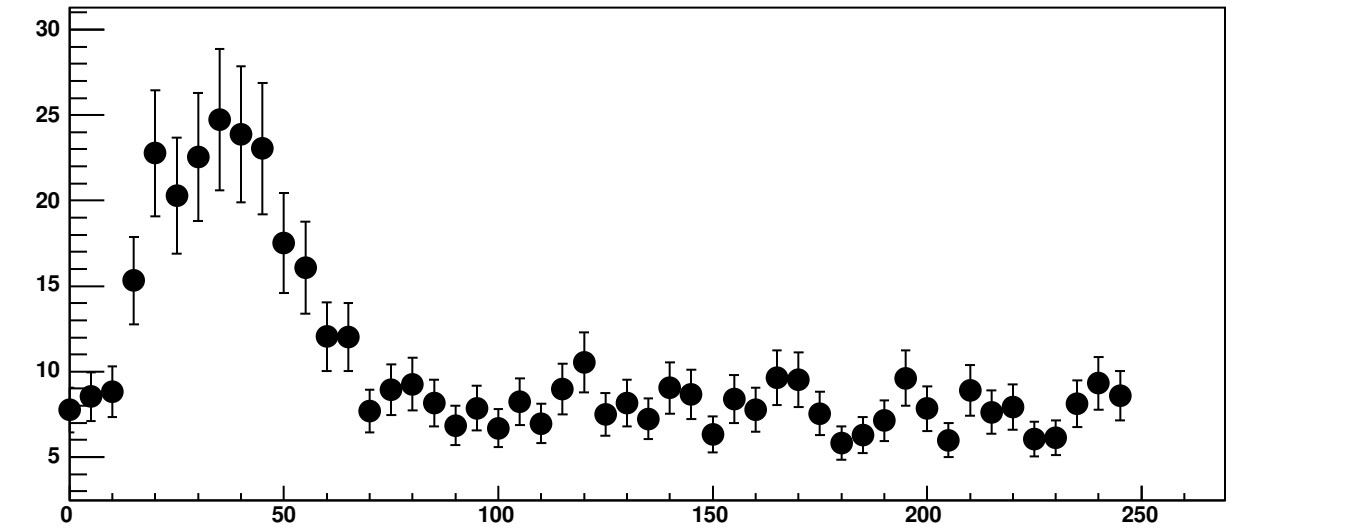

Chip 2, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 2, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold
1000
800
600
400
200
0
0
50
100
150
200
250

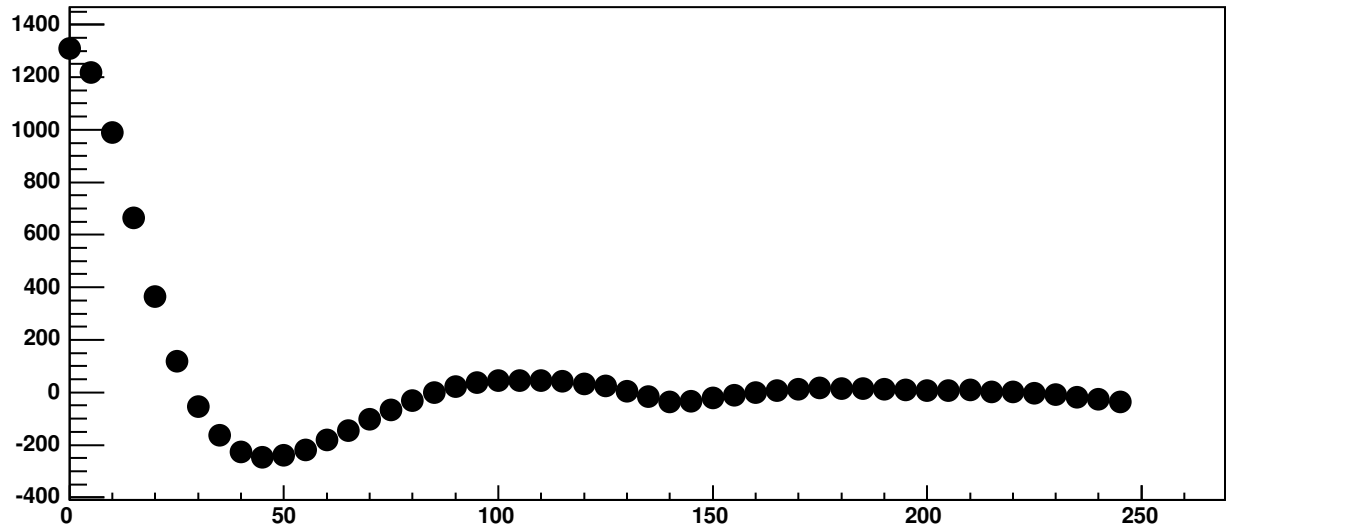
Chip 2, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold



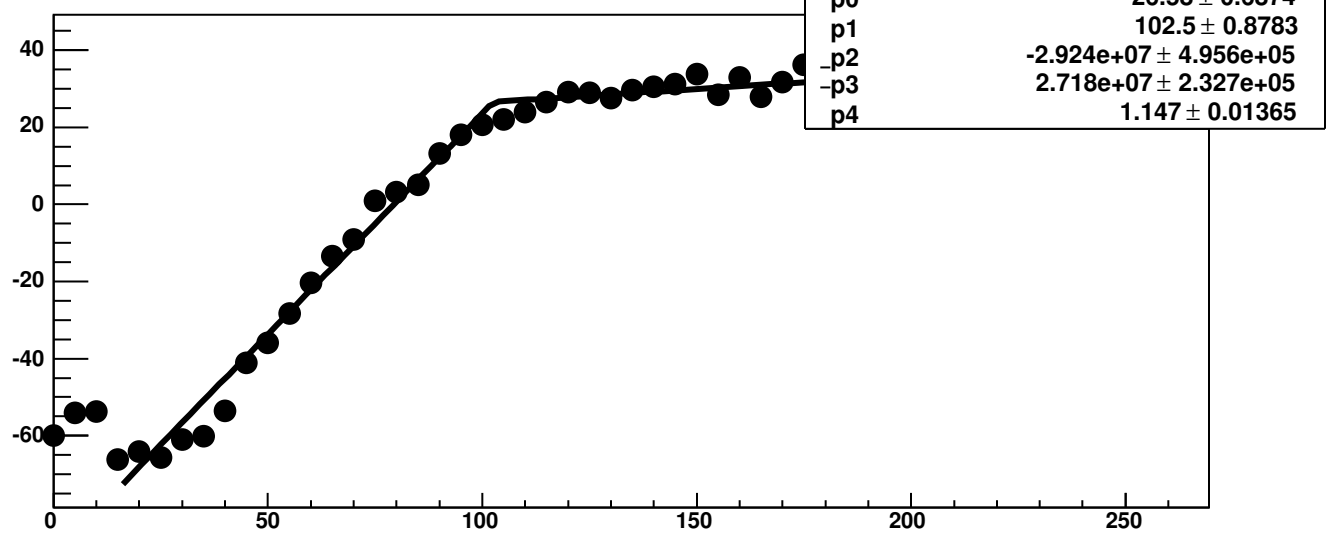
Chip 2, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



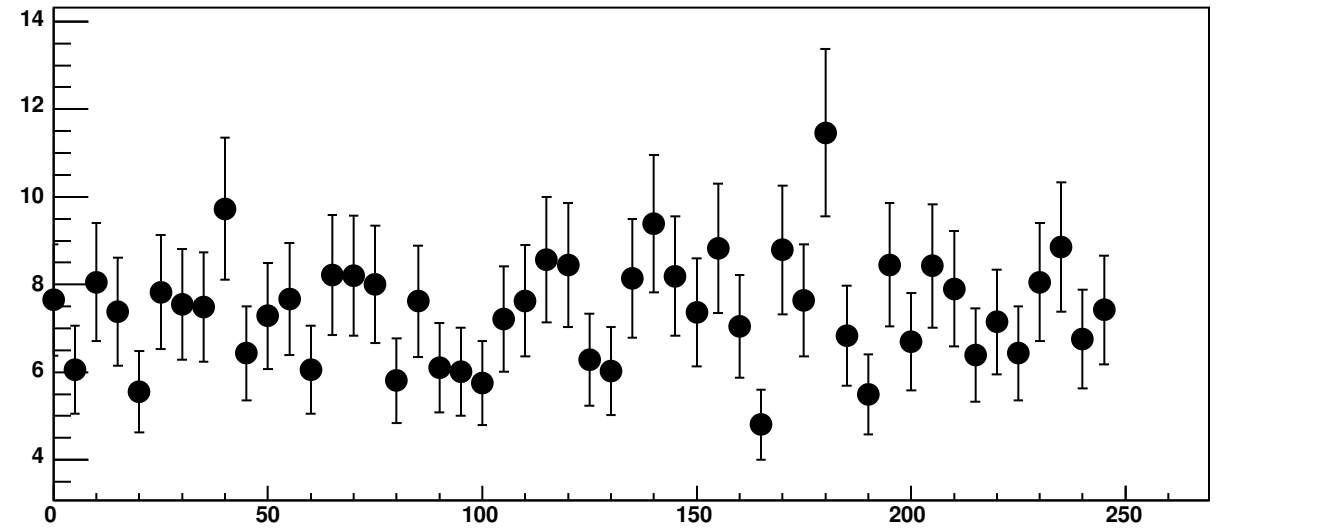
Chip 2, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold



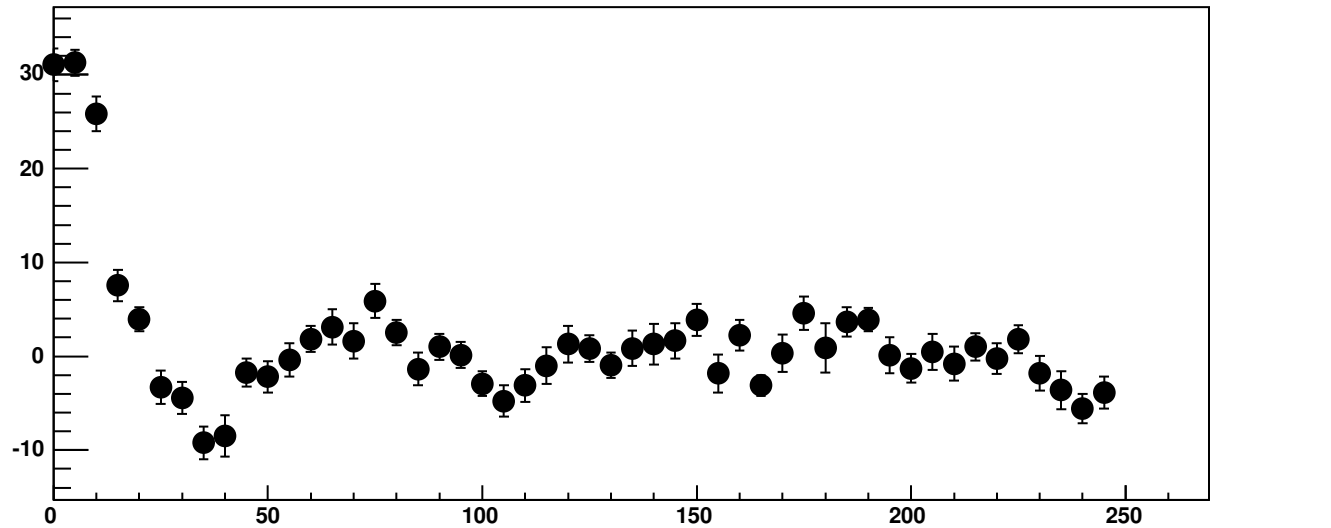
Chip 2, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold



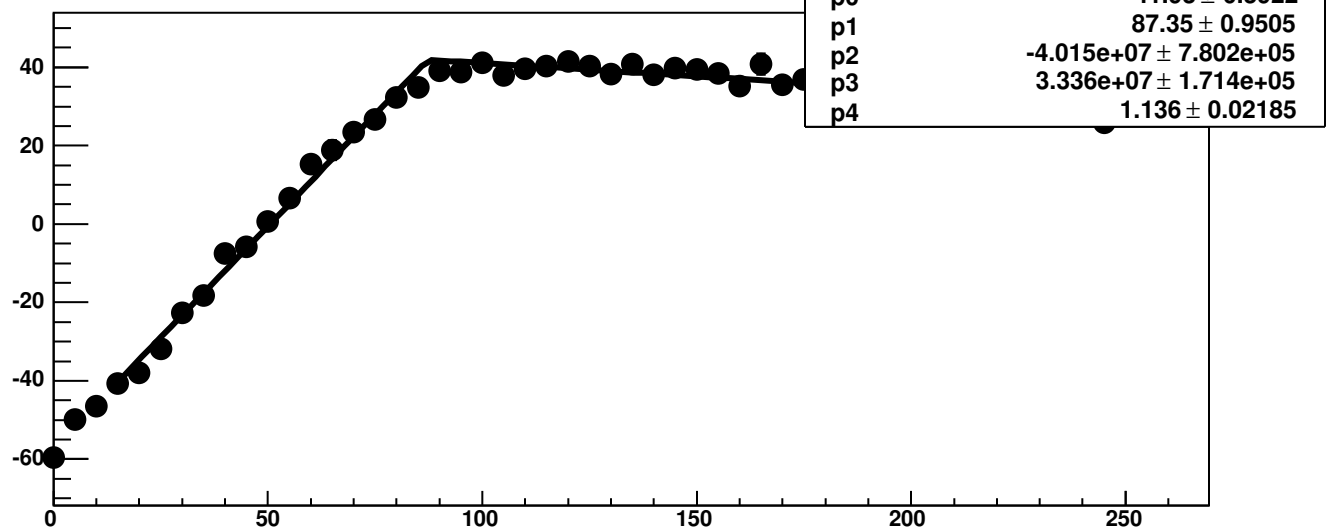
Chip 2, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



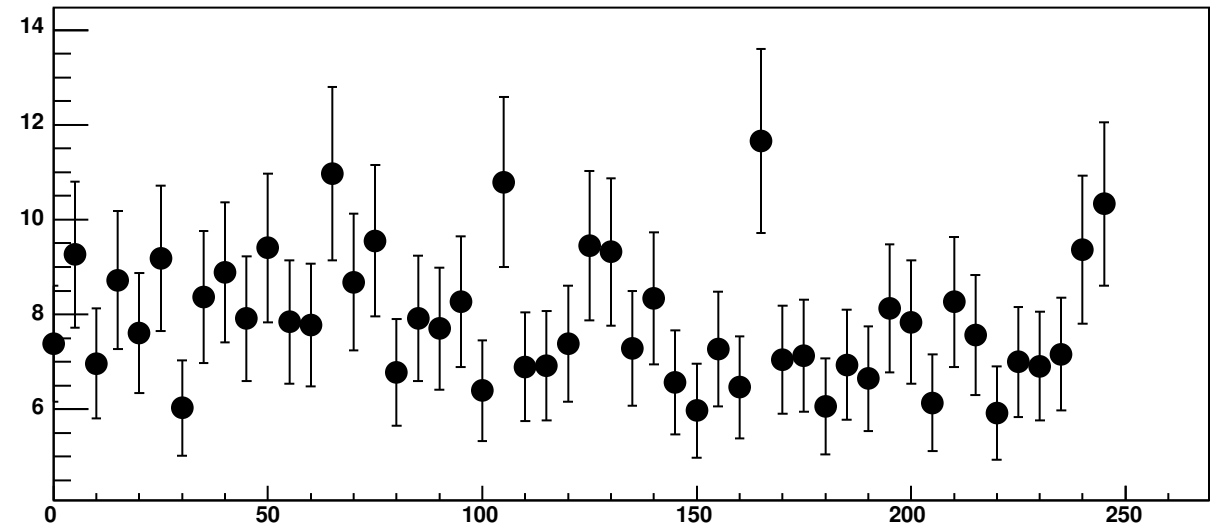
Chip 2, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold



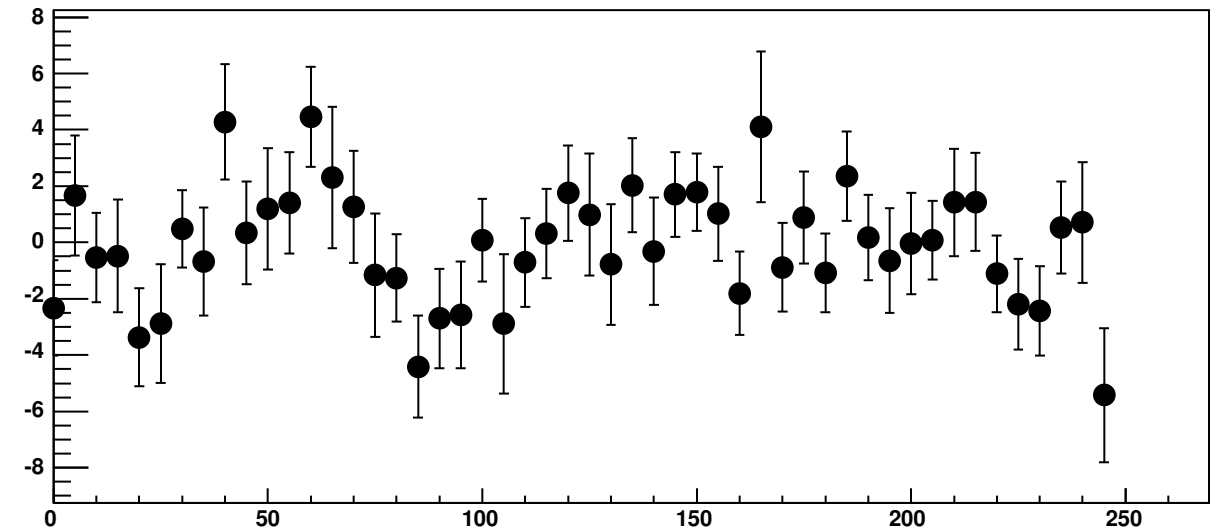
Chip 2, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold

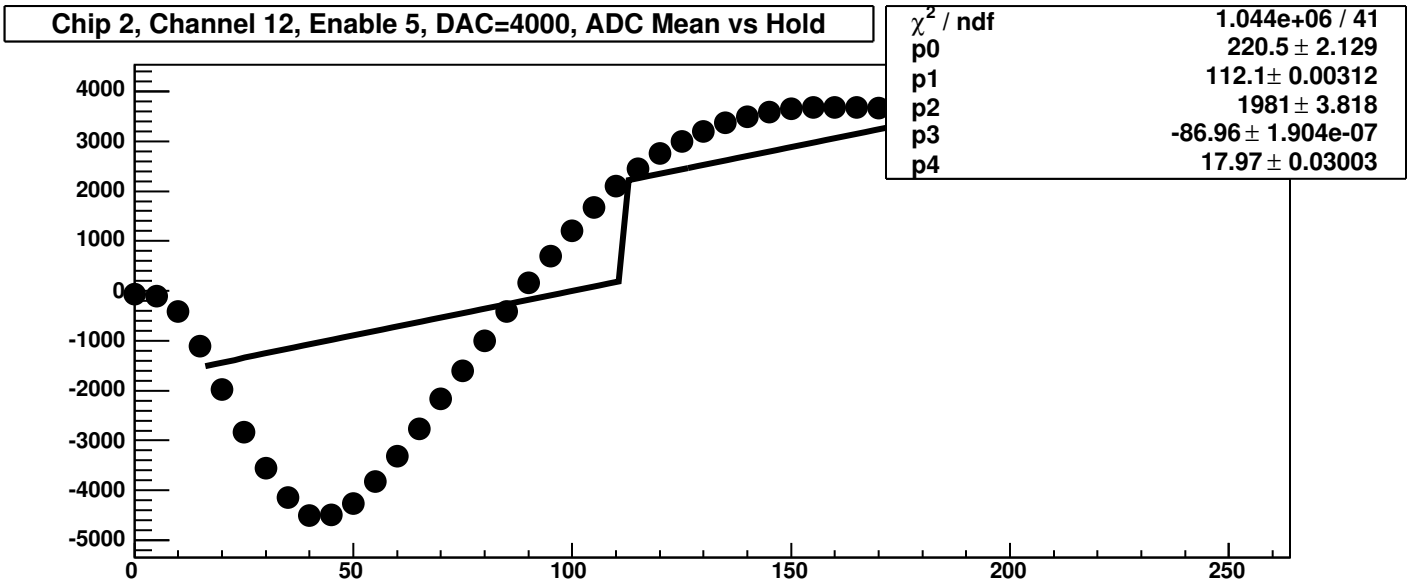


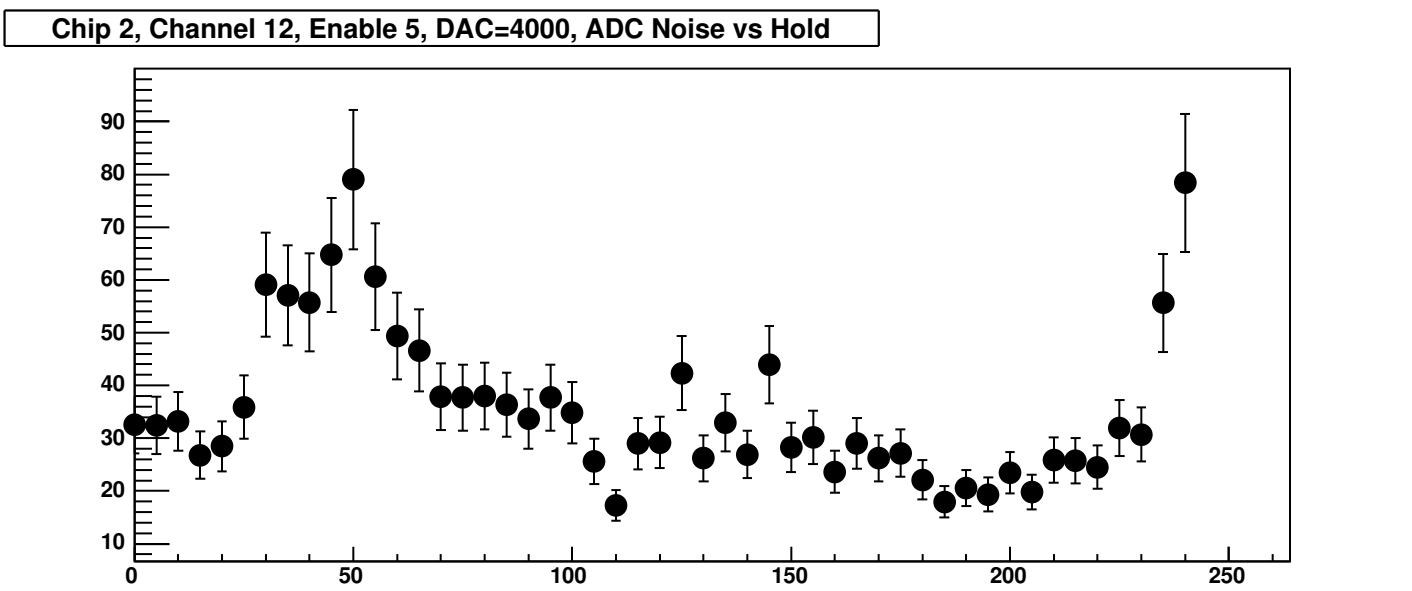
Chip 2, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold

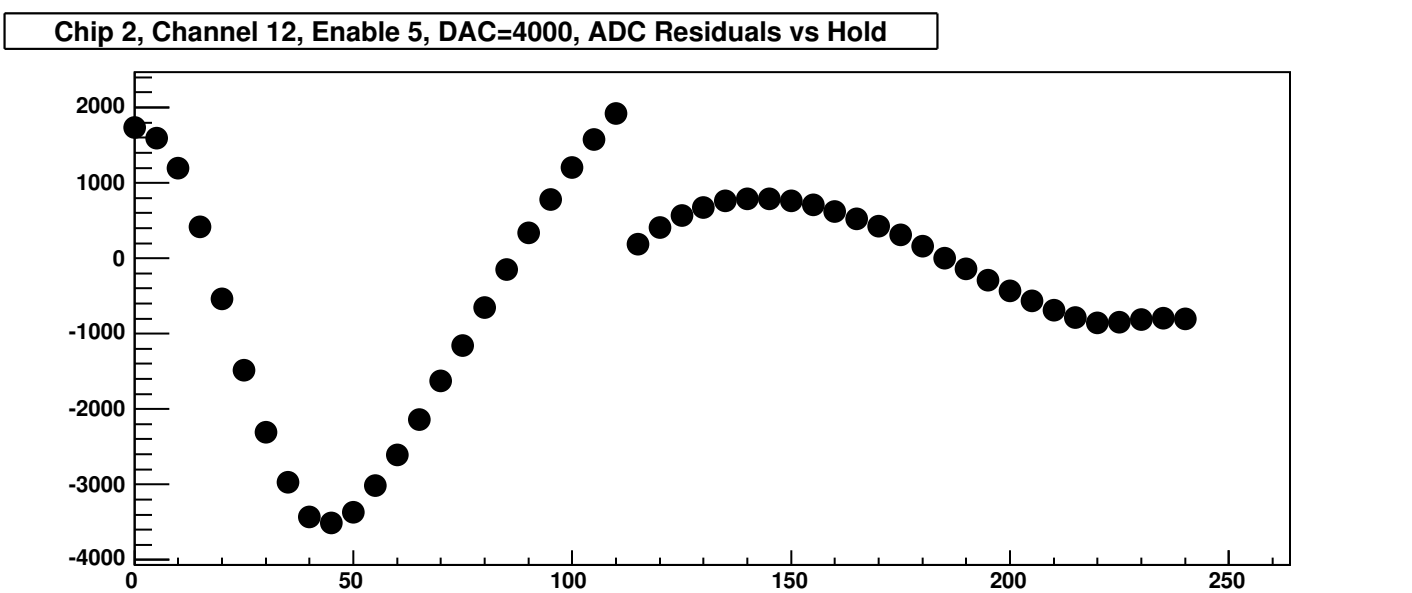


Chip 2, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold

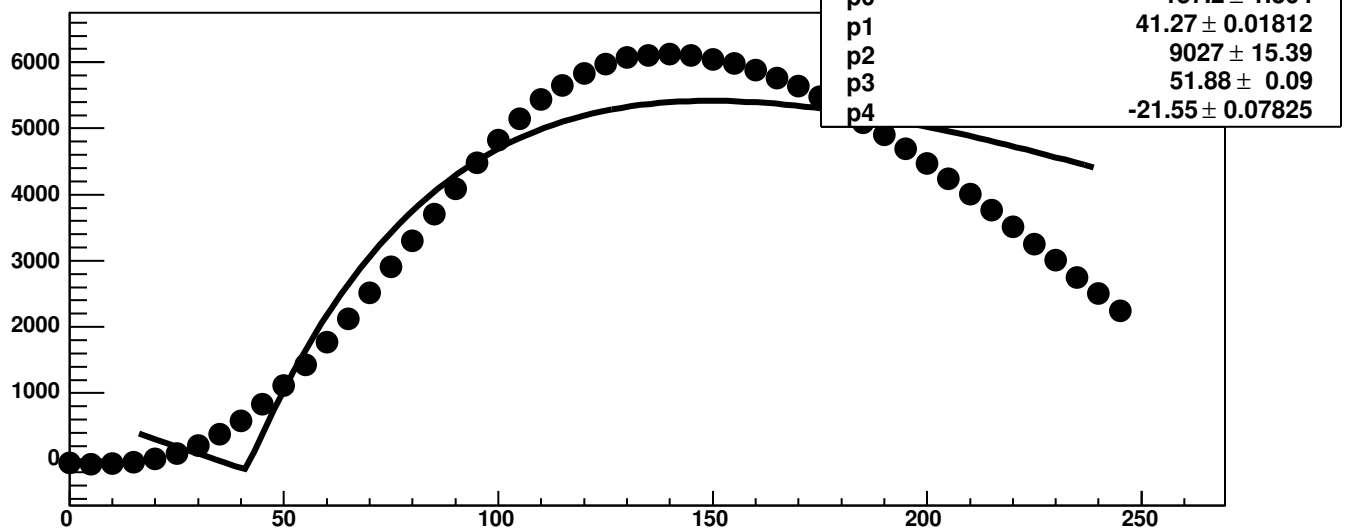



Chip 2, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.044e+06 / 41
p0 220.5 ± 2.129
p1 112.1 ± 0.00312
p2 1981 ± 3.818
p3 -86.96 ± 1.904e-07
p4 17.97 ± 0.03003
4000
3000
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

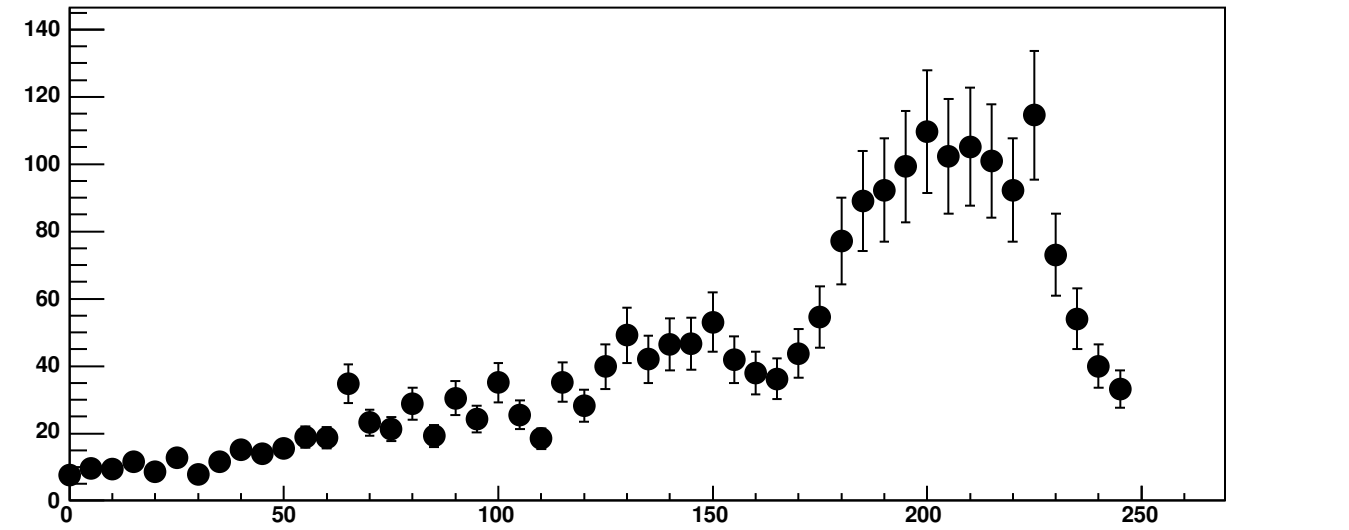

Chip 2, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold
90
80
70
60
50
40
30
20
10
0
50
100
150
200
250


Chip 2, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

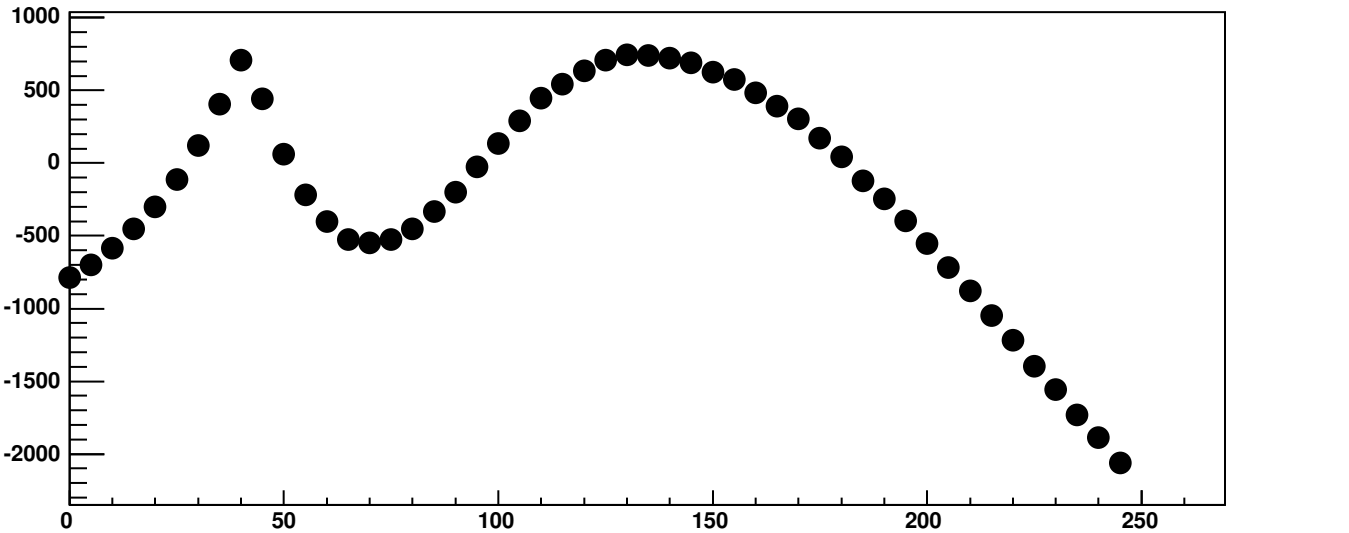
Chip 2, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold



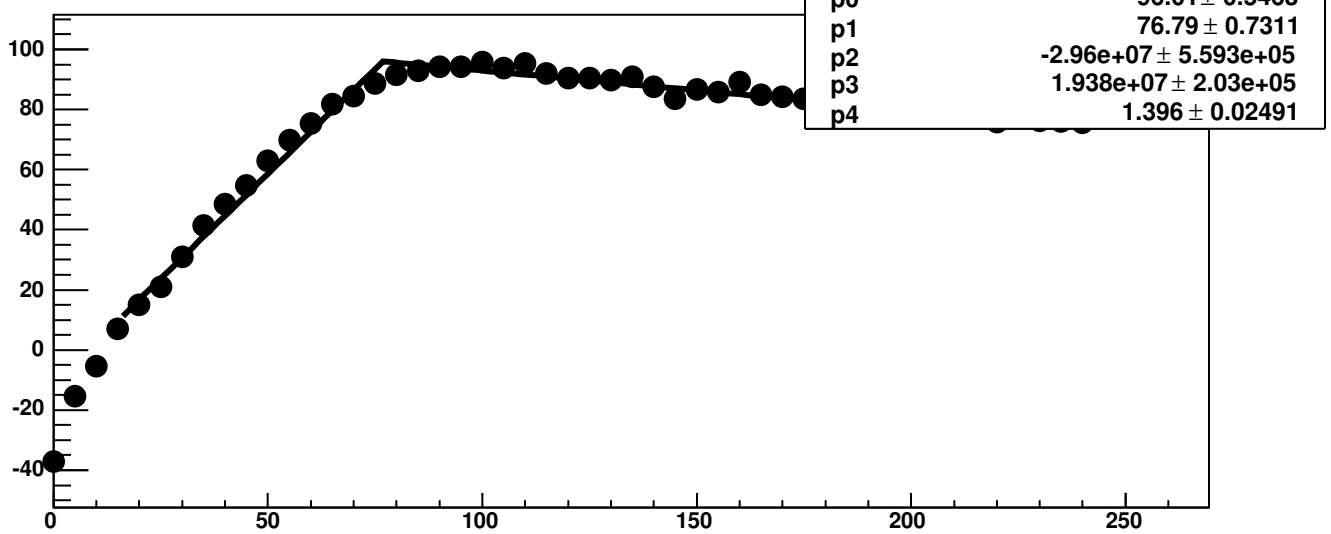
Chip 2, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



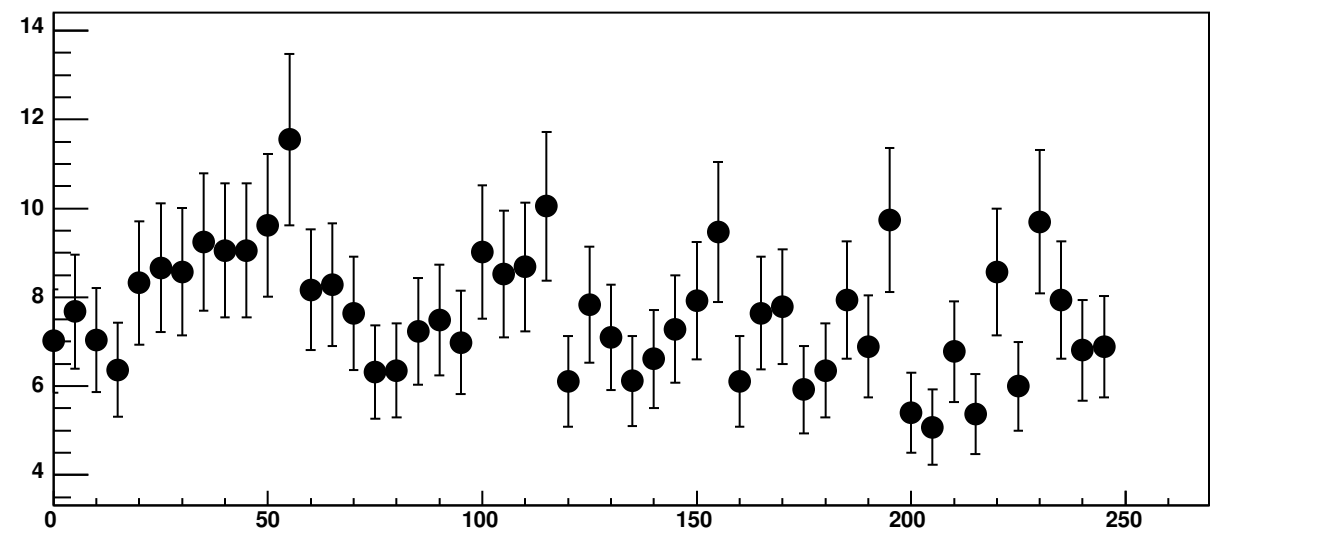
Chip 2, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold



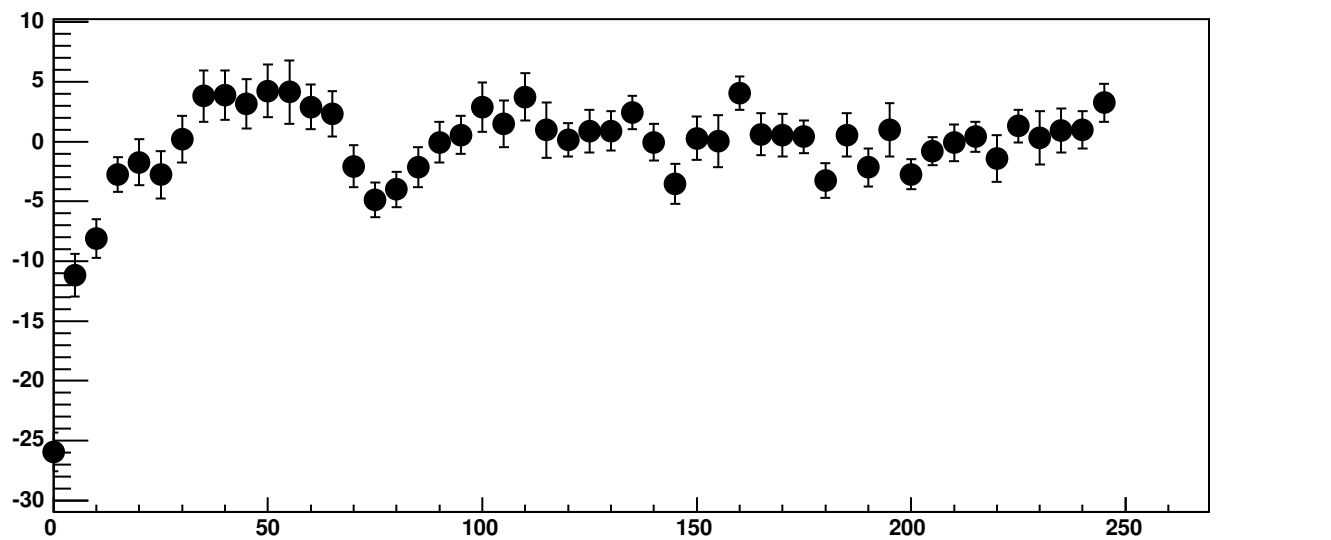
Chip 2, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold



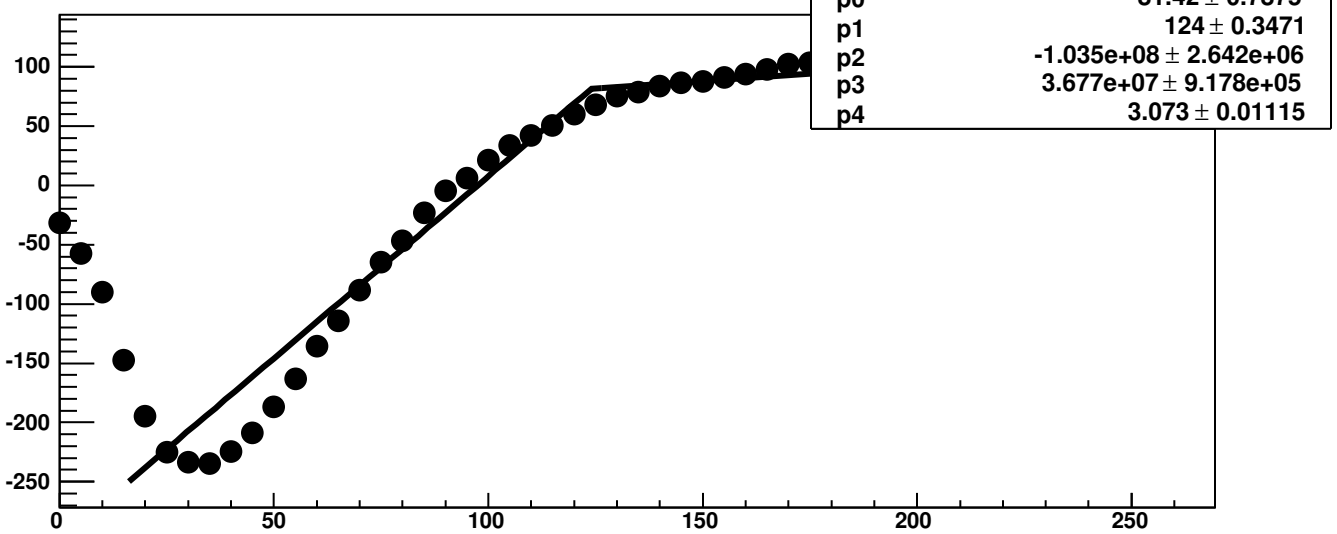
Chip 2, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold



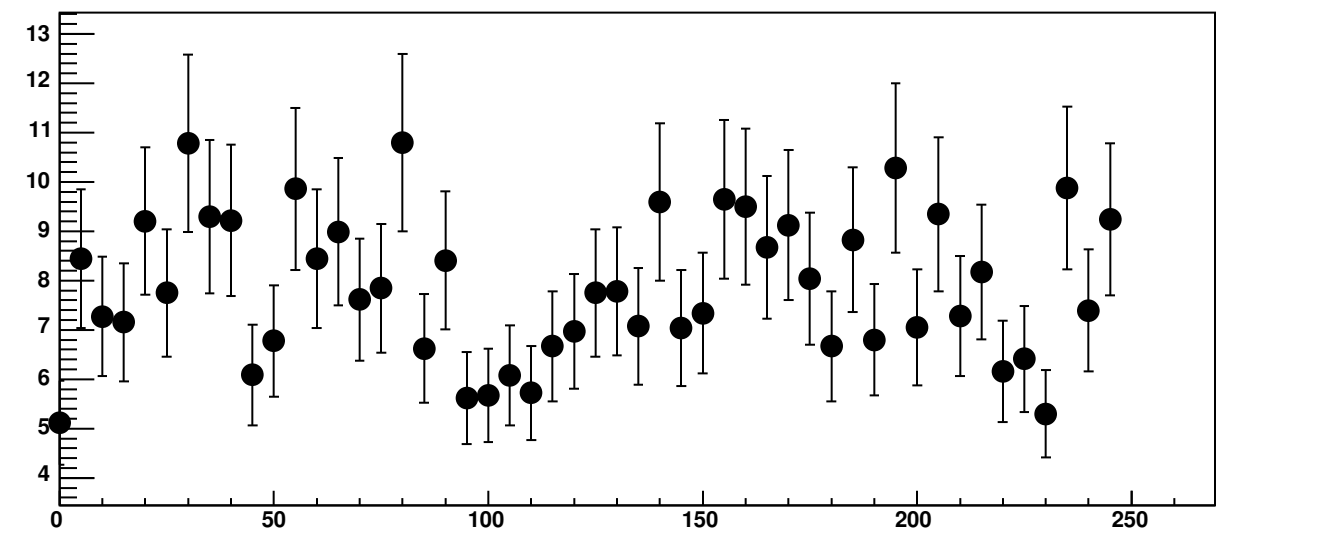
Chip 2, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold



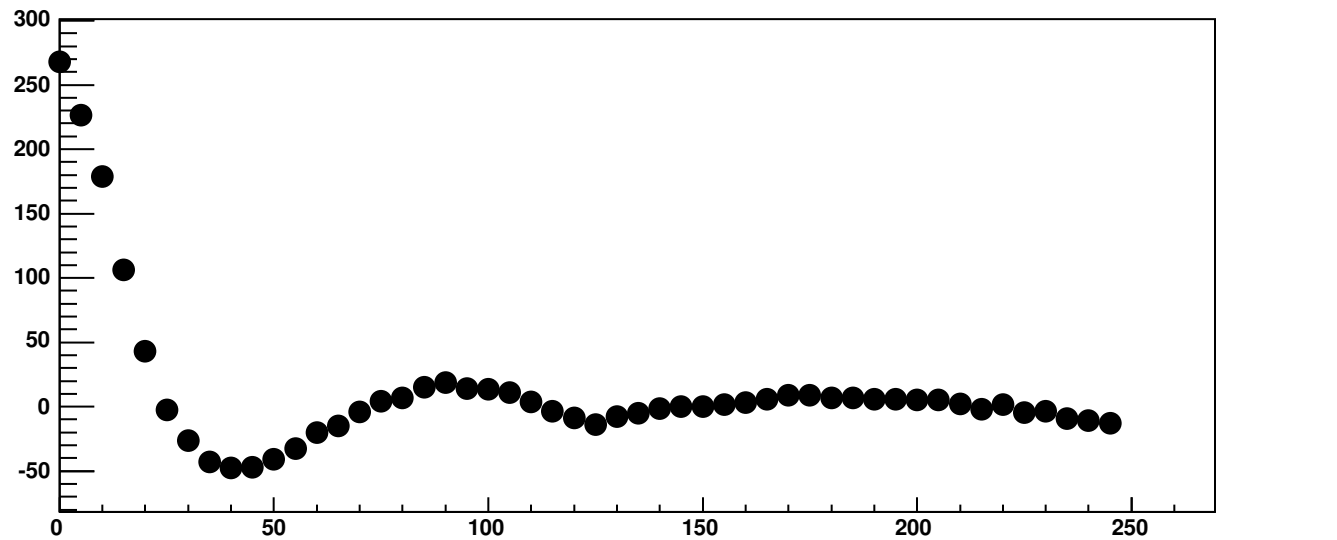
Chip 2, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold



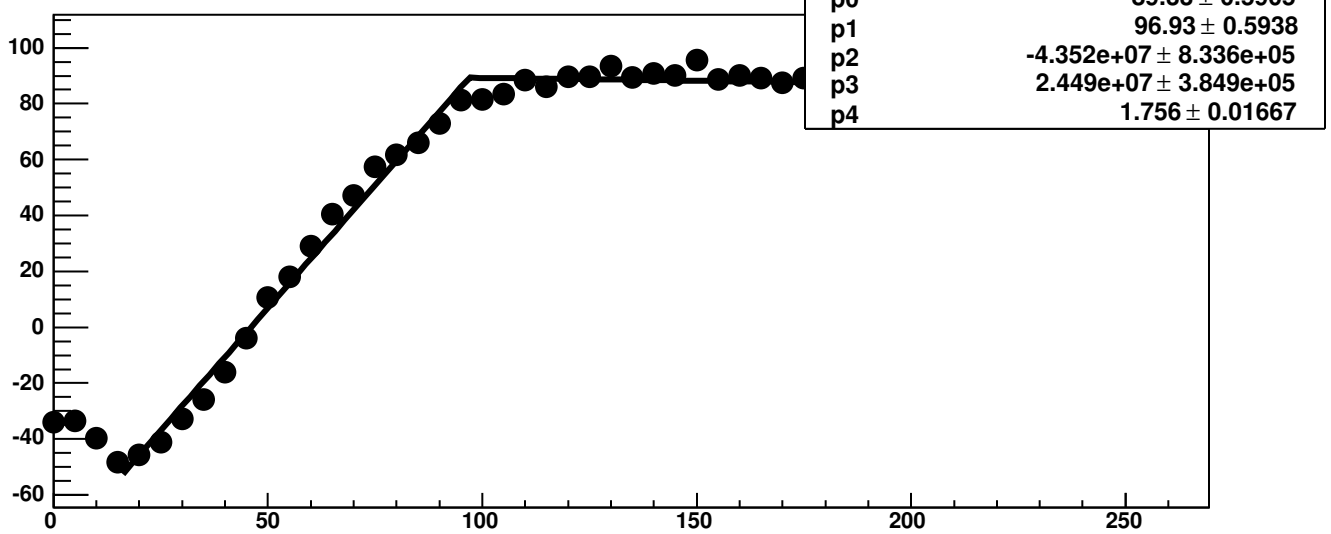
Chip 2, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



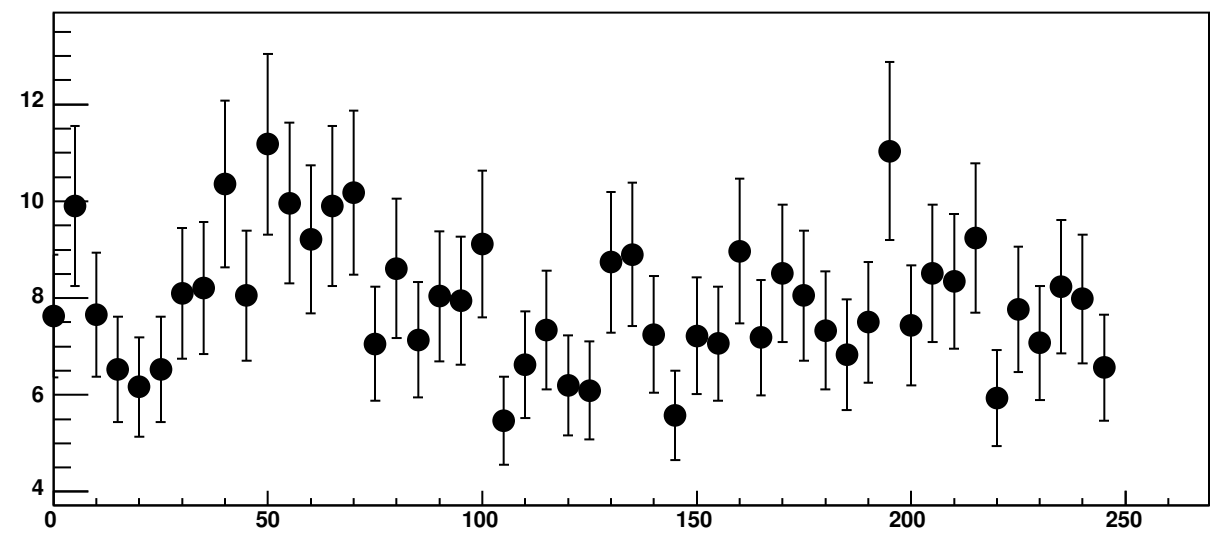
Chip 2, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold



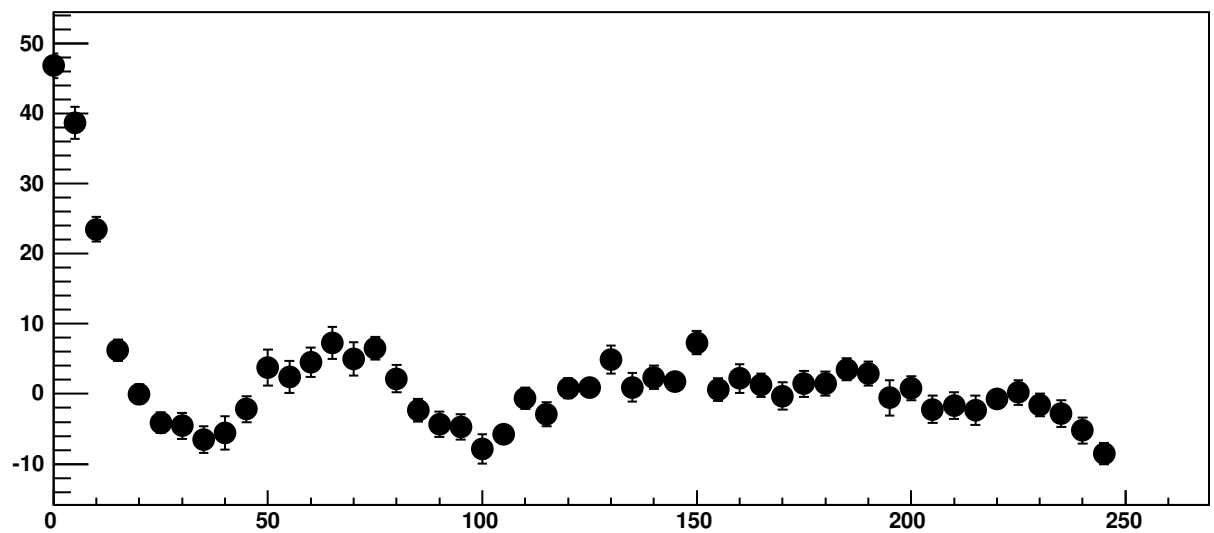
Chip 2, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold



Chip 2, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold

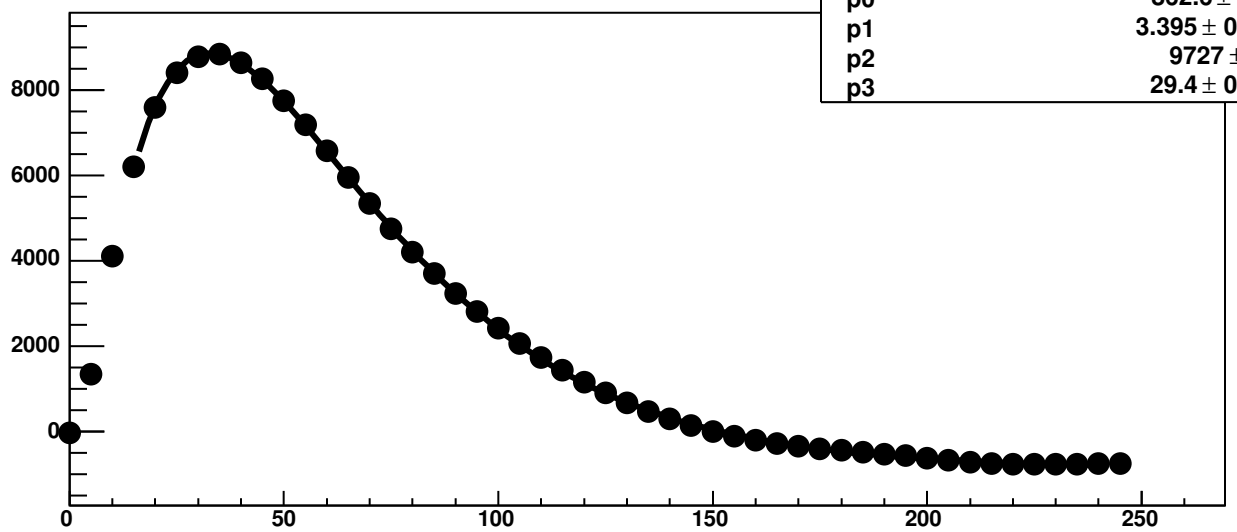


Chip 2, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

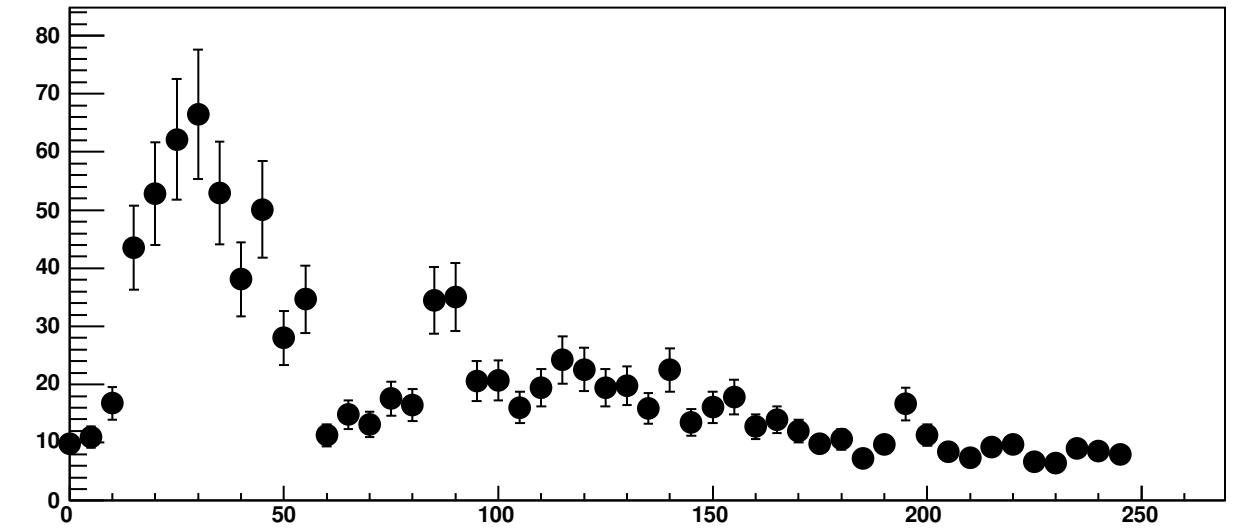


Chip 2, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold

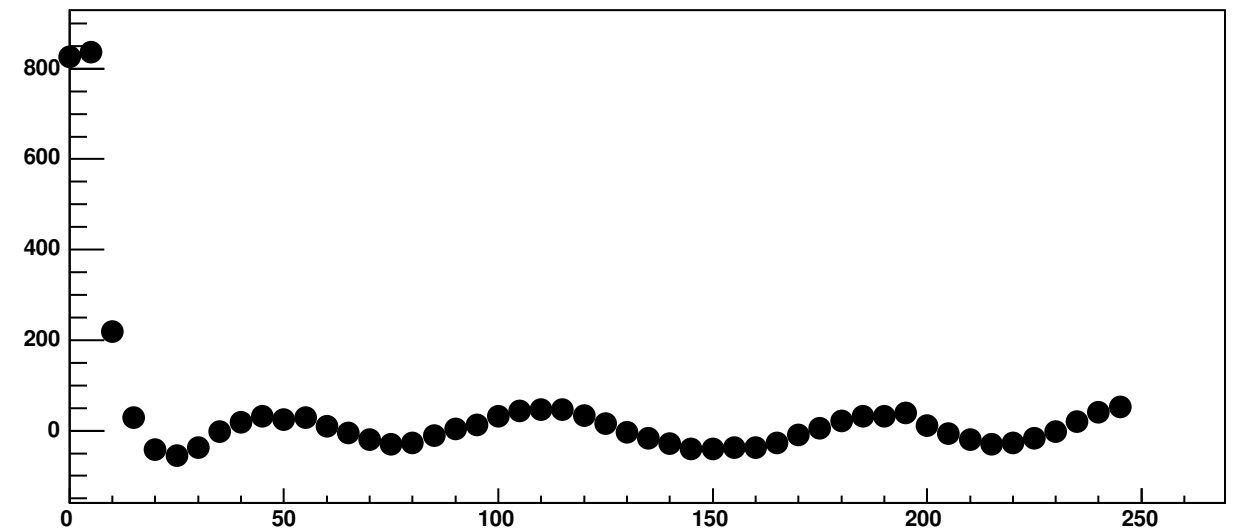
χ^2 / ndf	3268 / 42
p0	-862.6 ± 0.7092
p1	3.395 ± 0.02374
p2	9727 ± 3.105
p3	29.4 ± 0.01268

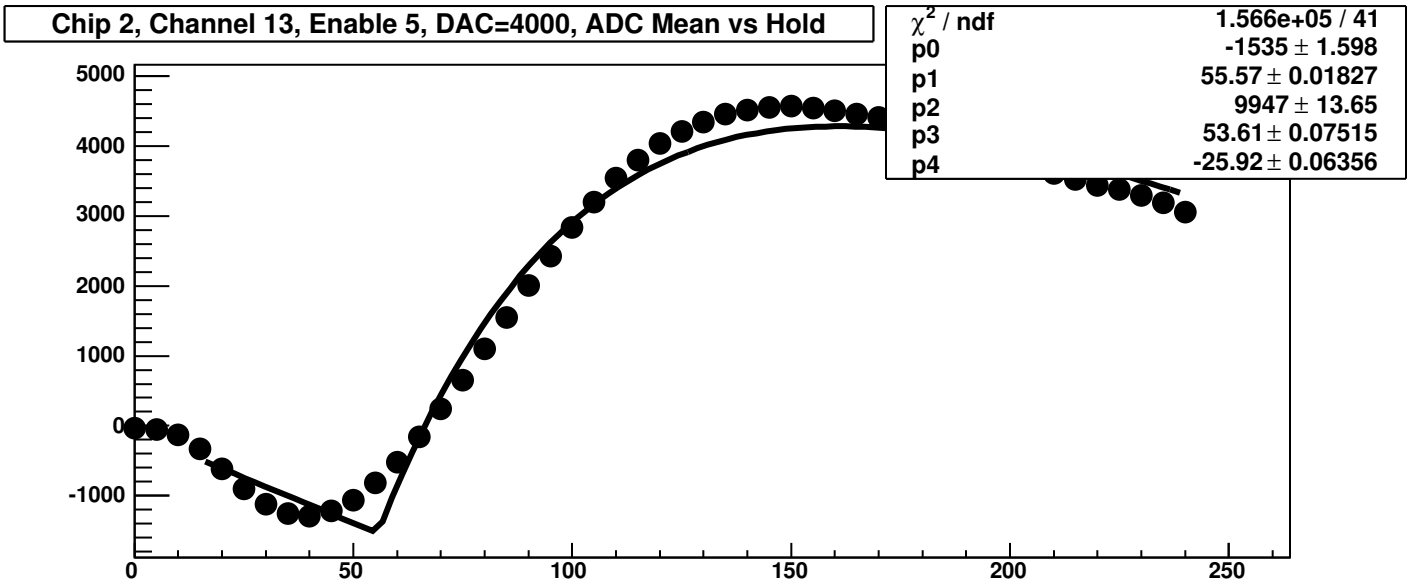


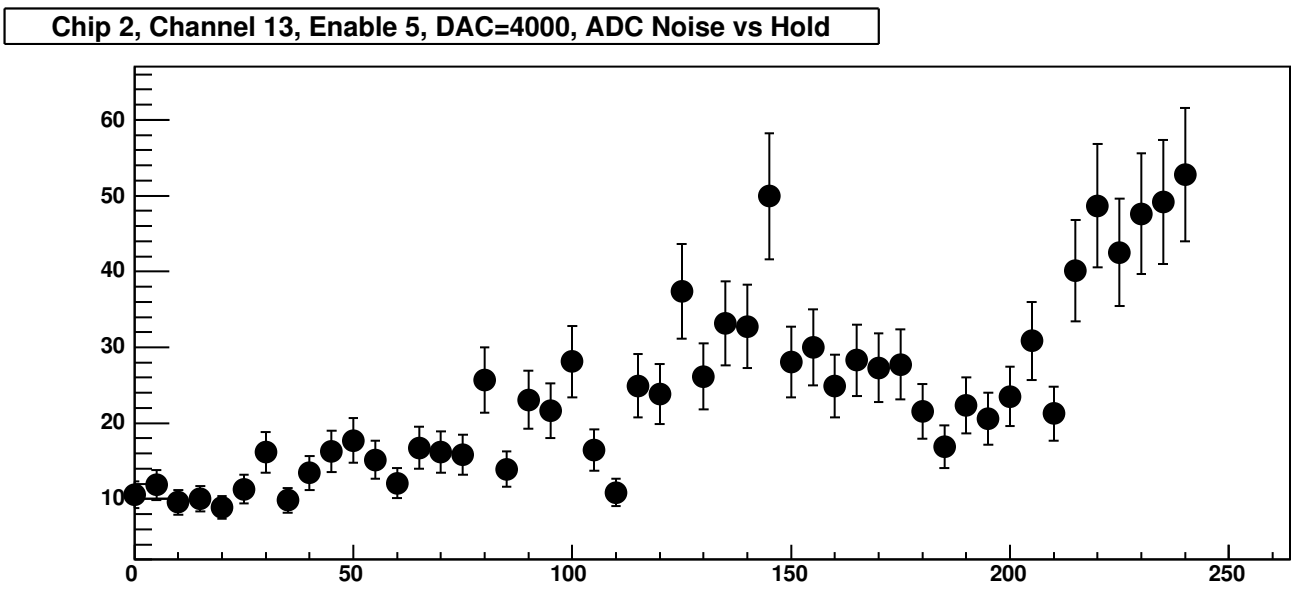
Chip 2, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold

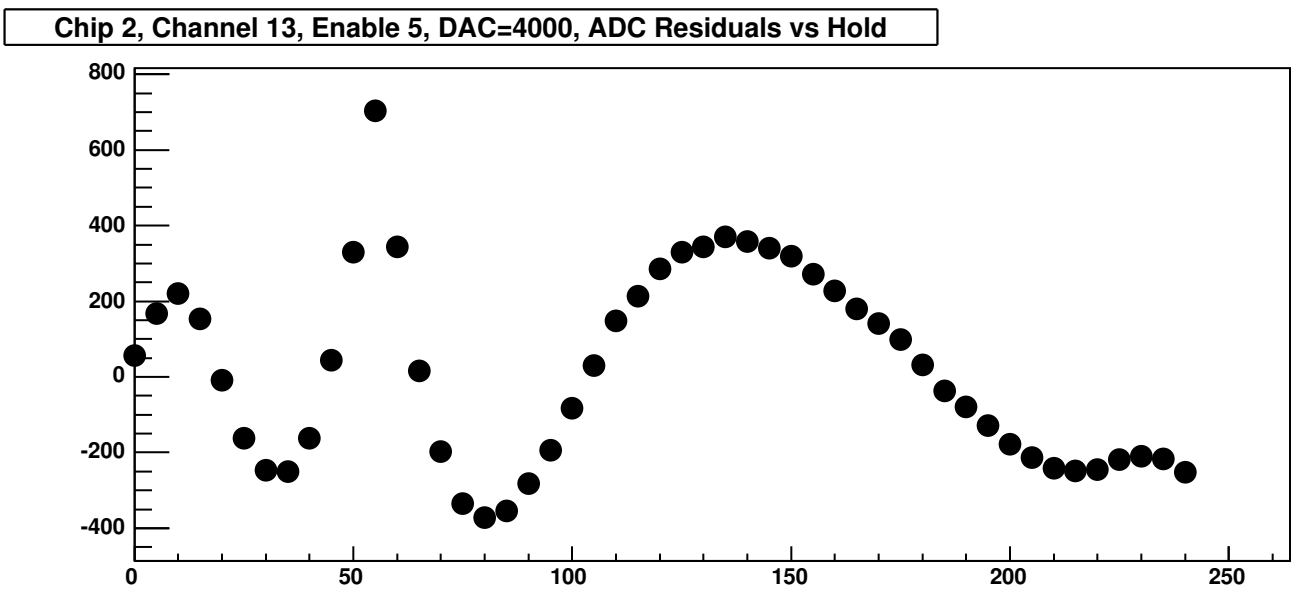


Chip 2, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold

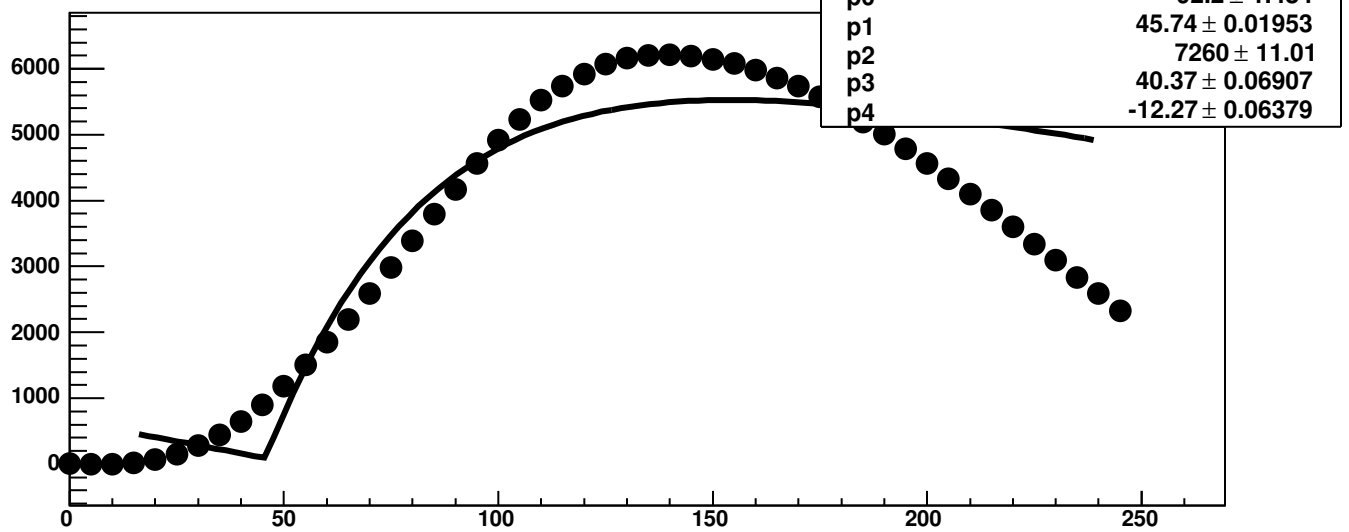



Chip 2, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.566e+05 / 41
p0 -1535 ± 1.598
p1 55.57 ± 0.01827
p2 9947 ± 13.65
p3 53.61 ± 0.07515
p4 -25.92 ± 0.06356
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

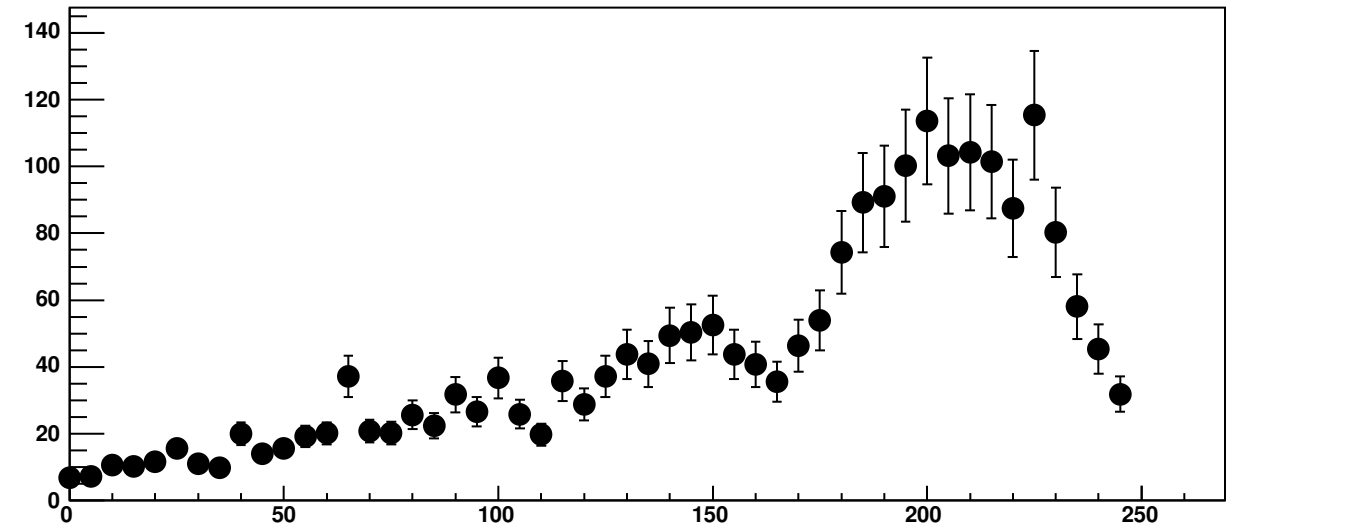

Chip 2, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold
60
50
40
30
20
10
0
50
100
150
200
250


Chip 2, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold
800
600
400
200
0
-200
-400
0
50
100
150
200
250

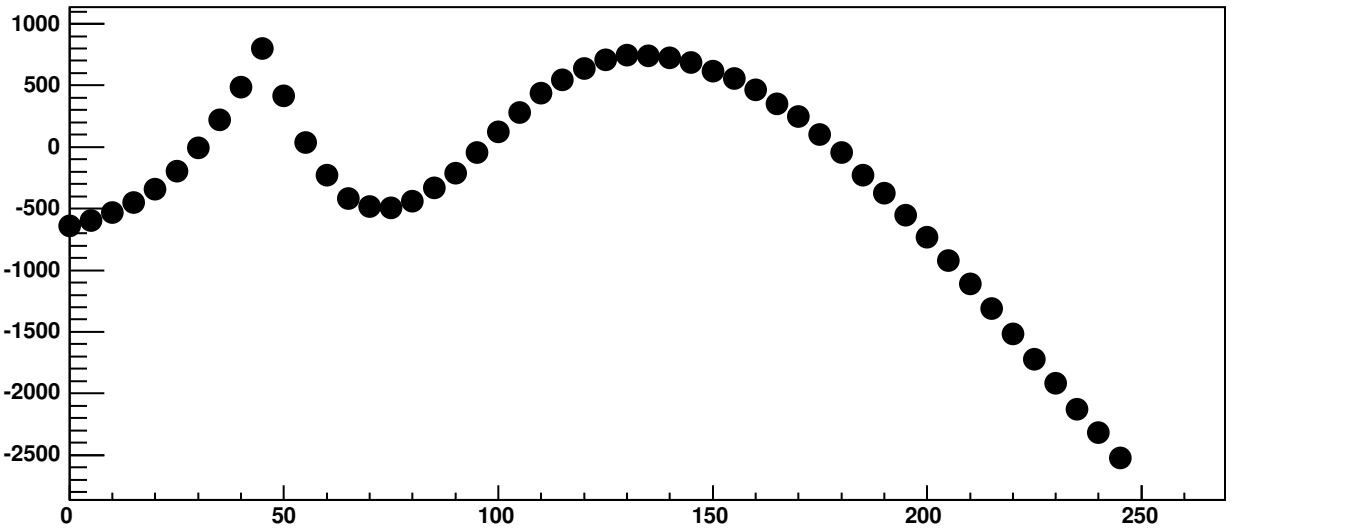
Chip 2, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold

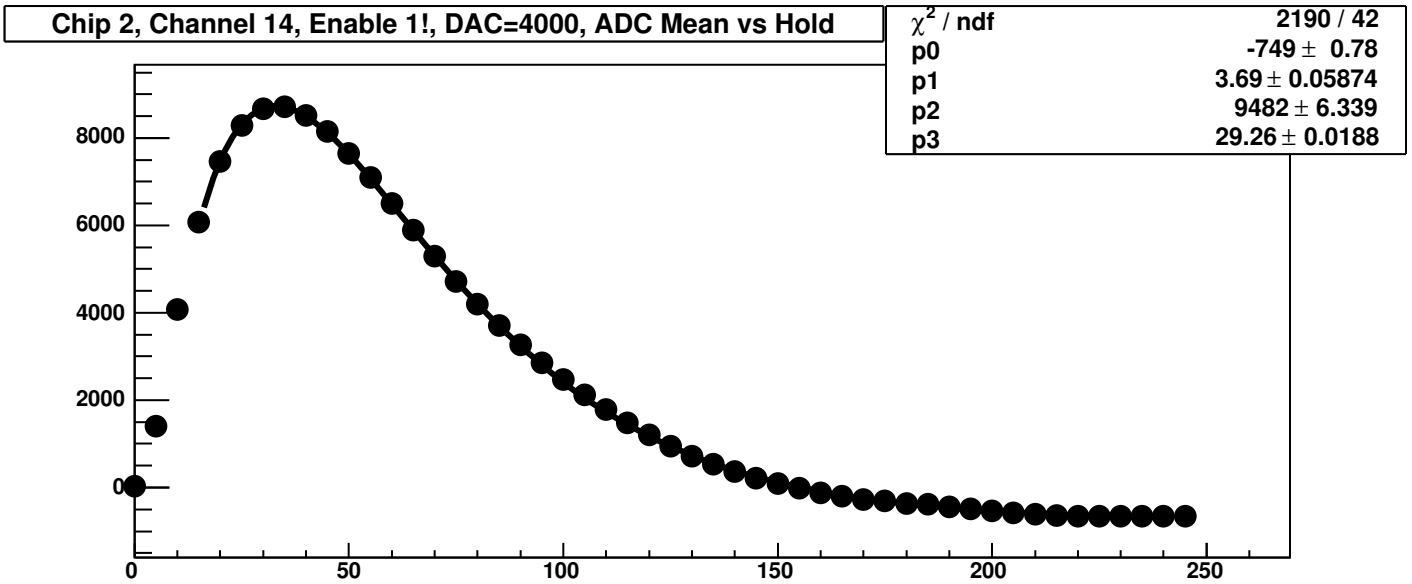


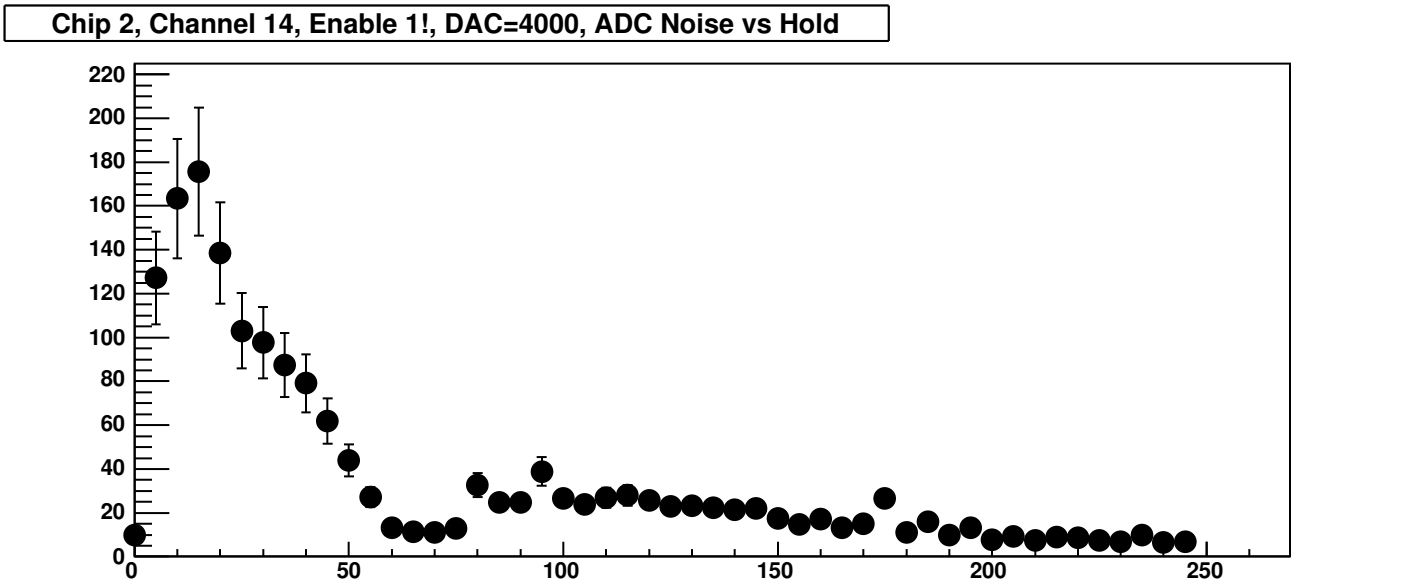
Chip 2, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold

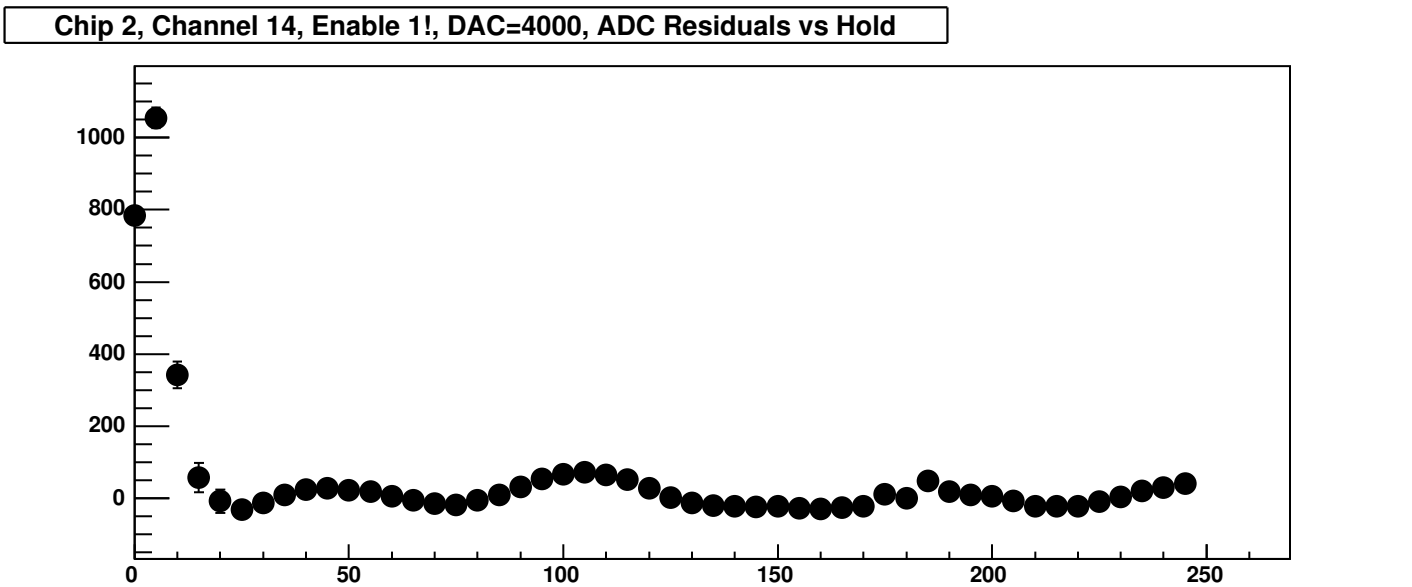


Chip 2, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

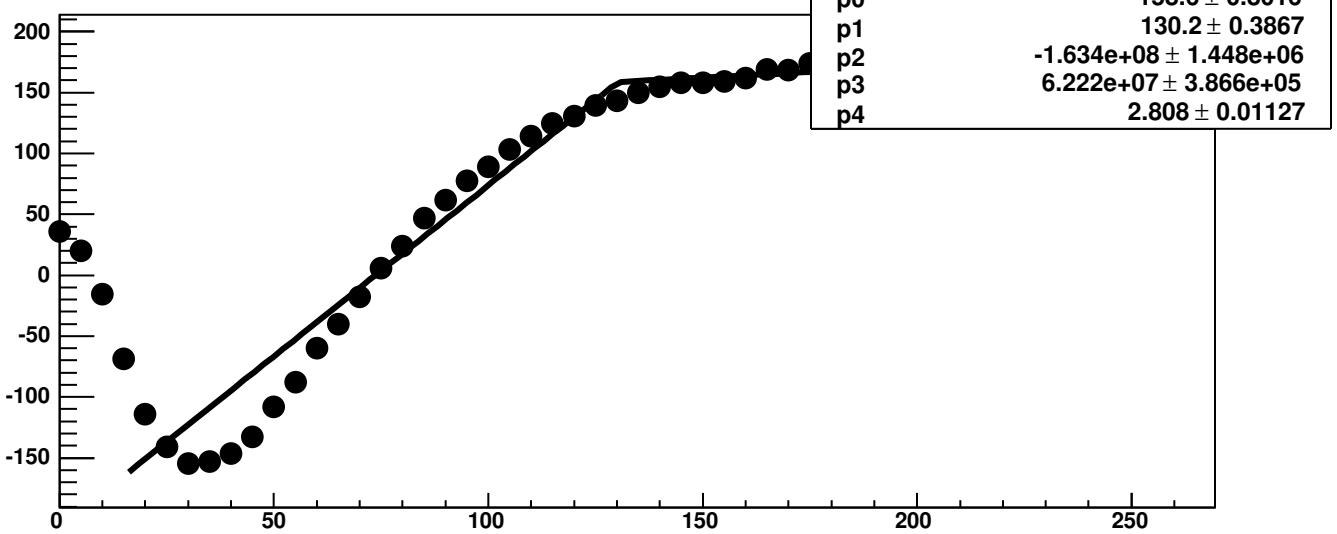



Chip 2, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 2190 / 42
p0 -749 ± 0.78
p1 3.69 ± 0.05874
p2 9482 ± 6.339
p3 29.26 ± 0.0188
8000
6000
4000
2000
0
0
50
100
150
200
250

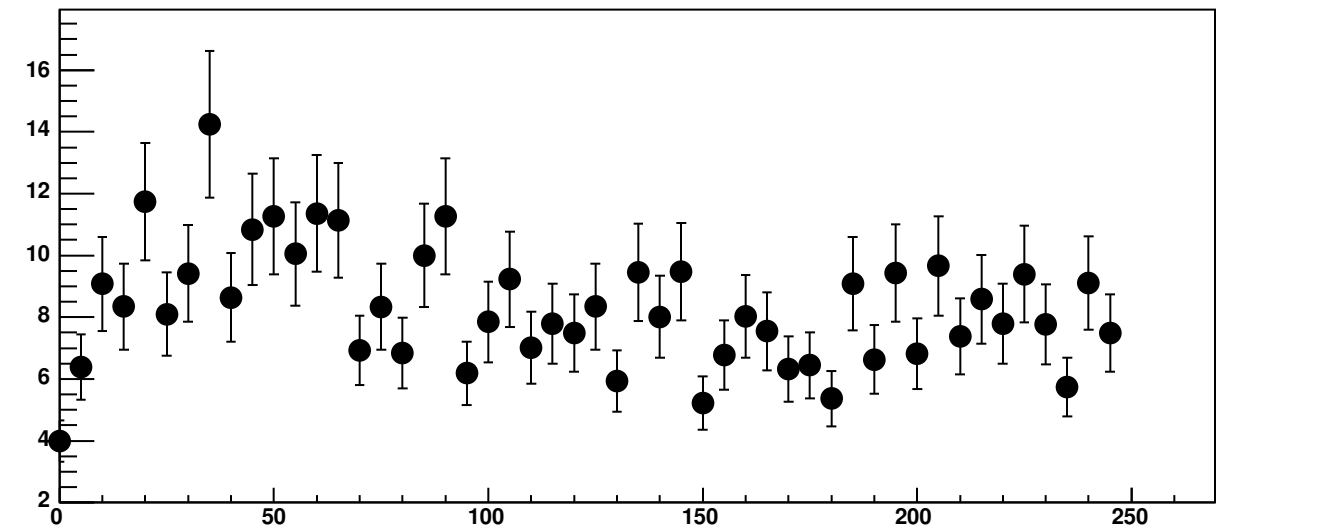

Chip 2, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 2, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold
1000
800
600
400
200
0
0
50
100
150
200
250

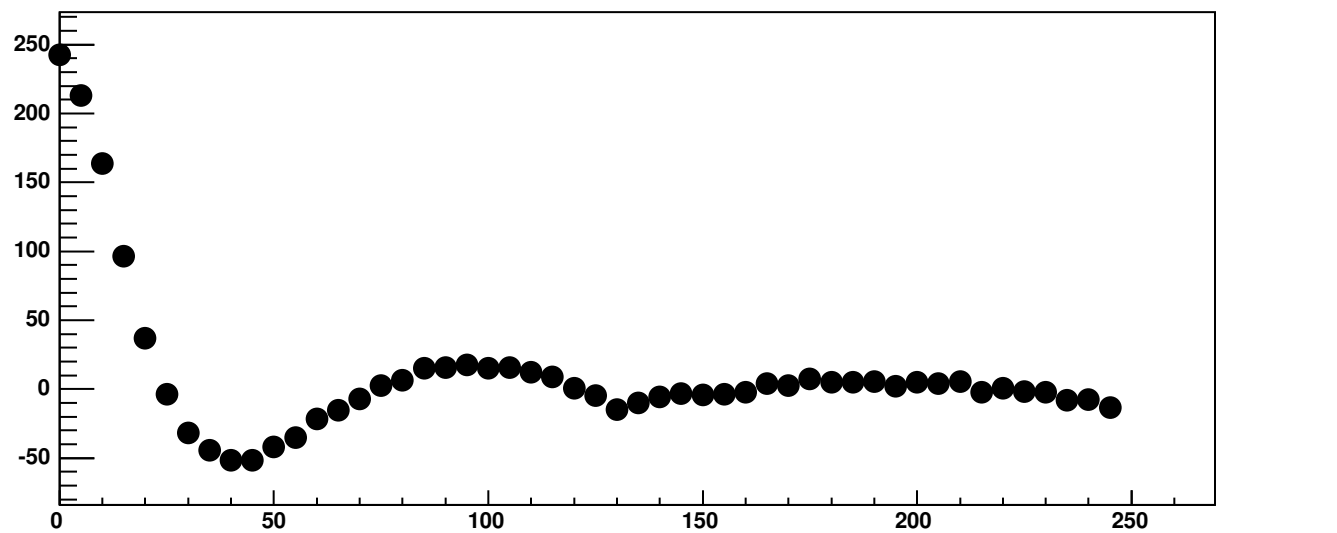
Chip 2, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold



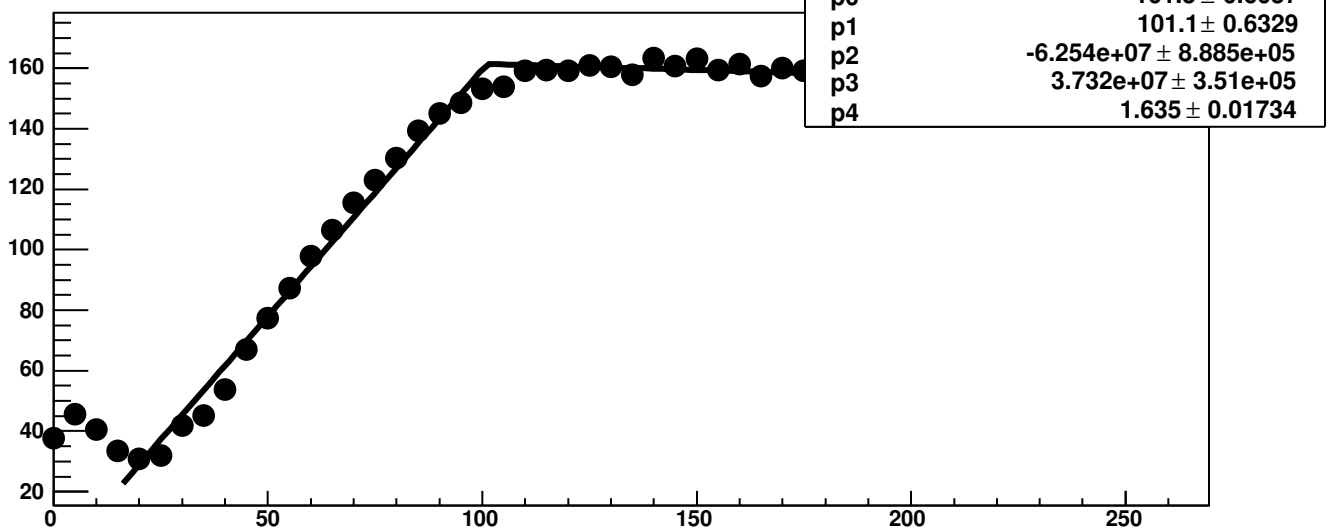
Chip 2, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



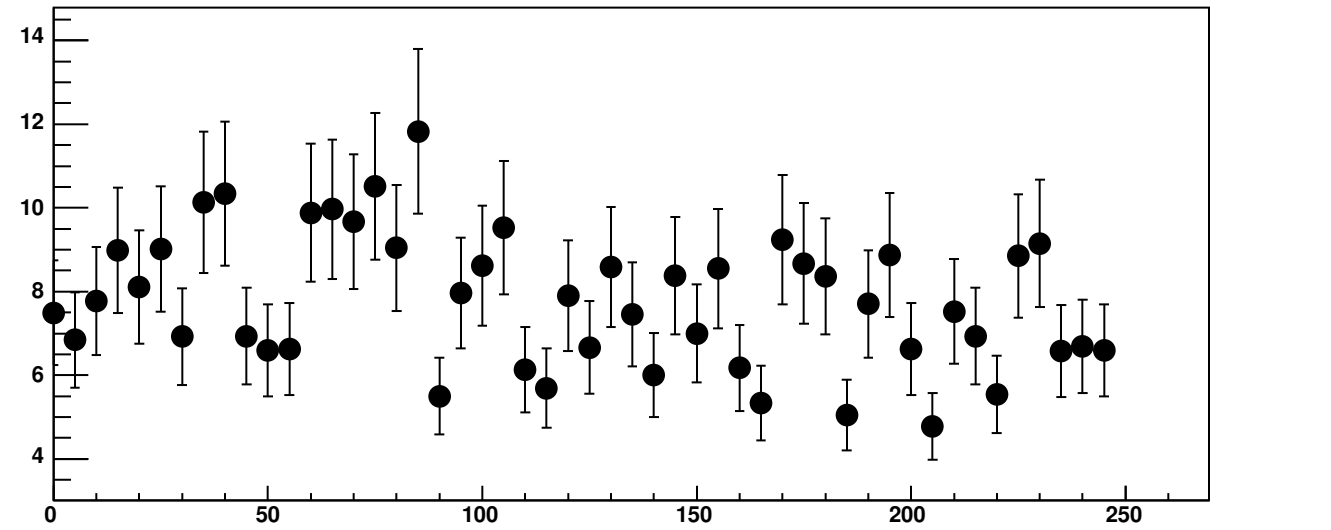
Chip 2, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold



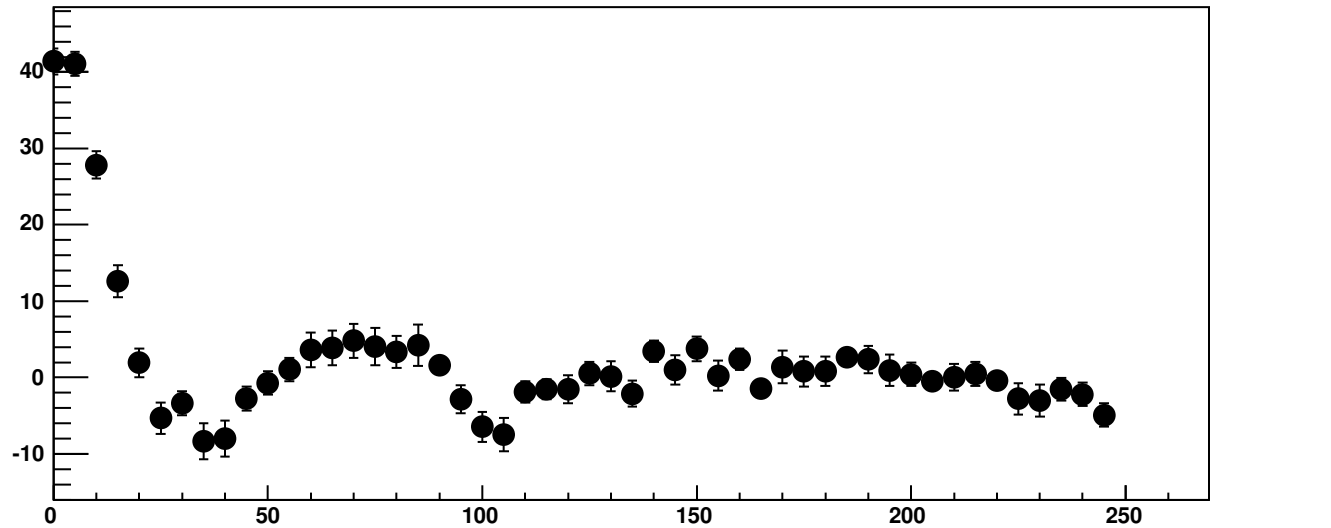
Chip 2, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

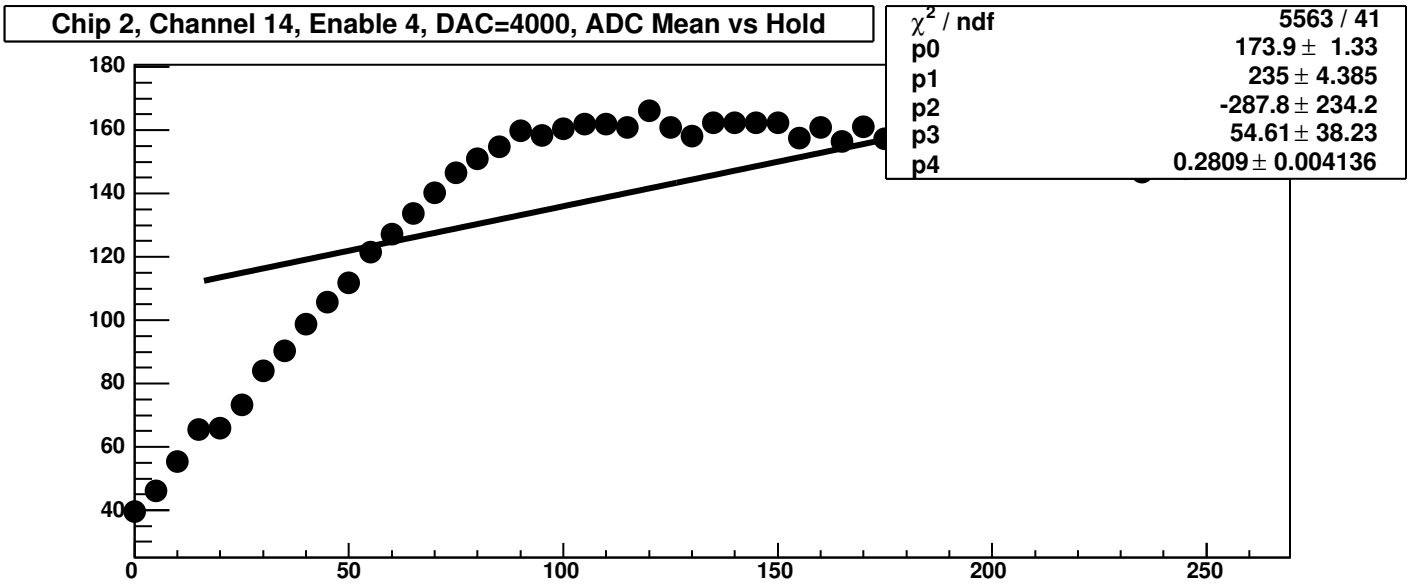


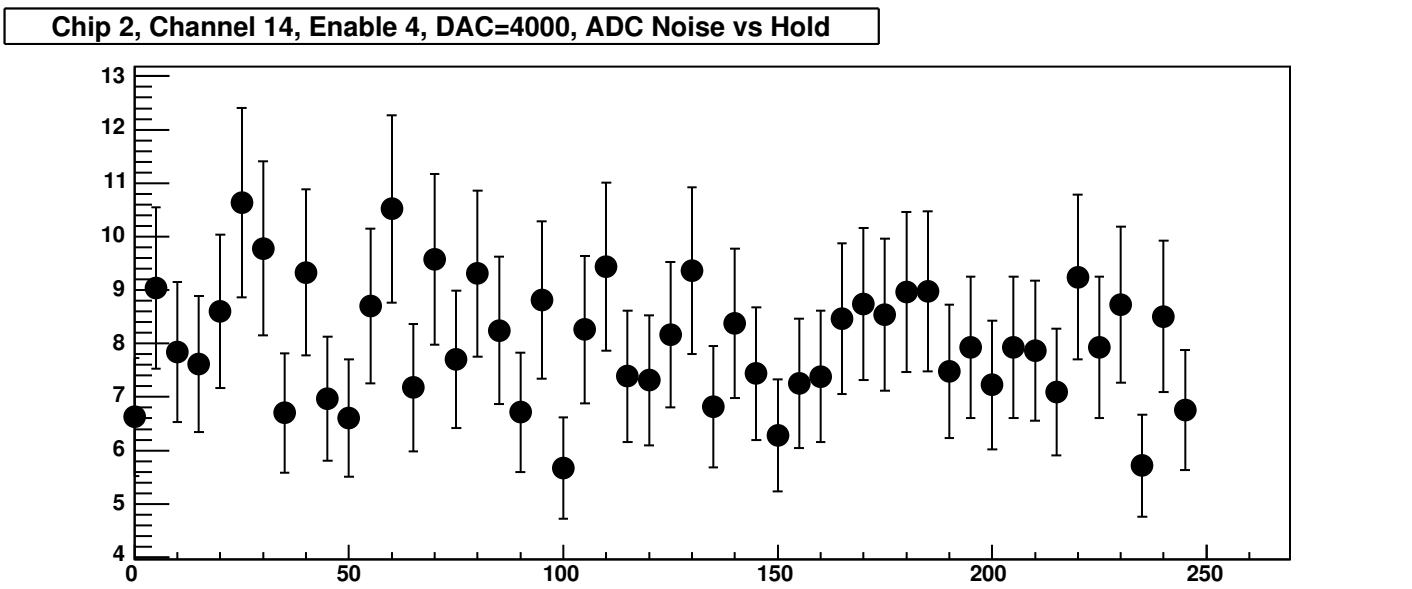
Chip 2, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold

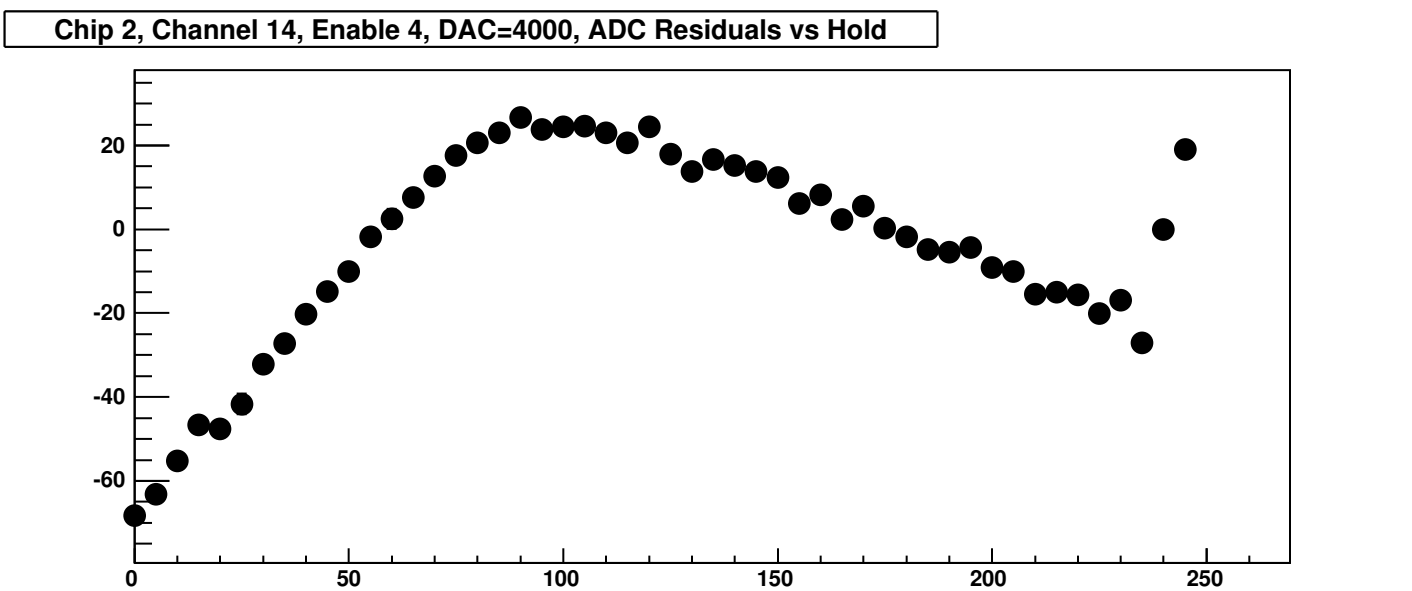


Chip 2, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

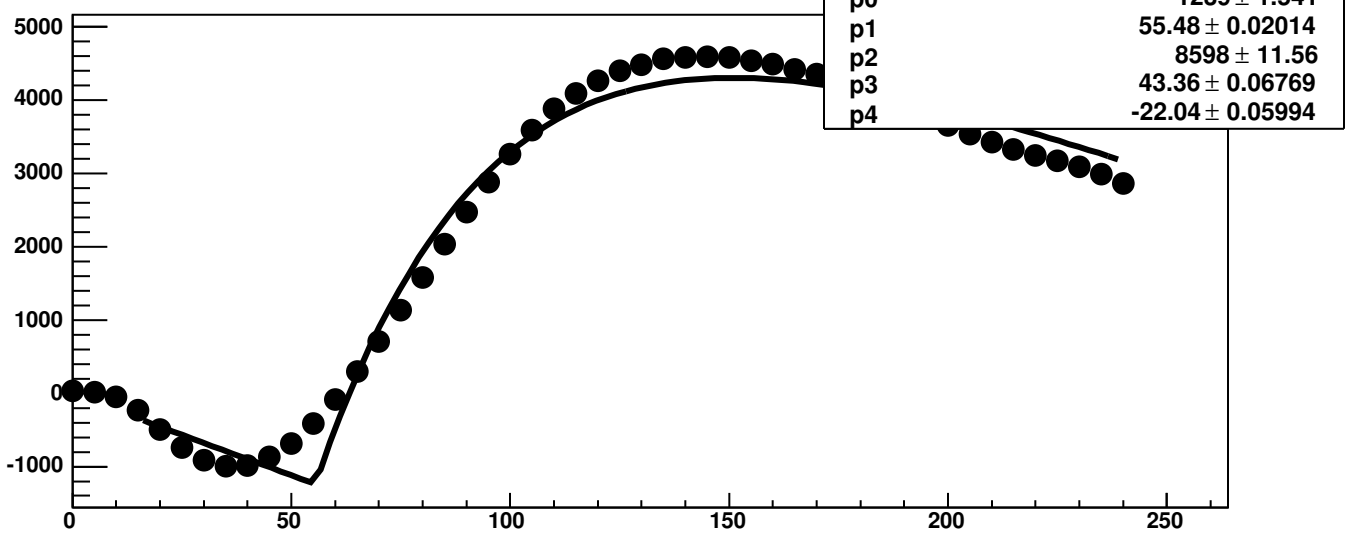



Chip 2, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 5563 / 41
p0 173.9 ± 1.33
p1 235 ± 4.385
p2 -287.8 ± 234.2
p3 54.61 ± 38.23
p4 0.2809 ± 0.004136
180
160
140
120
100
80
60
40
0
50
100
150
200
250

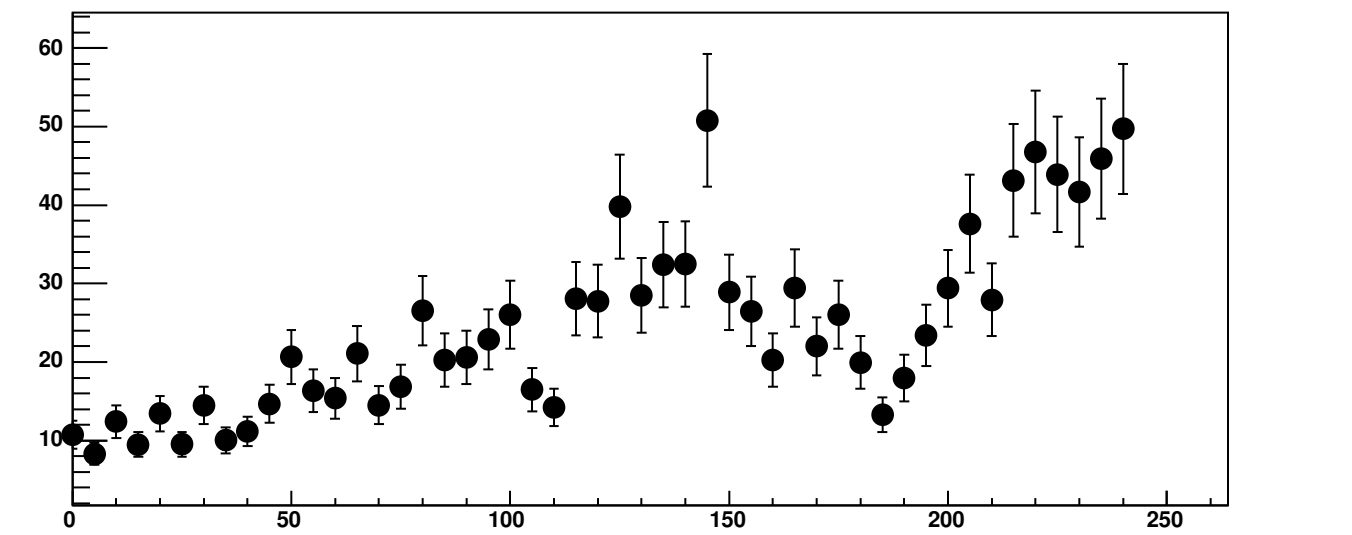

Chip 2, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 2, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
0
50
100
150
200
250

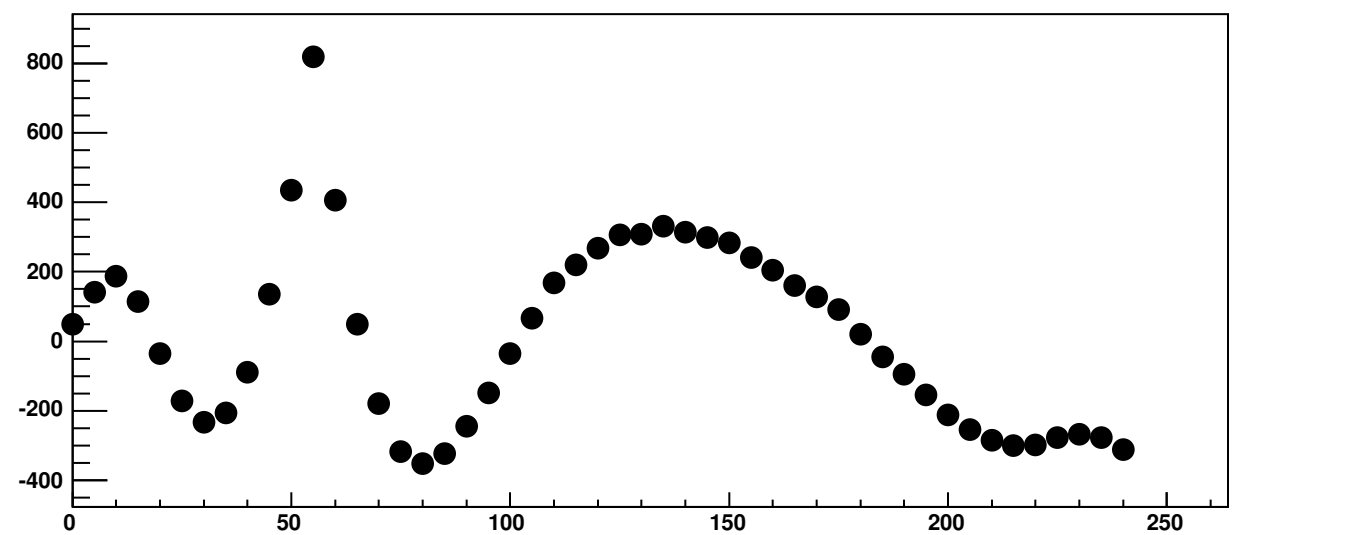
Chip 2, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold



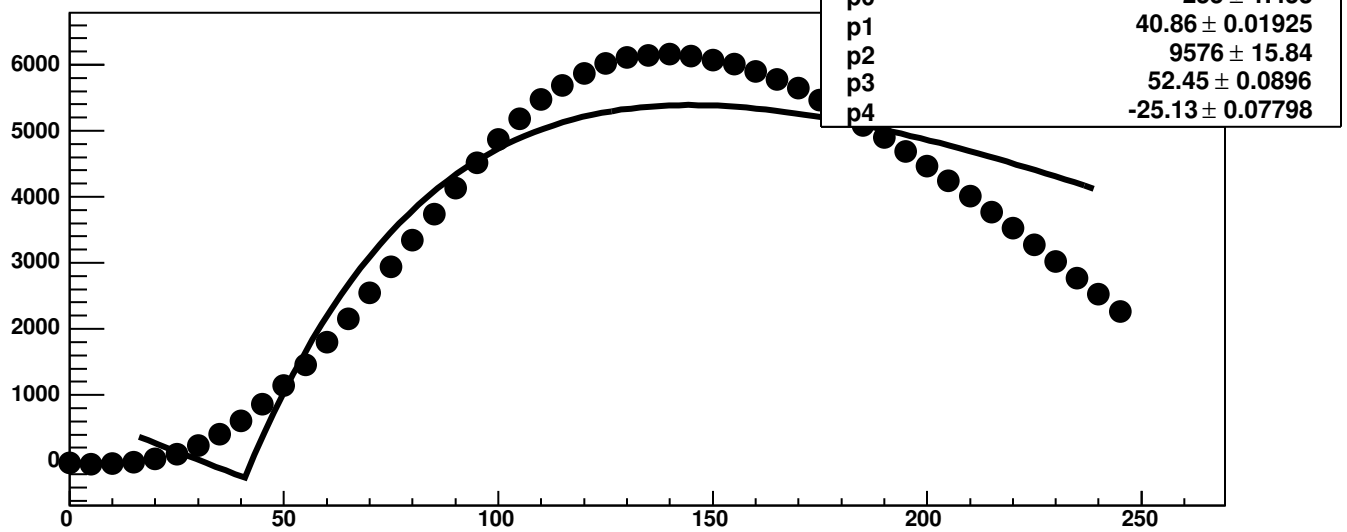
Chip 2, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold



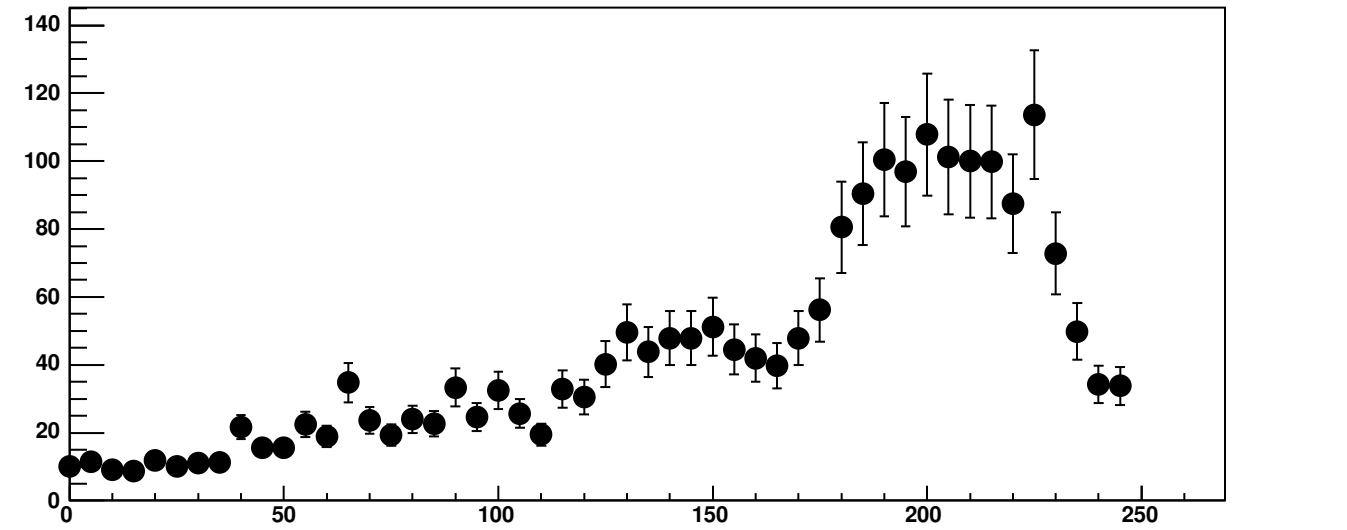
Chip 2, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold



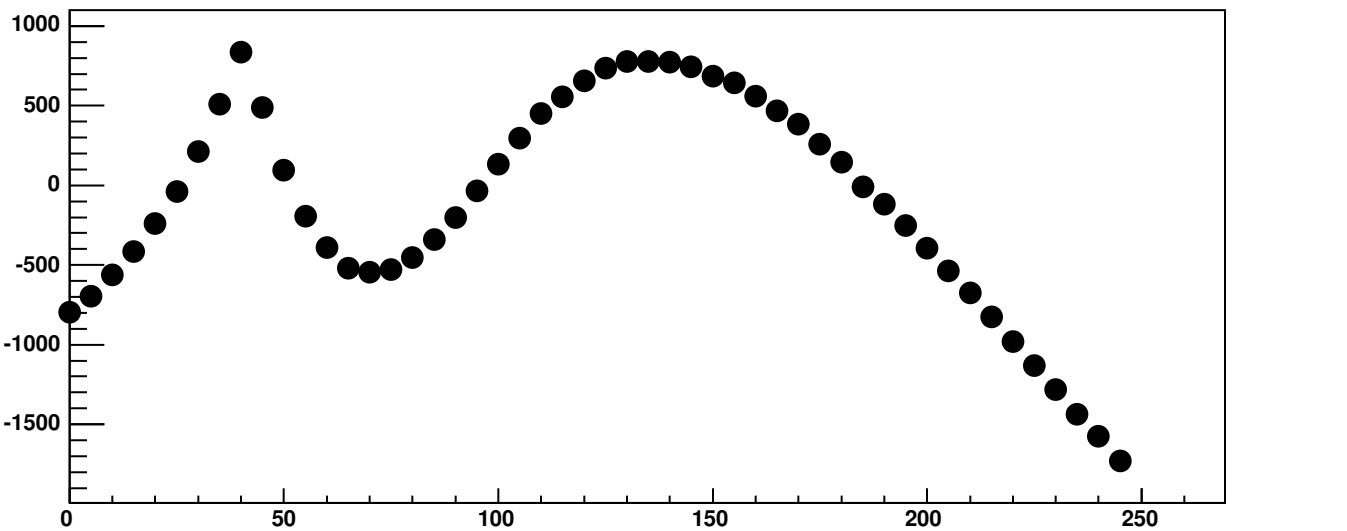
Chip 2, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold



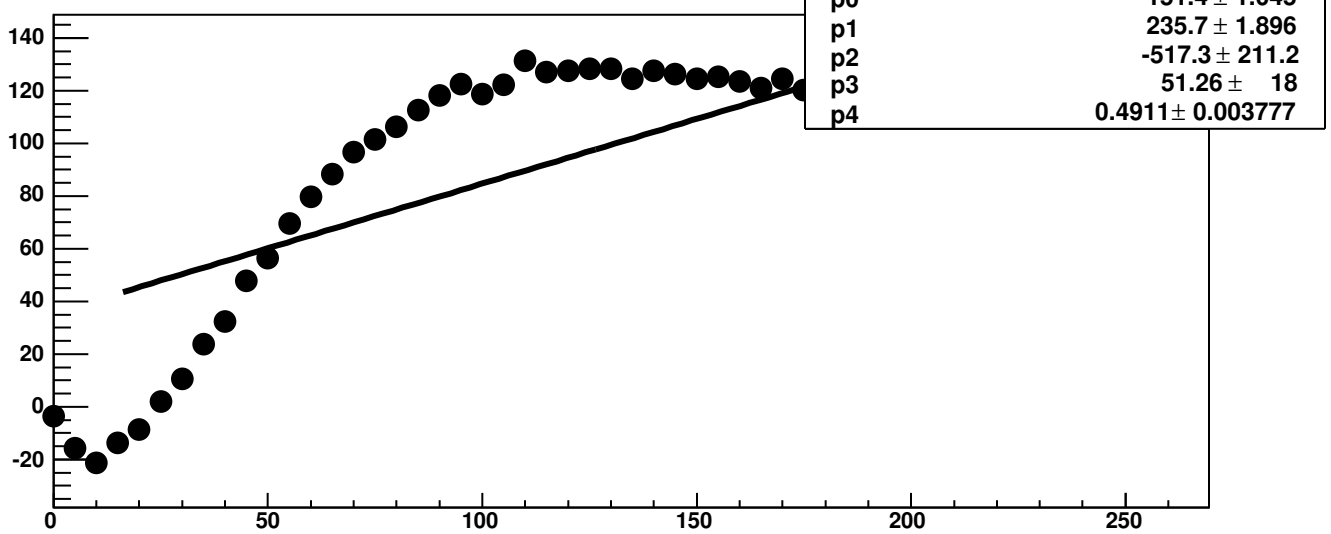
Chip 2, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



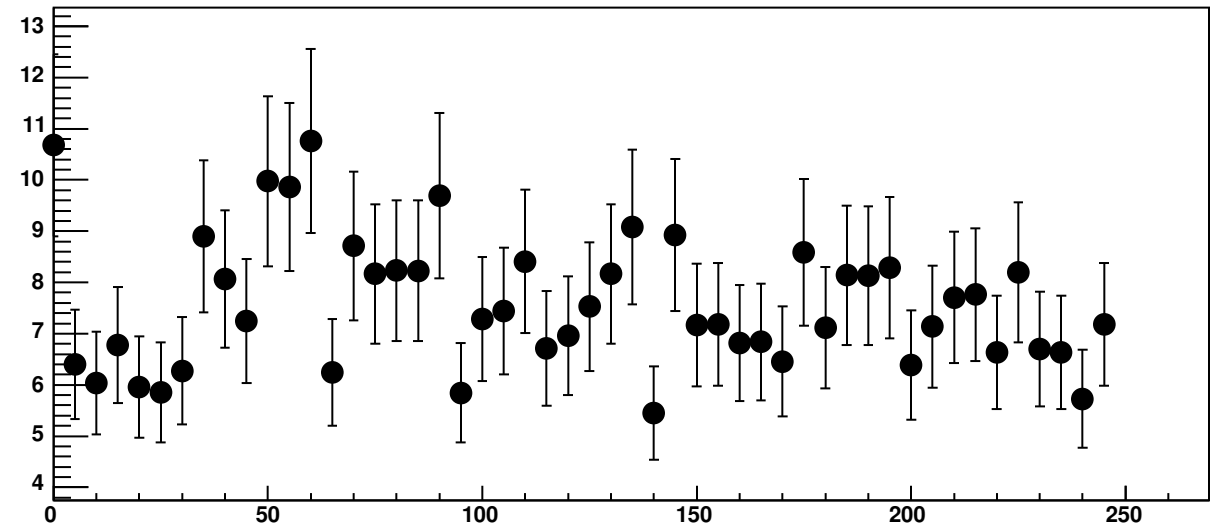
Chip 2, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold



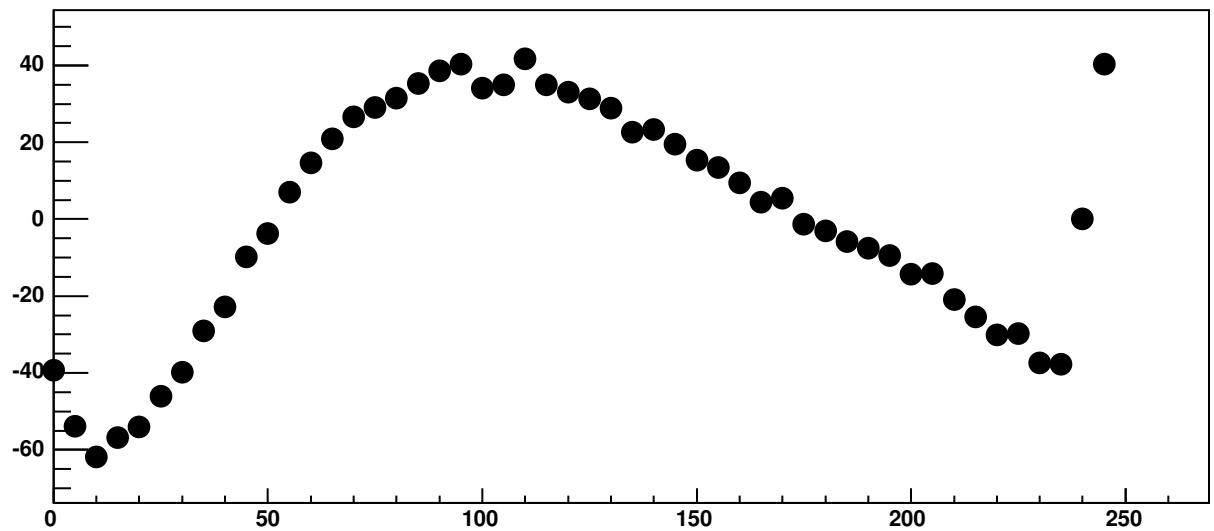
Chip 2, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold



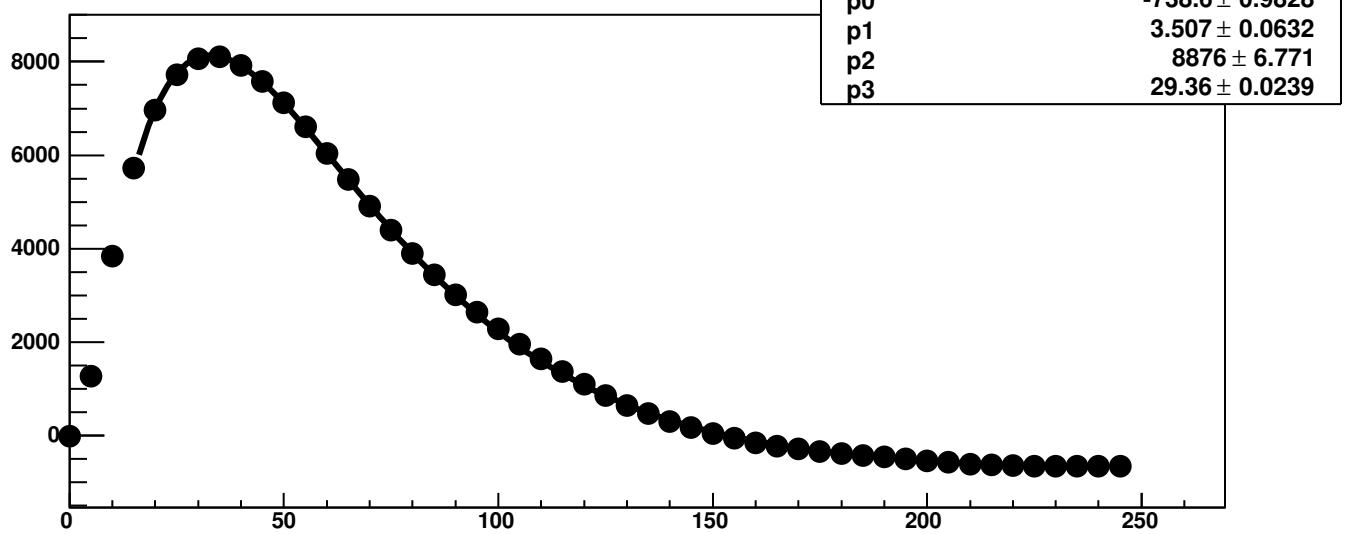
Chip 2, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



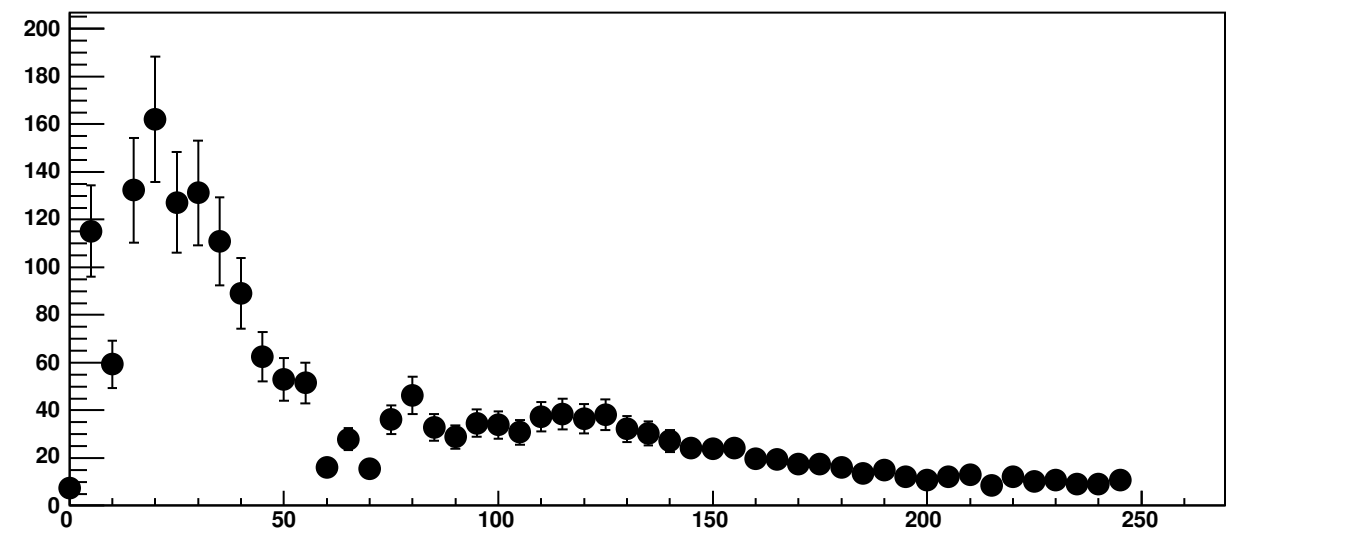
Chip 2, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold



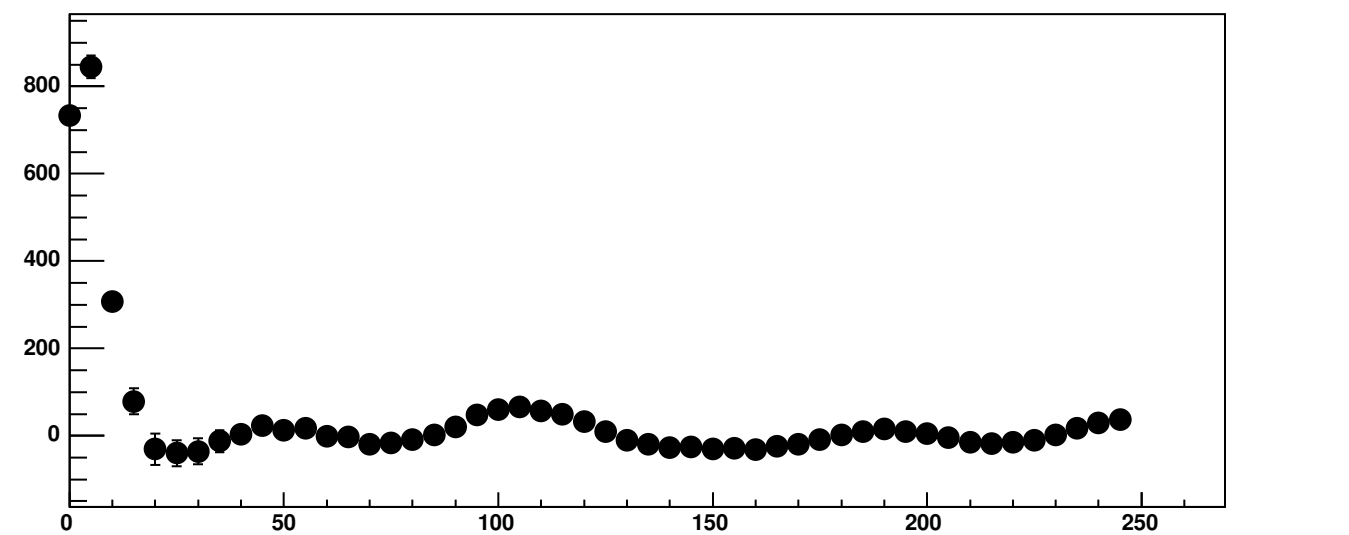
Chip 2, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold



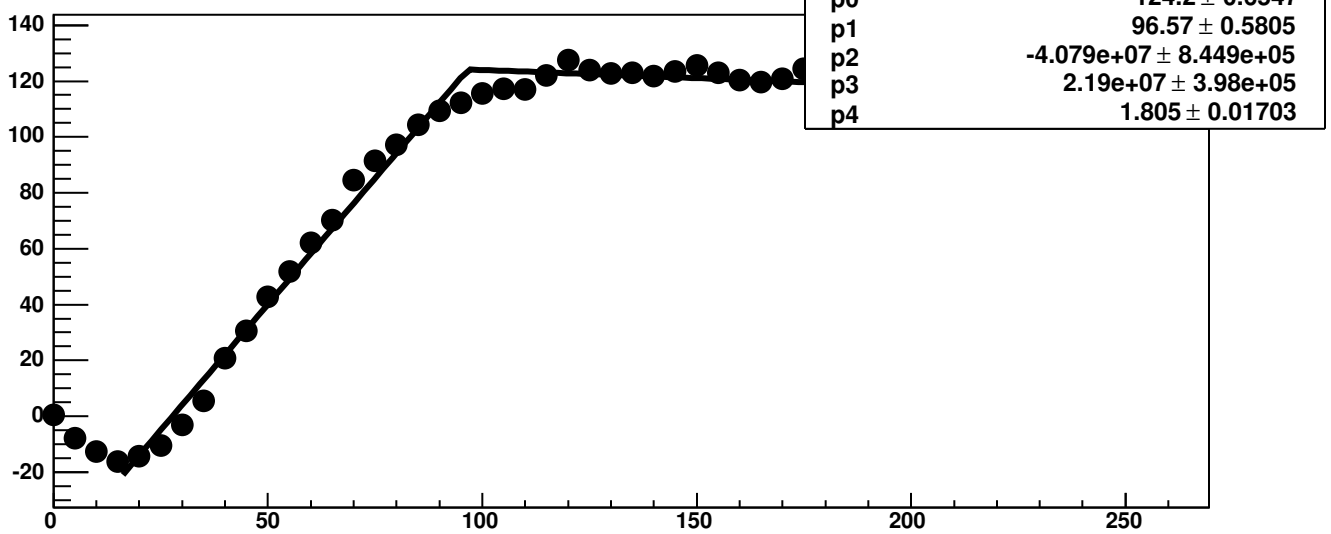
Chip 2, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold



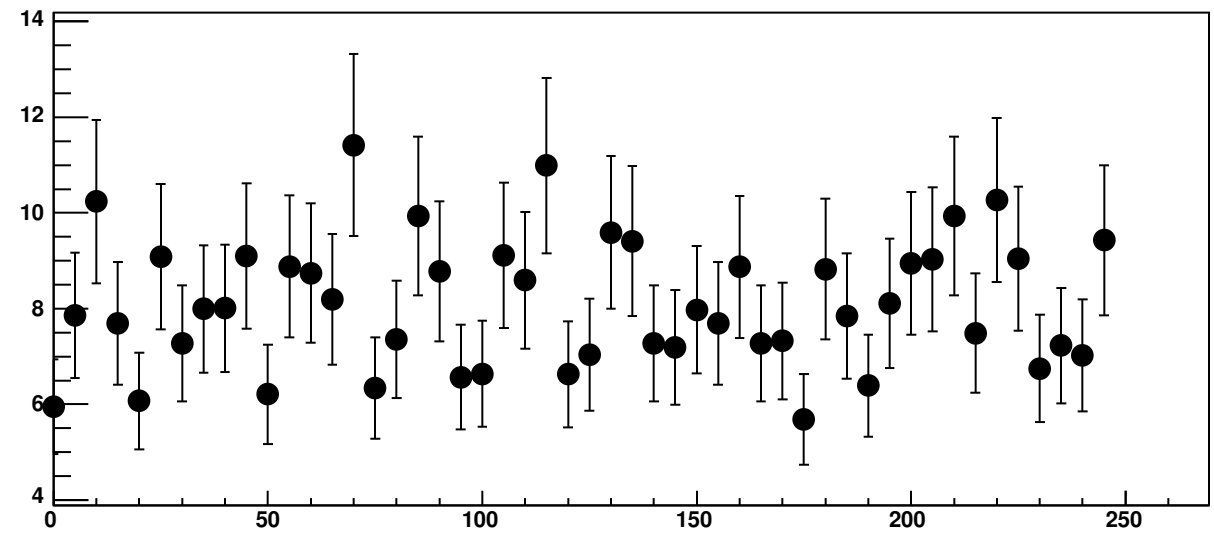
Chip 2, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold



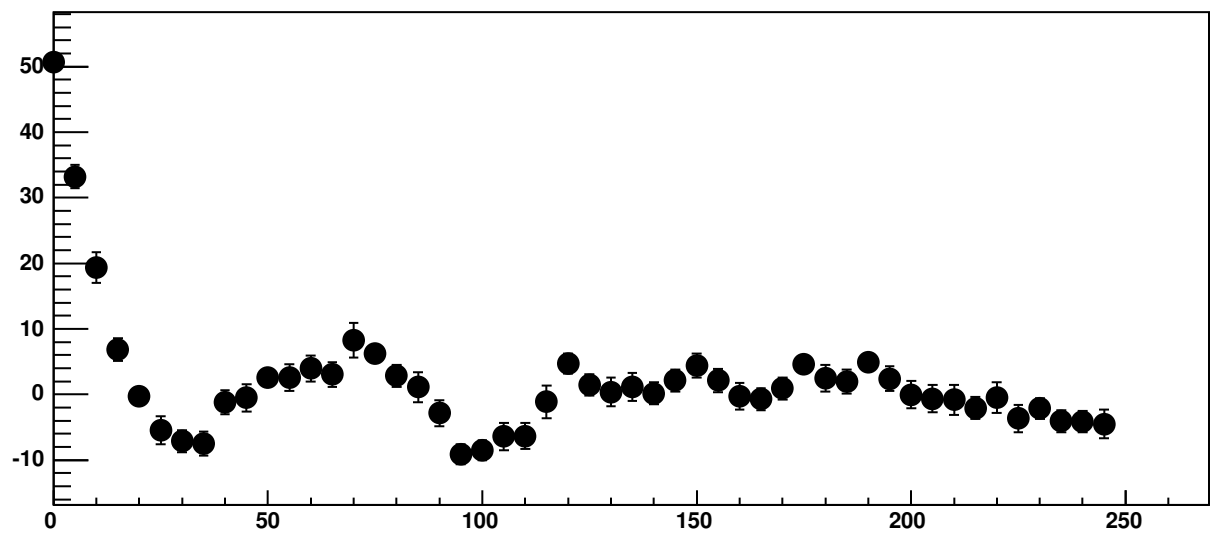
Chip 2, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold



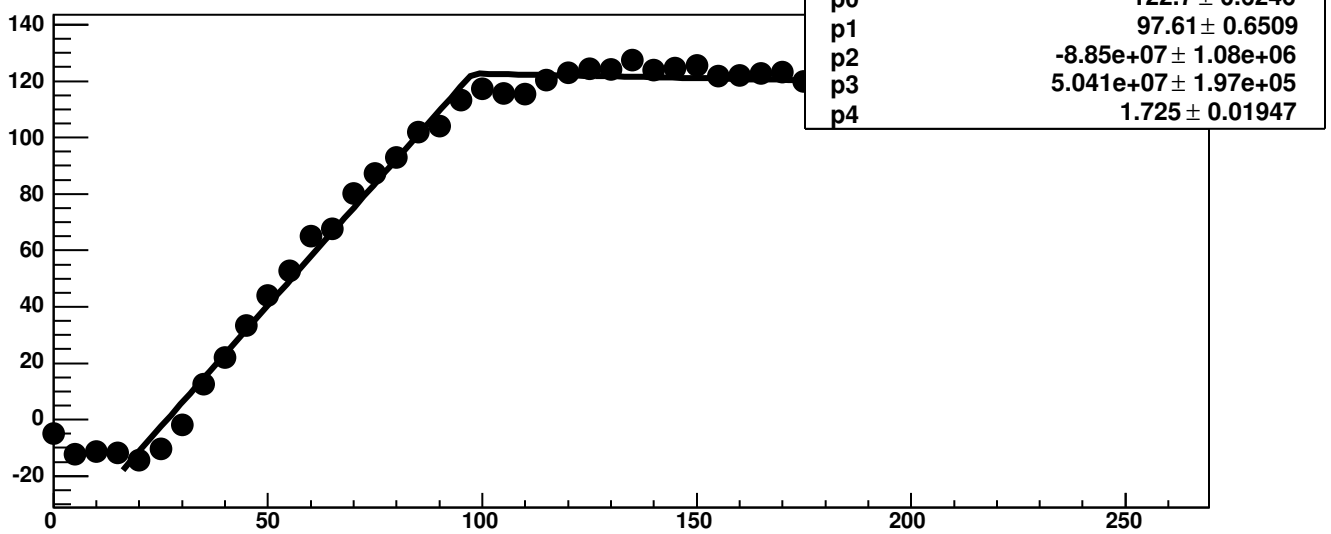
Chip 2, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



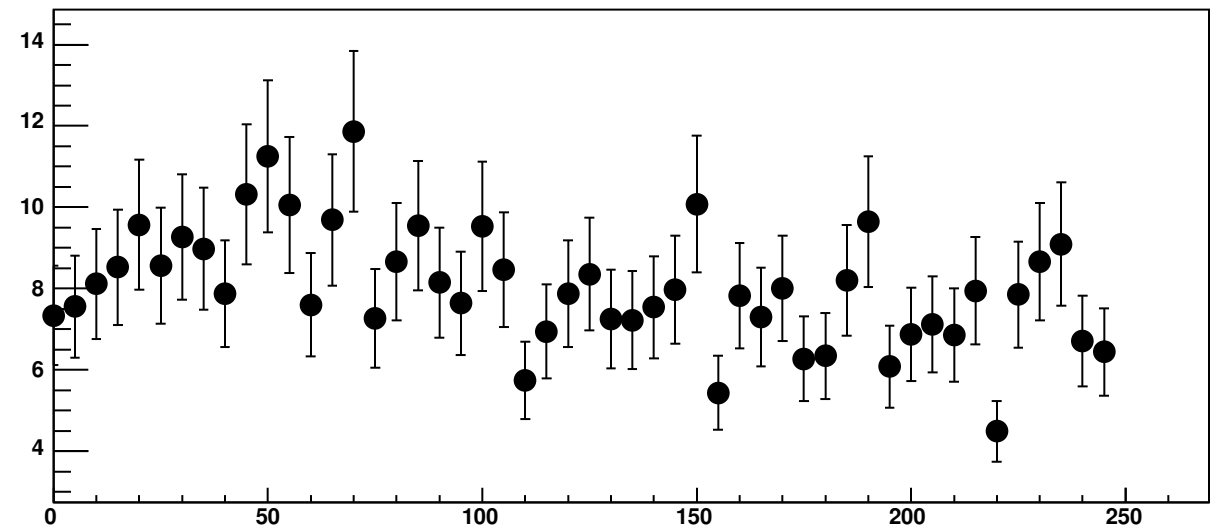
Chip 2, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold



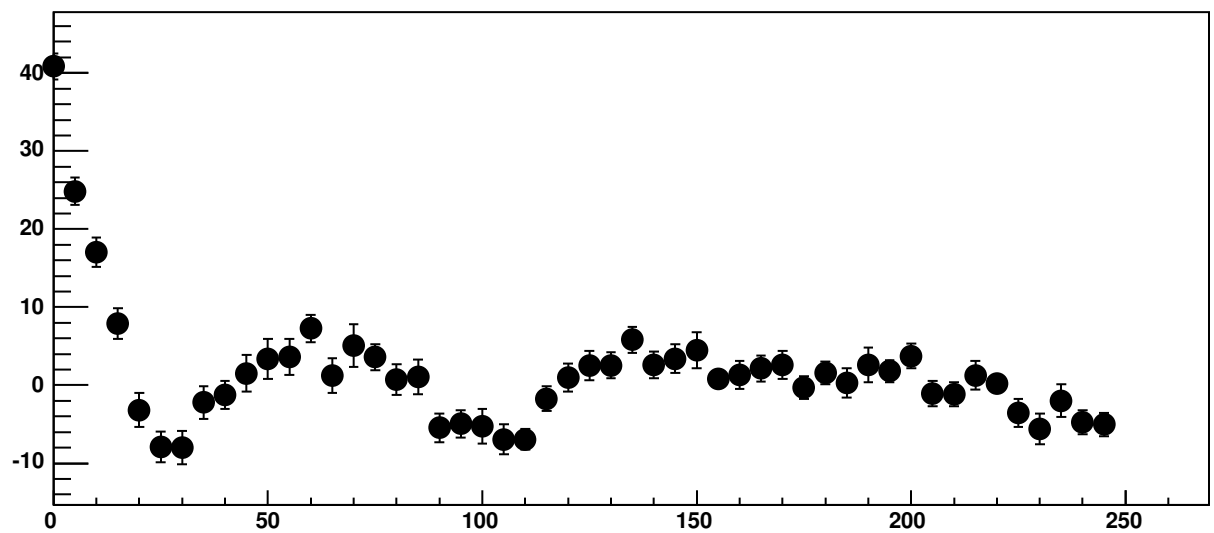
Chip 2, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold



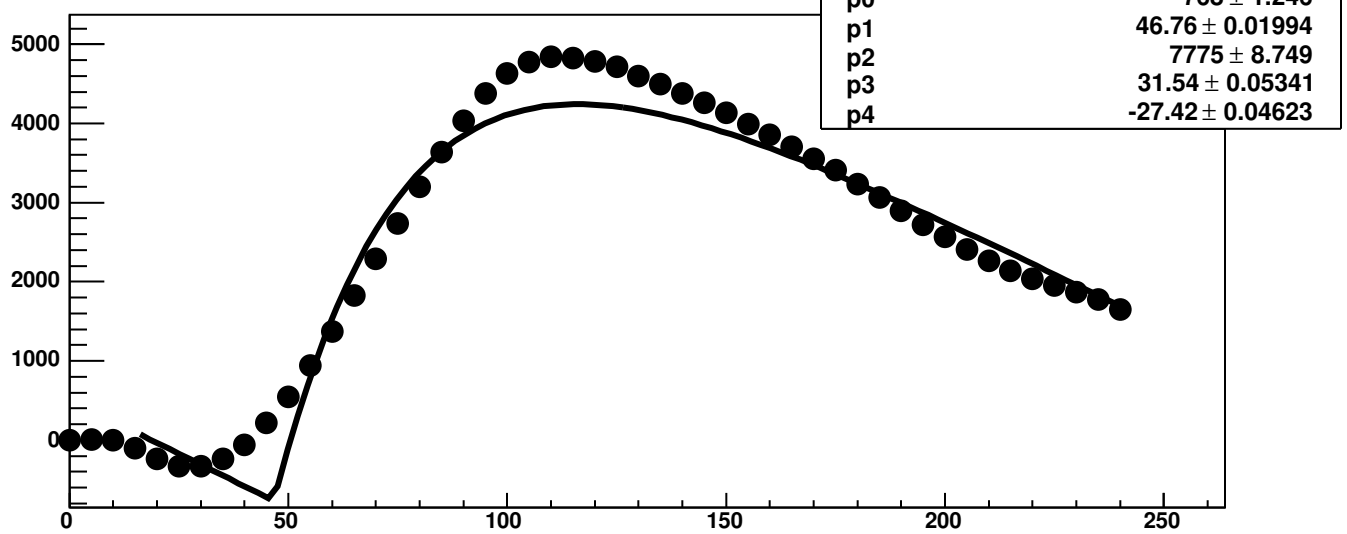
Chip 2, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



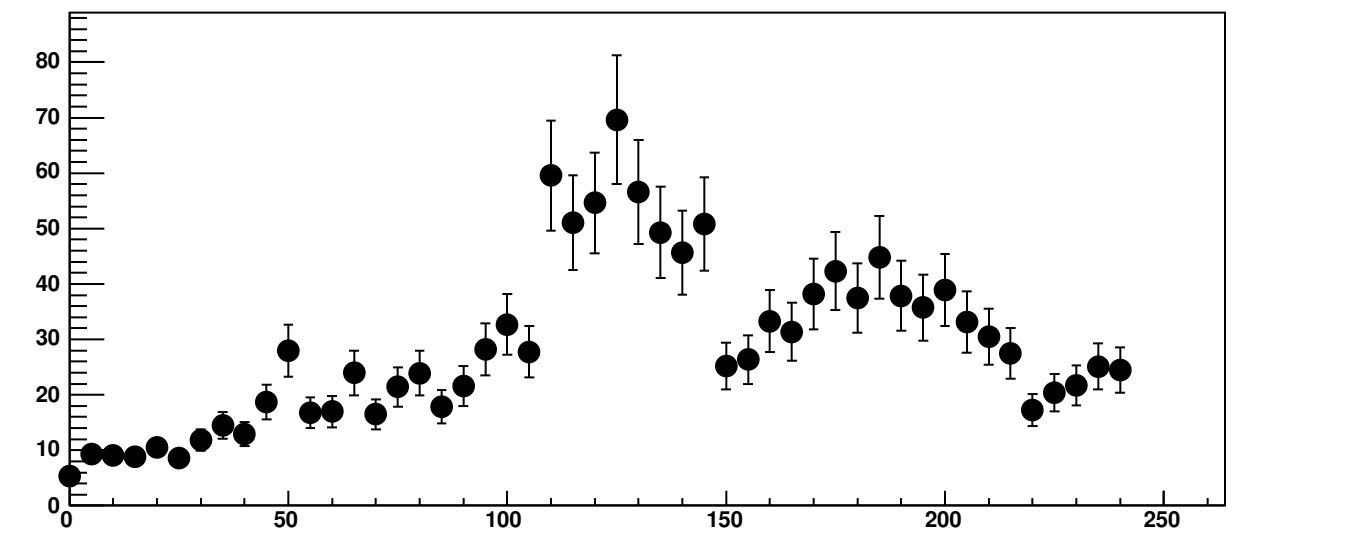
Chip 2, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold



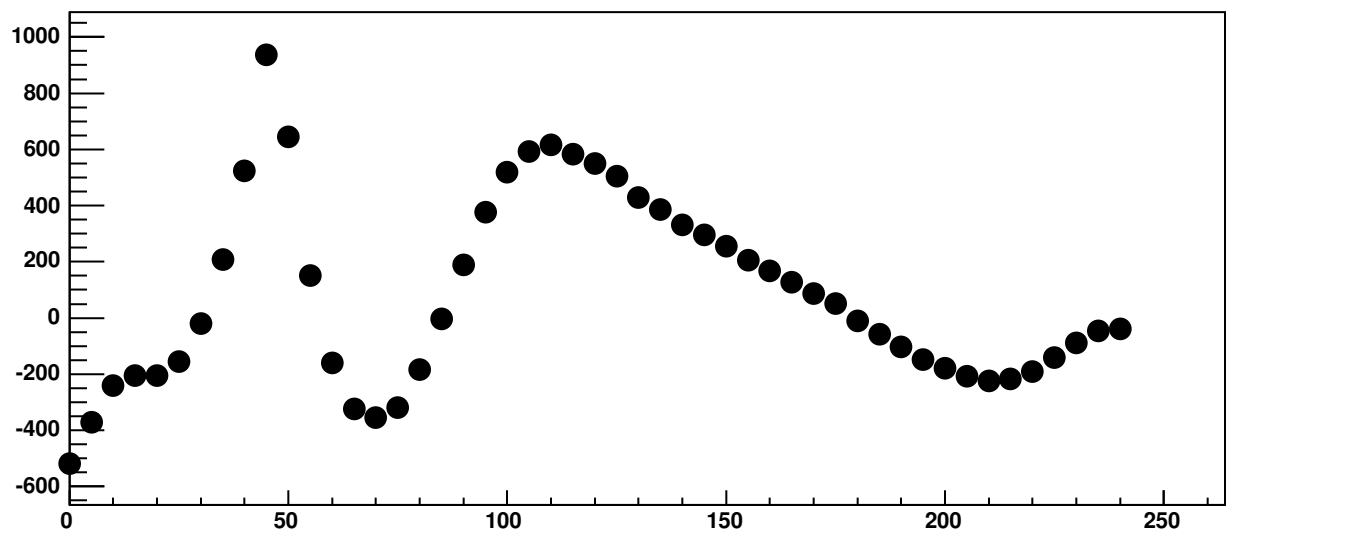
Chip 2, Channel 15, Enable 5, DAC=4000, ADC Mean vs Hold



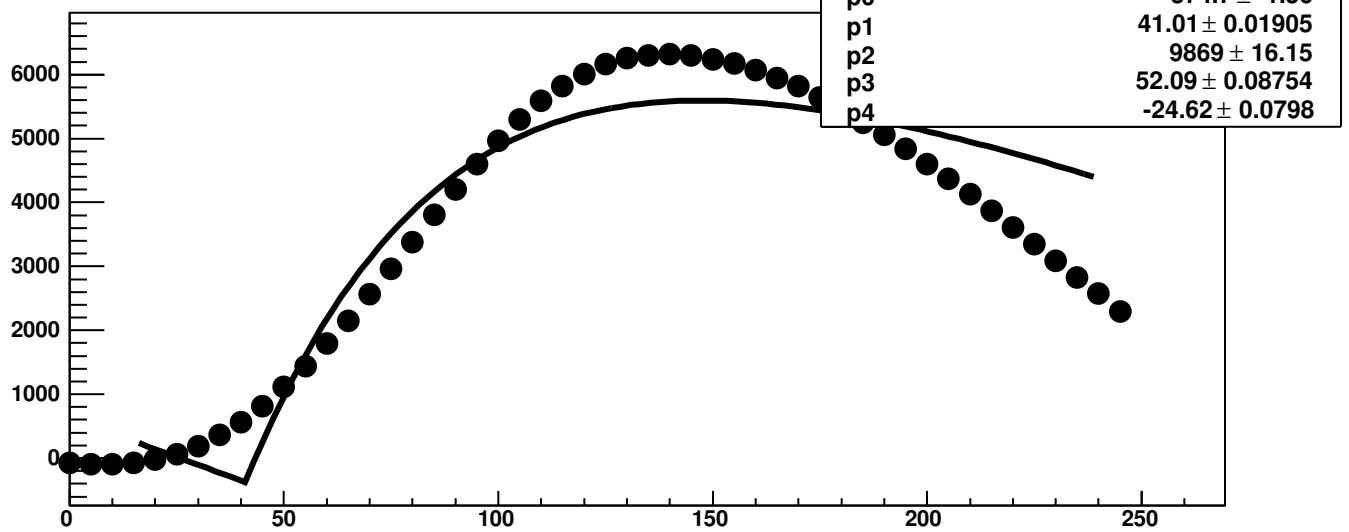
Chip 2, Channel 15, Enable 5, DAC=4000, ADC Noise vs Hold



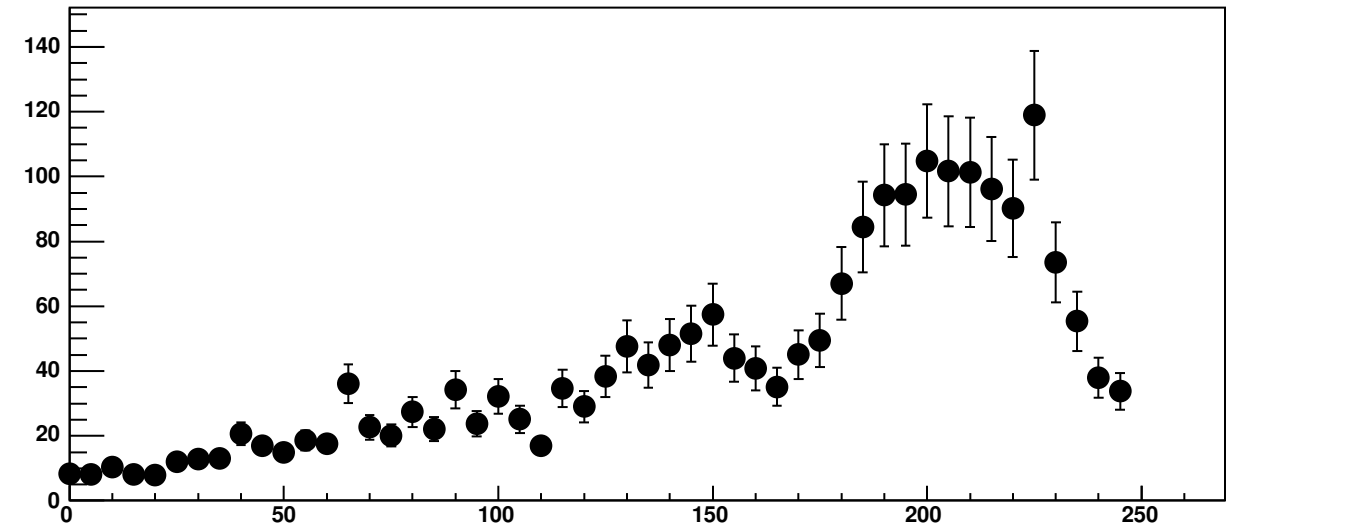
Chip 2, Channel 15, Enable 5, DAC=4000, ADC Residuals vs Hold



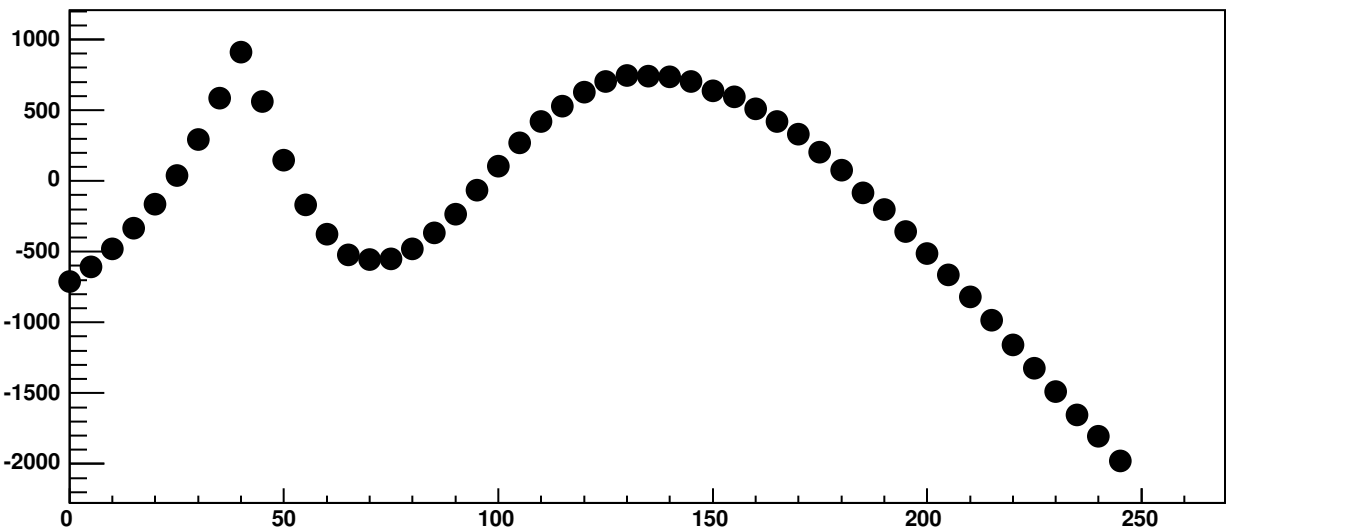
Chip 2, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold



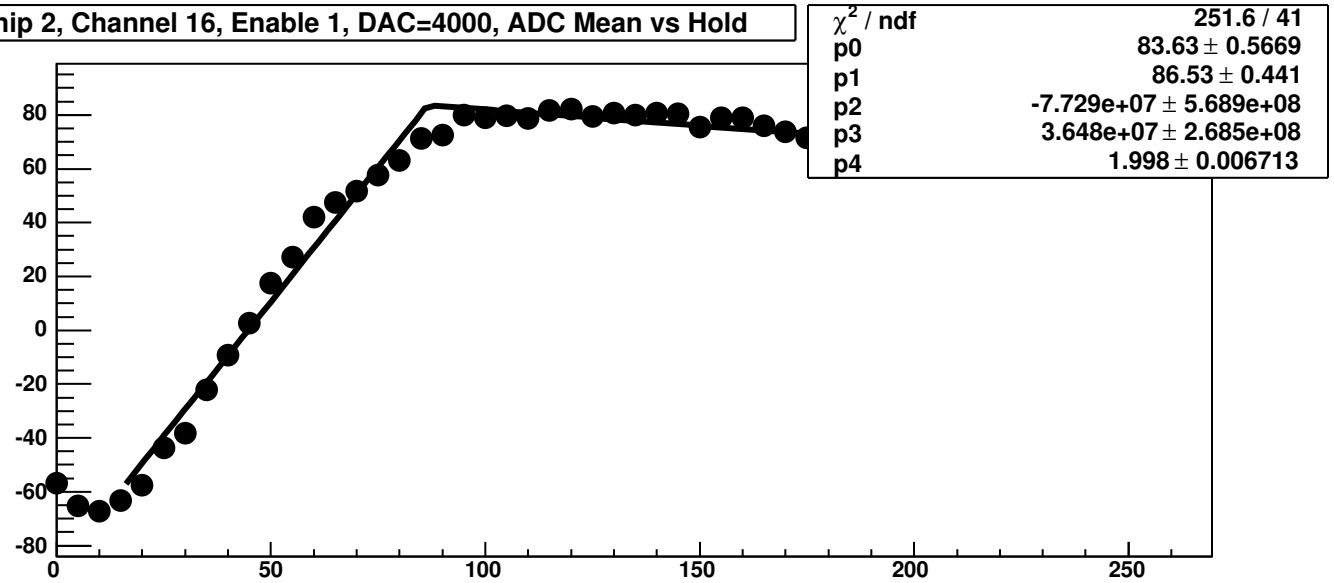
Chip 2, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



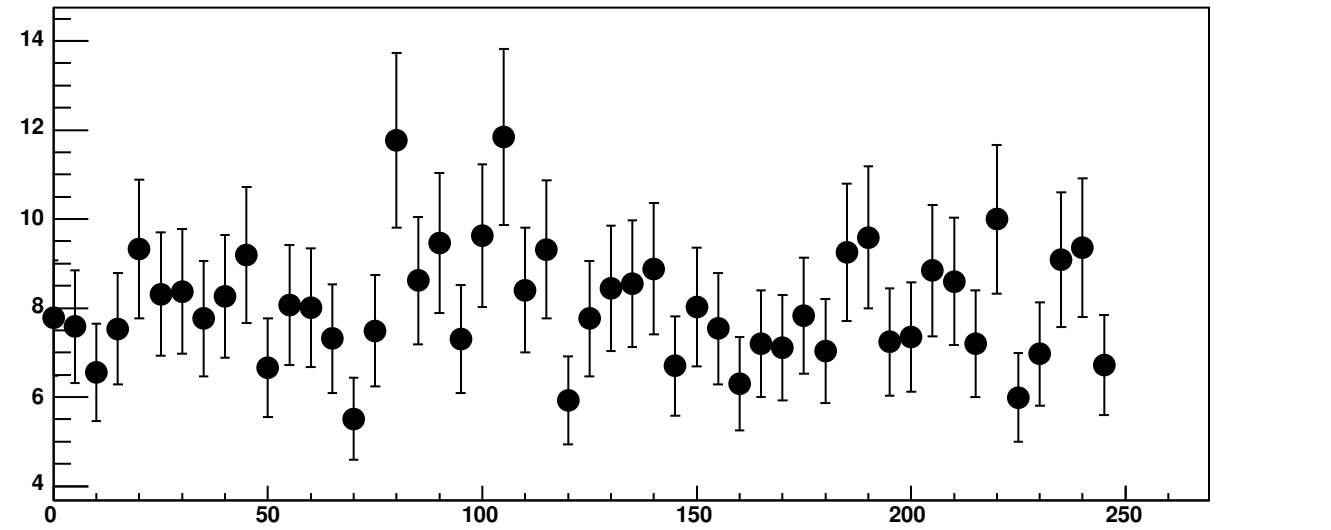
Chip 2, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold



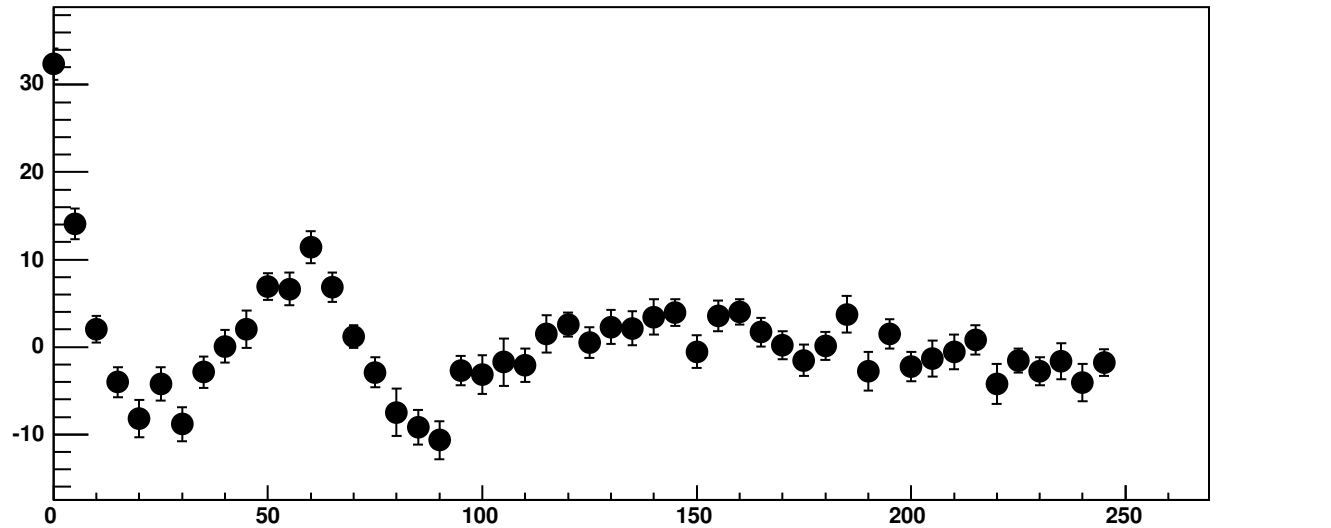
Chip 2, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold



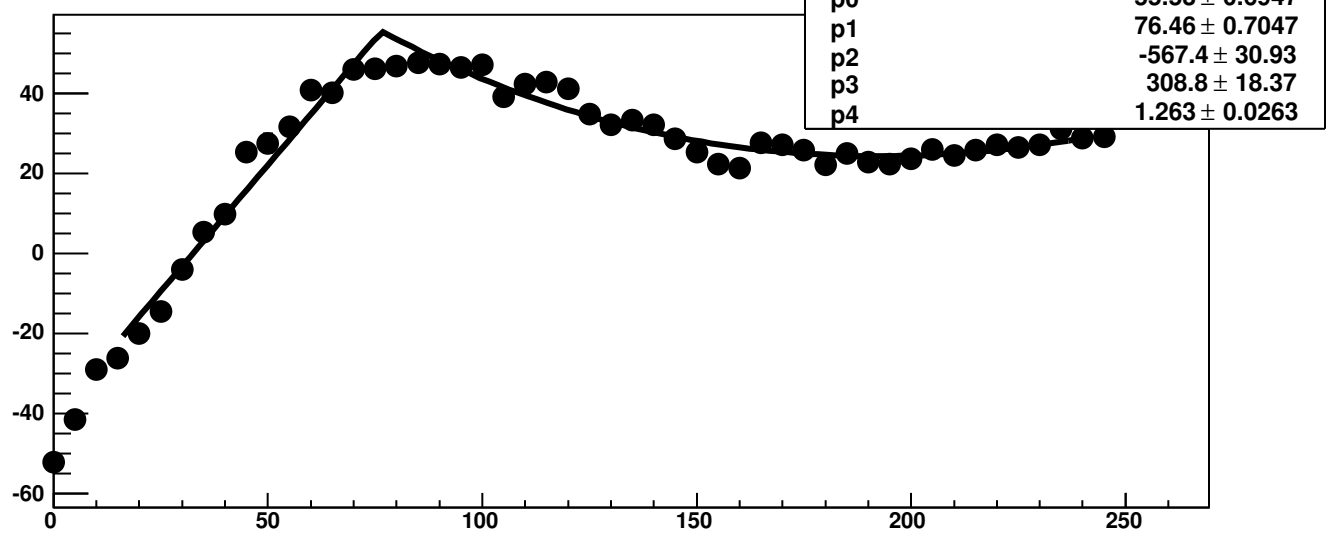
Chip 2, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



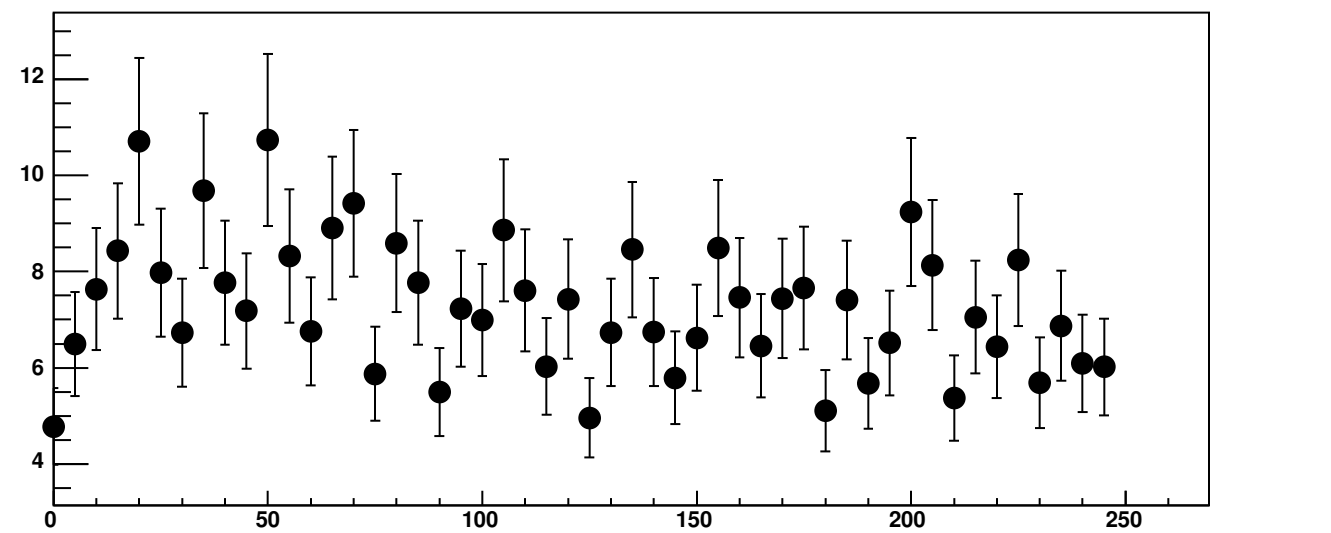
Chip 2, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold



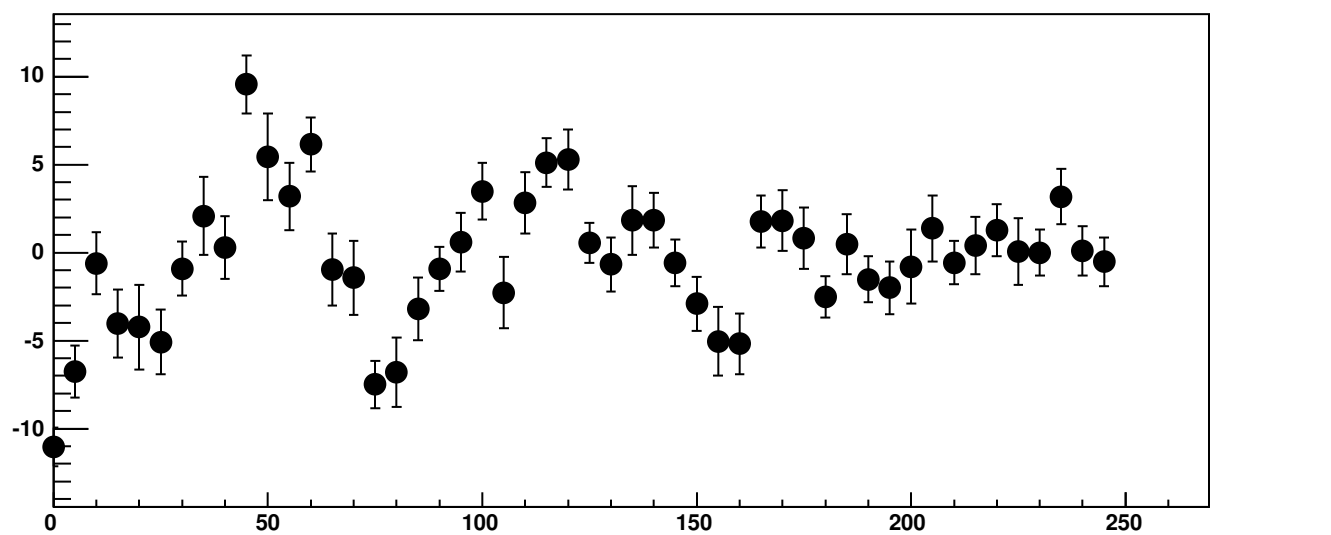
Chip 2, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold



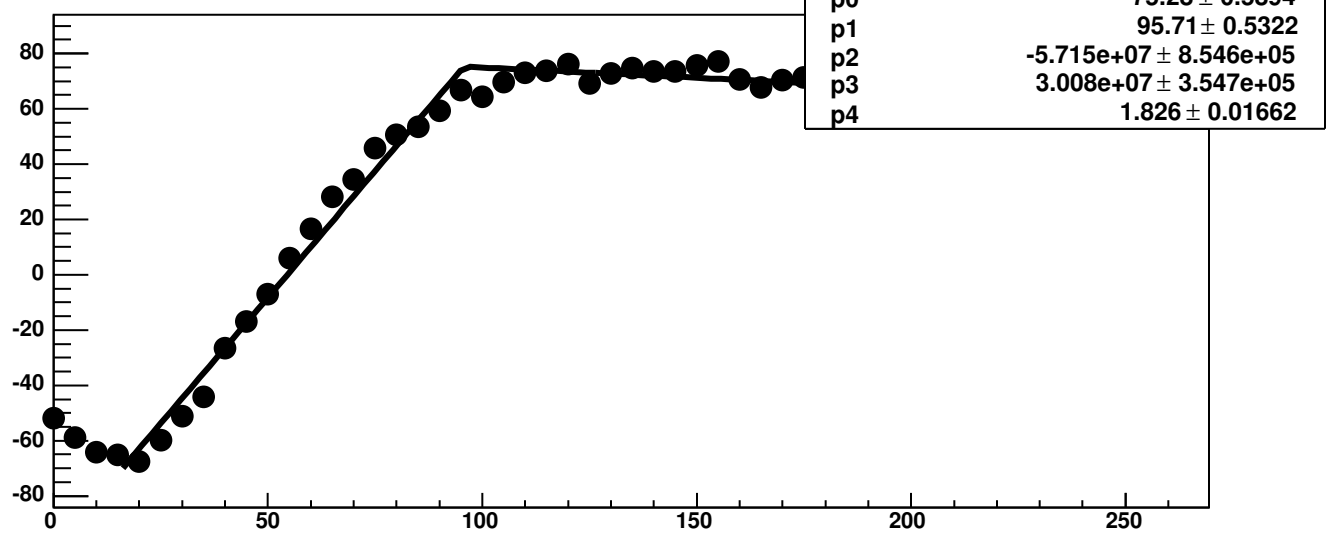
Chip 2, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold



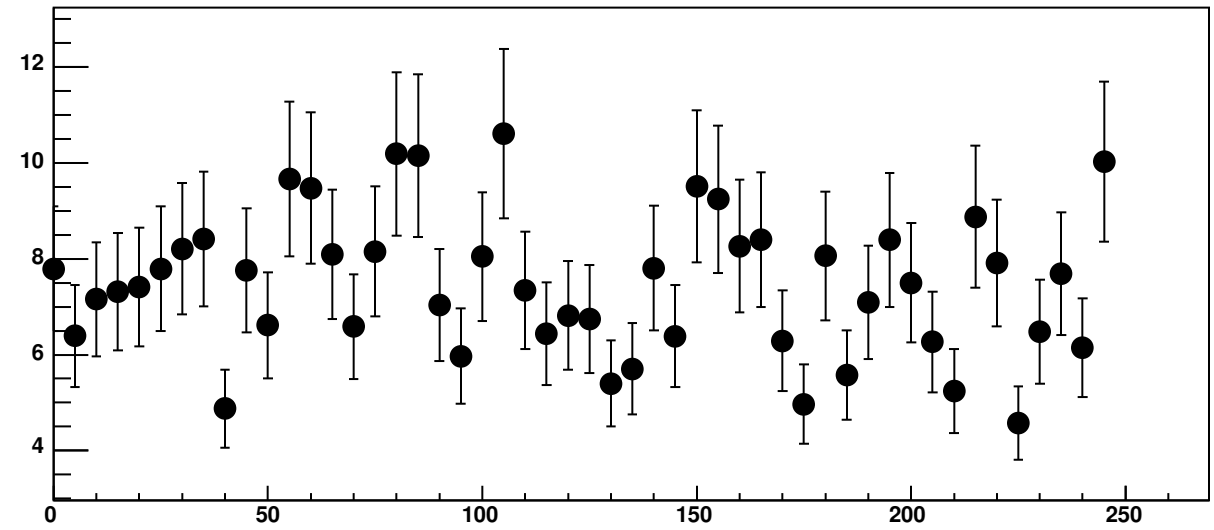
Chip 2, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold



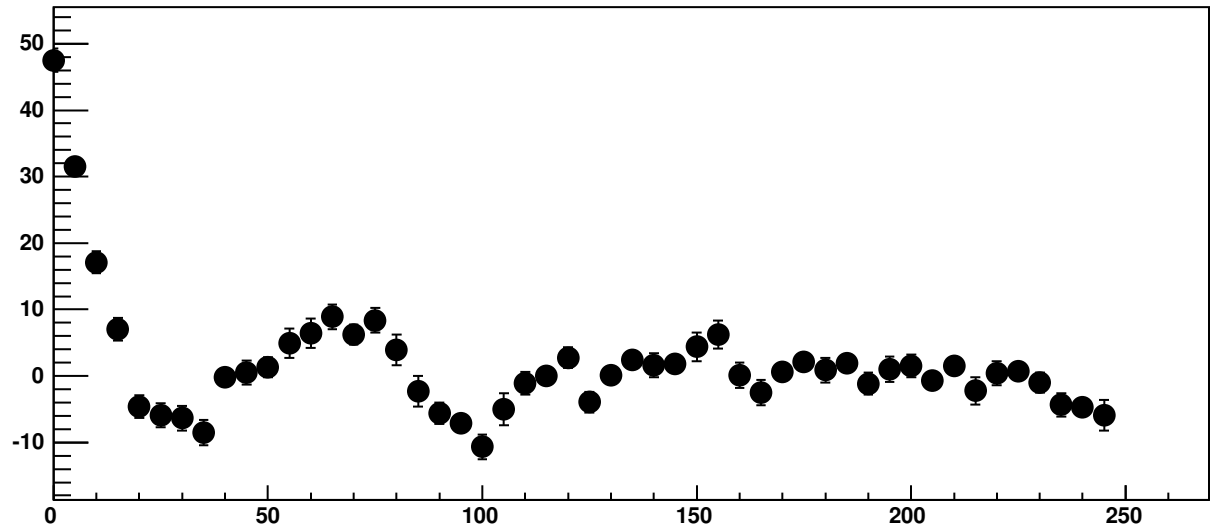
Chip 2, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold



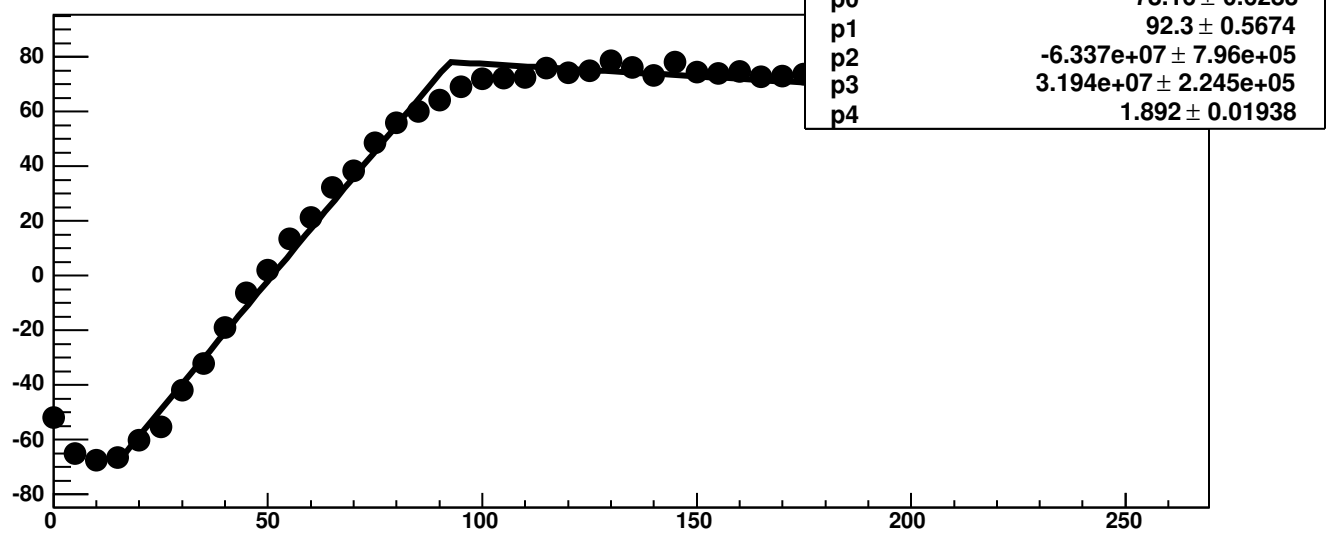
Chip 2, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



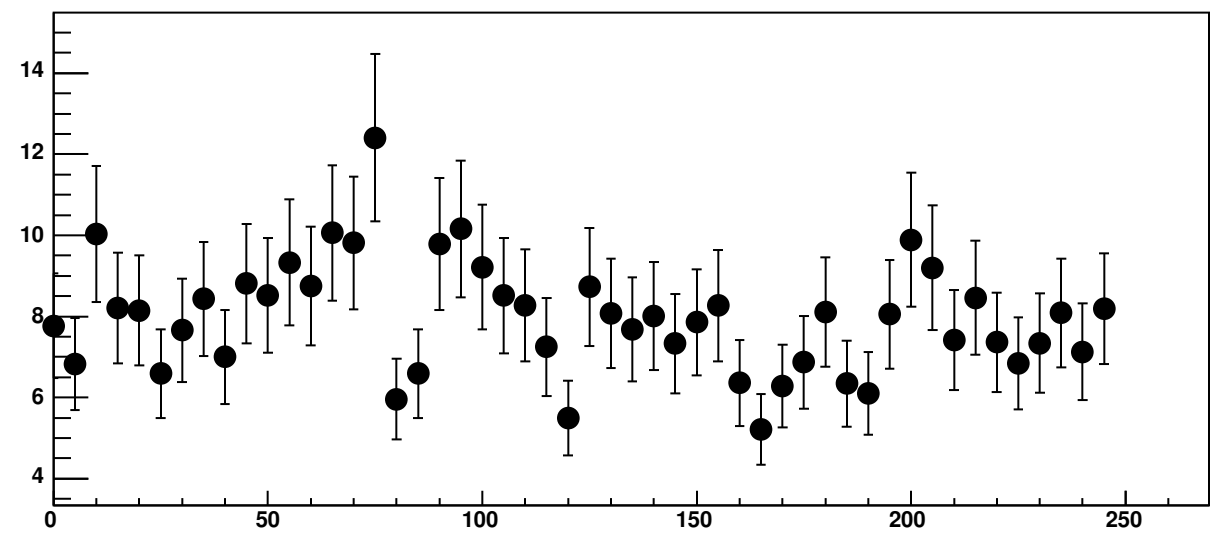
Chip 2, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold



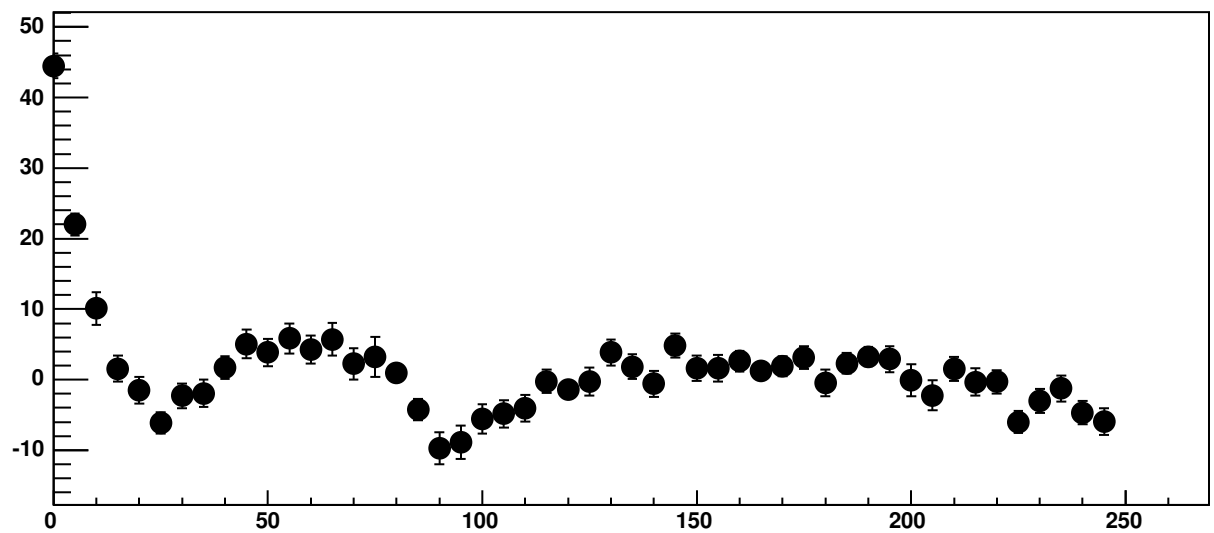
Chip 2, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold



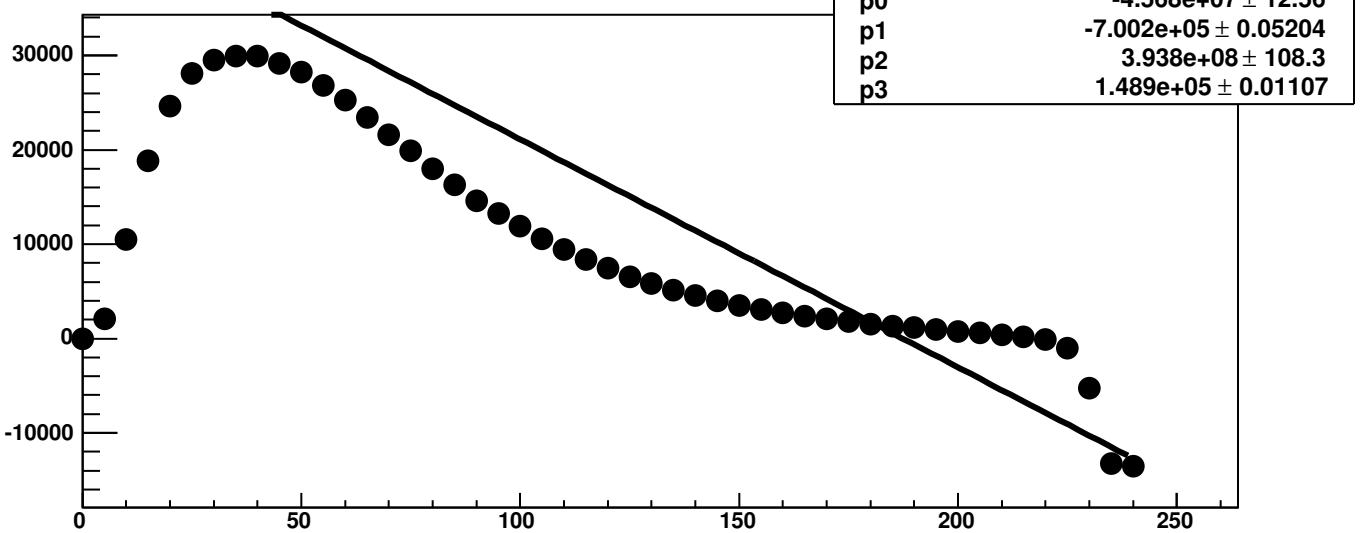
Chip 2, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



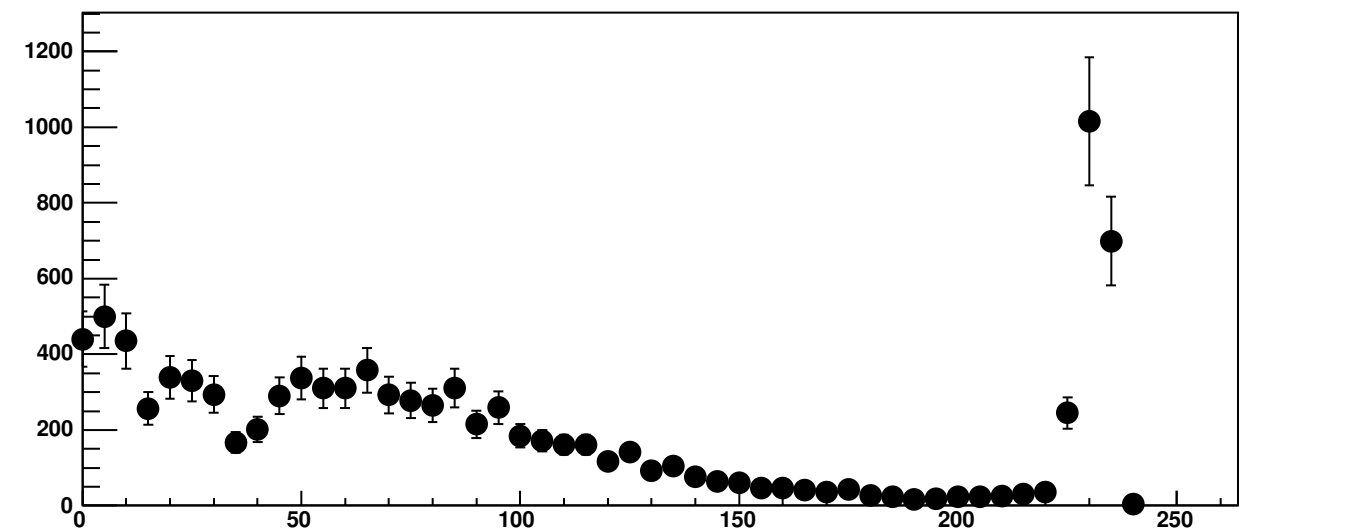
Chip 2, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold



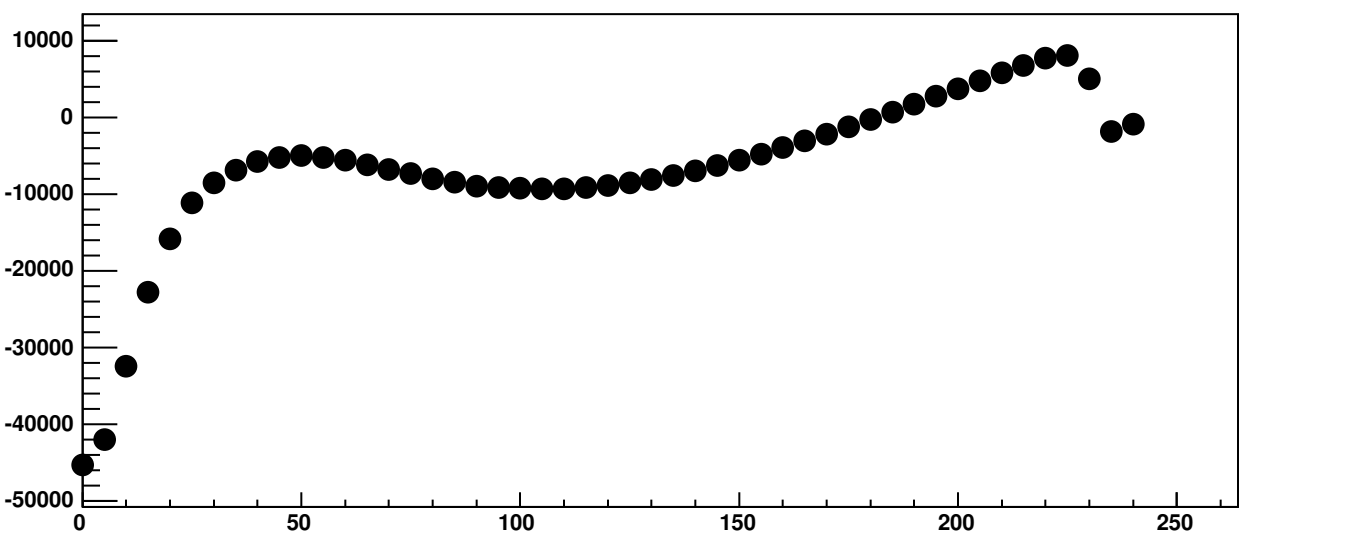
Chip 2, Channel 16, Enable 5!, DAC=4000, ADC Mean vs Hold

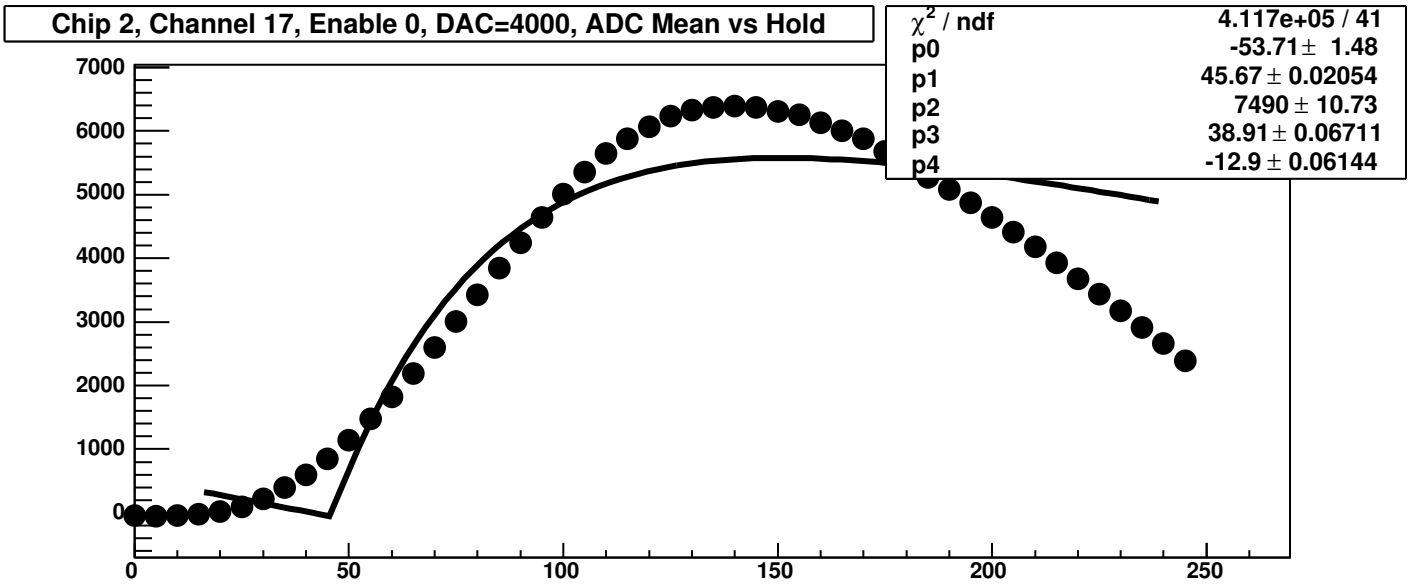


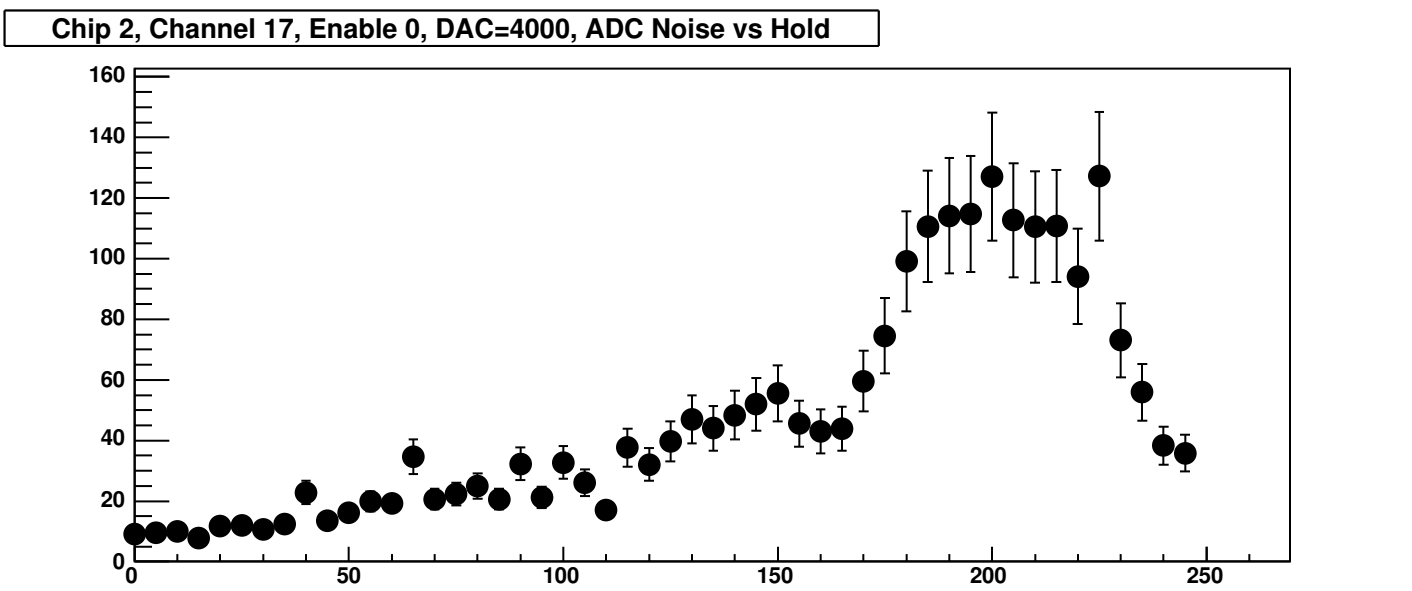
Chip 2, Channel 16, Enable 5!, DAC=4000, ADC Noise vs Hold

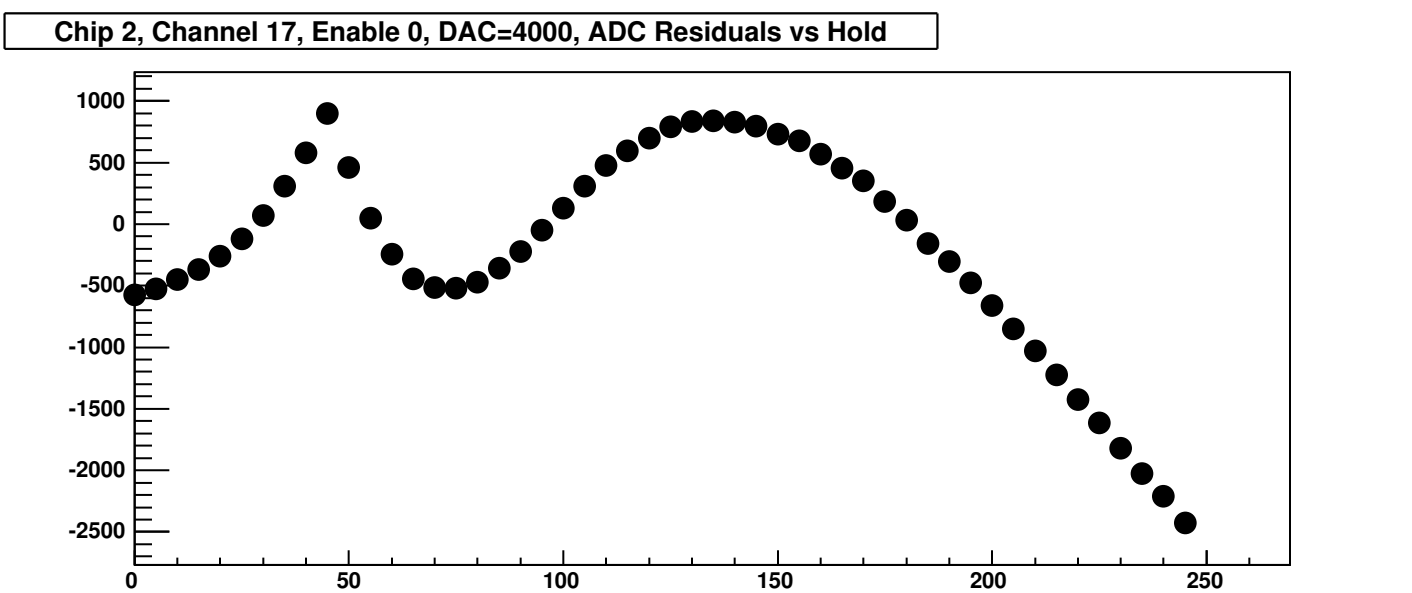


Chip 2, Channel 16, Enable 5!, DAC=4000, ADC Residuals vs Hold

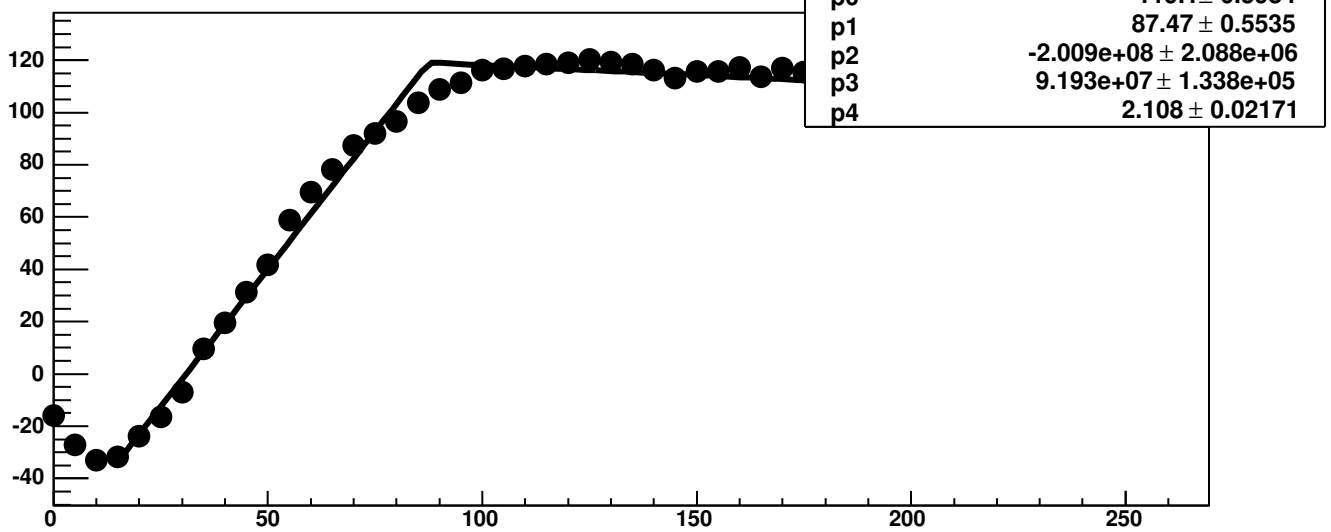



Chip 2, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.117e+05 / 41
p0 -53.71 ± 1.48
p1 45.67 ± 0.02054
p2 7490 ± 10.73
p3 38.91 ± 0.06711
p4 -12.9 ± 0.06144
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

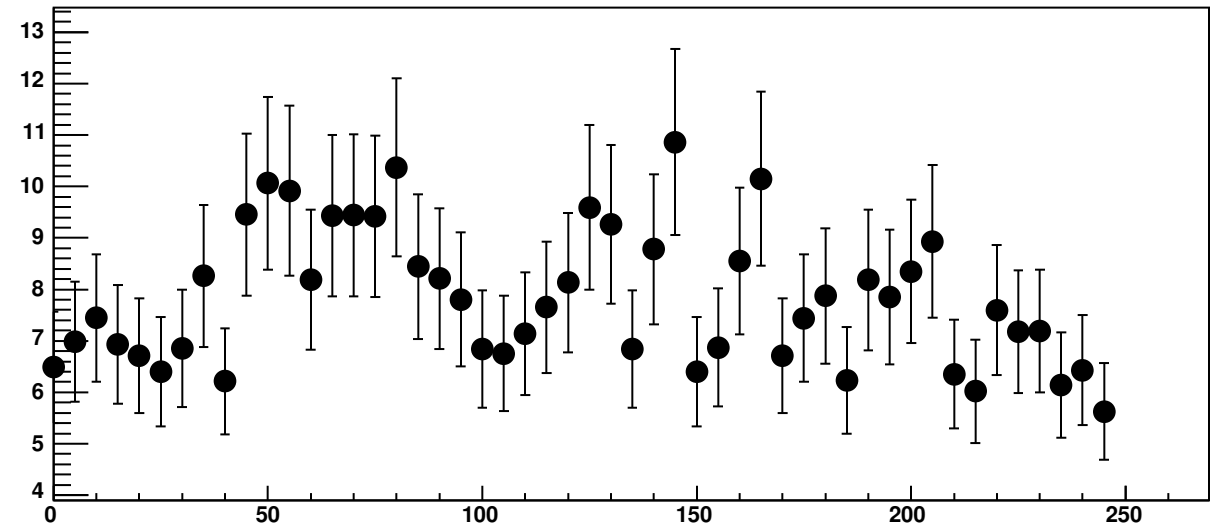

Chip 2, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 2, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
0
50
100
150
200
250

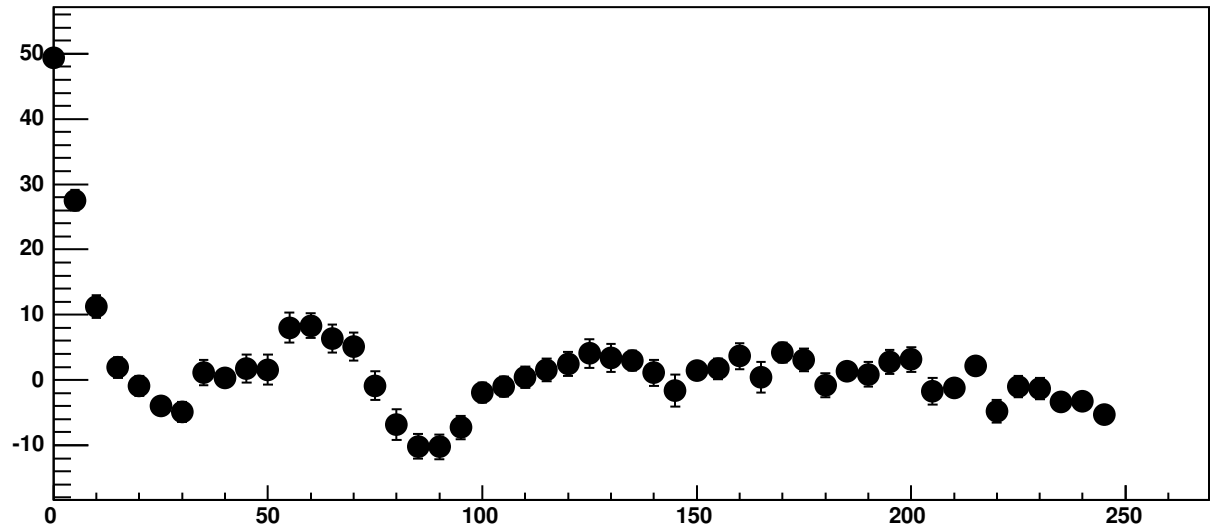
Chip 2, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 2, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold

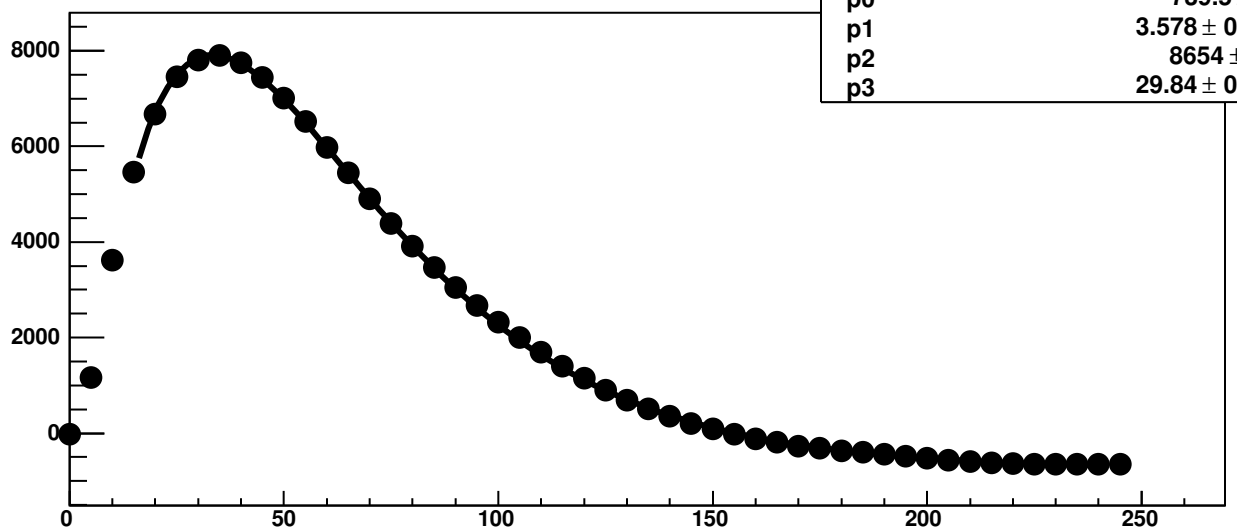


Chip 2, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

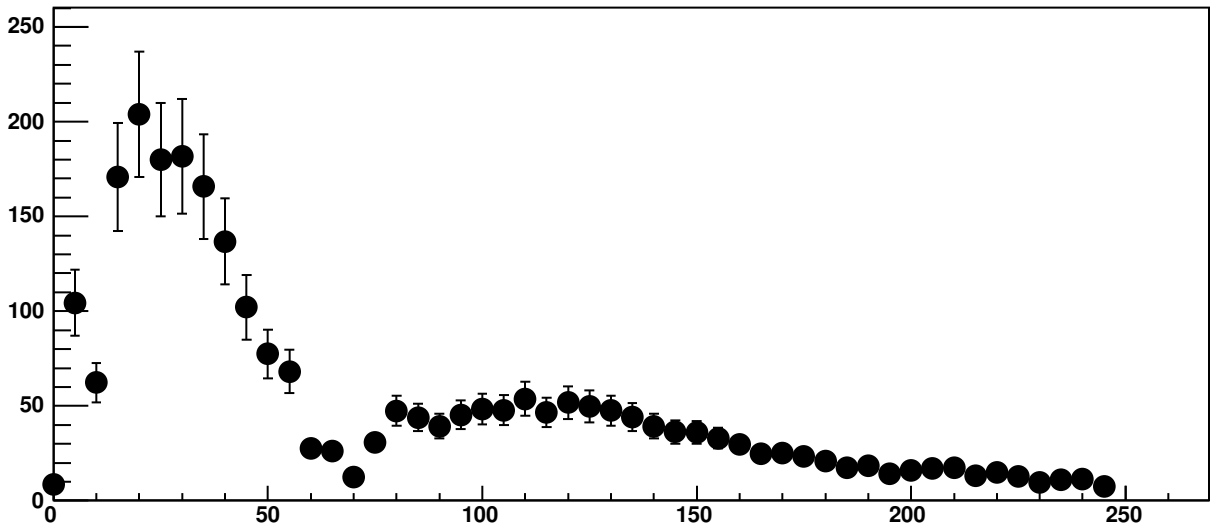


Chip 2, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold

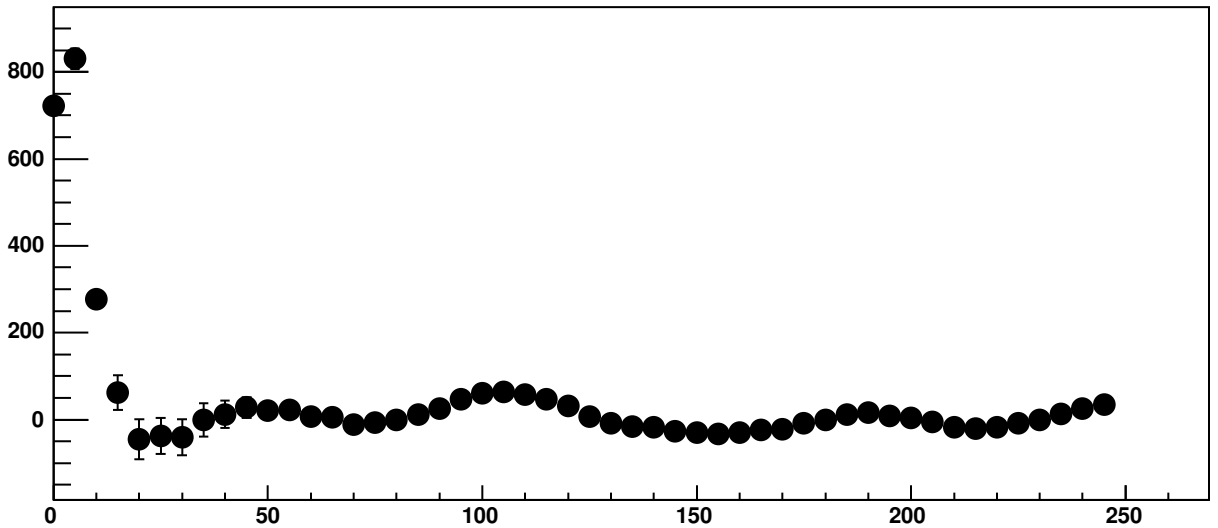
χ^2 / ndf	531.9 / 42
p0	-739.5 ± 1.27
p1	3.578 ± 0.08366
p2	8654 ± 9.005
p3	29.84 ± 0.03344



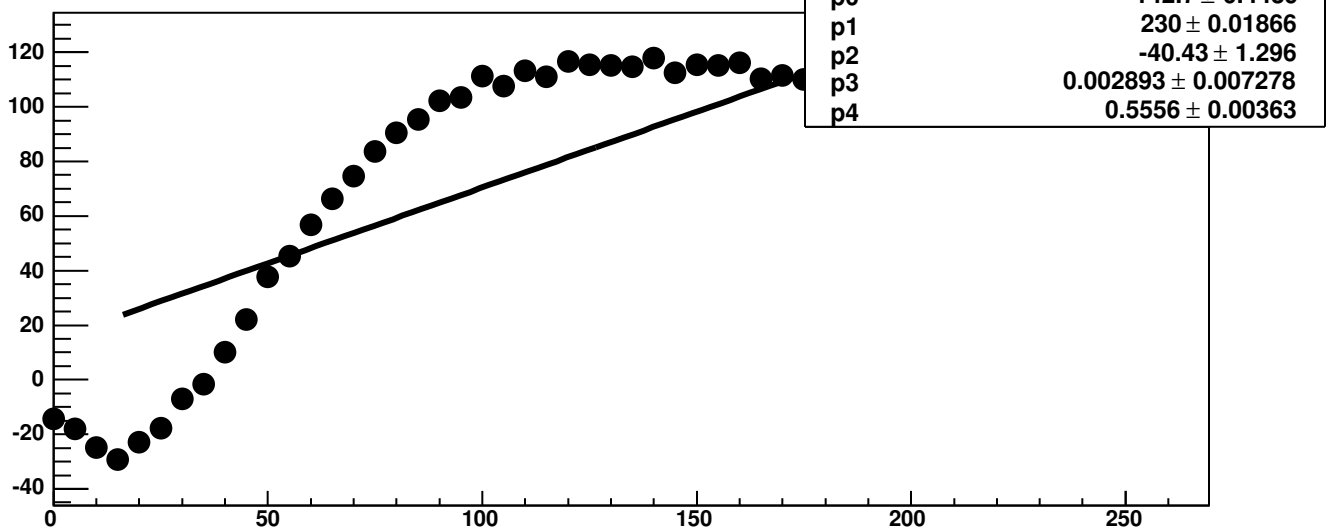
Chip 2, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold



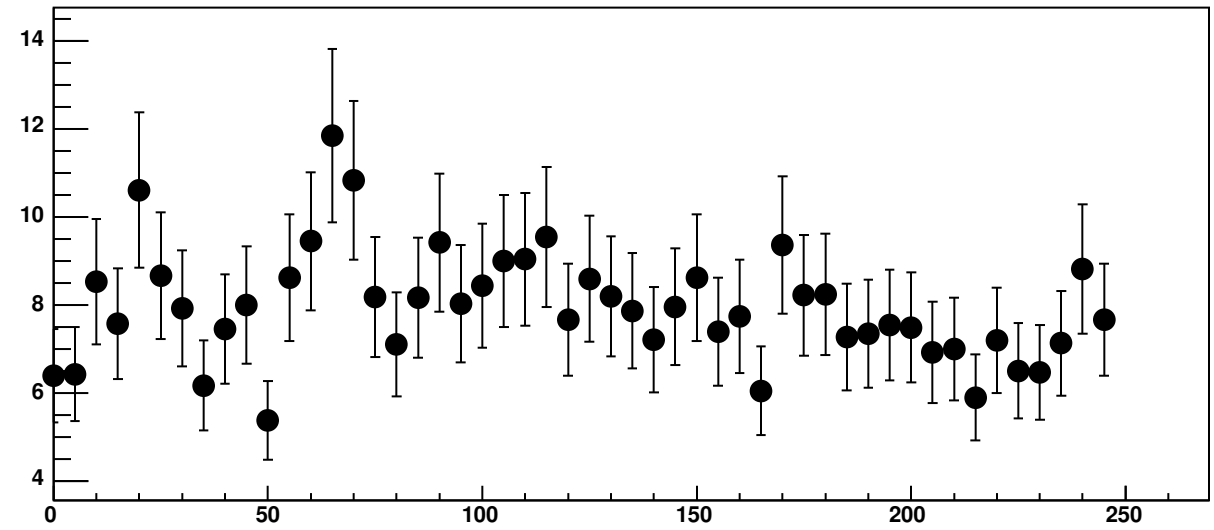
Chip 2, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold



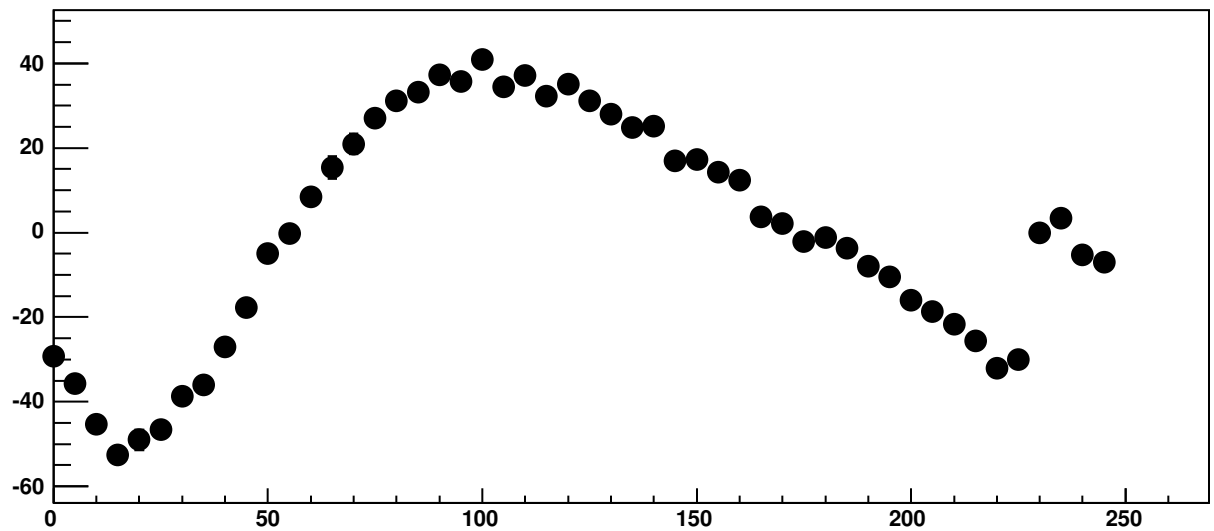
Chip 2, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



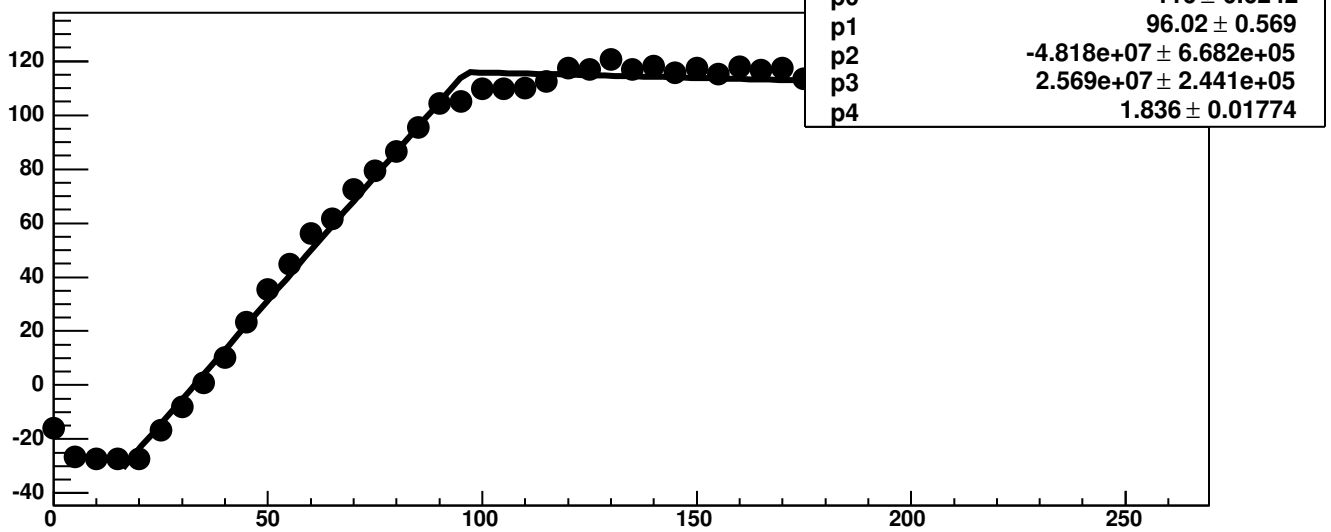
Chip 2, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



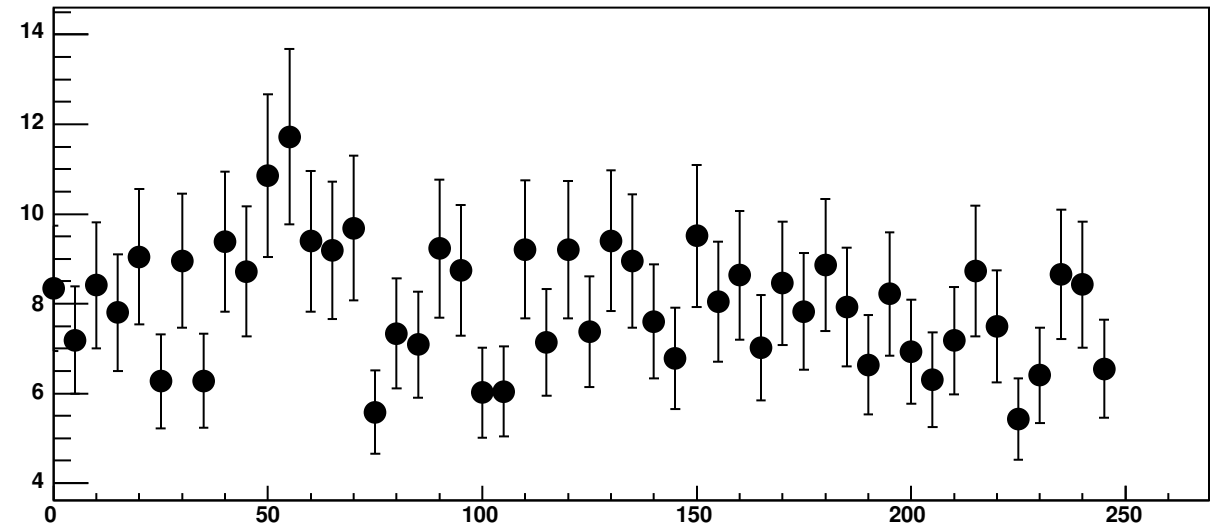
Chip 2, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold



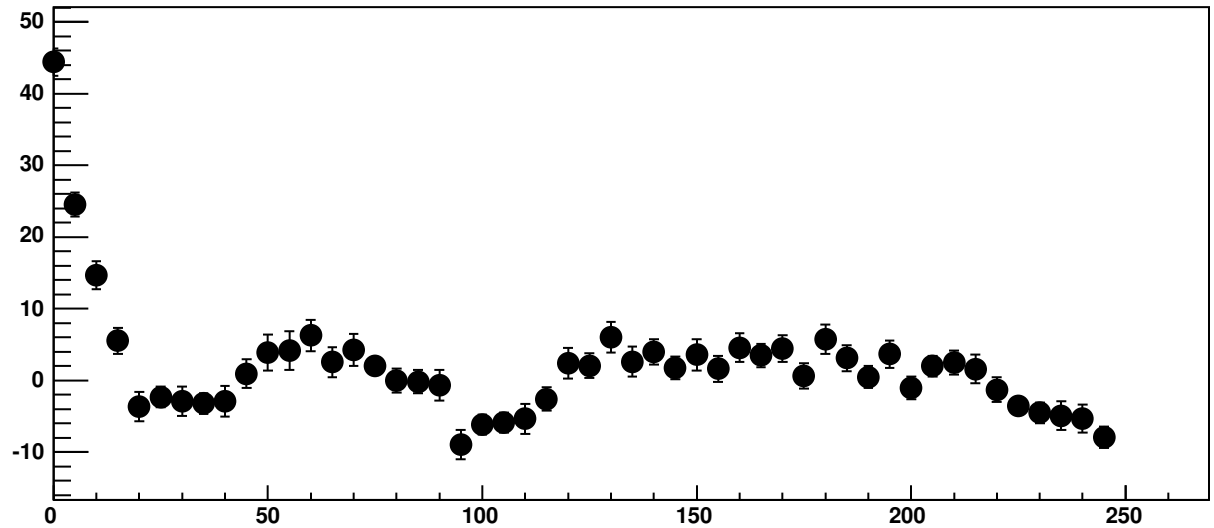
Chip 2, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



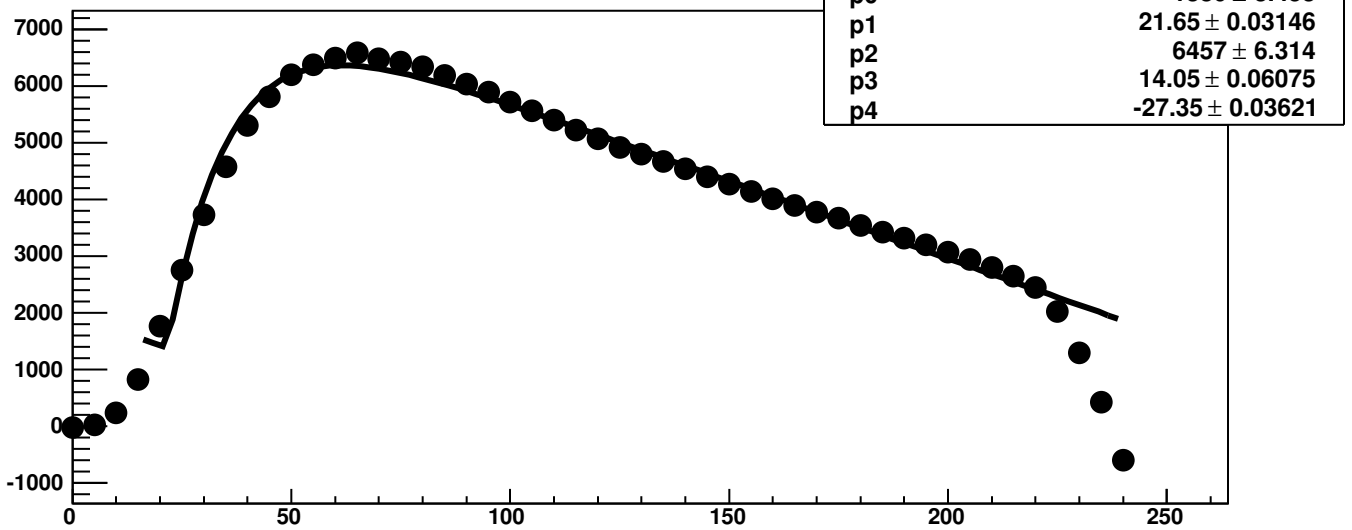
Chip 2, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



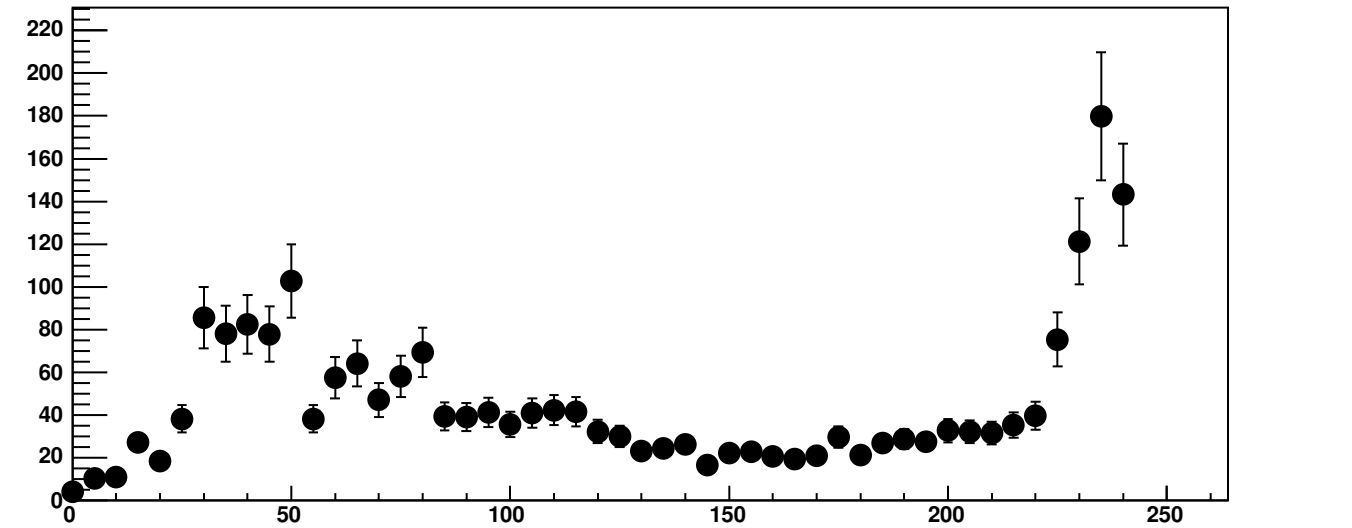
Chip 2, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold



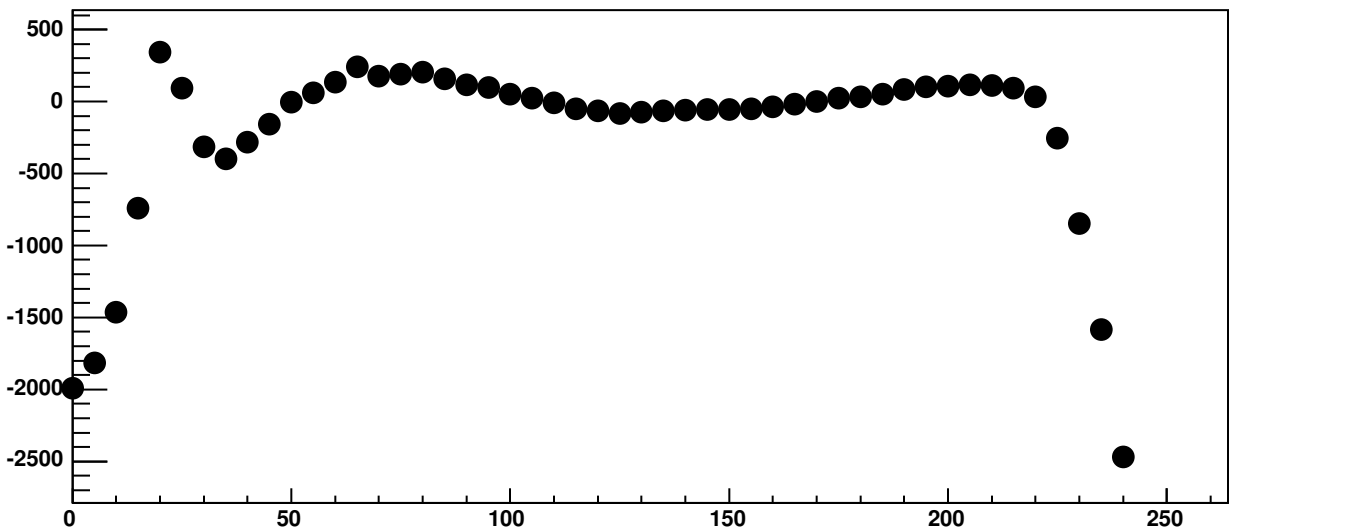
Chip 2, Channel 17, Enable 5, DAC=4000, ADC Mean vs Hold

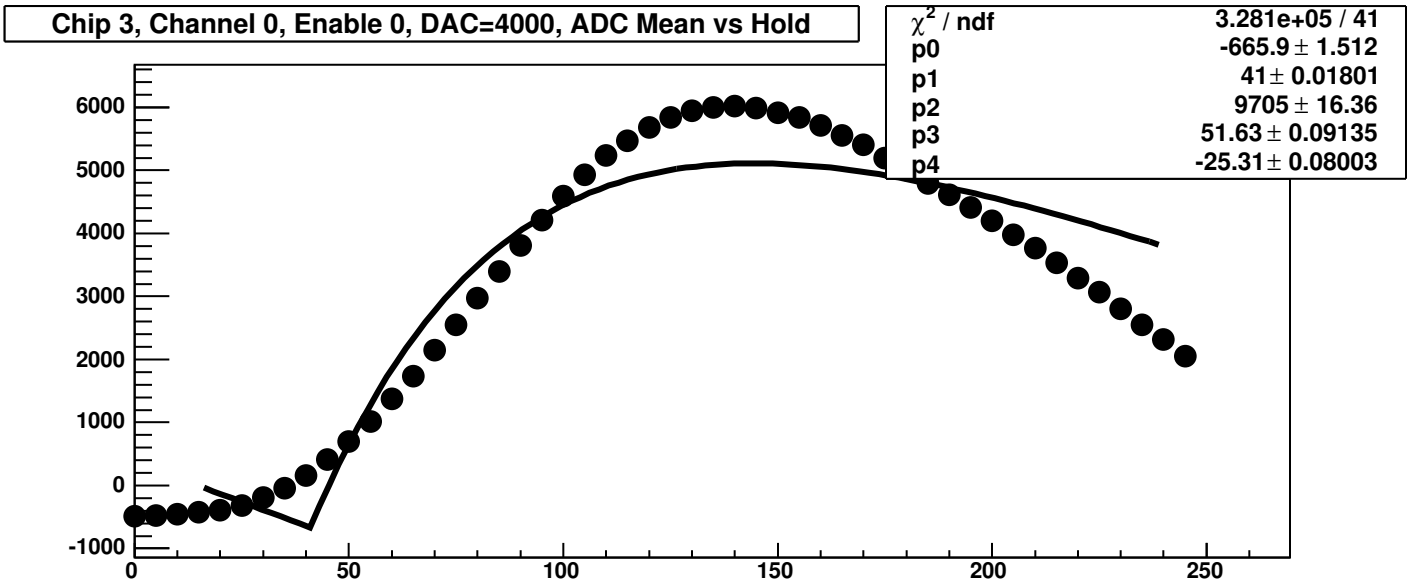


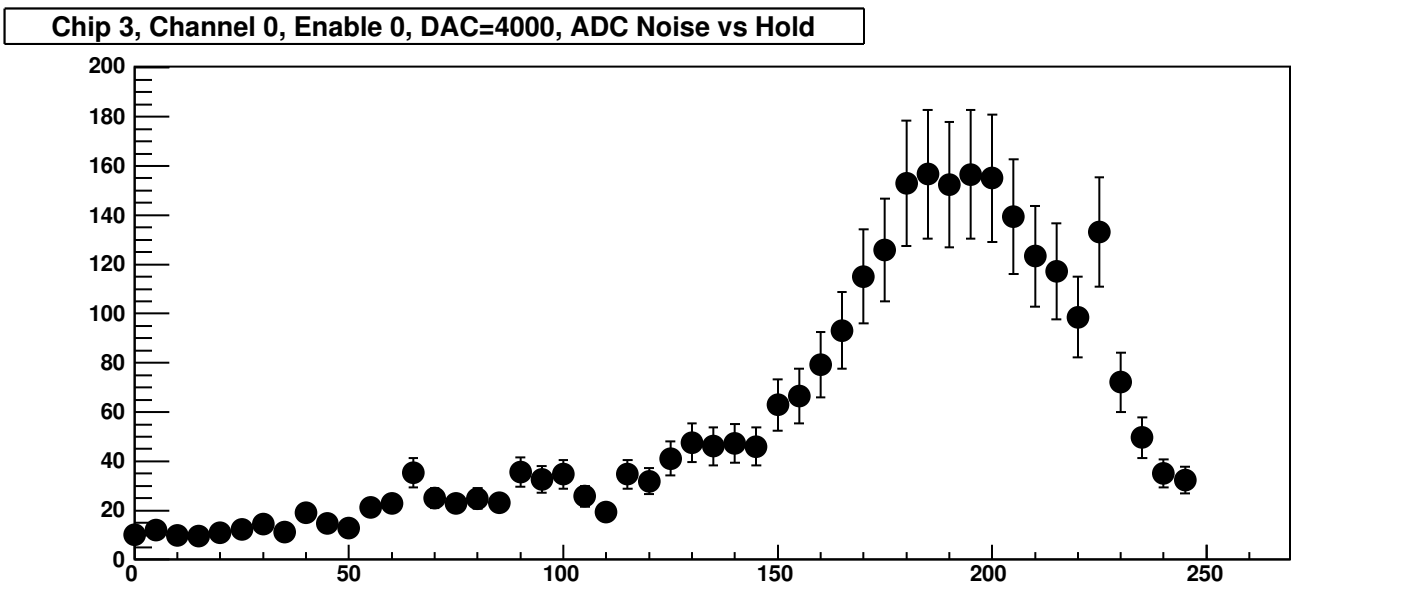
Chip 2, Channel 17, Enable 5, DAC=4000, ADC Noise vs Hold

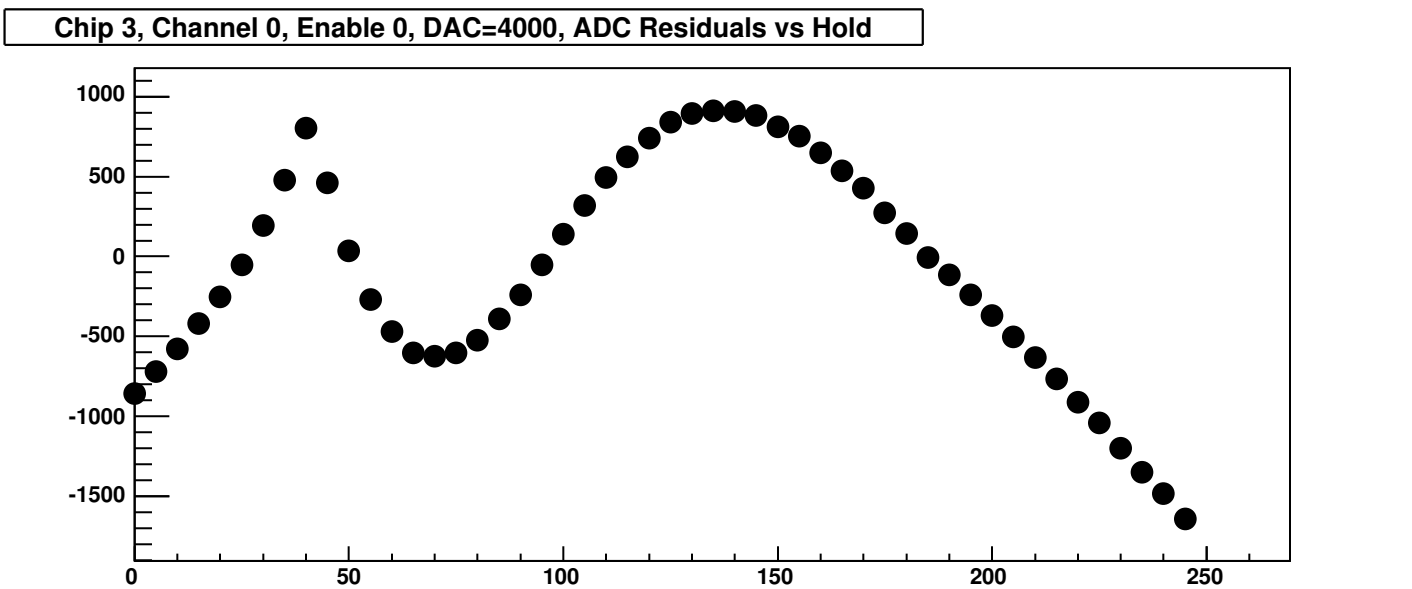


Chip 2, Channel 17, Enable 5, DAC=4000, ADC Residuals vs Hold

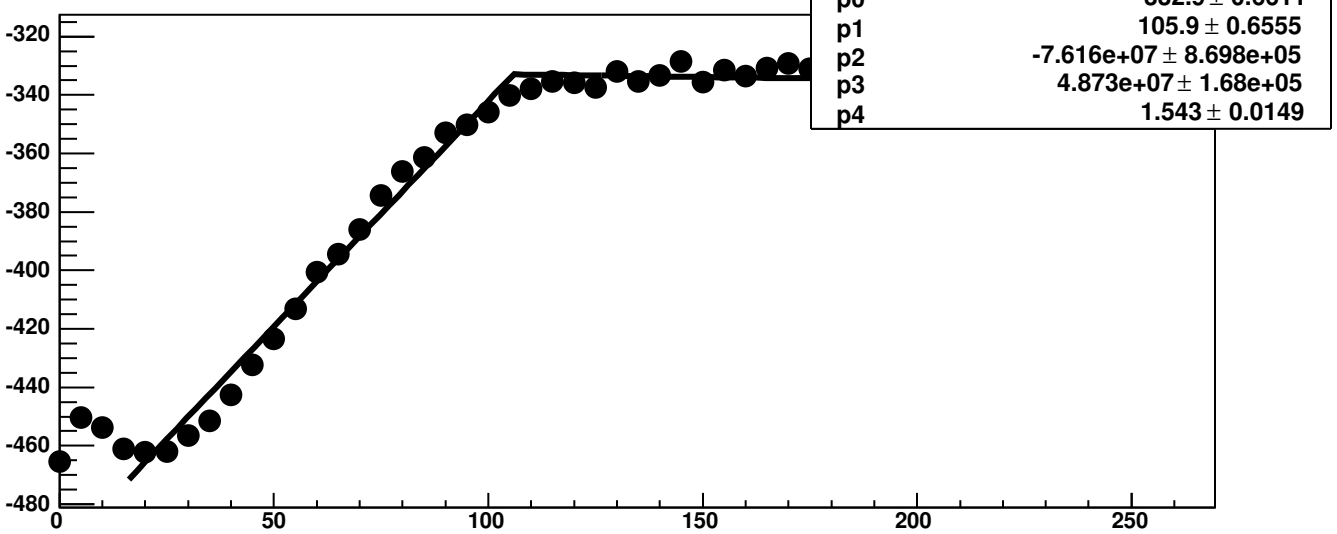



Chip 3, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.281e+05 / 41
p0 -665.9 ± 1.512
p1 41 ± 0.01801
p2 9705 ± 16.36
p3 51.63 ± 0.09135
p4 -25.31 ± 0.08003
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

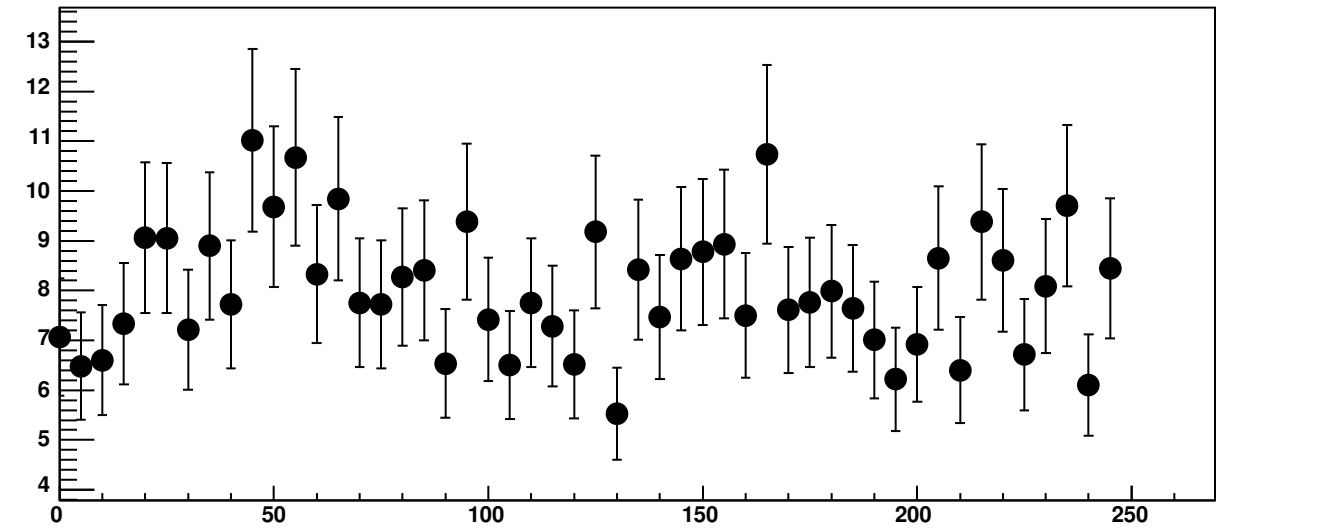

Chip 3, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 3, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

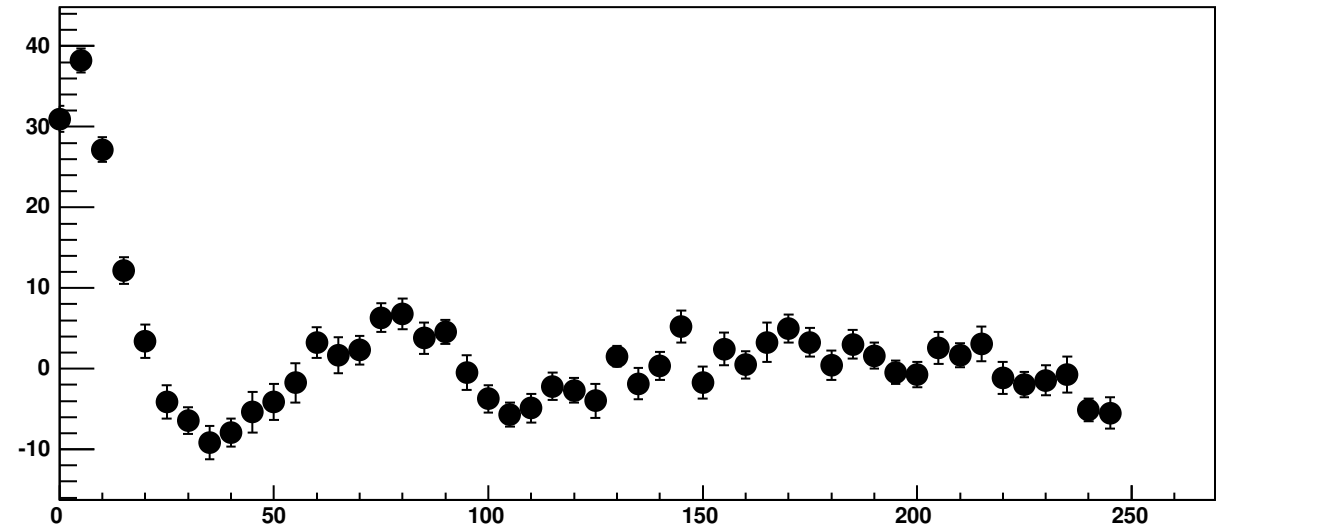
Chip 3, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold

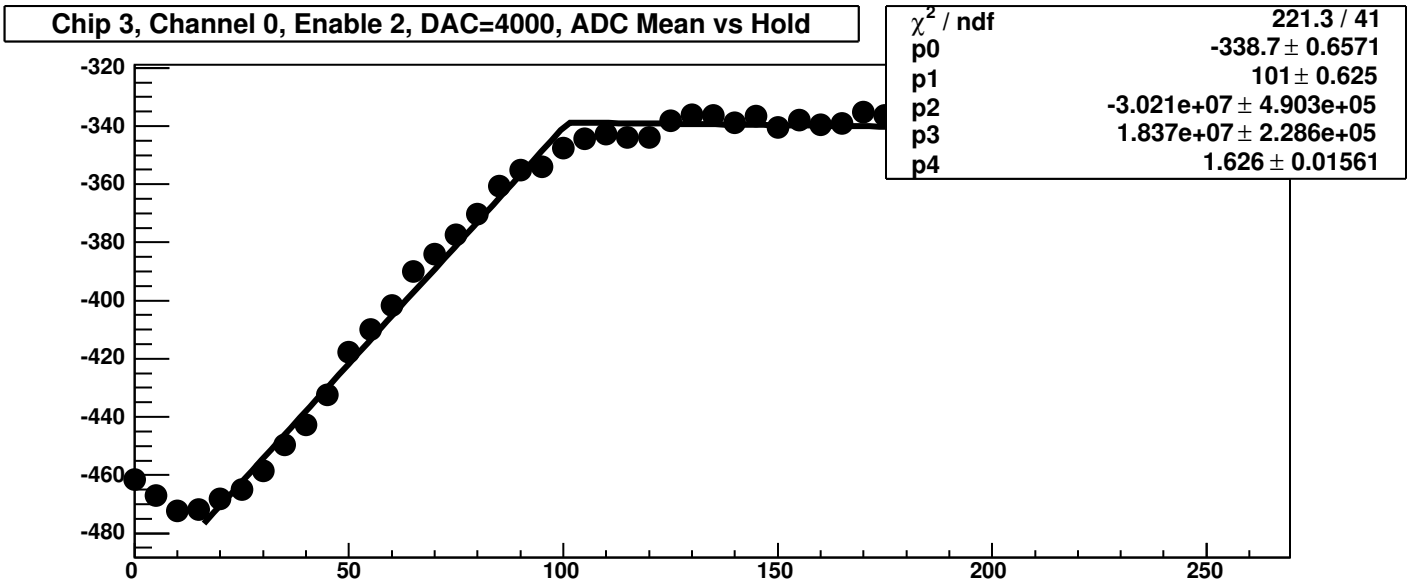


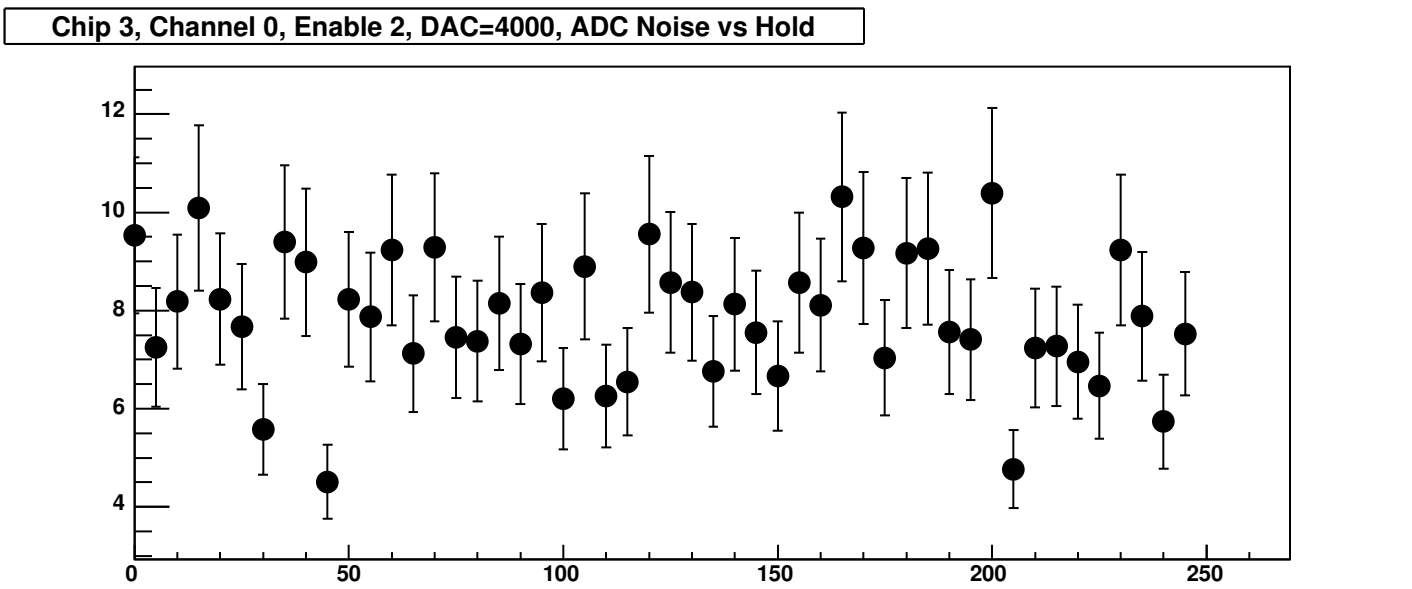
Chip 3, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold

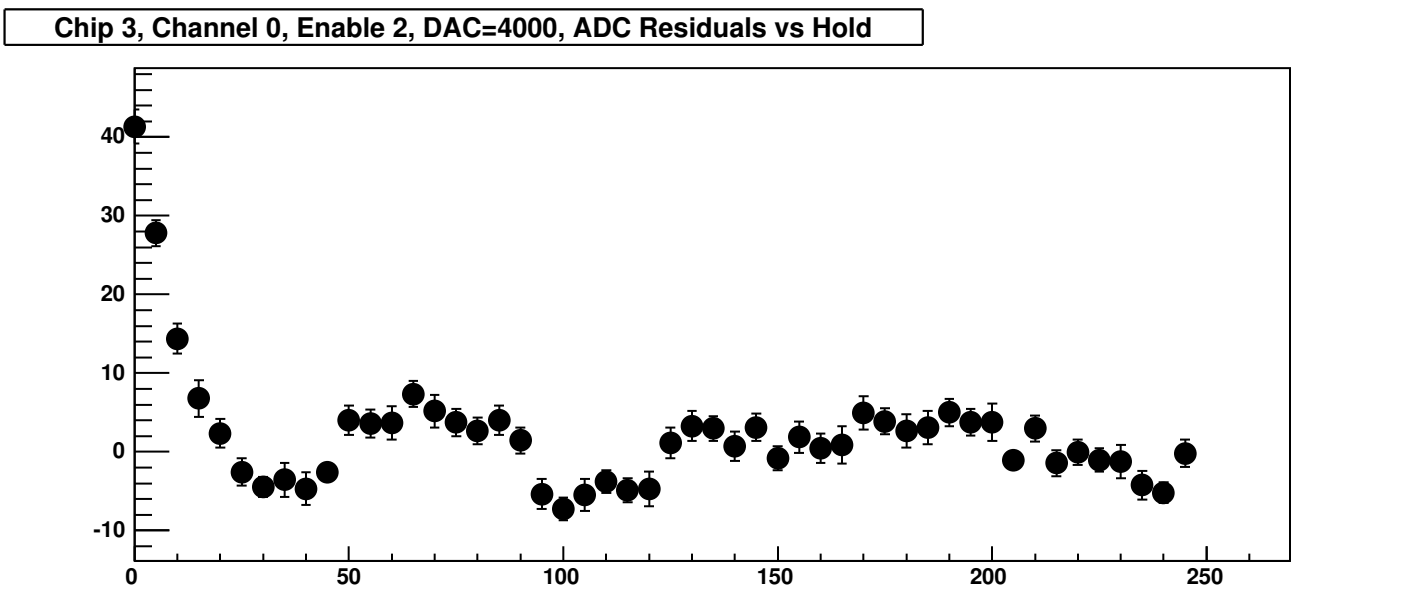


Chip 3, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold



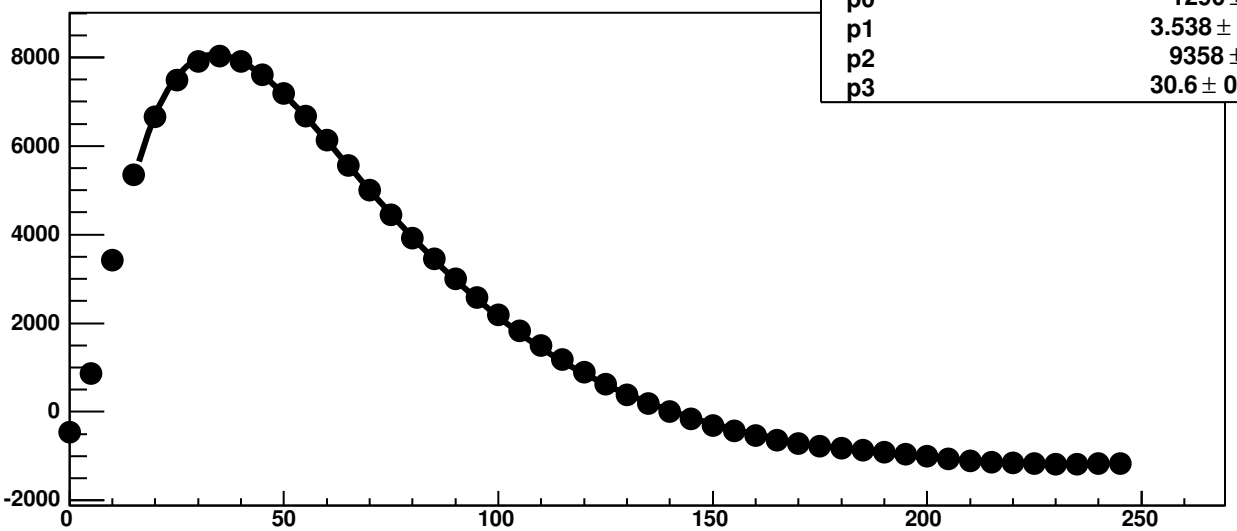

Chip 3, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 221.3 / 41
p0 -338.7 ± 0.6571
p1 101 ± 0.625
p2 -3.021e+07 ± 4.903e+05
p3 1.837e+07 ± 2.286e+05
p4 1.626 ± 0.01561
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
50
100
150
200
250


Chip 3, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

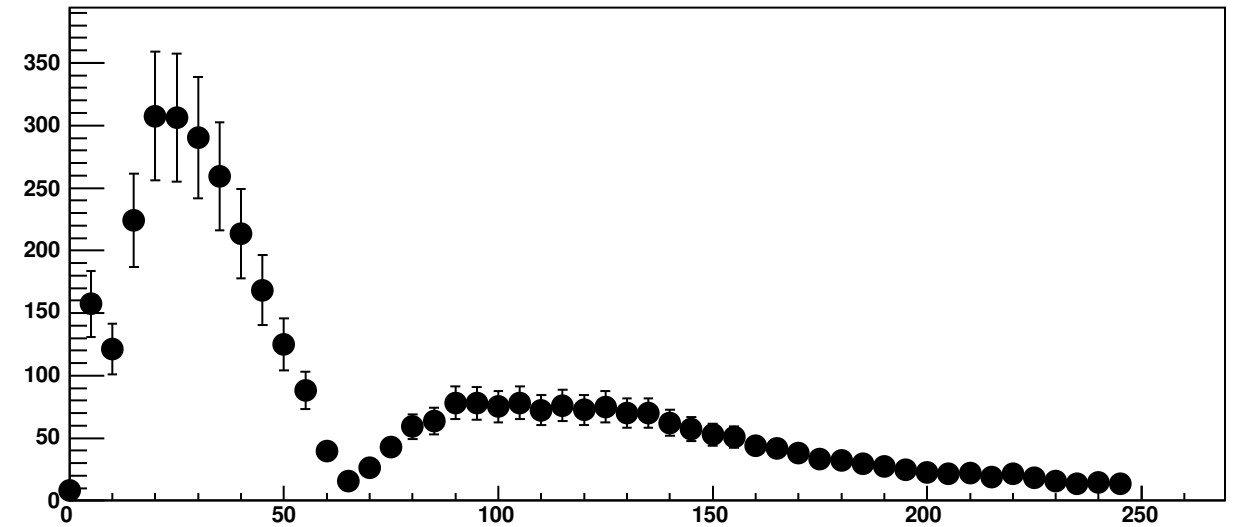

Chip 3, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

Chip 3, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold

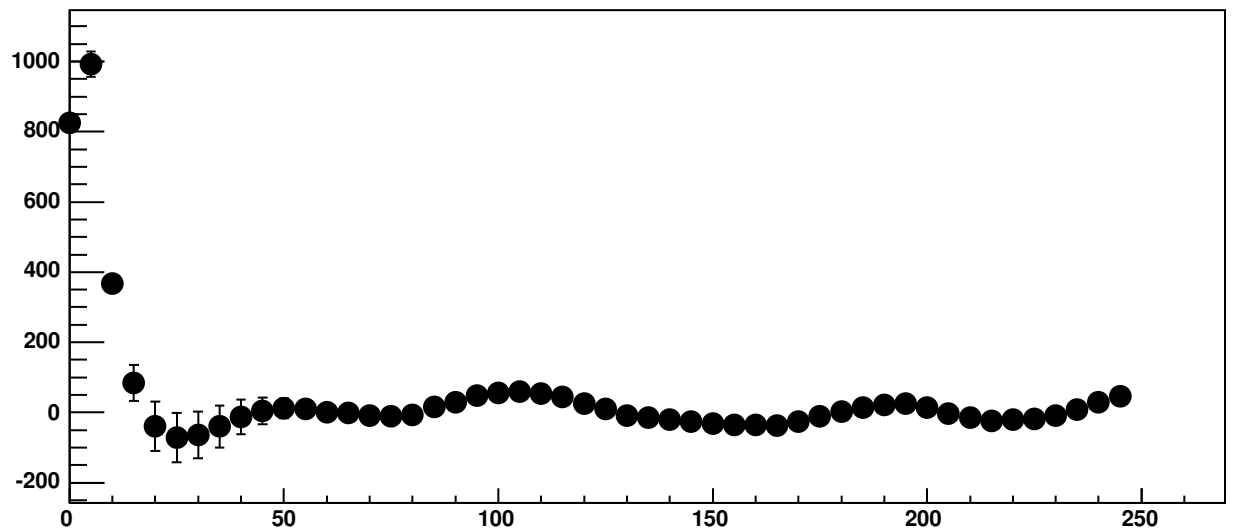
χ^2 / ndf	333 / 42
p0	-1290 ± 1.847
p1	3.538 ± 0.1079
p2	9358 ± 12.18
p3	30.6 ± 0.04403



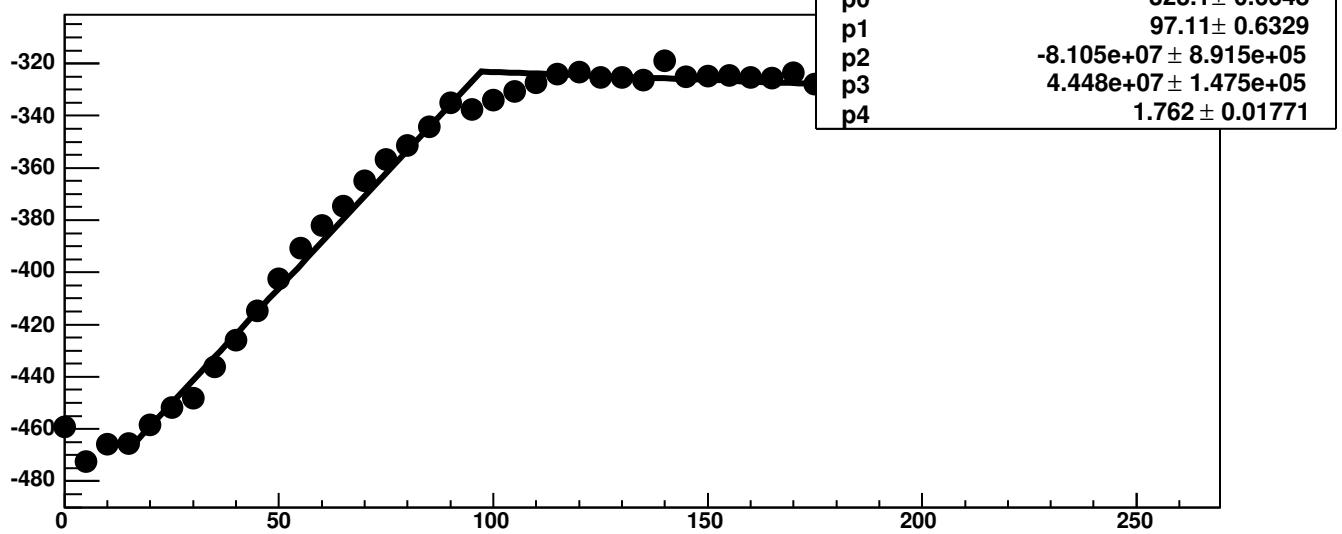
Chip 3, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold



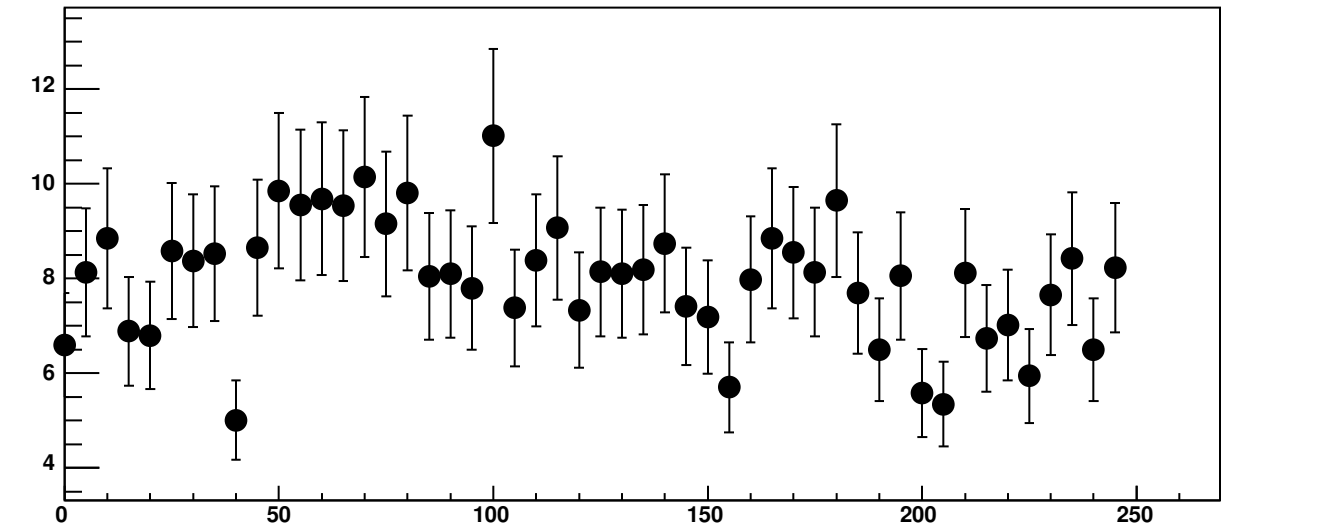
Chip 3, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold



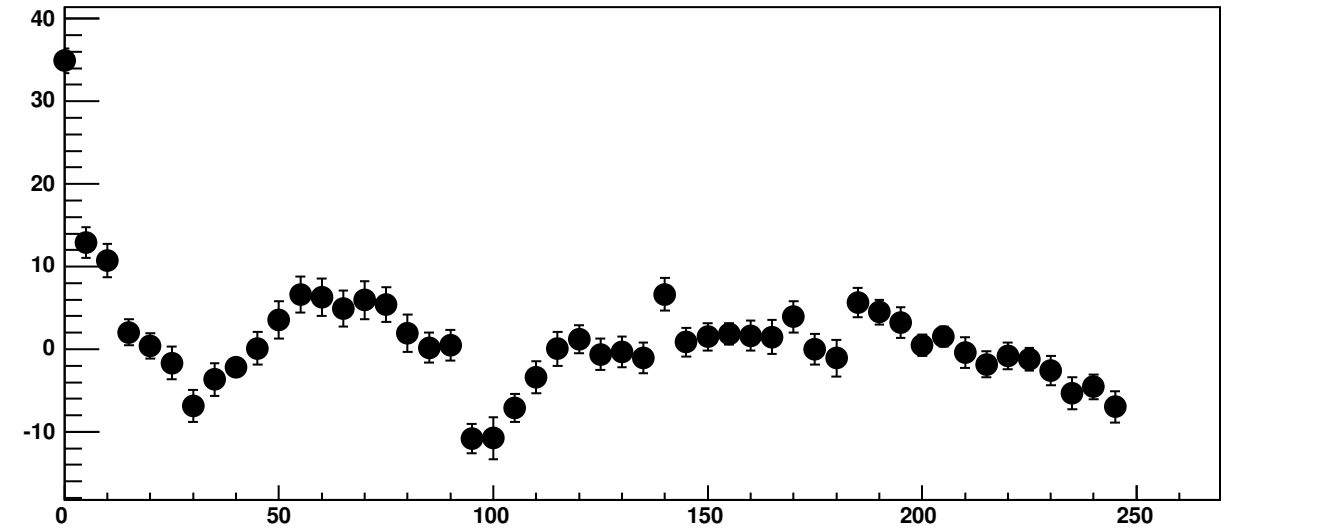
Chip 3, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold



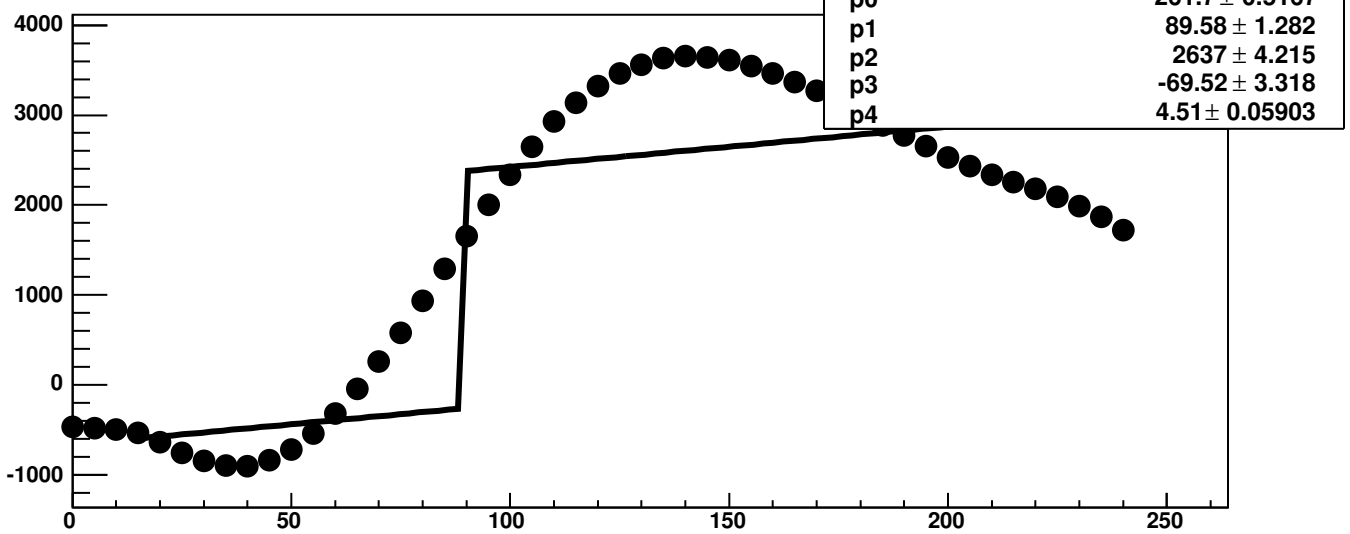
Chip 3, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



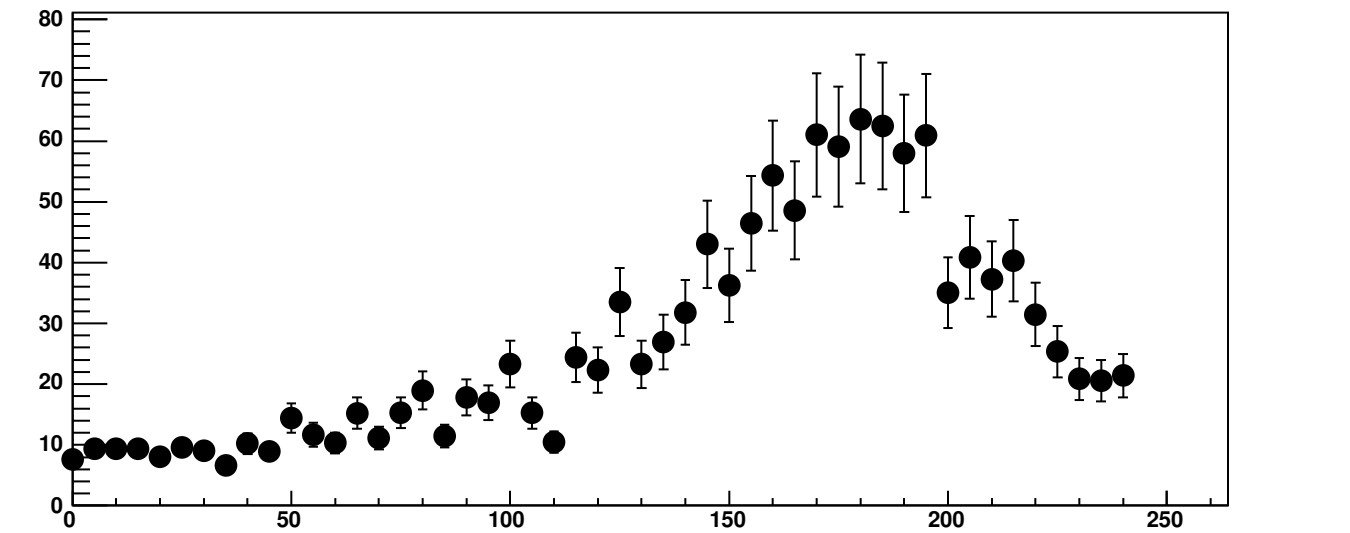
Chip 3, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold



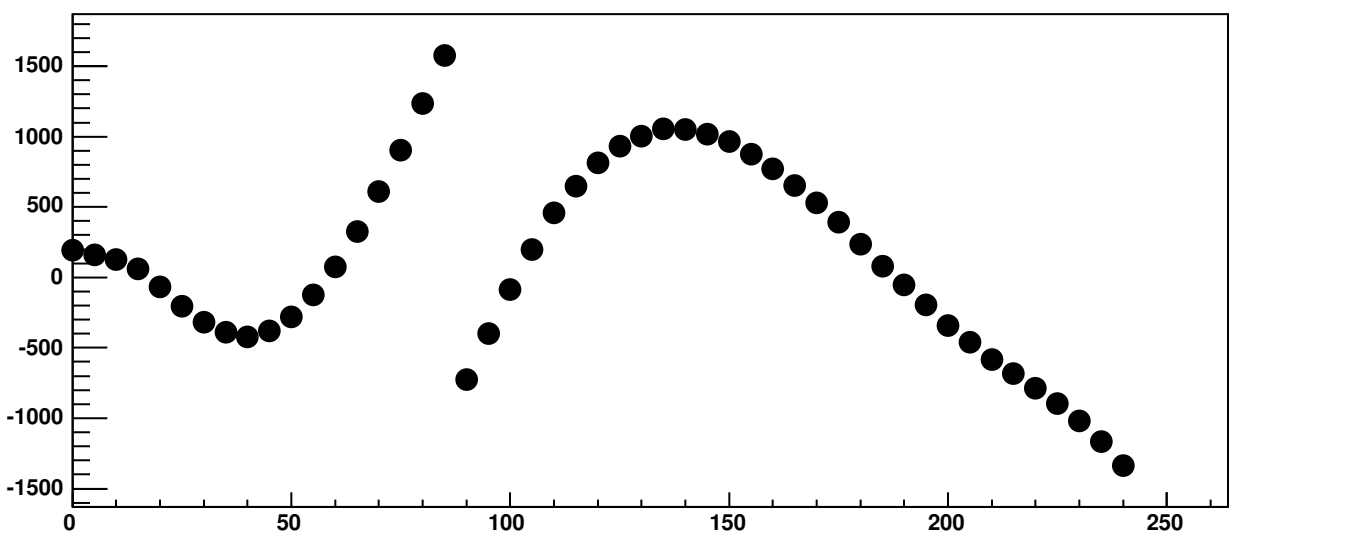
Chip 3, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold



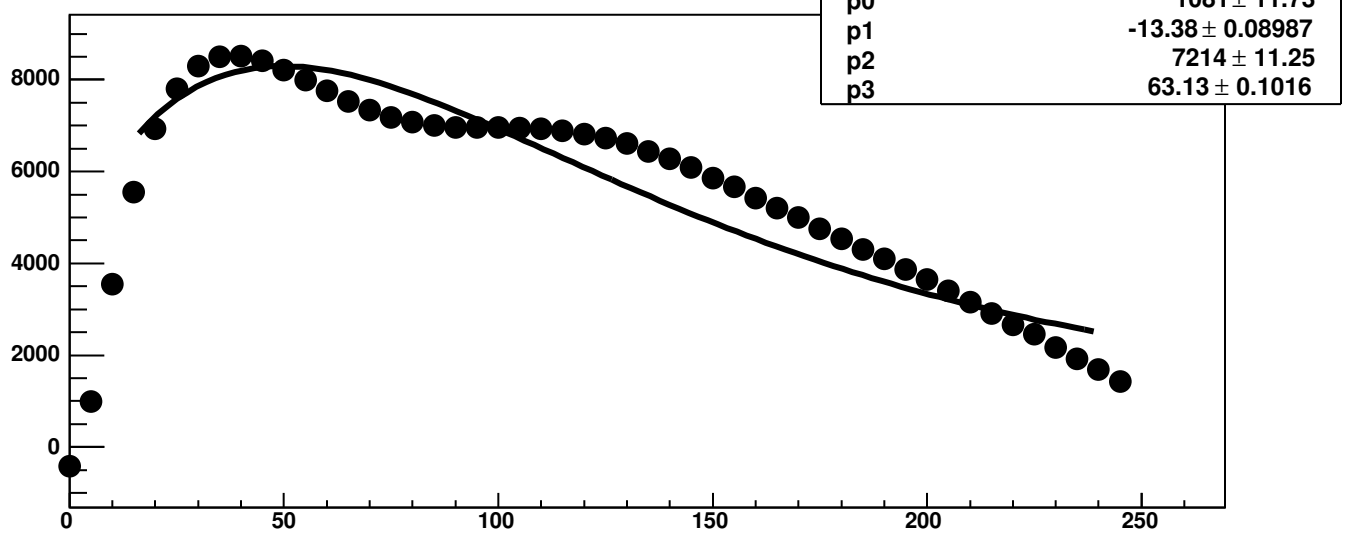
Chip 3, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



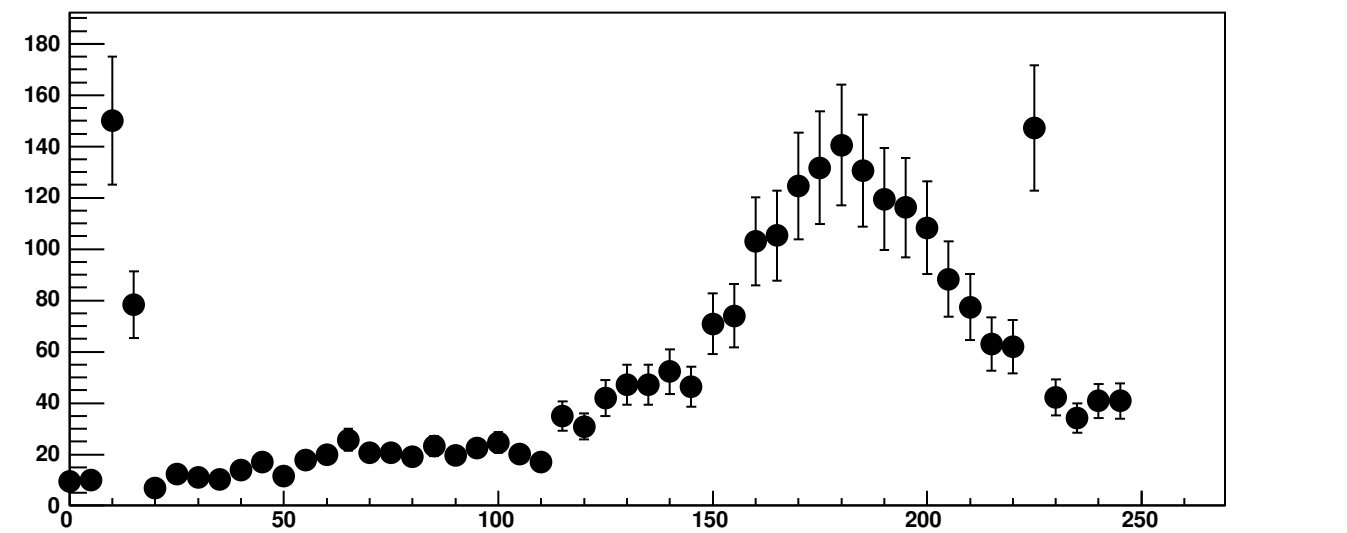
Chip 3, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold



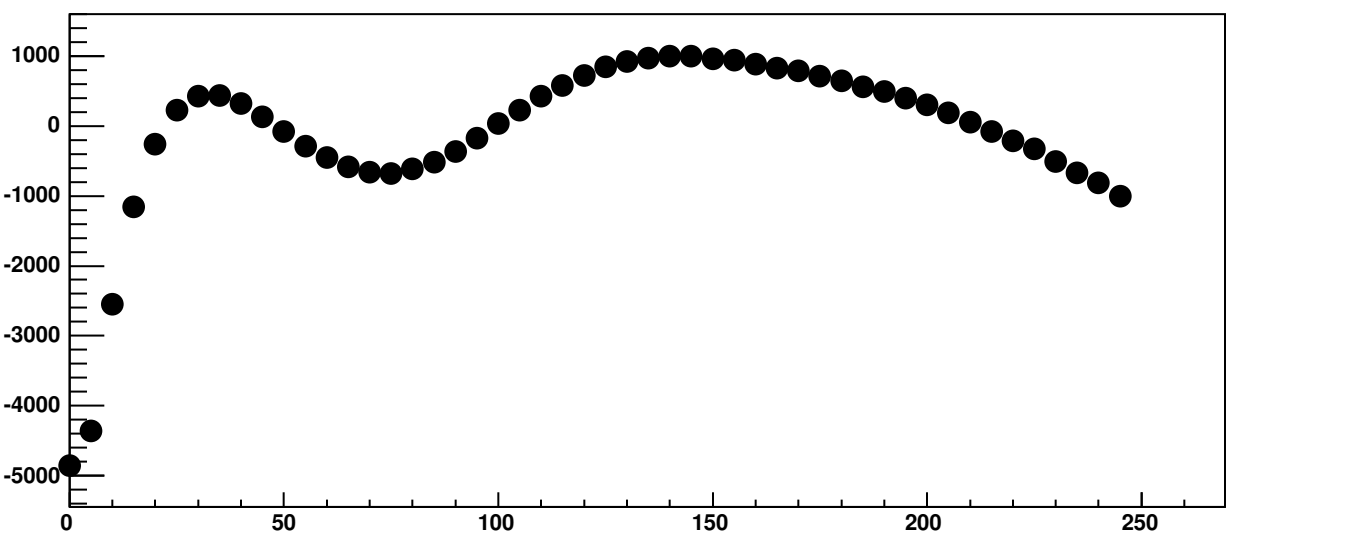
Chip 3, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold



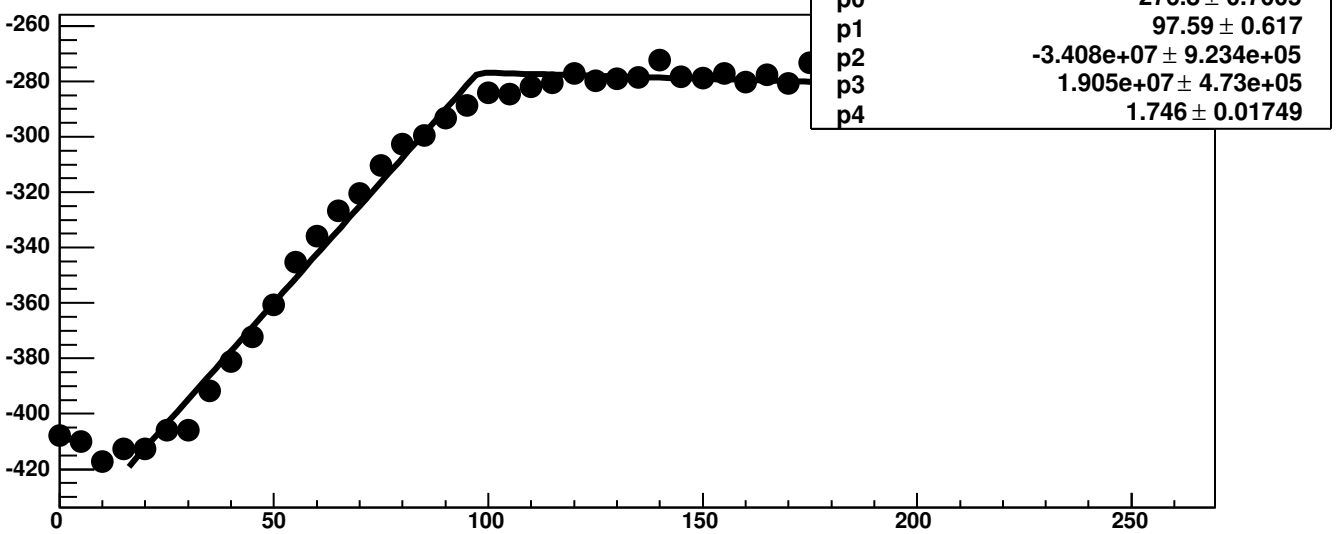
Chip 3, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold



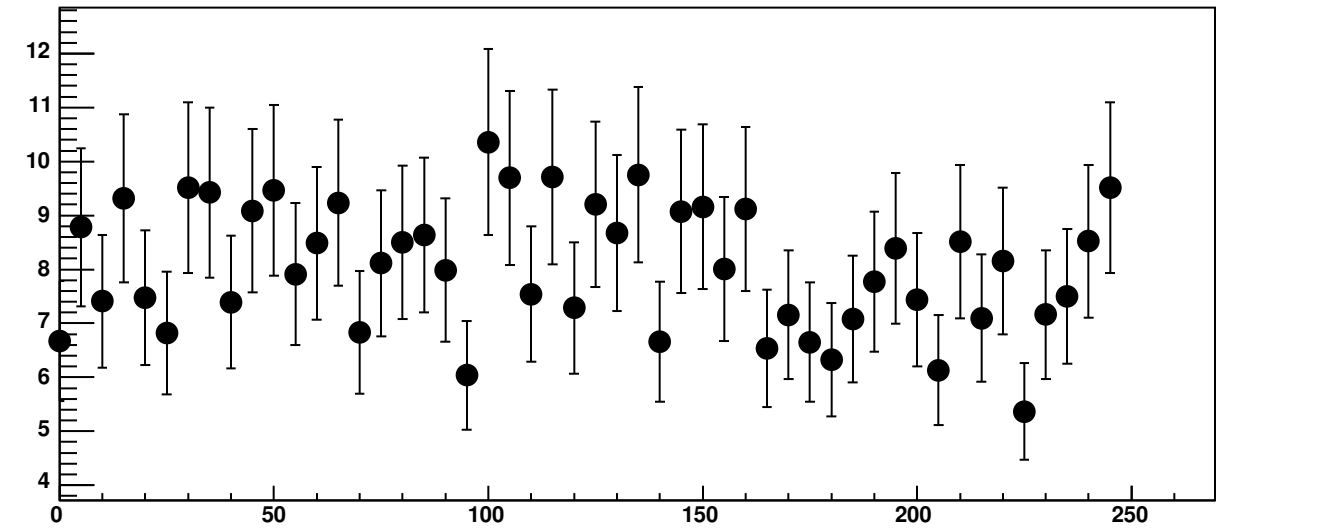
Chip 3, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold



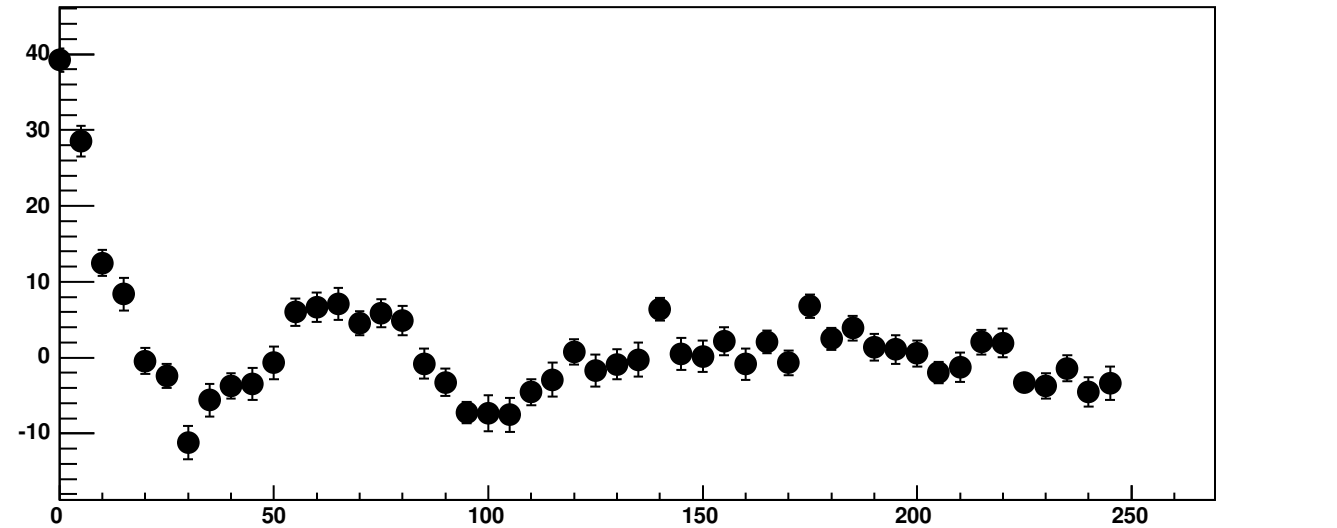
Chip 3, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold

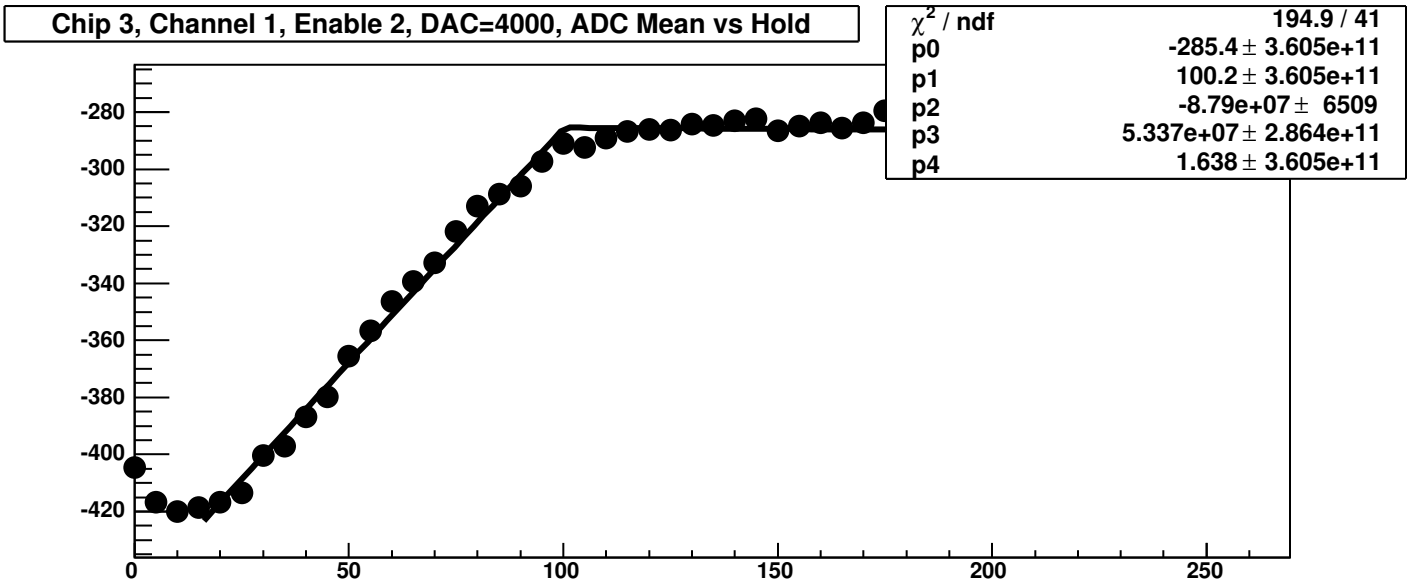


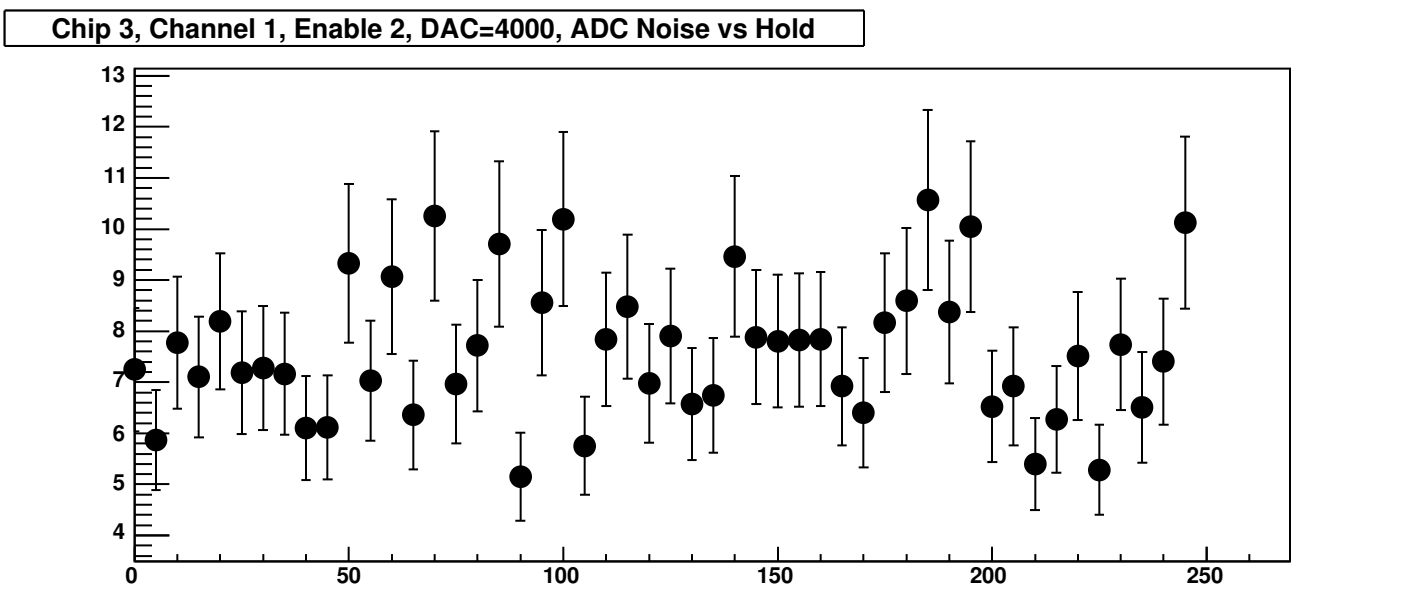
Chip 3, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold

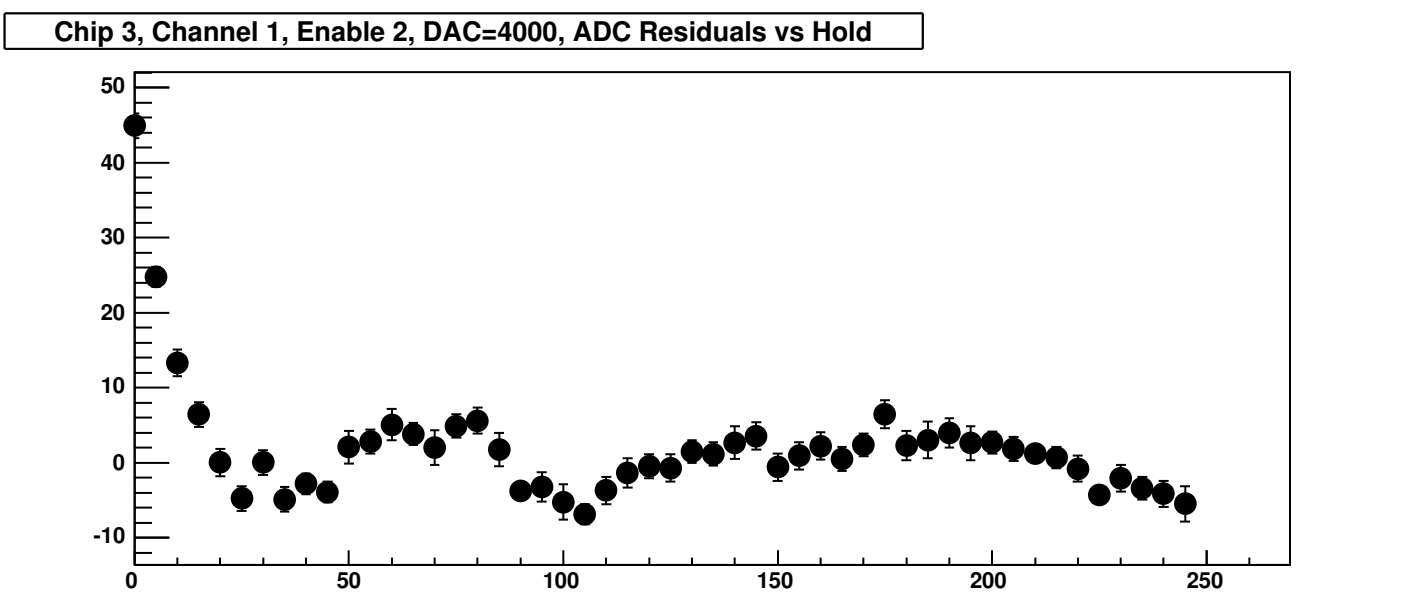


Chip 3, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

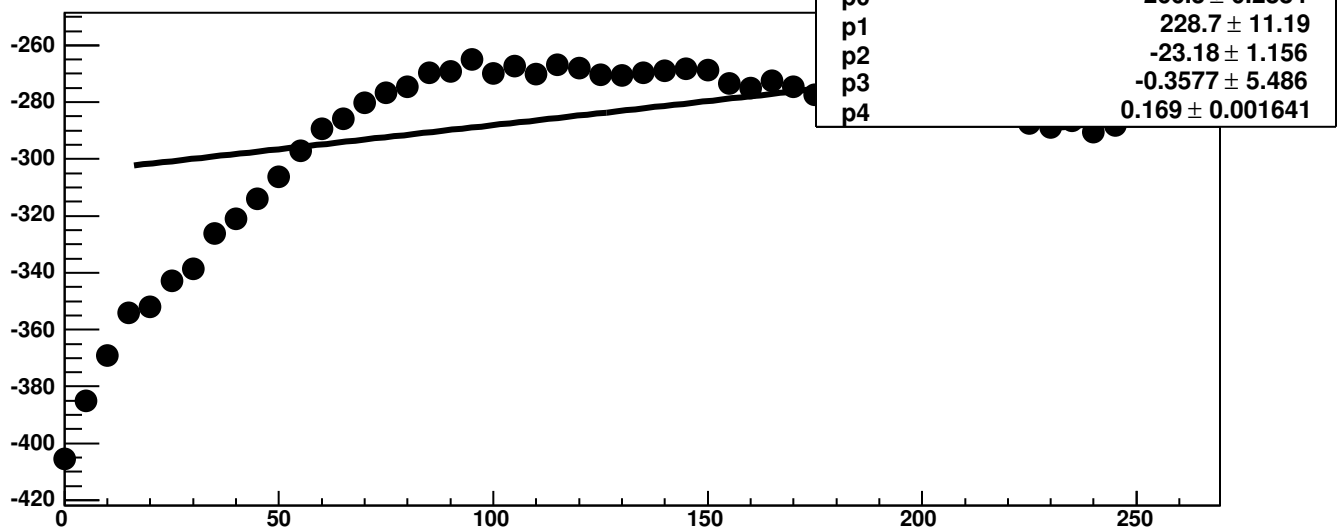



Chip 3, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 194.9 / 41
p0 -285.4 ± 3.605e+11
p1 100.2 ± 3.605e+11
p2 -8.79e+07 ± 6509
p3 5.337e+07 ± 2.864e+11
p4 1.638 ± 3.605e+11
-280
-300
-320
-340
-360
-380
-400
-420
0
50
100
150
200
250

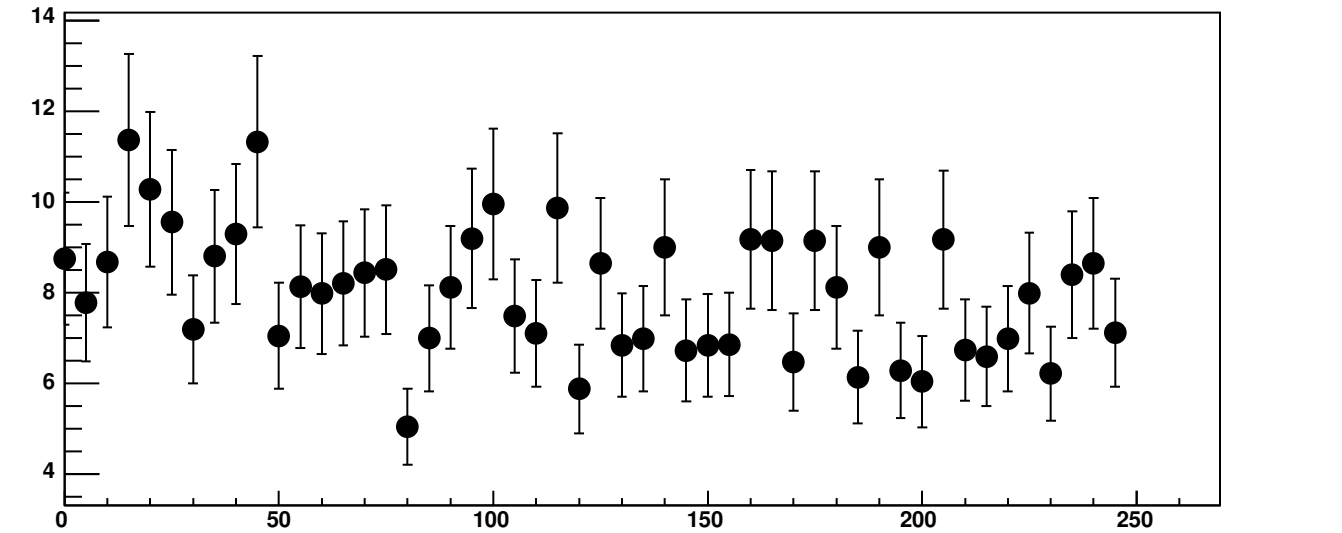

Chip 3, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 3, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250

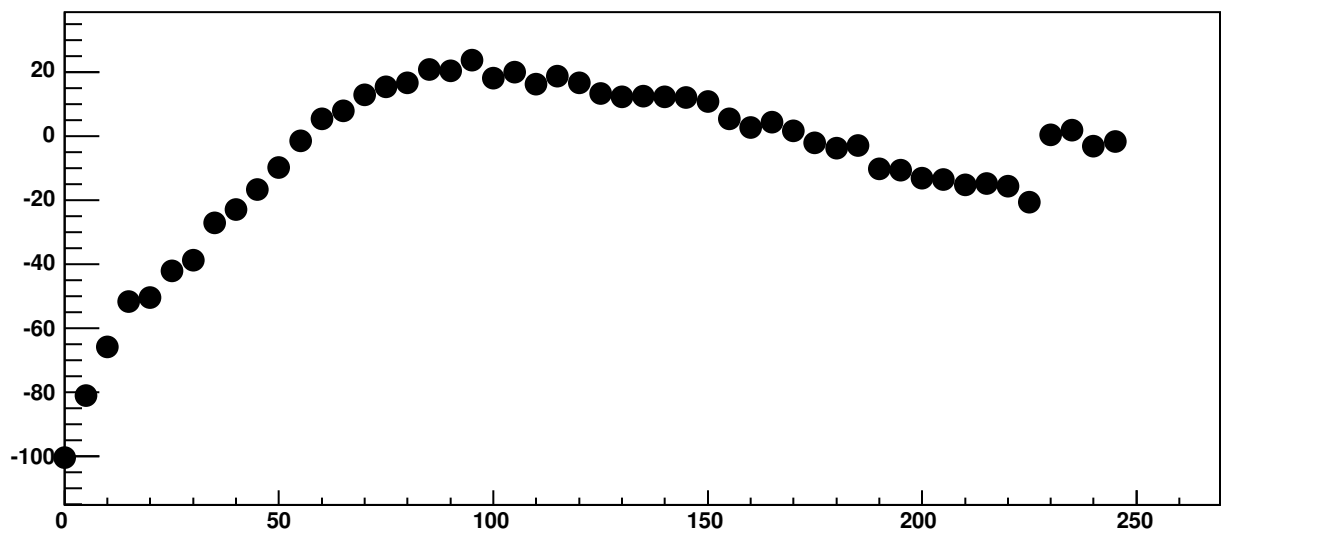
Chip 3, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold

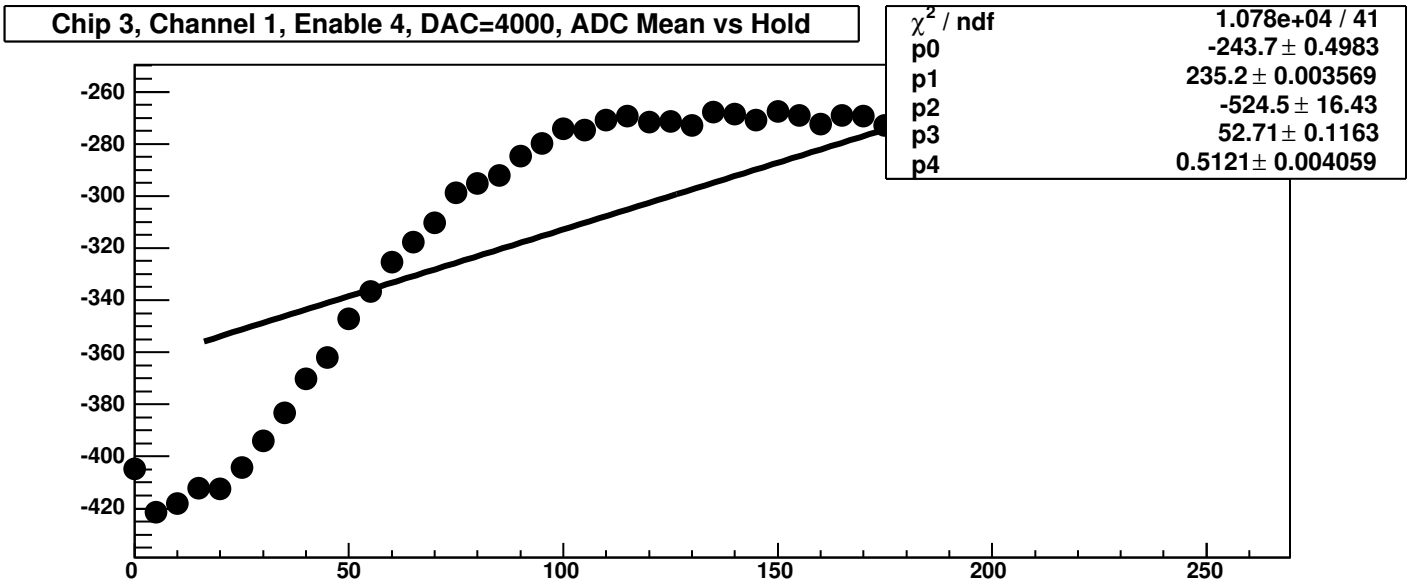


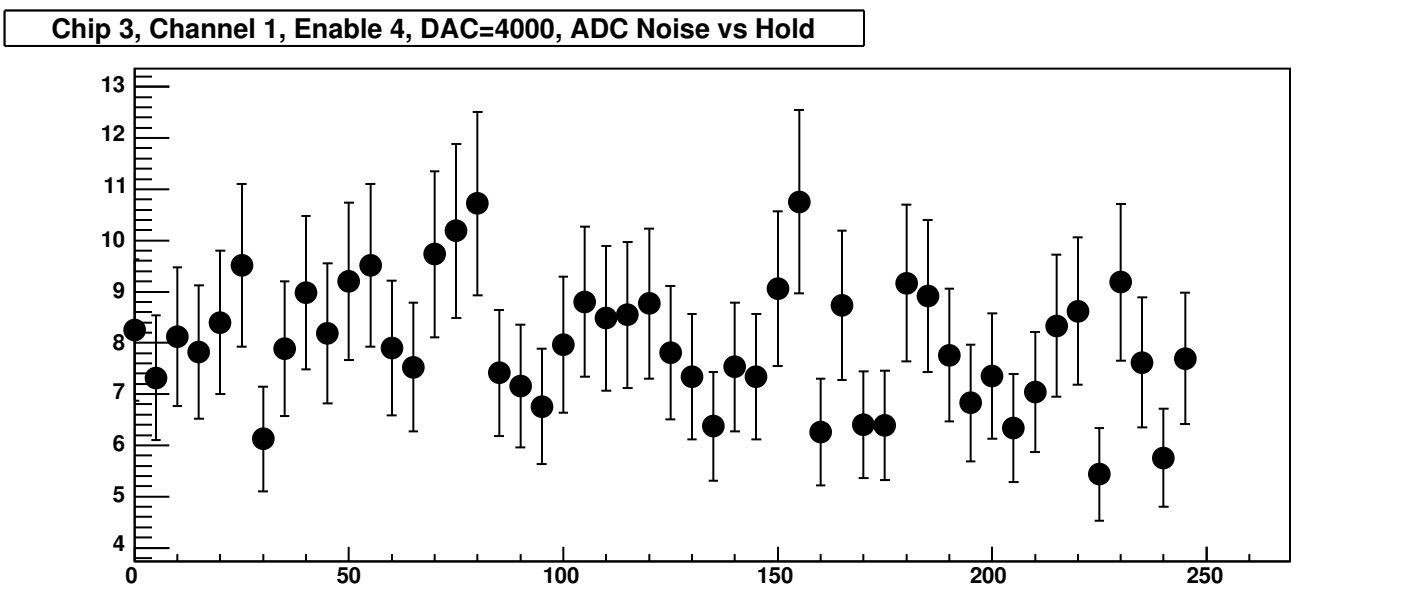
Chip 3, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold

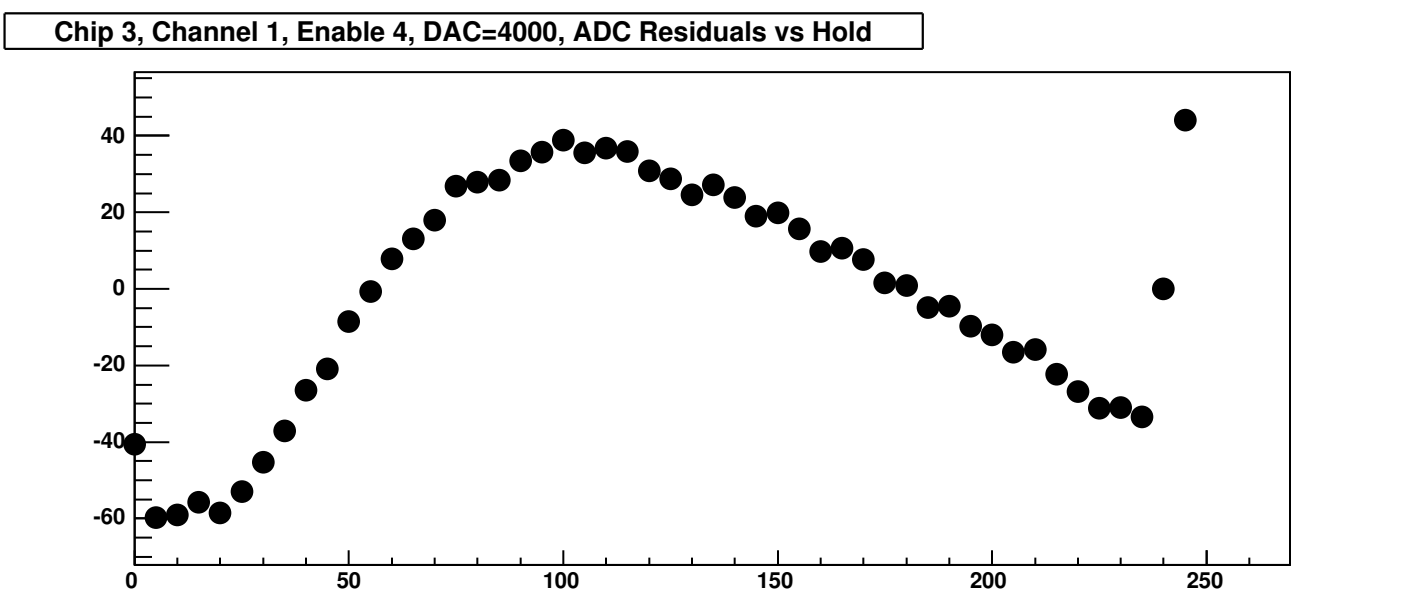


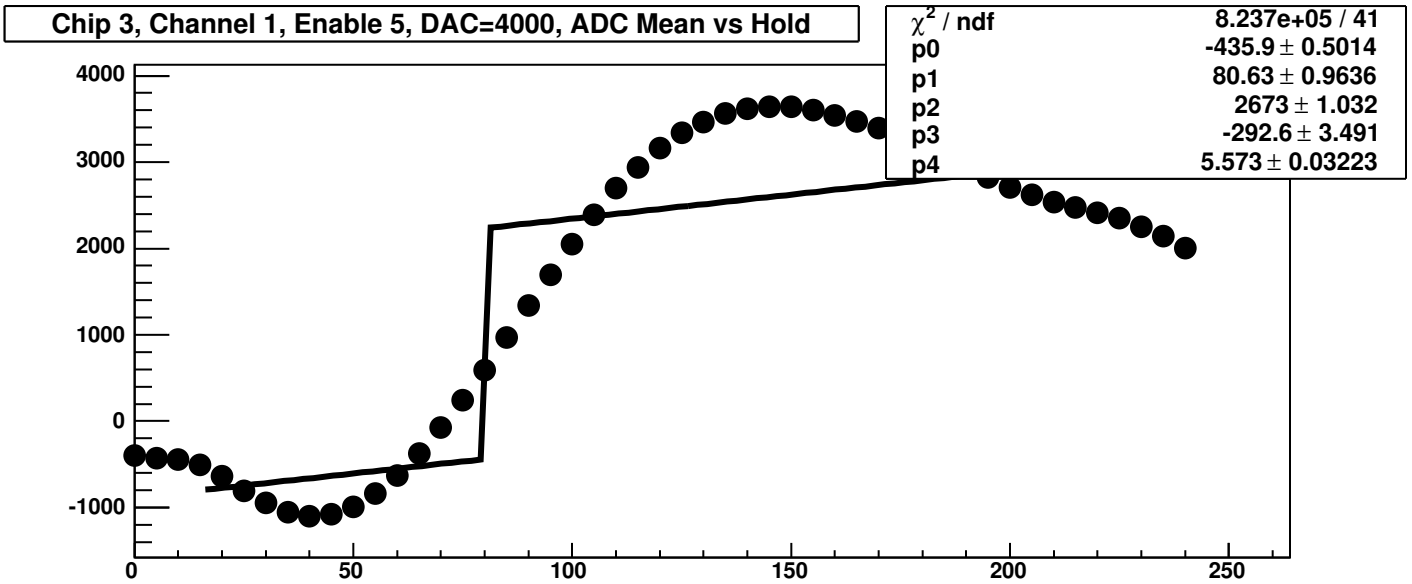
Chip 3, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold

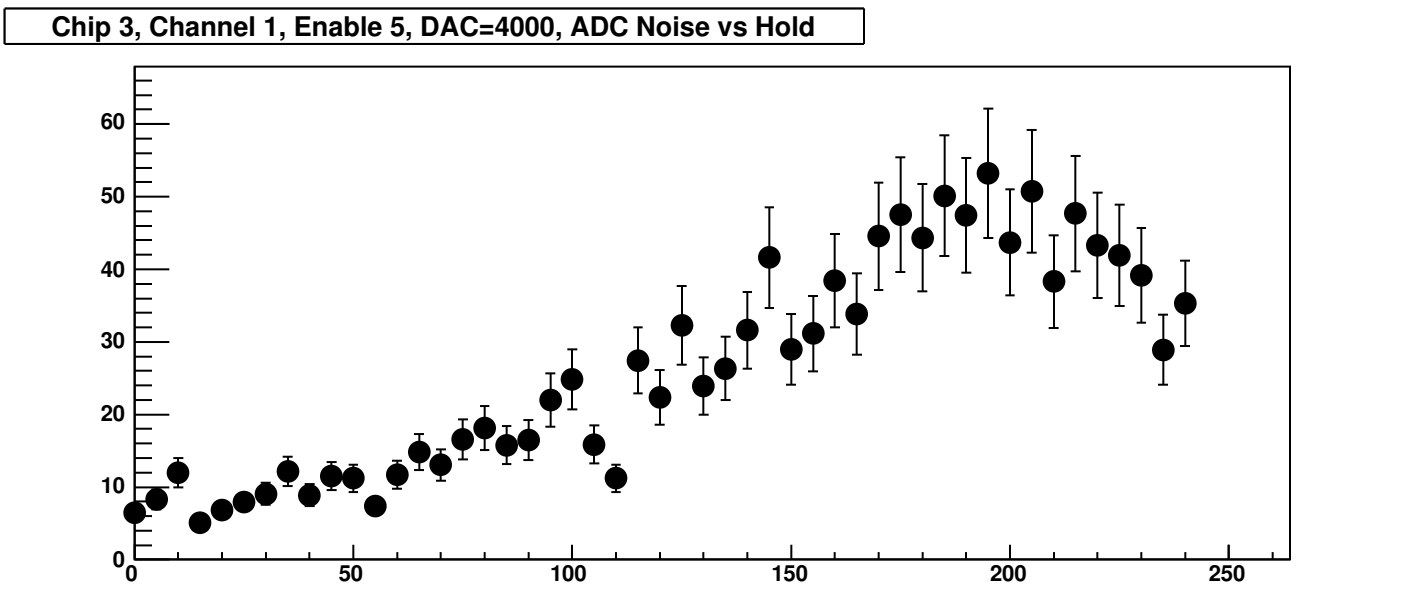


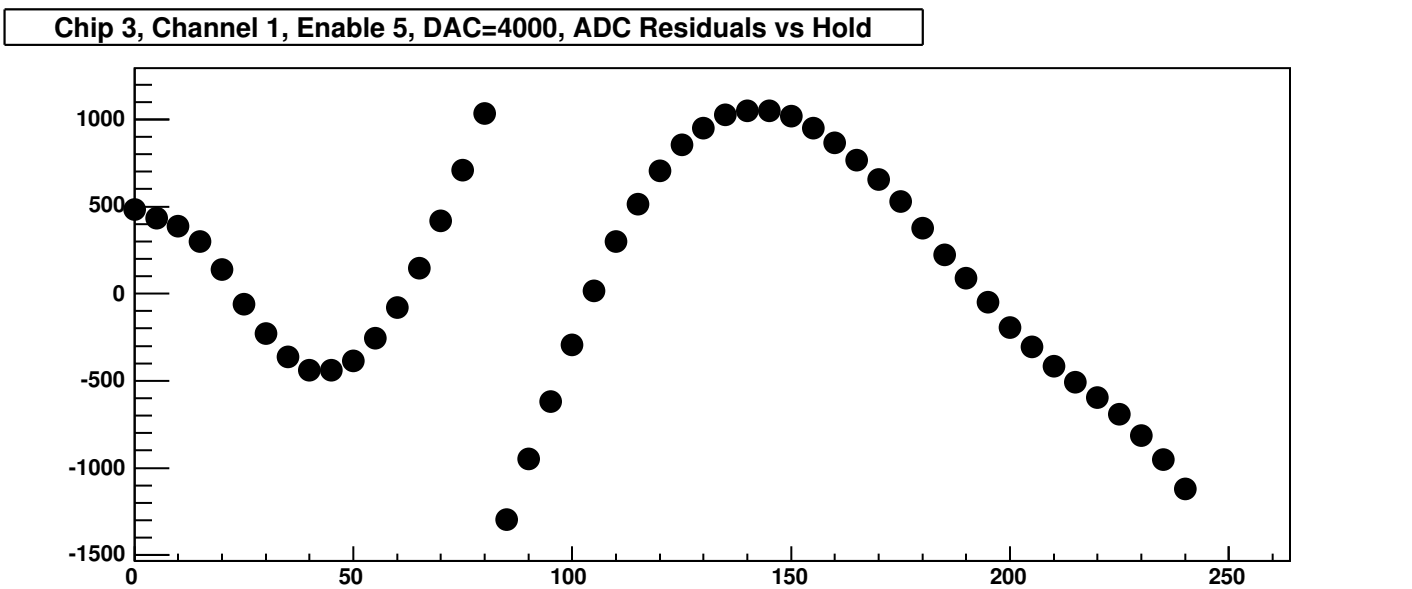

Chip 3, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.078e+04 / 41
p0 -243.7 ± 0.4983
p1 235.2 ± 0.003569
p2 -524.5 ± 16.43
p3 52.71 ± 0.1163
p4 0.5121 ± 0.004059
-260
-280
-300
-320
-340
-360
-380
-400
-420
0
50
100
150
200
250

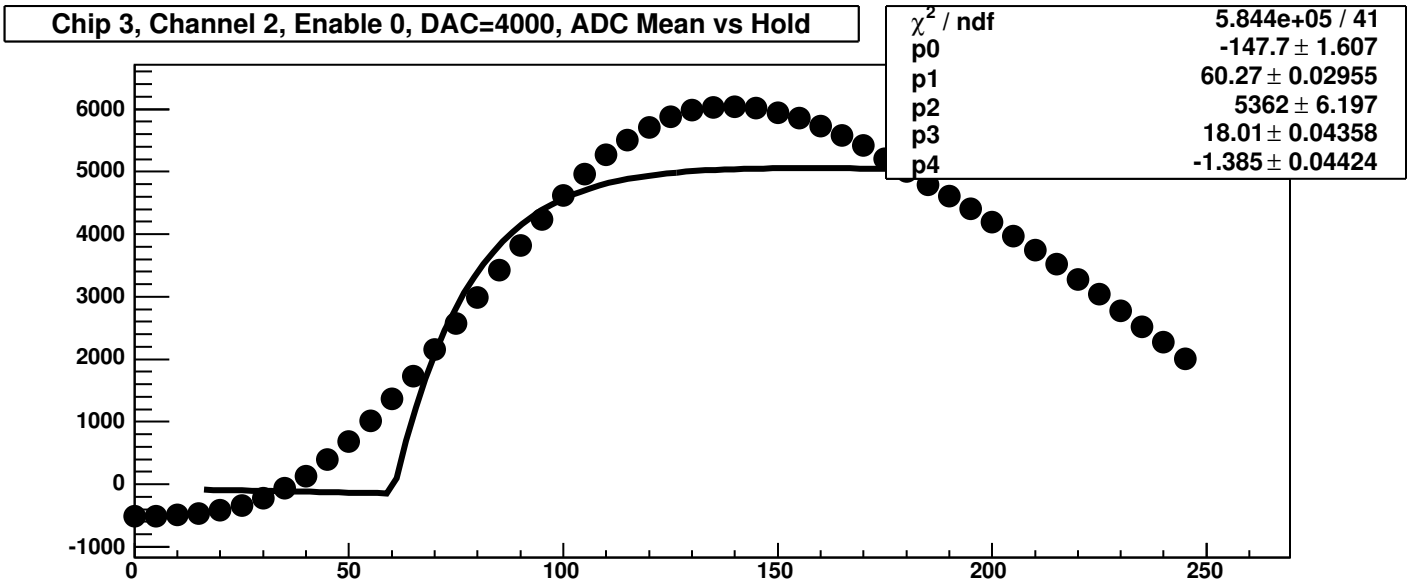

Chip 3, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

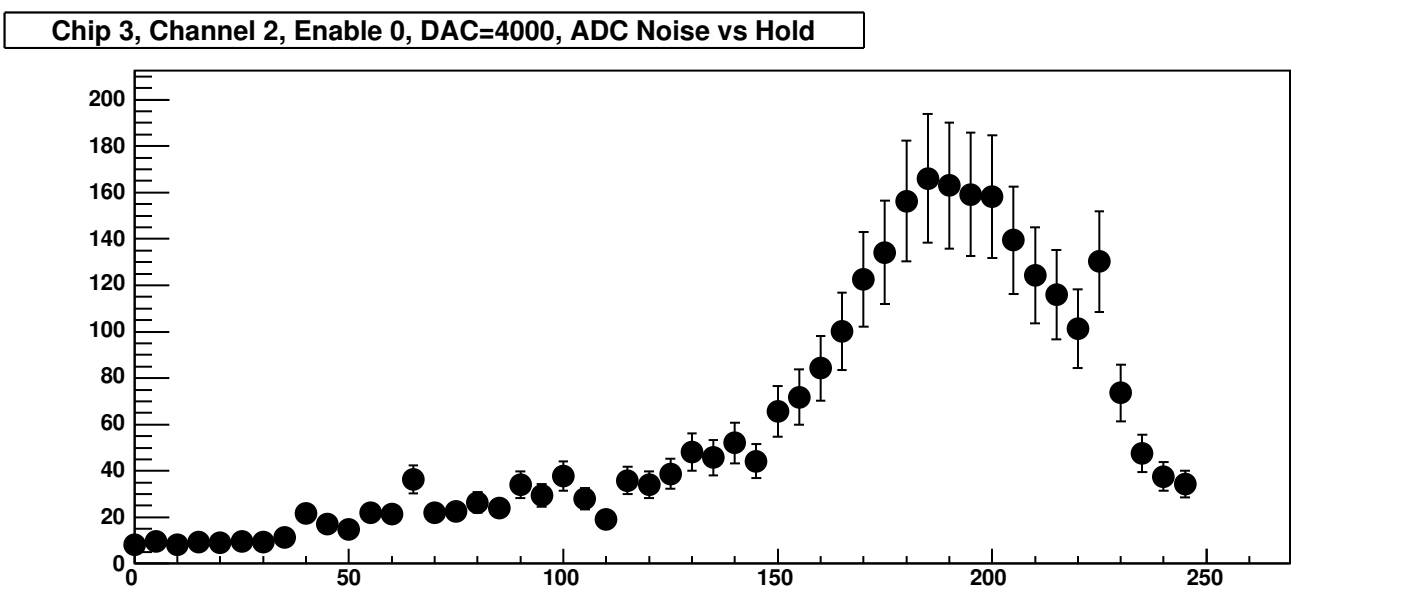

Chip 3, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
0
50
100
150
200
250

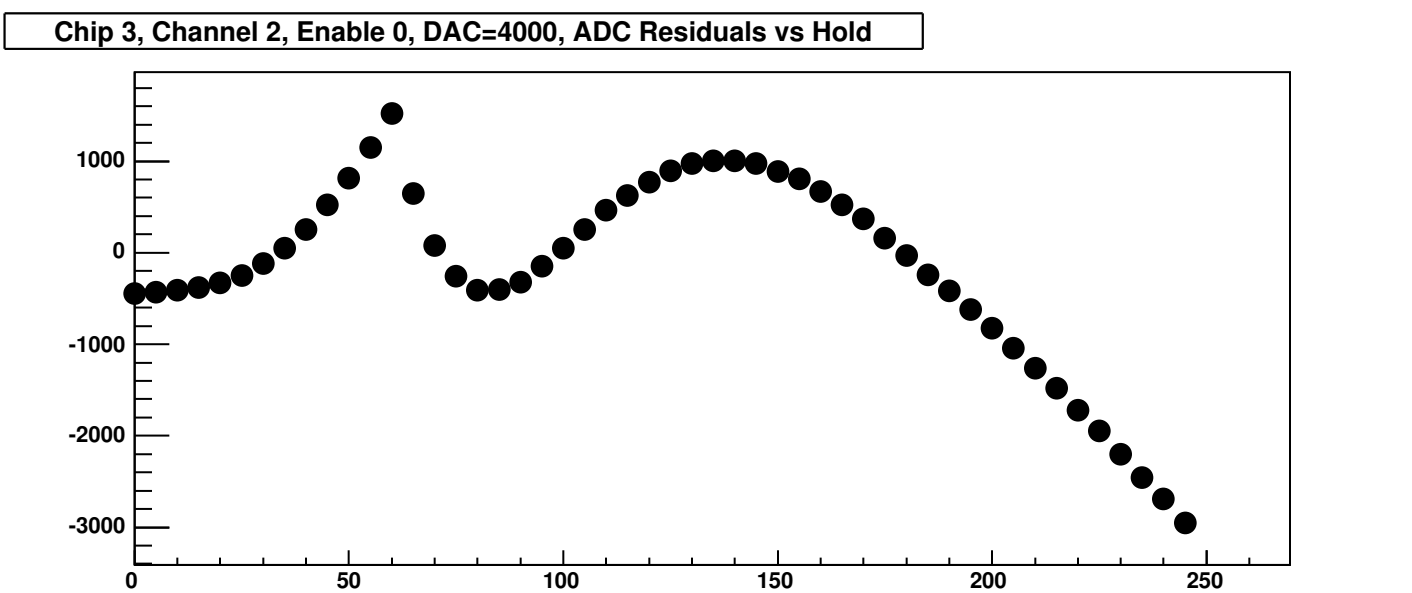

Chip 3, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 8.237e+05 / 41
p0 -435.9 ± 0.5014
p1 80.63 ± 0.9636
p2 2673 ± 1.032
p3 -292.6 ± 3.491
p4 5.573 ± 0.03223
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

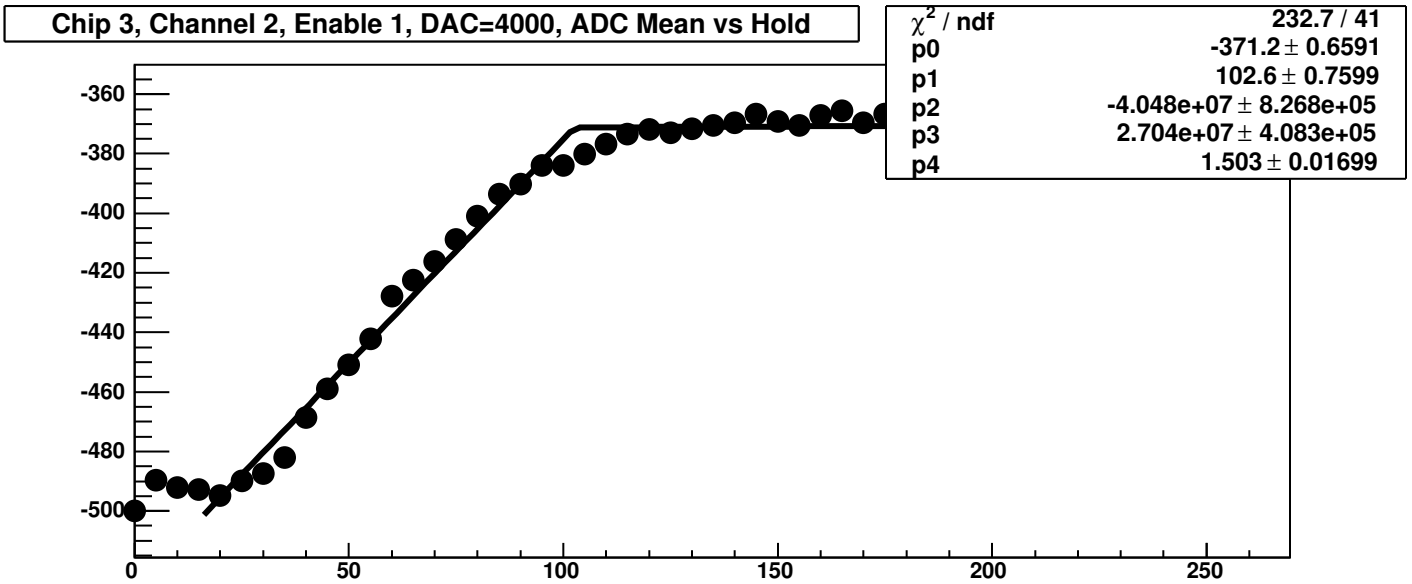

Chip 3, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold
60
50
40
30
20
10
0
0
50
100
150
200
250

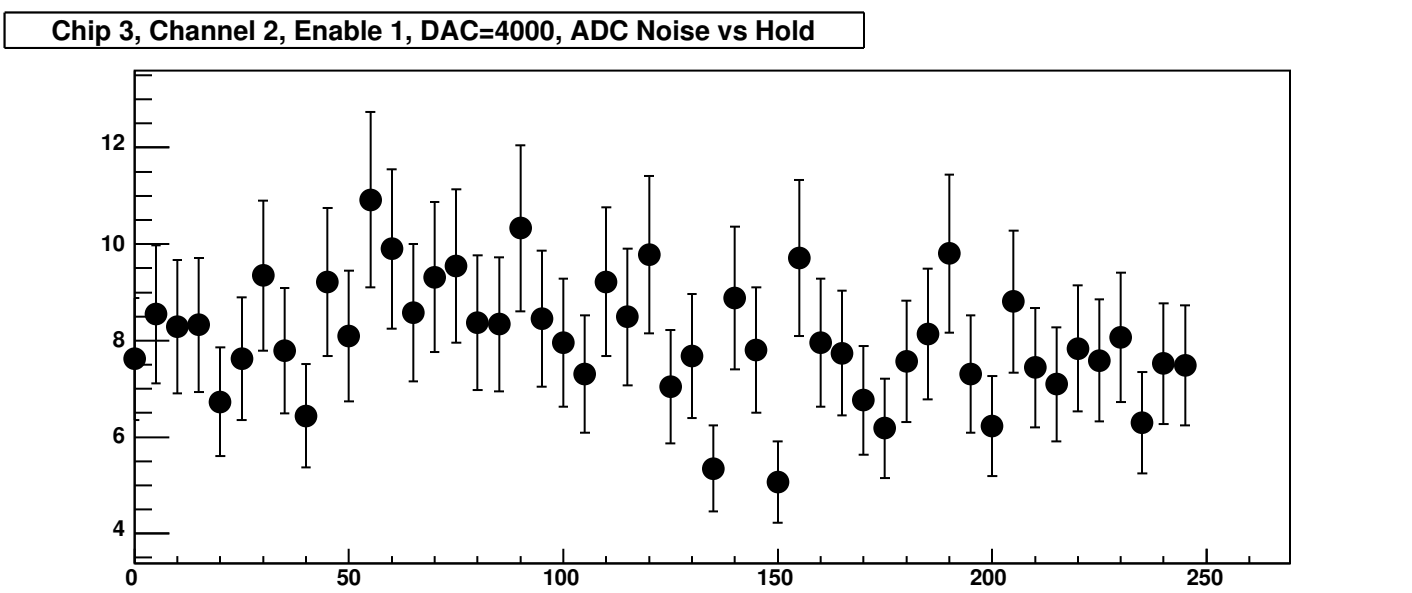

Chip 3, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

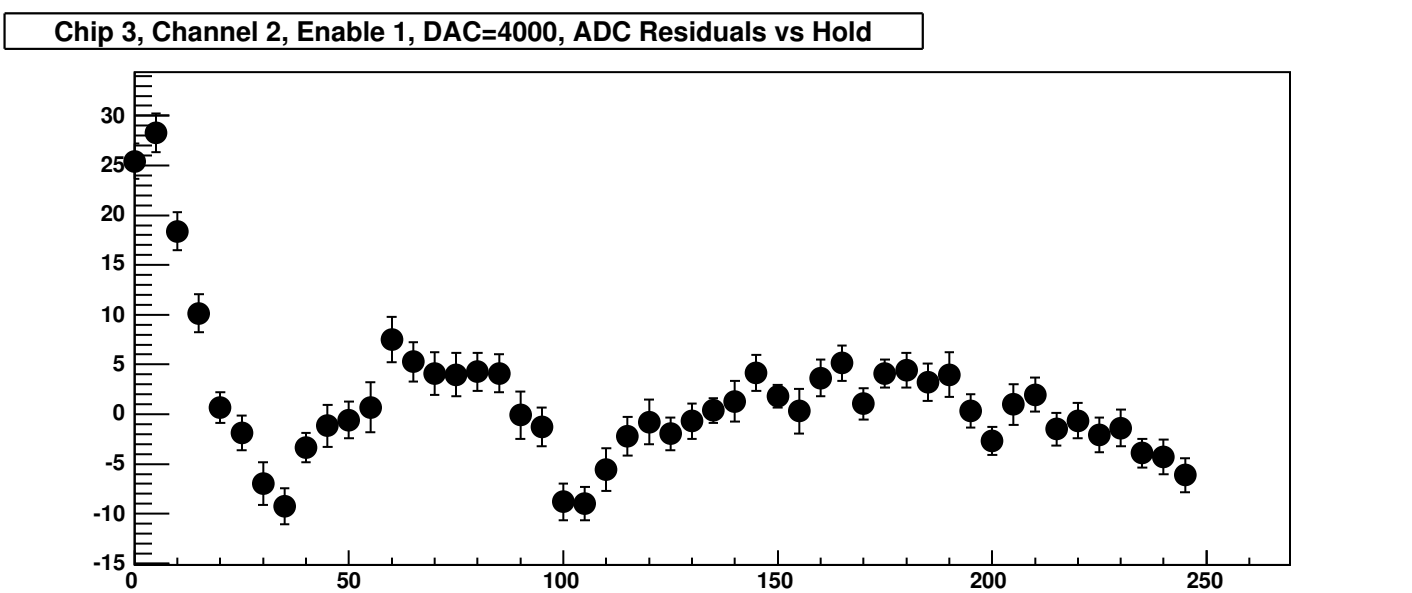

Chip 3, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.844e+05 / 41
p0 -147.7 ± 1.607
p1 60.27 ± 0.02955
p2 5362 ± 6.197
p3 18.01 ± 0.04358
p4 -1.385 ± 0.04424
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

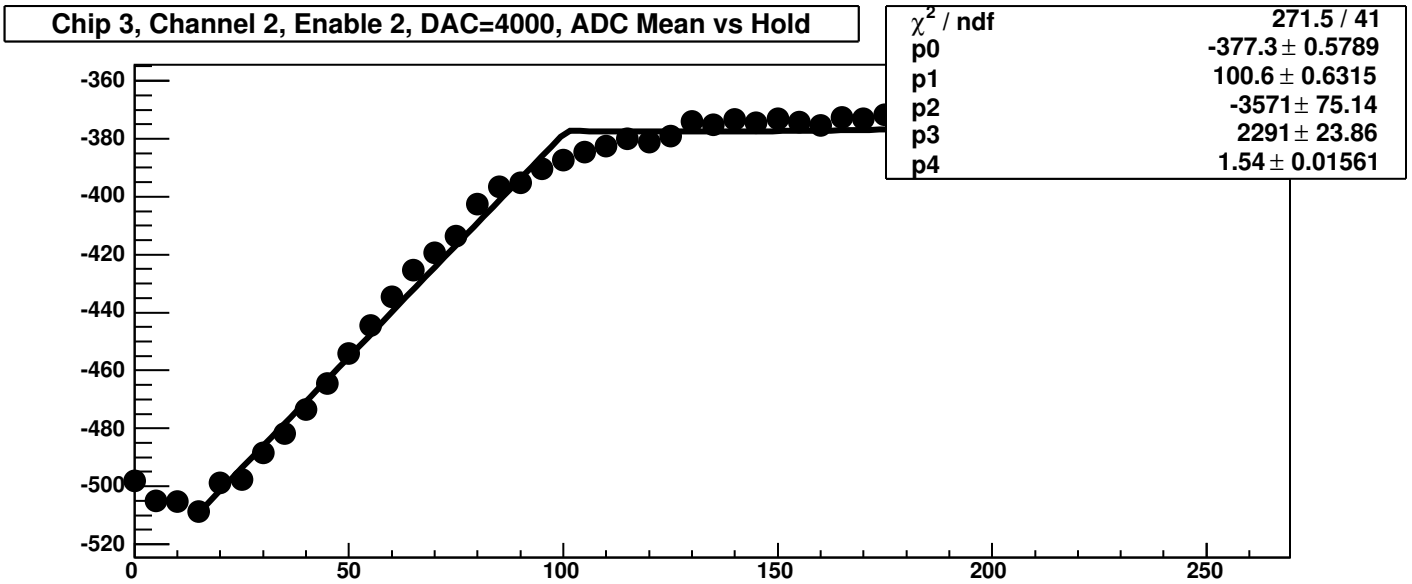

Chip 3, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

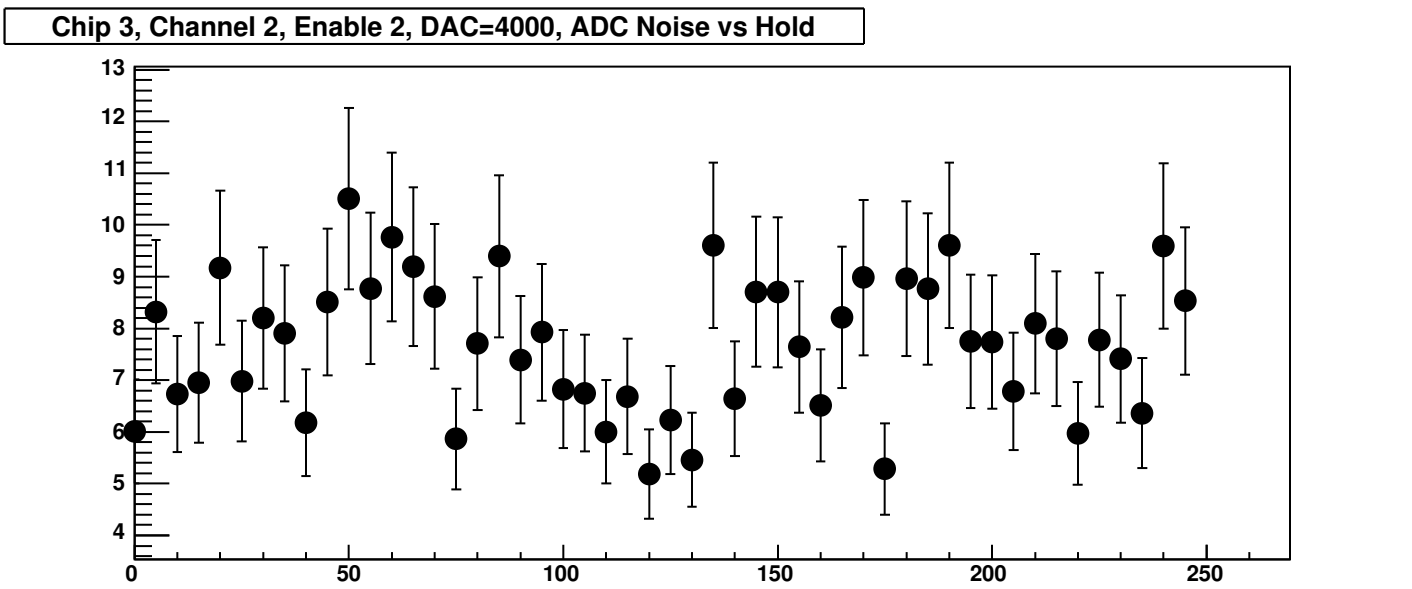

Chip 3, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

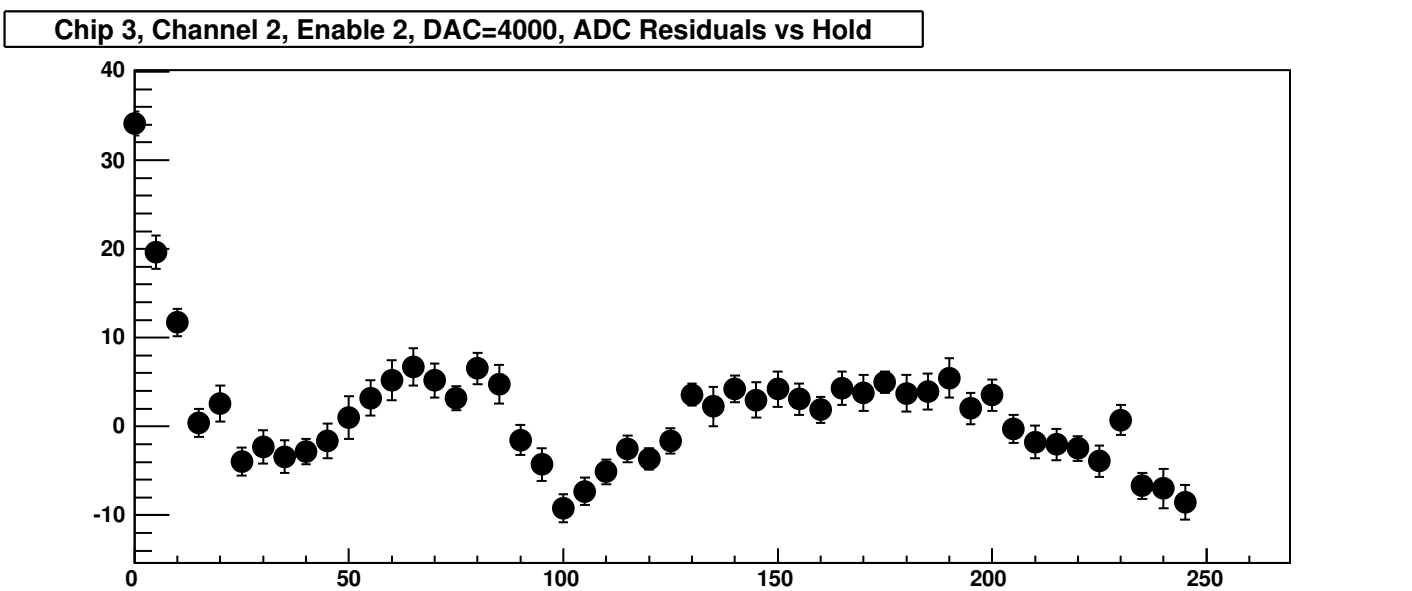

Chip 3, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 232.7 / 41
p0 -371.2 ± 0.6591
p1 102.6 ± 0.7599
p2 -4.048e+07 ± 8.268e+05
p3 2.704e+07 ± 4.083e+05
p4 1.503 ± 0.01699
-360
-380
-400
-420
-440
-460
-480
-500
0
50
100
150
200
250


Chip 3, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 3, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

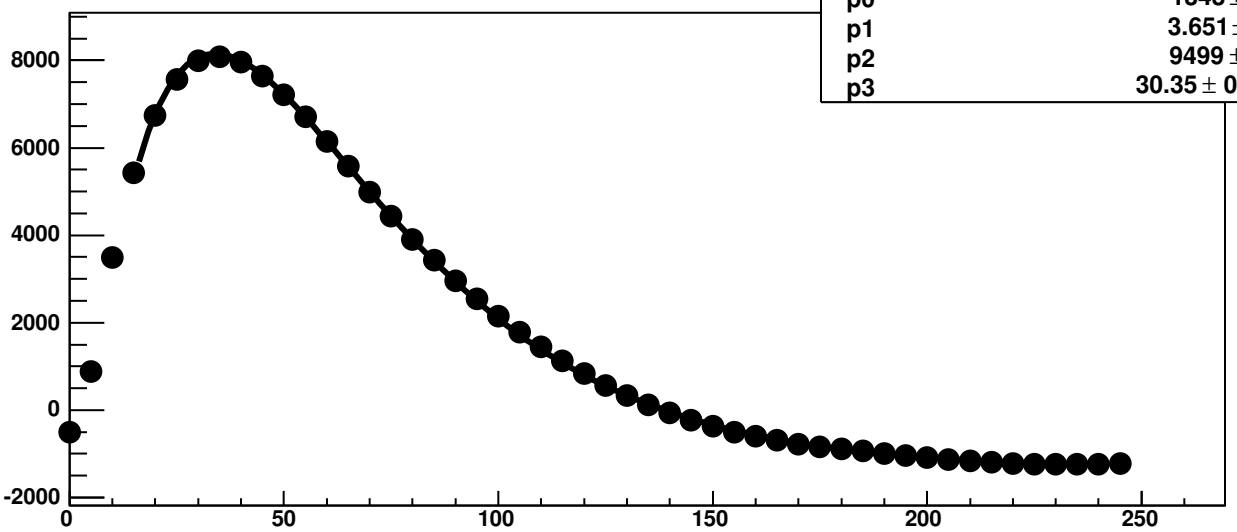

Chip 3, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 271.5 / 41
p0 -377.3 ± 0.5789
p1 100.6 ± 0.6315
p2 -3571 ± 75.14
p3 2291 ± 23.86
p4 1.54 ± 0.01561
-360
-380
-400
-420
-440
-460
-480
-500
-520
0
50
100
150
200
250


Chip 3, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

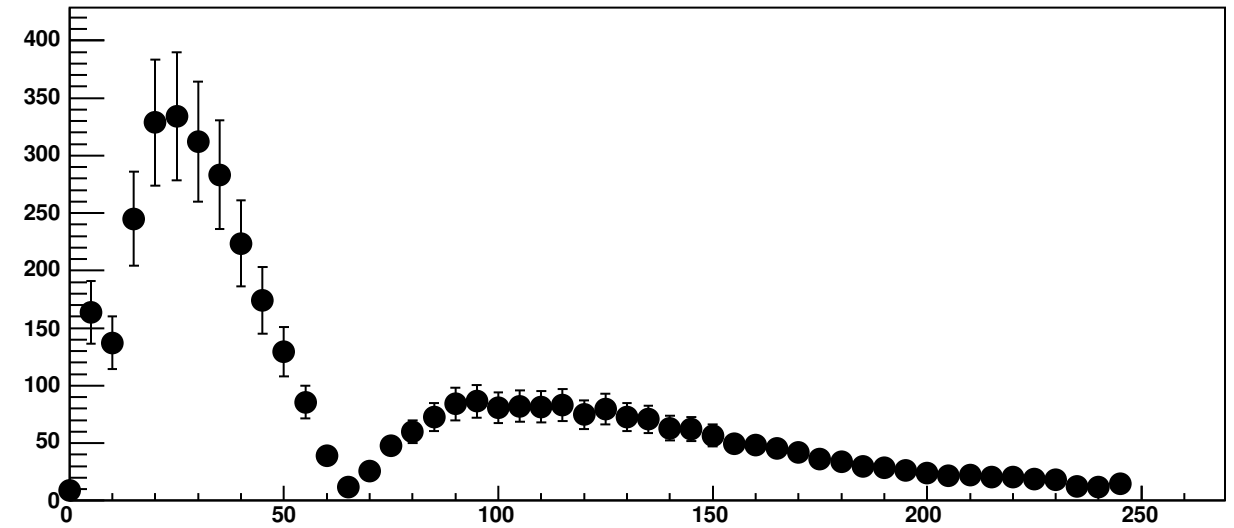

Chip 3, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

Chip 3, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold

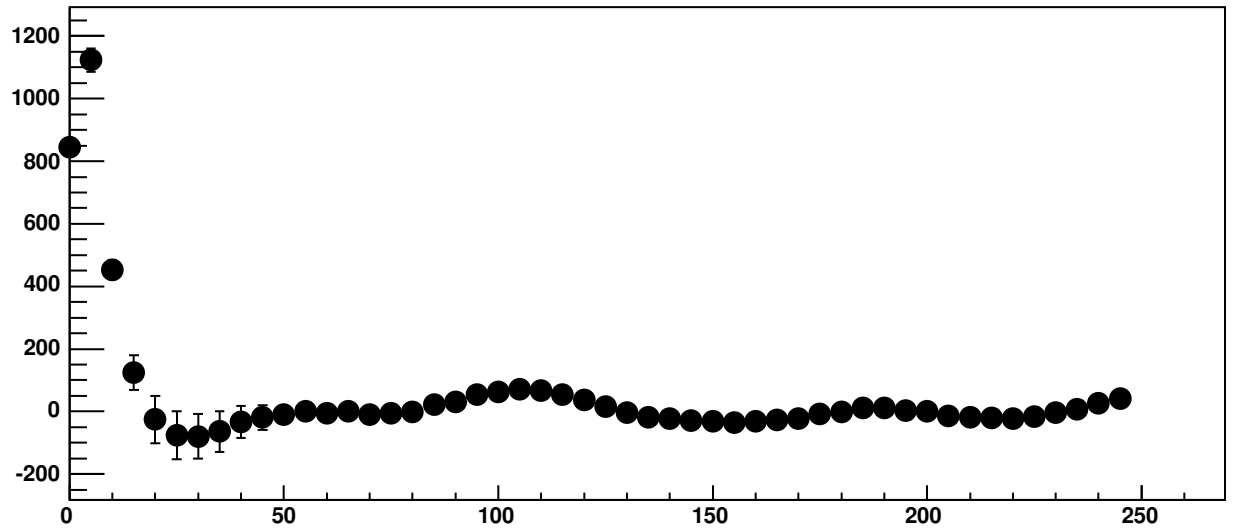
χ^2 / ndf	304.3 / 42
p0	-1343 ± 1.785
p1	3.651 ± 0.113
p2	9499 ± 13.02
p3	30.35 ± 0.04465

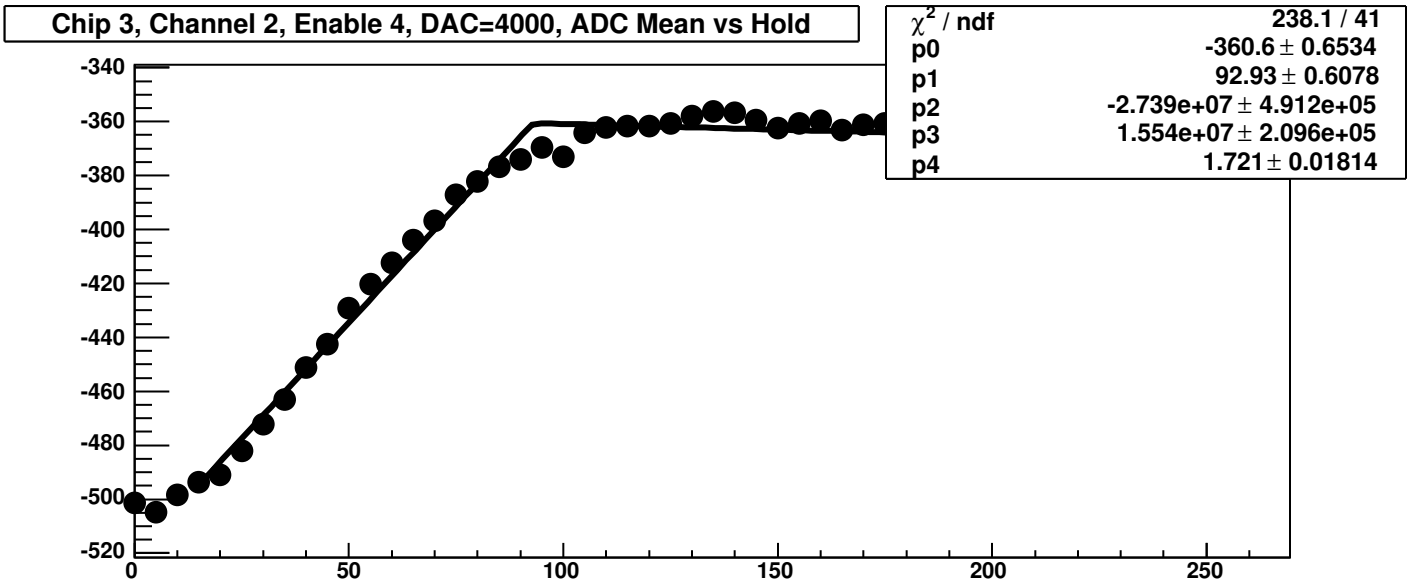


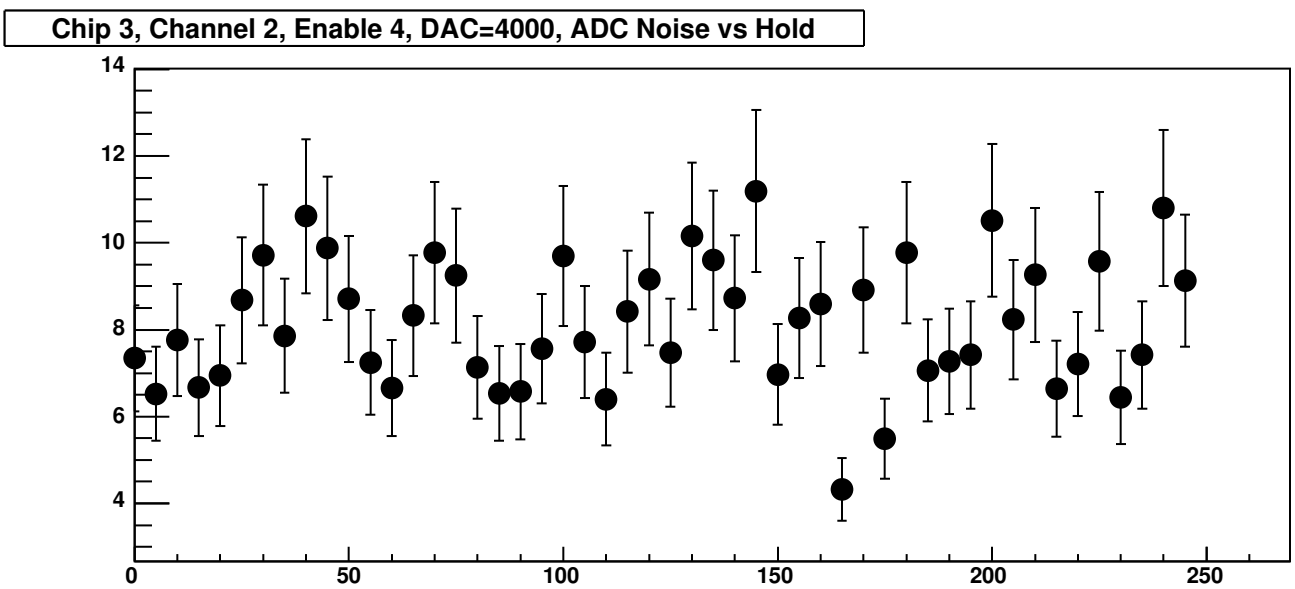
Chip 3, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold

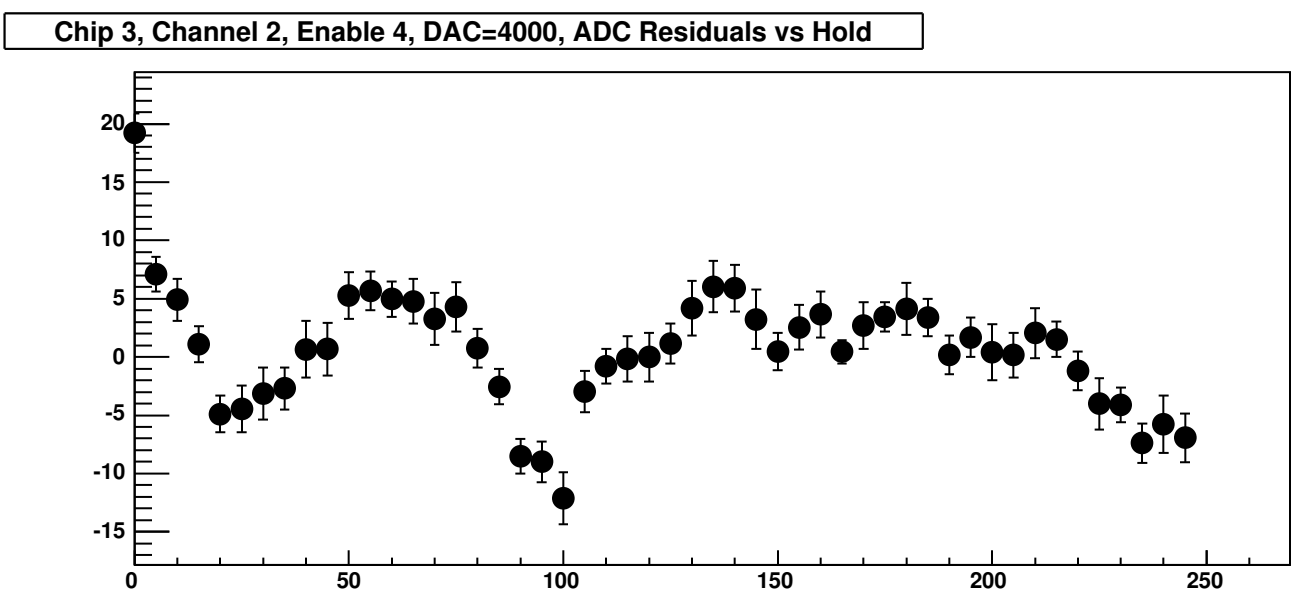


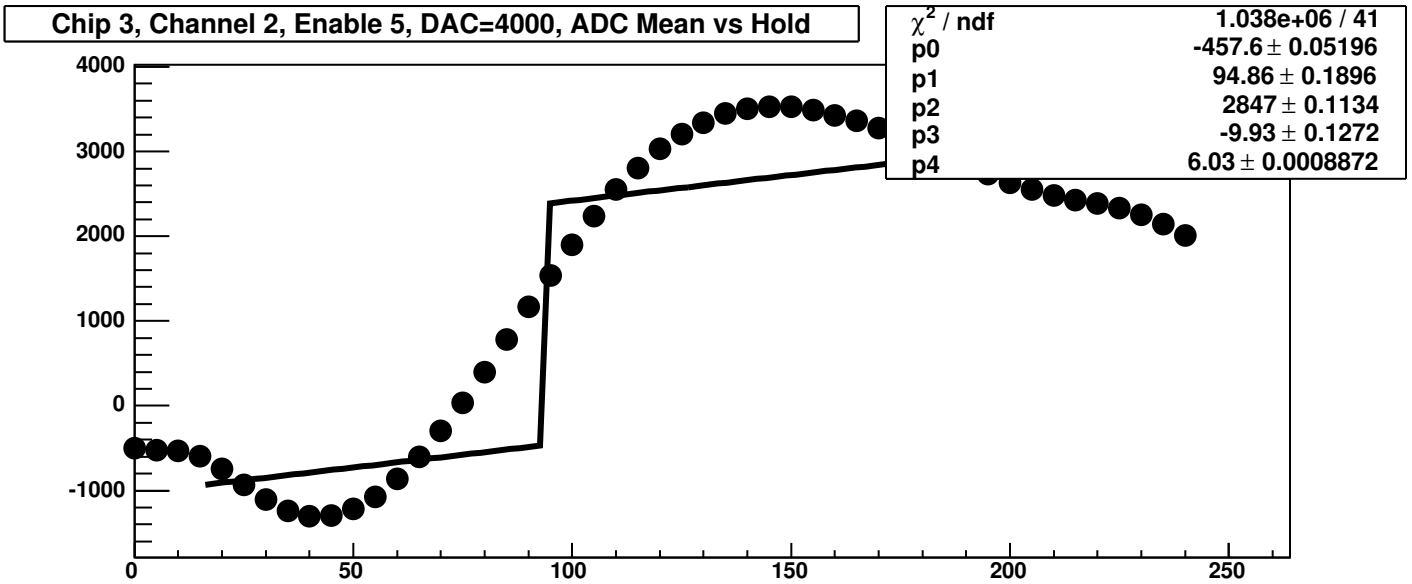
Chip 3, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold

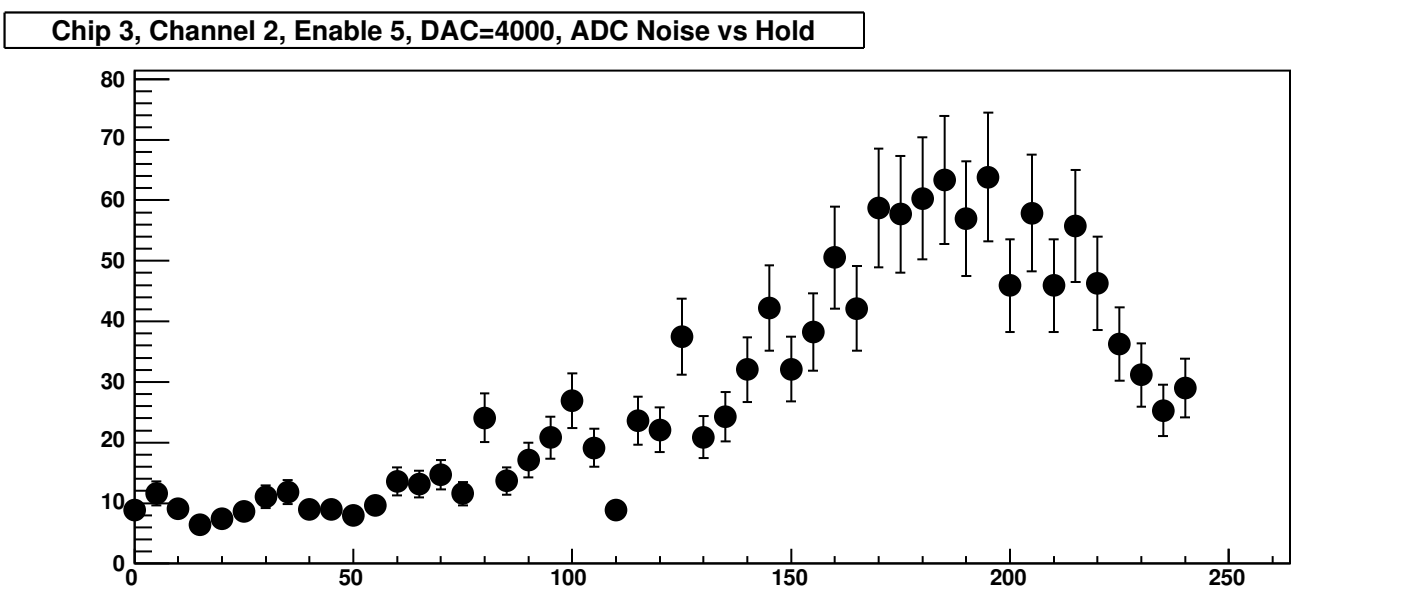


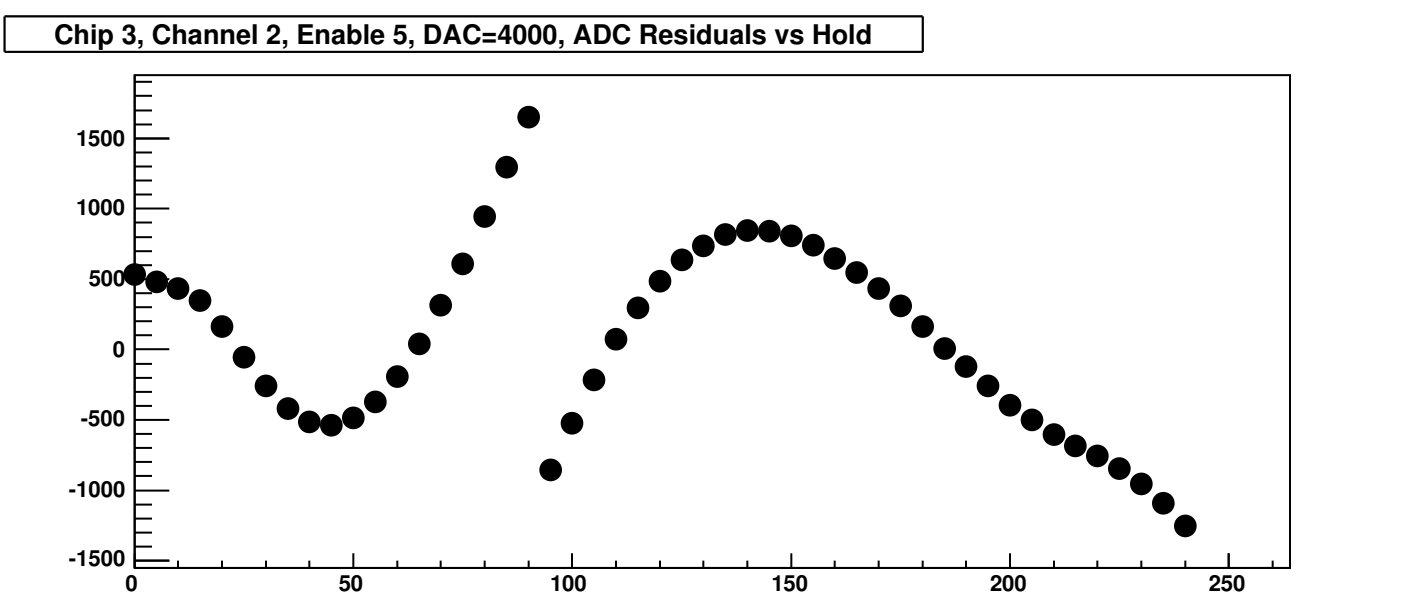

Chip 3, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 238.1 / 41
p0 -360.6 ± 0.6534
p1 92.93 ± 0.6078
p2 -2.739e+07 ± 4.912e+05
p3 1.554e+07 ± 2.096e+05
p4 1.721 ± 0.01814
-340
-360
-380
-400
-420
-440
-460
-480
-500
-520
0
50
100
150
200
250

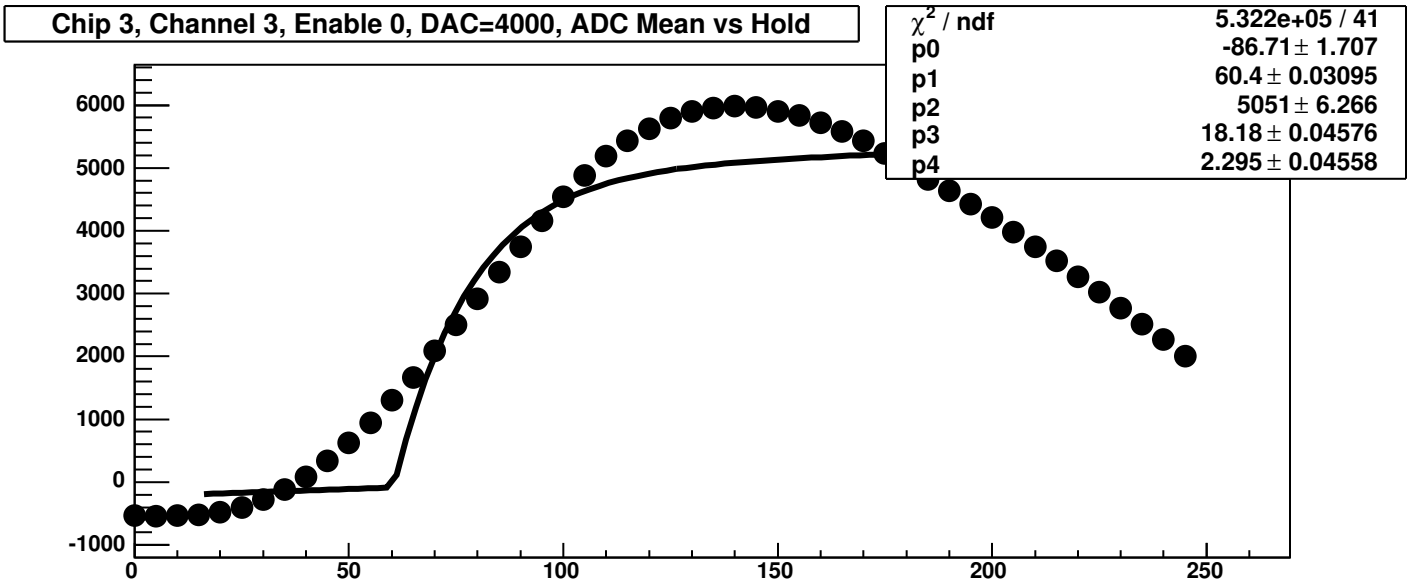

Chip 3, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

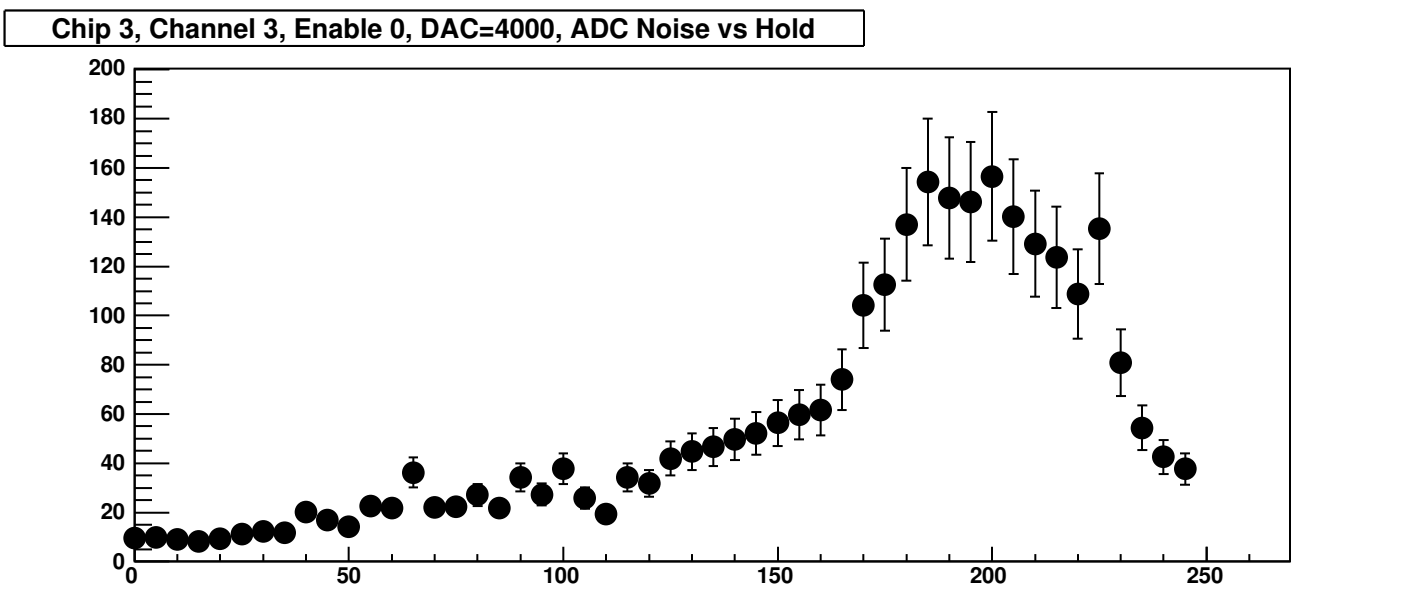

Chip 3, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

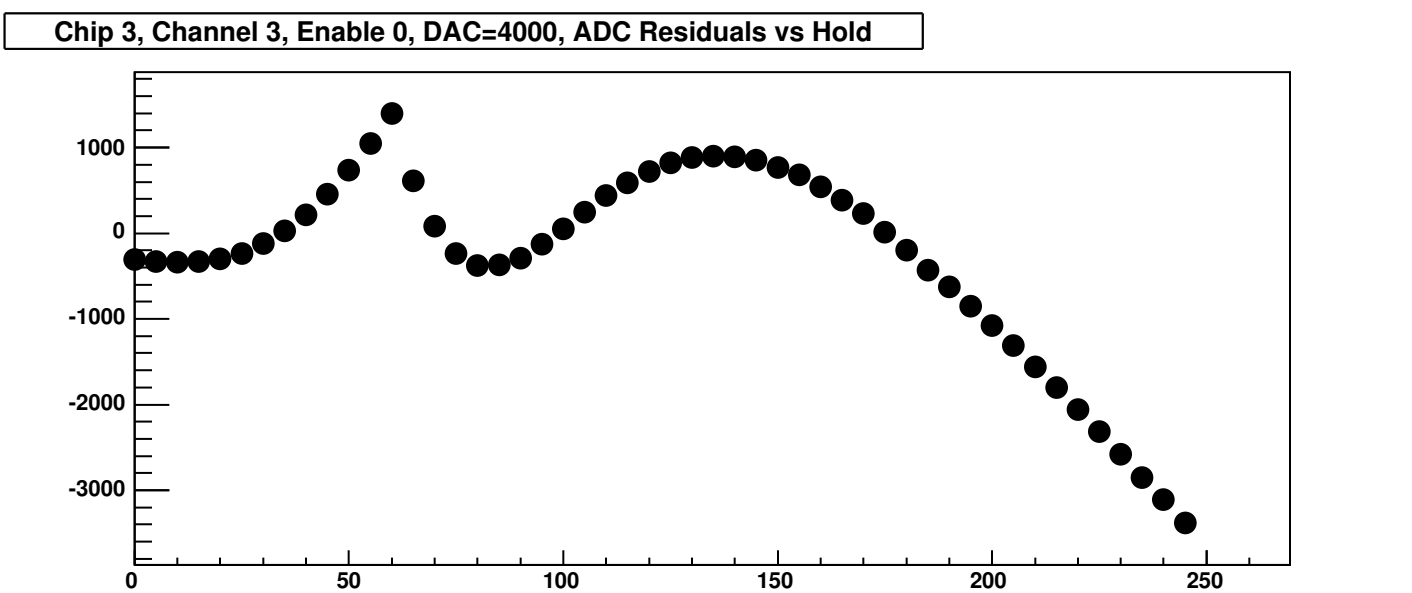

Chip 3, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.038e+06 / 41
p0 -457.6 ± 0.05196
p1 94.86 ± 0.1896
p2 2847 ± 0.1134
p3 -9.93 ± 0.1272
p4 6.03 ± 0.0008872
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

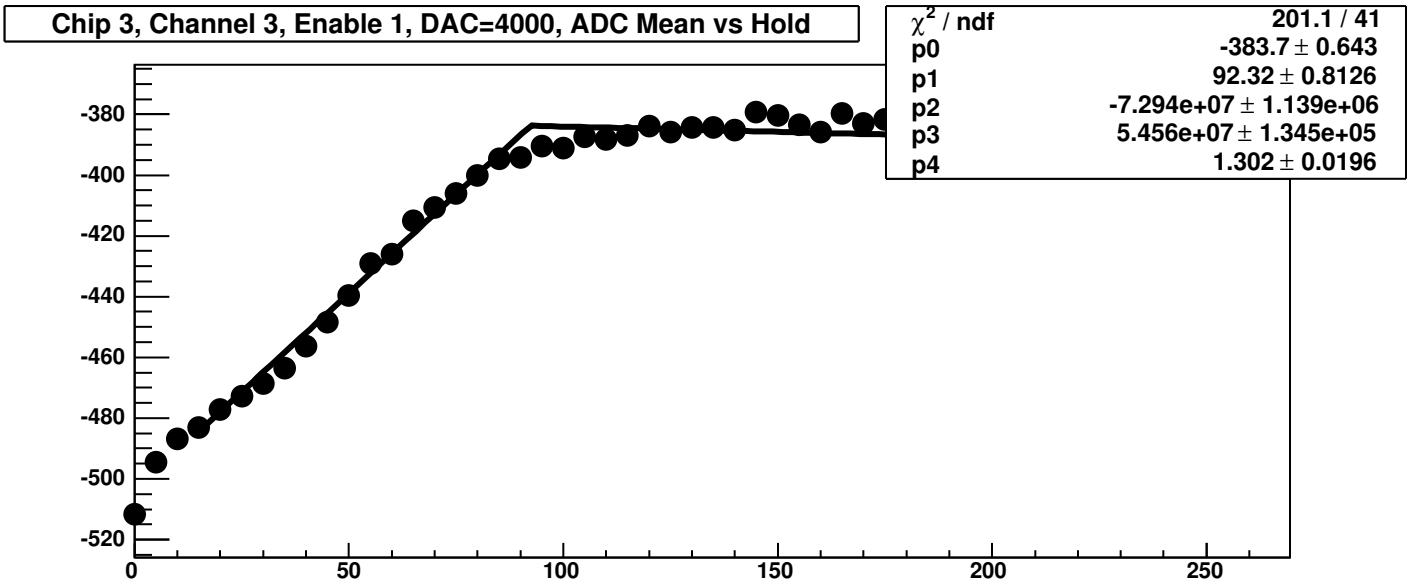

Chip 3, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250

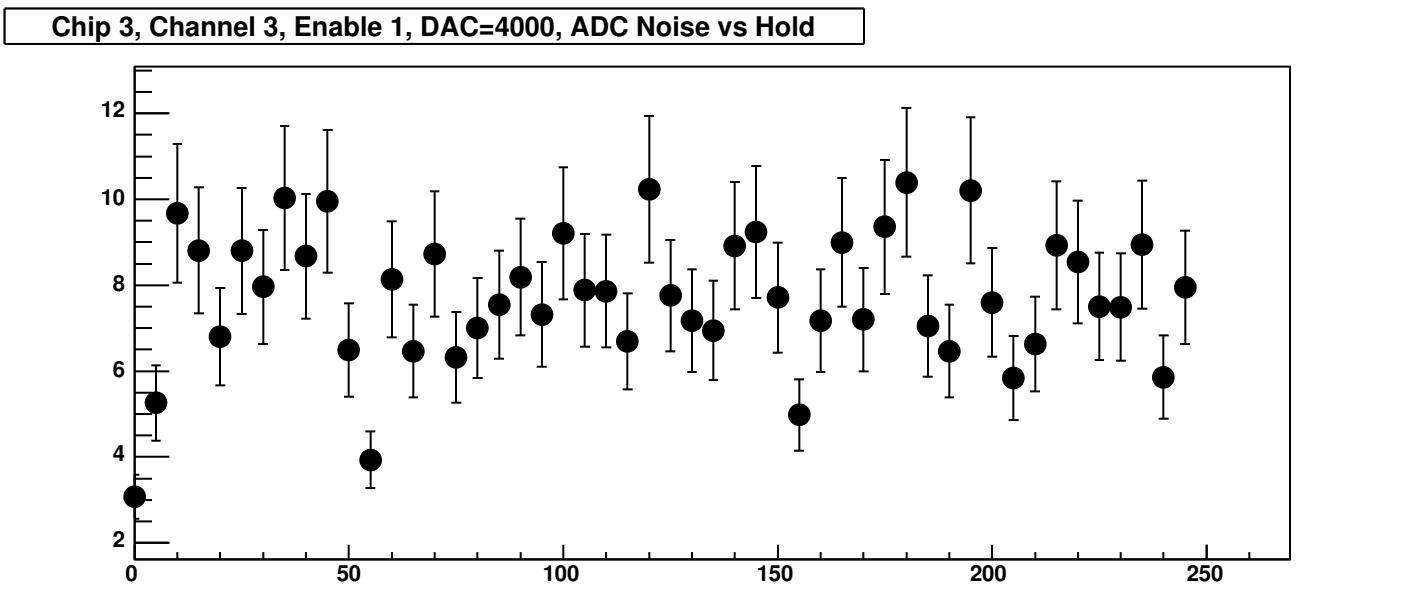

Chip 3, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold
1500
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

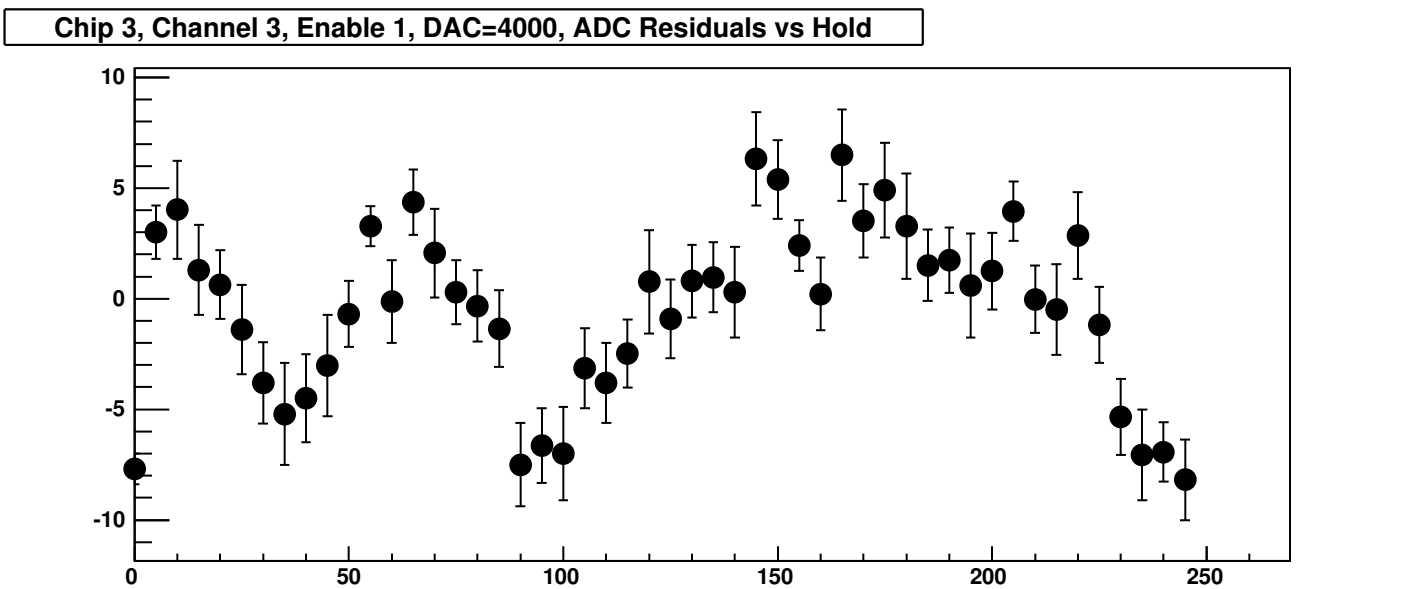

Chip 3, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.322e+05 / 41
p0 -86.71 ± 1.707
p1 60.4 ± 0.03095
p2 5051 ± 6.266
p3 18.18 ± 0.04576
p4 2.295 ± 0.04558
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

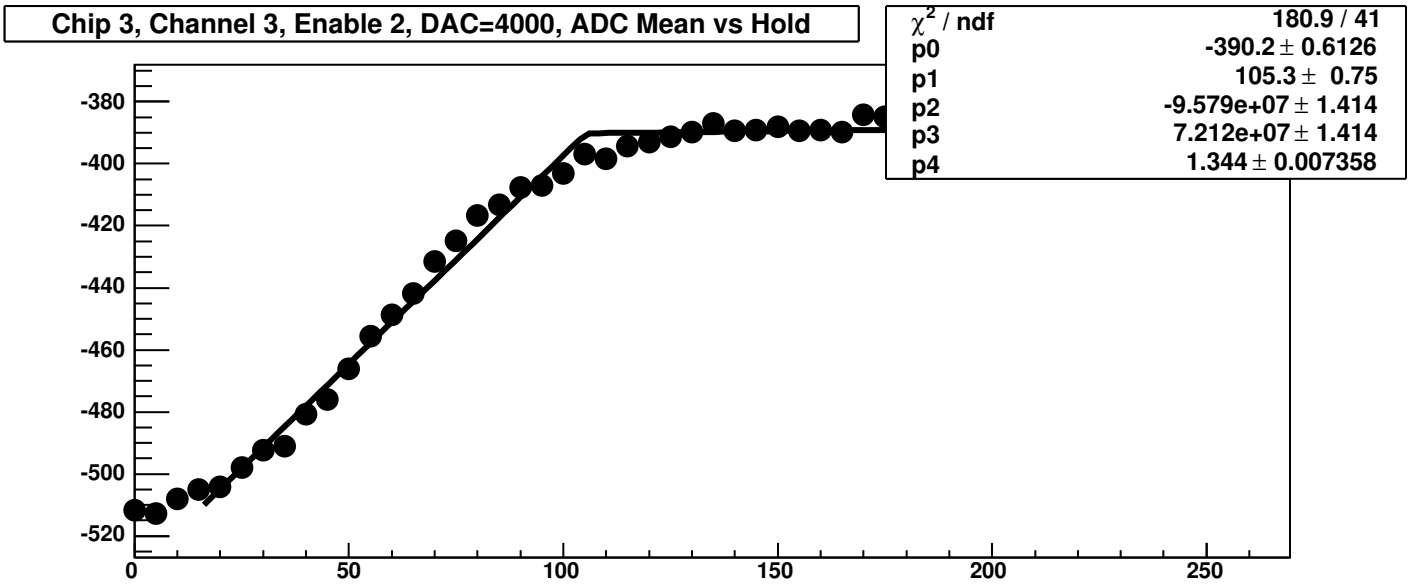

Chip 3, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

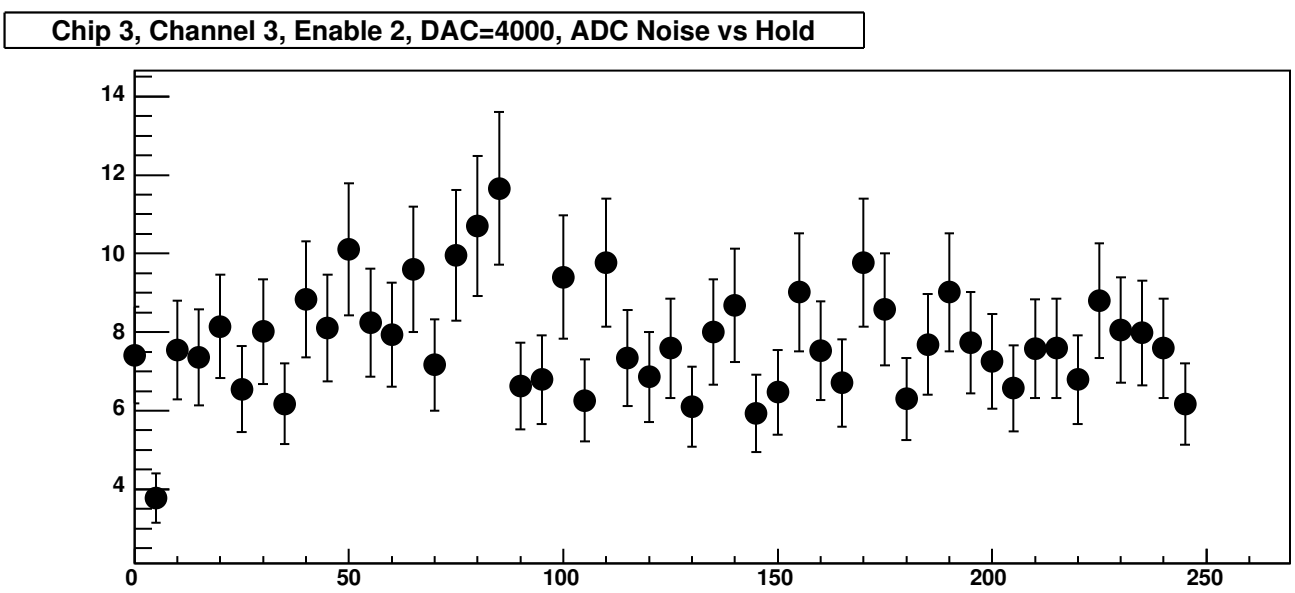

Chip 3, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

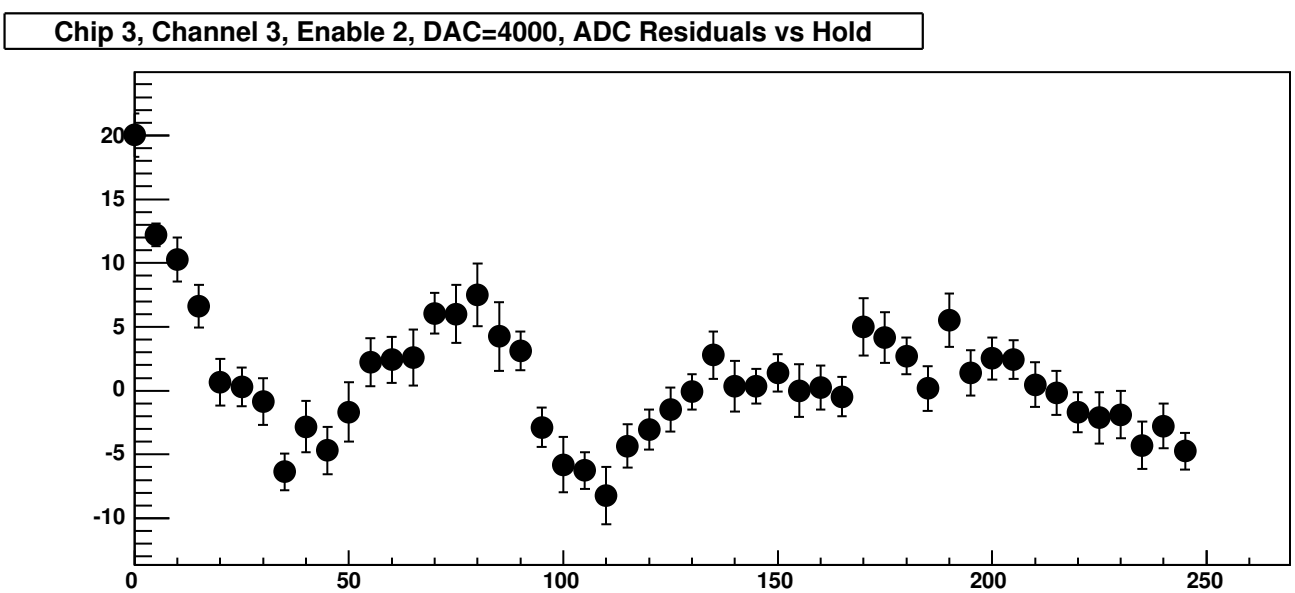

Chip 3, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 201.1 / 41
p0 -383.7 ± 0.643
p1 92.32 ± 0.8126
p2 -7.294e+07 ± 1.139e+06
p3 5.456e+07 ± 1.345e+05
p4 1.302 ± 0.0196
-380
-400
-420
-440
-460
-480
-500
-520
0
50
100
150
200
250

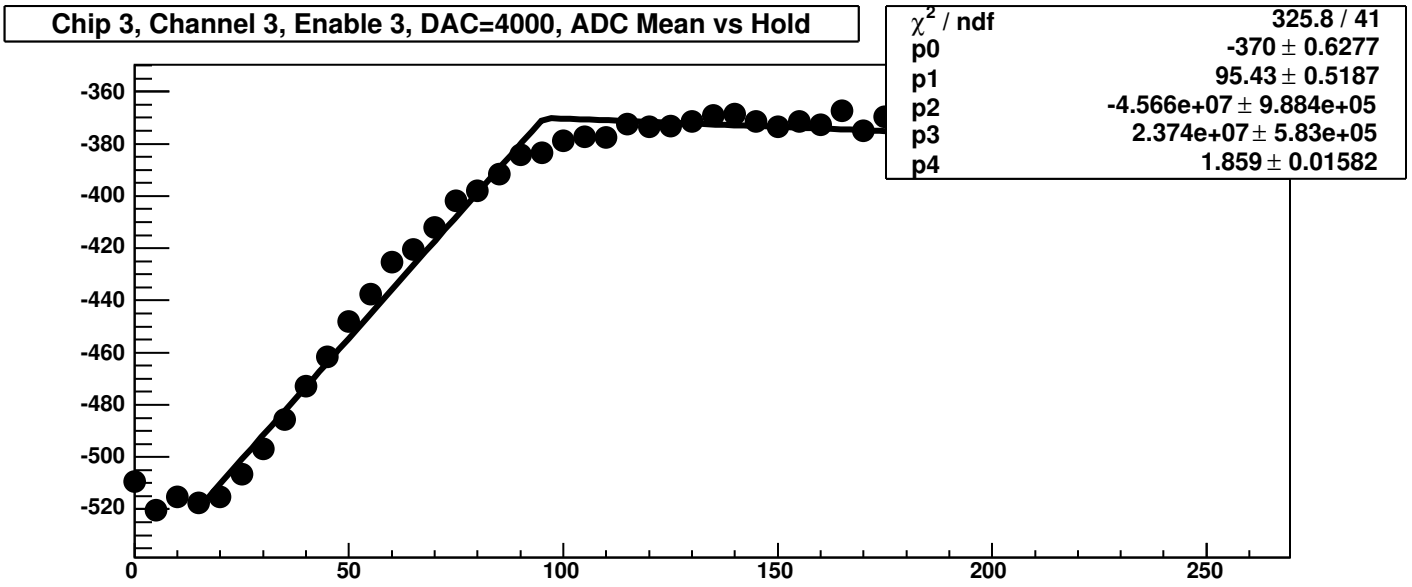

Chip 3, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
2
0
50
100
150
200
250

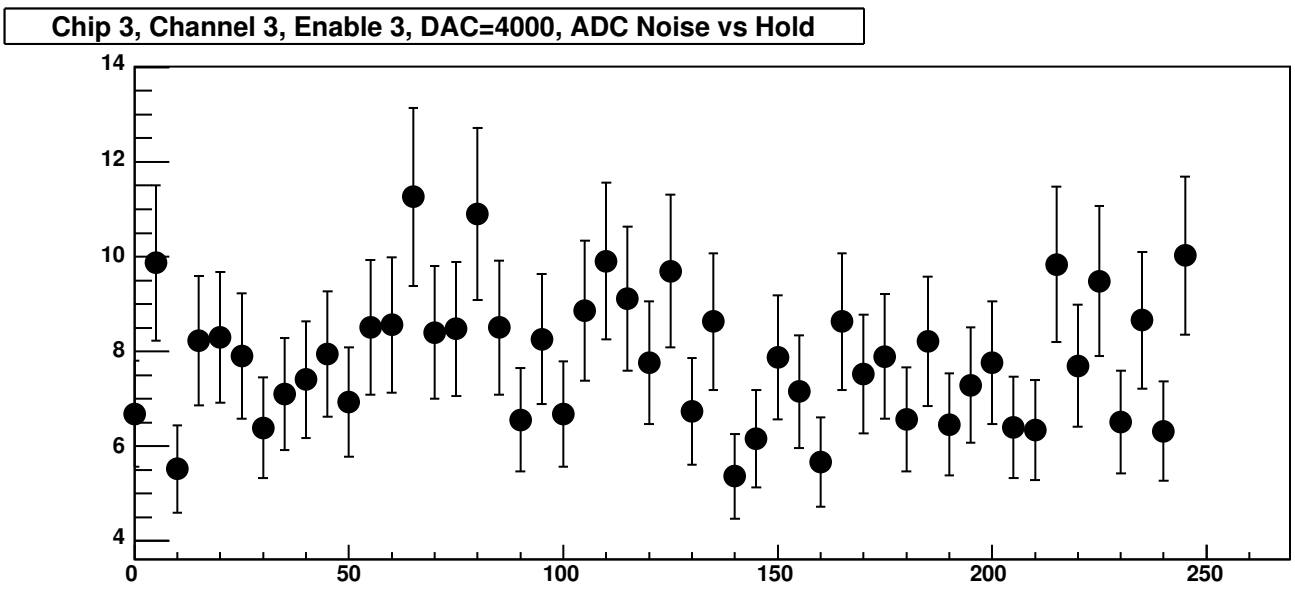

Chip 3, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250

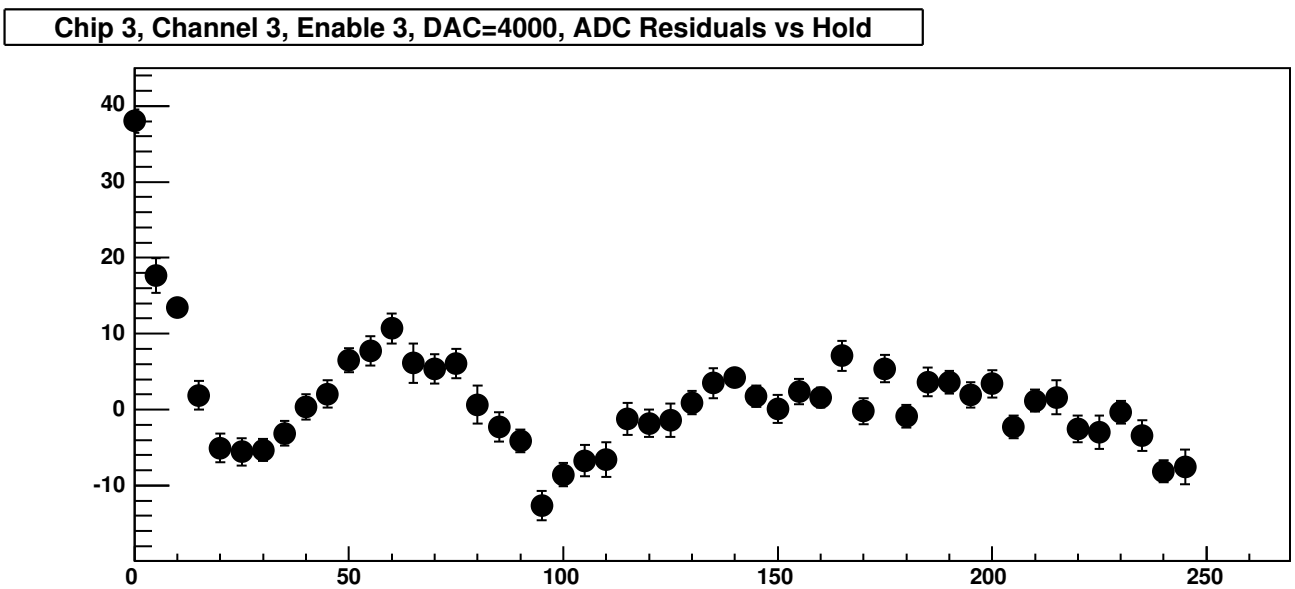

Chip 3, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 180.9 / 41
p0 -390.2 ± 0.6126
p1 105.3 ± 0.75
p2 -9.579e+07 ± 1.414
p3 7.212e+07 ± 1.414
p4 1.344 ± 0.007358
-380
-400
-420
-440
-460
-480
-500
-520
0
50
100
150
200
250


Chip 3, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

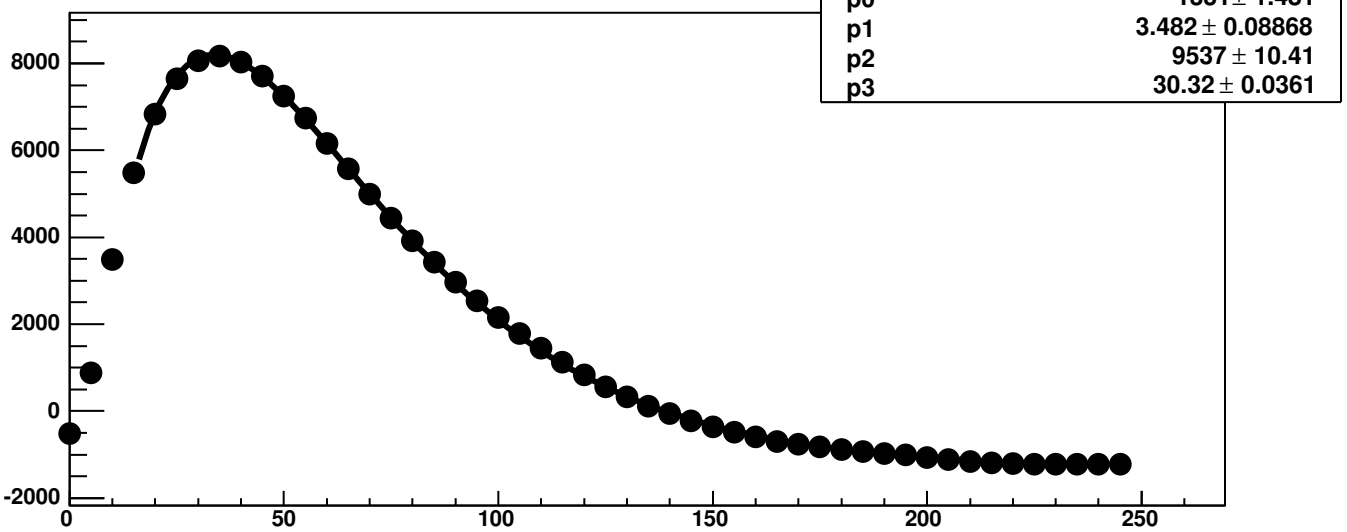

Chip 3, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
0
50
100
150
200
250


Chip 3, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 325.8 / 41
p0 -370 ± 0.6277
p1 95.43 ± 0.5187
p2 -4.566e+07 ± 9.884e+05
p3 2.374e+07 ± 5.83e+05
p4 1.859 ± 0.01582
-360
-380
-400
-420
-440
-460
-480
-500
-520
0
50
100
150
200
250

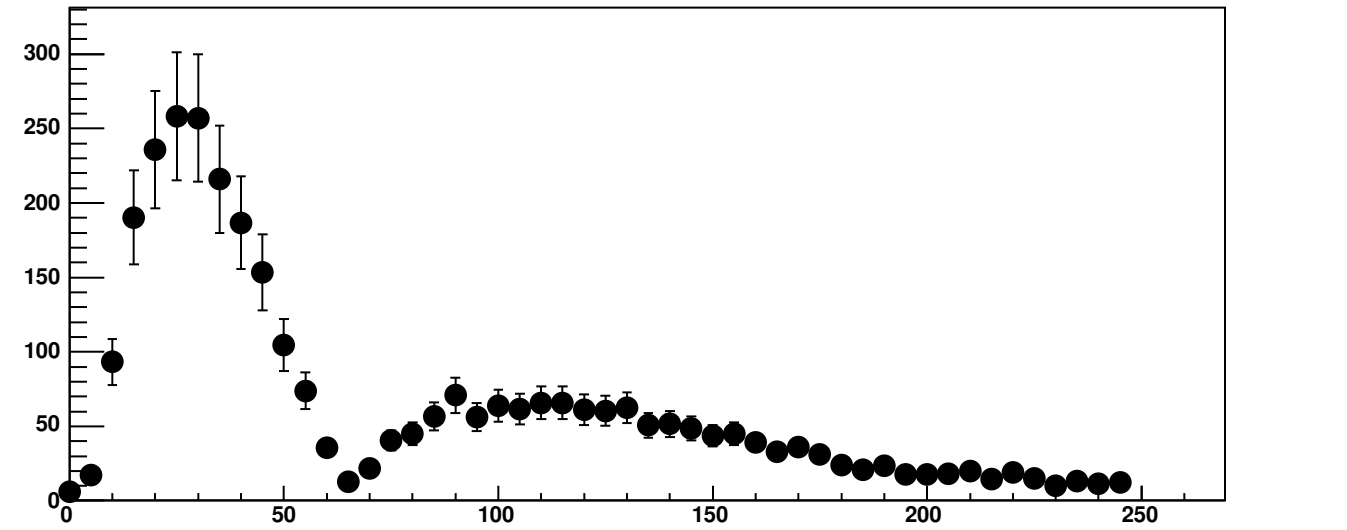

Chip 3, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 3, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

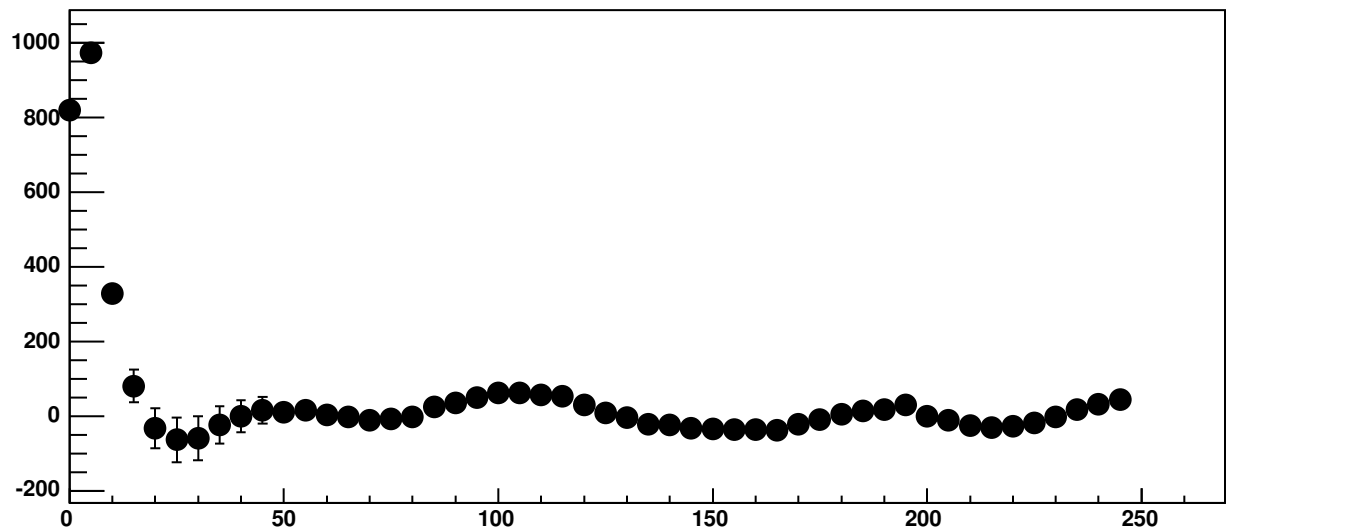
Chip 3, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold

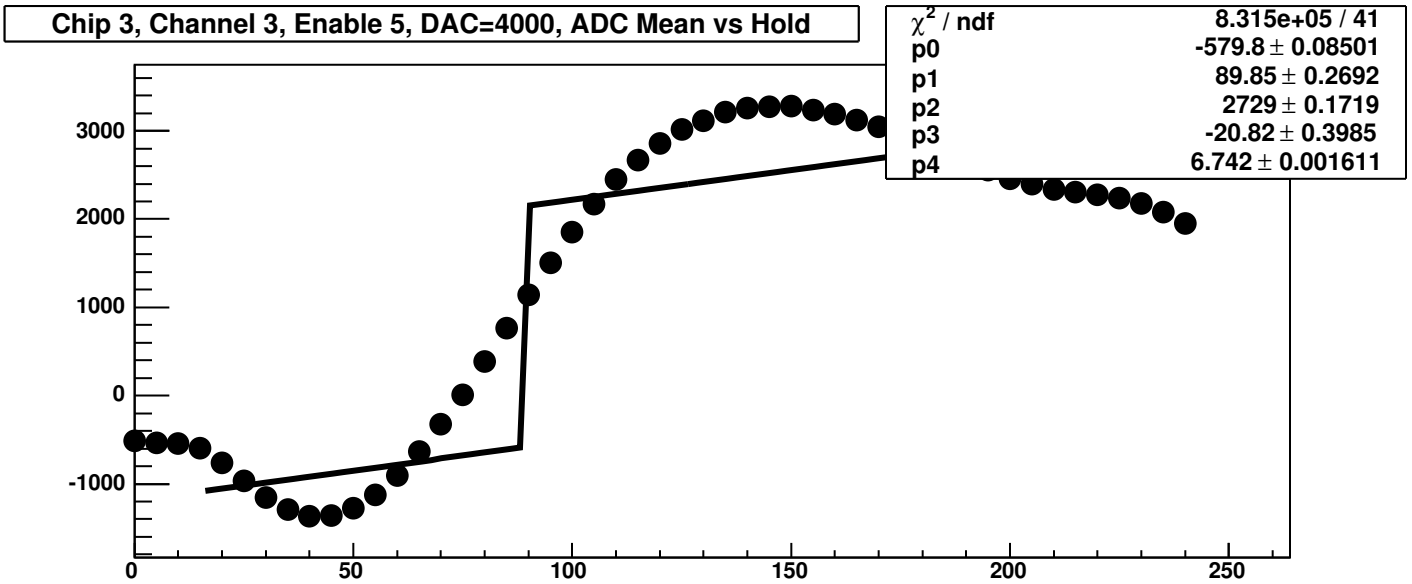


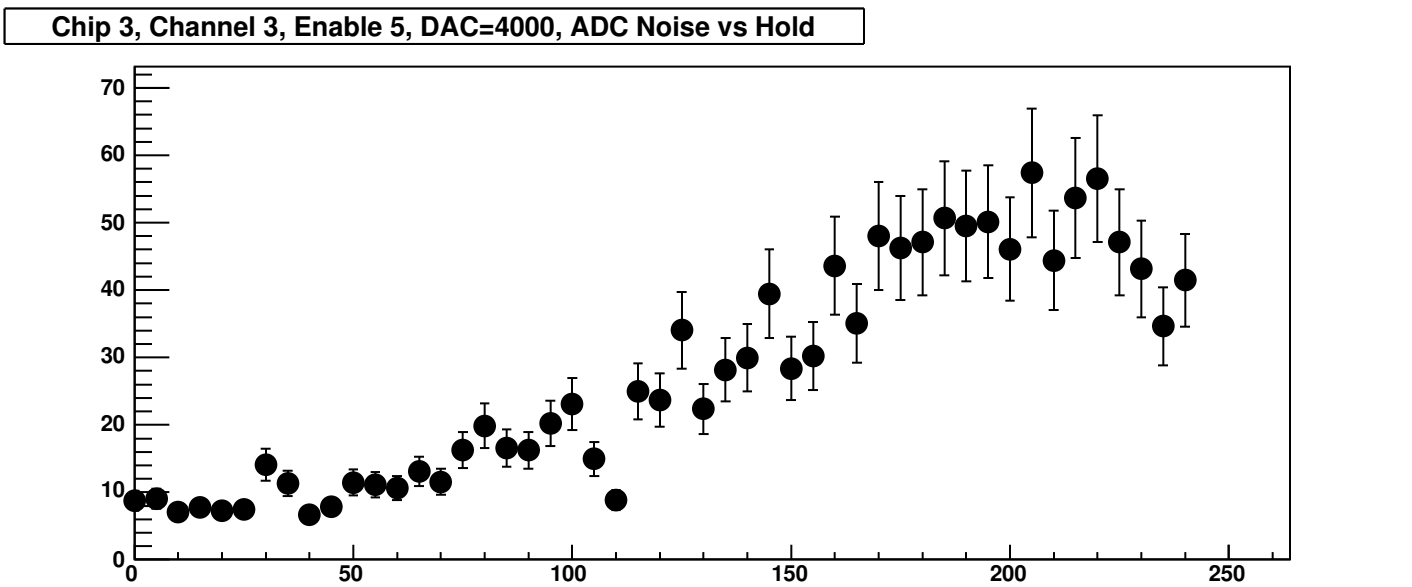
Chip 3, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold

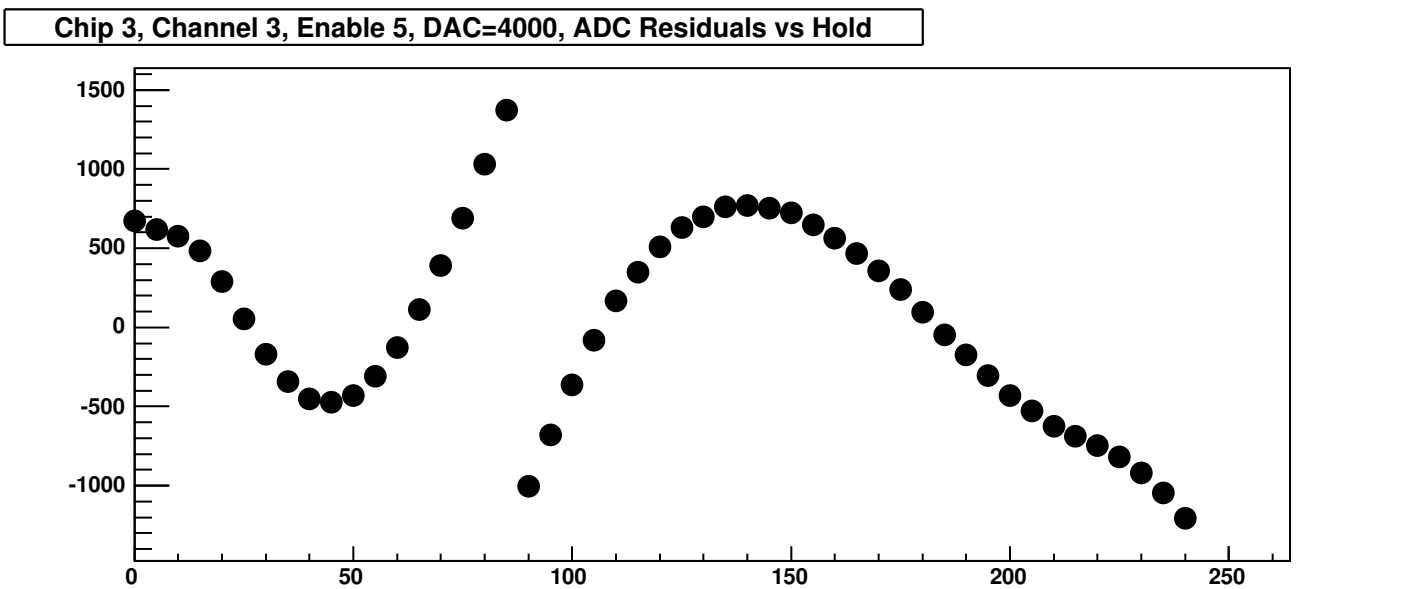


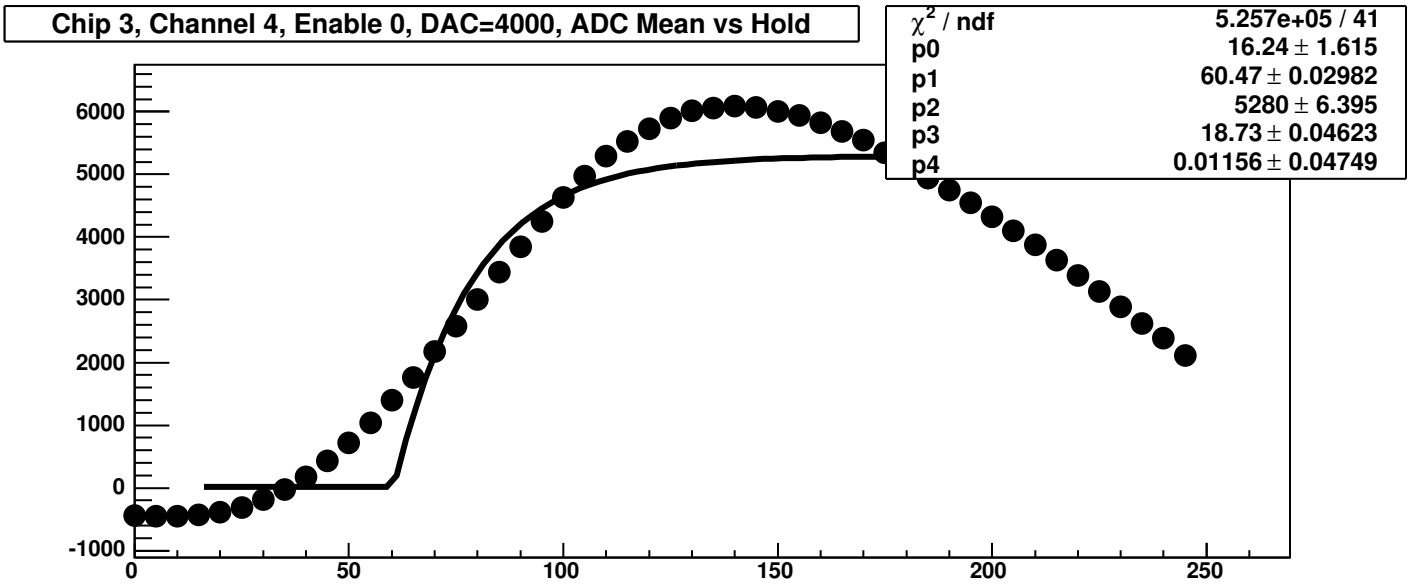
Chip 3, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold

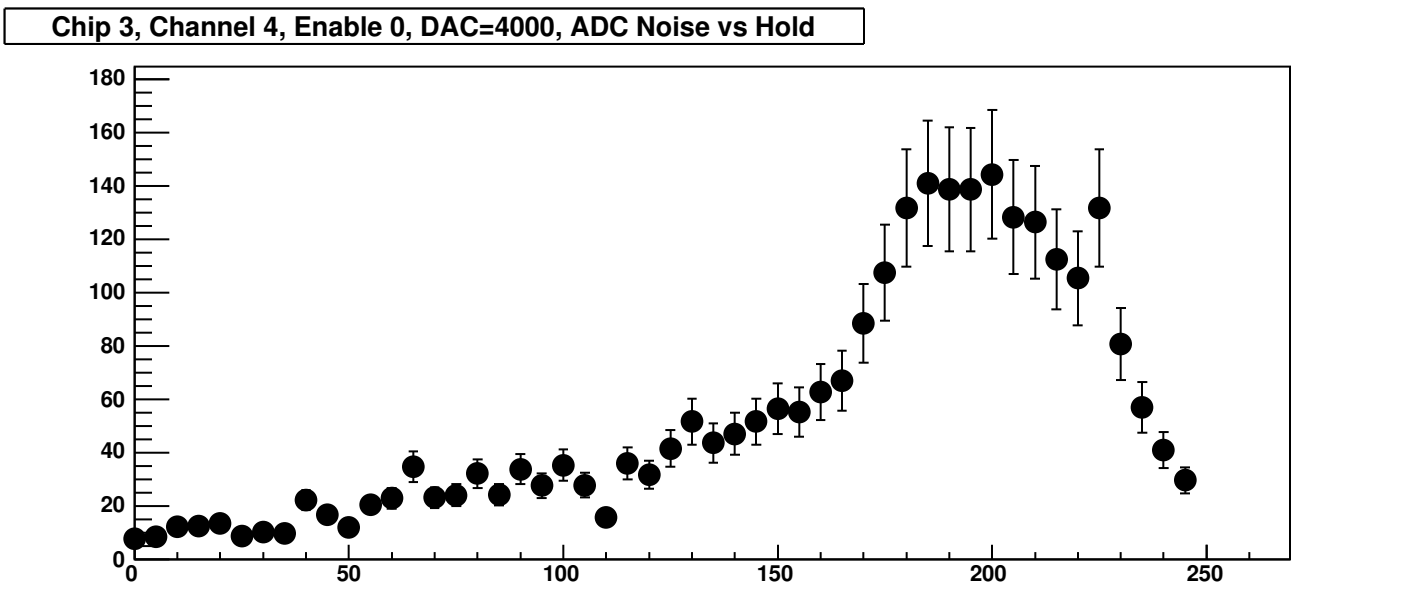


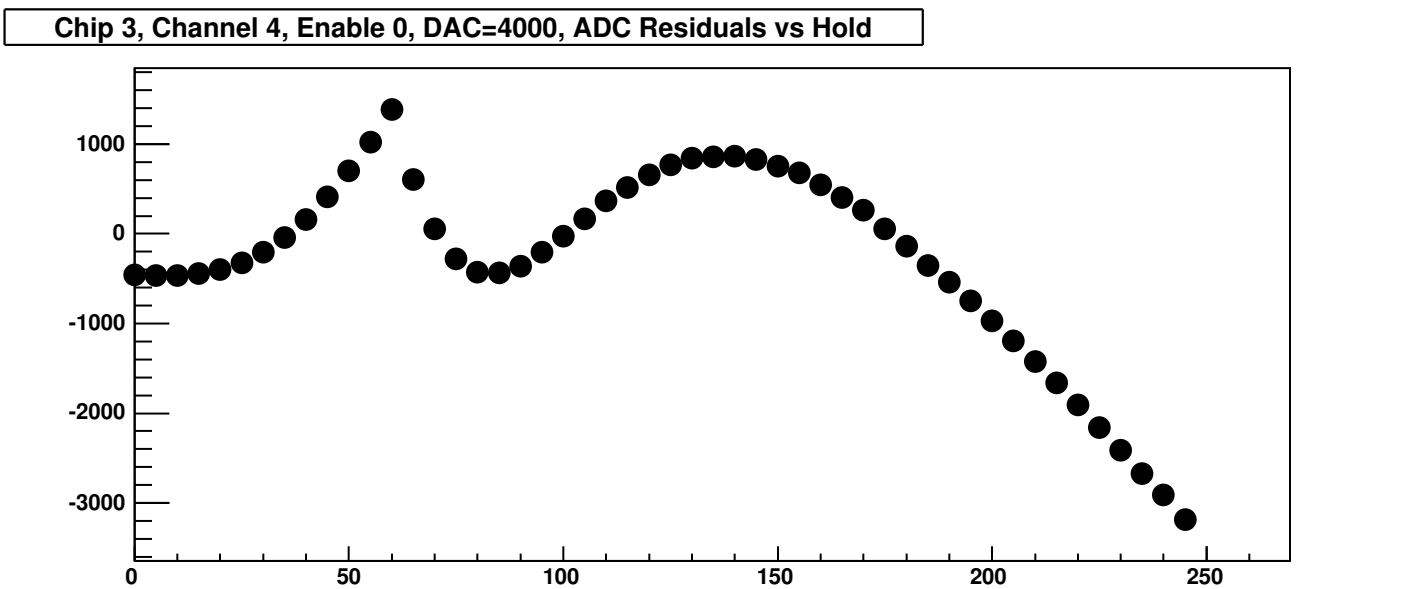

Chip 3, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 8.315e+05 / 41
p0 -579.8 ± 0.08501
p1 89.85 ± 0.2692
p2 2729 ± 0.1719
p3 -20.82 ± 0.3985
p4 6.742 ± 0.001611
3000
2000
1000
0
-1000
0
50
100
150
200
250

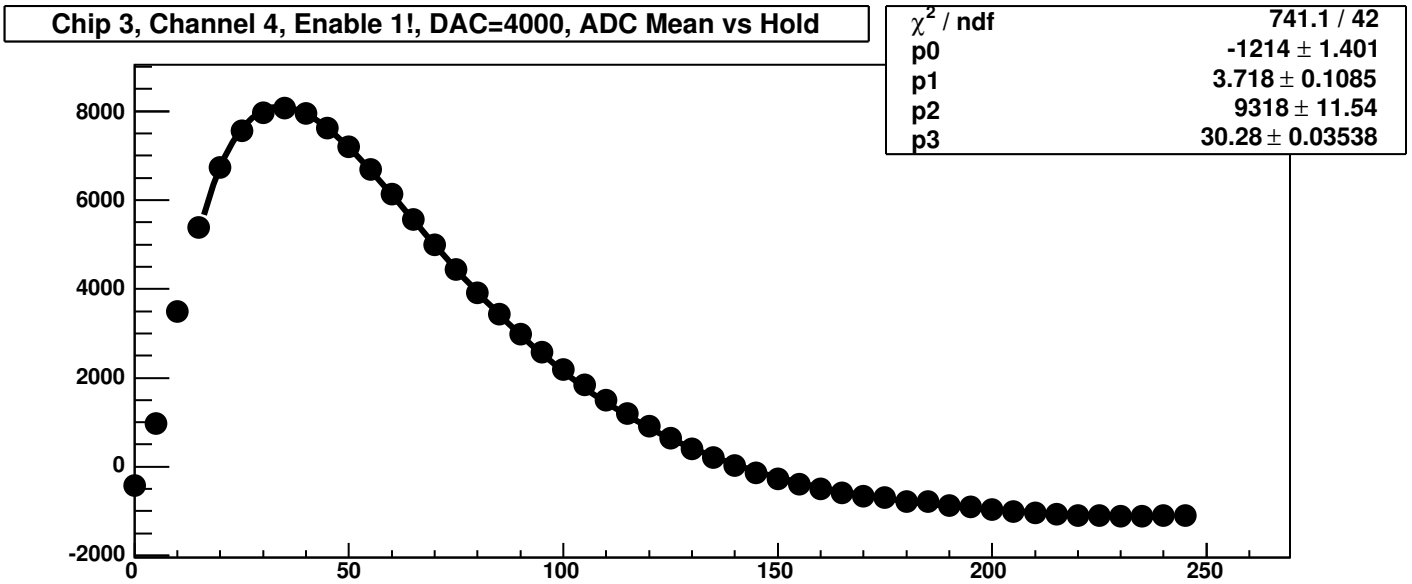

Chip 3, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold
70
60
50
40
30
20
10
0
0
50
100
150
200
250

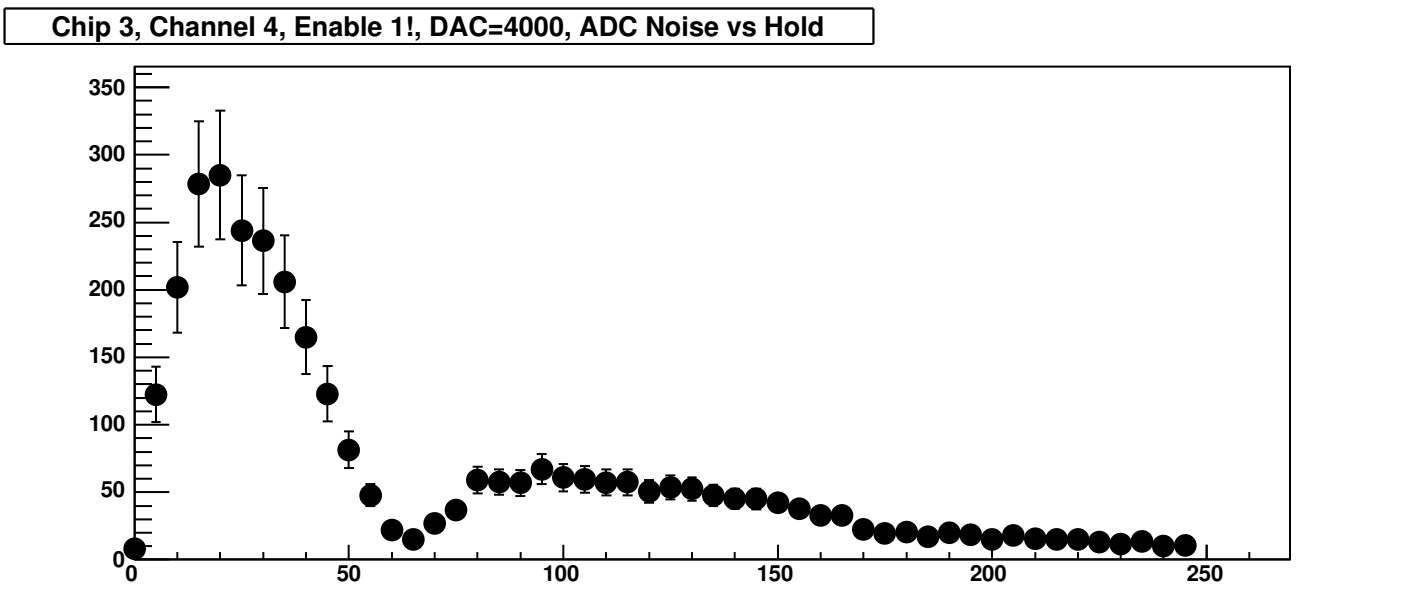

Chip 3, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold
1500
1000
500
0
-500
-1000
0
50
100
150
200
250

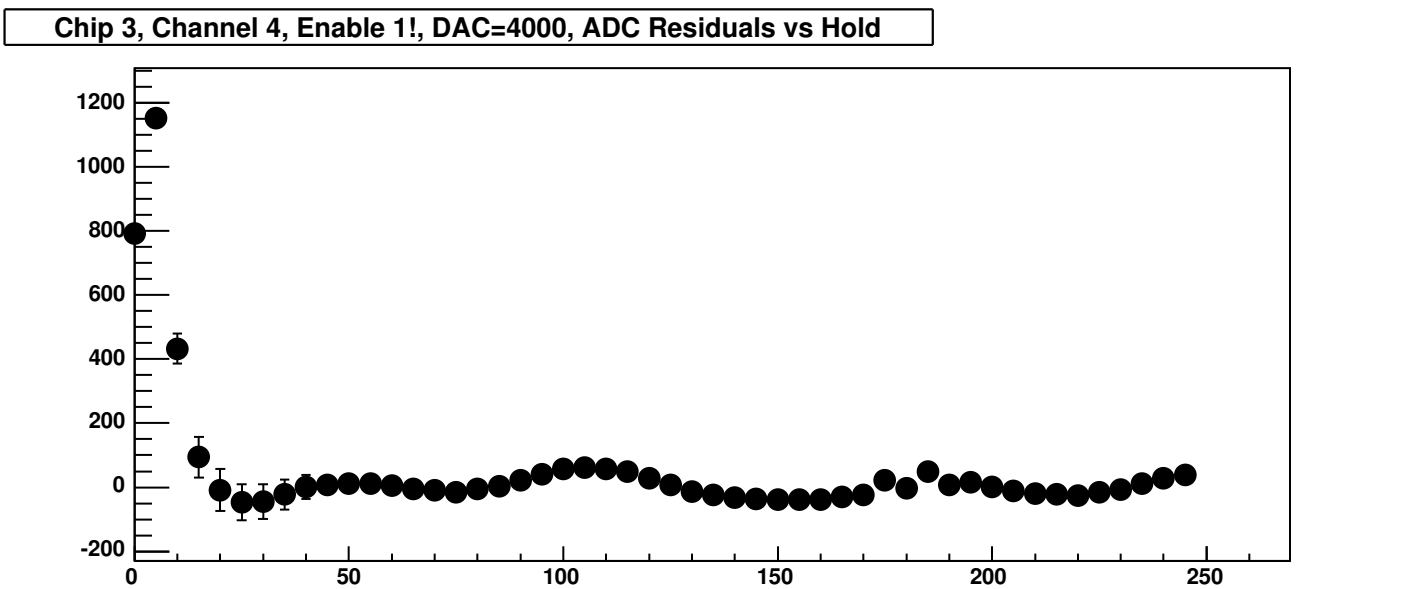

Chip 3, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.257e+05 / 41
p0 16.24 ± 1.615
p1 60.47 ± 0.02982
p2 5280 ± 6.395
p3 18.73 ± 0.04623
p4 0.01156 ± 0.04749
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

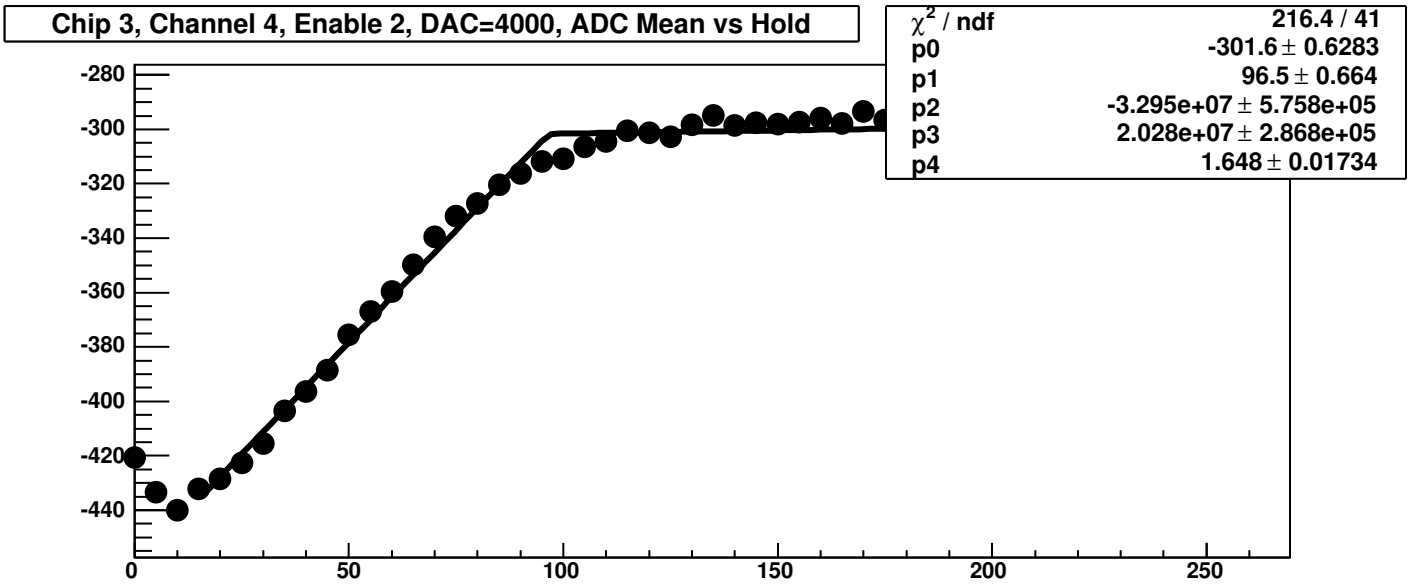

Chip 3, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

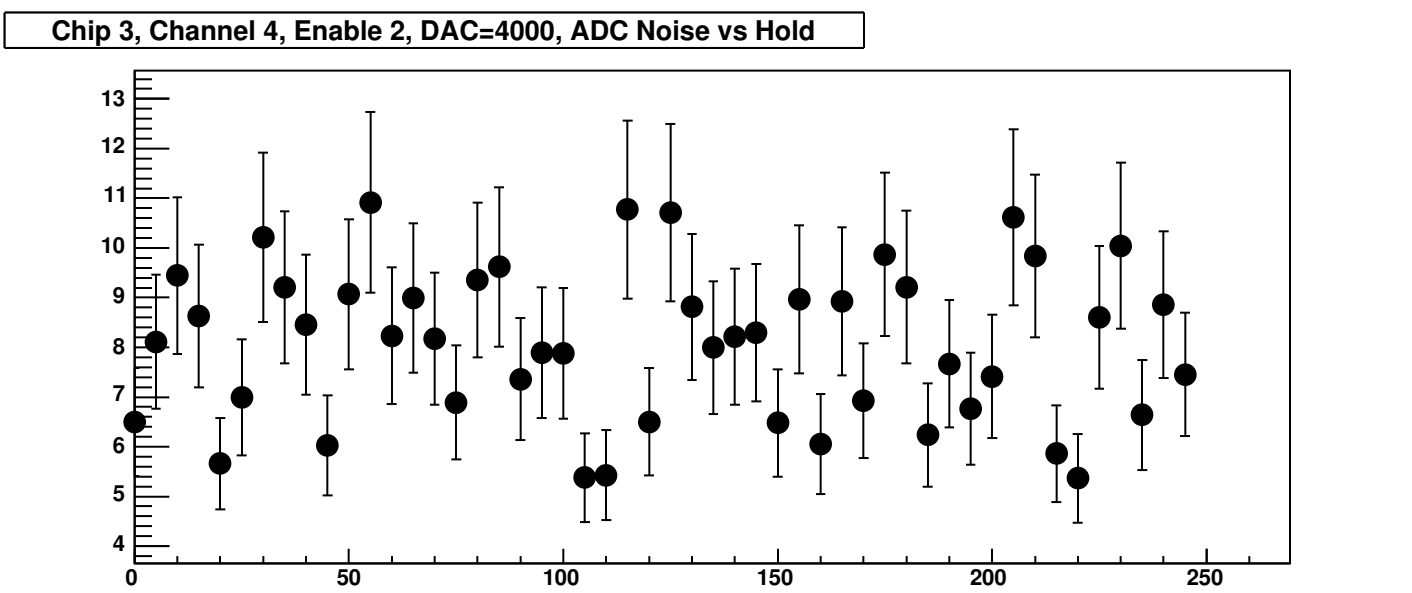

Chip 3, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

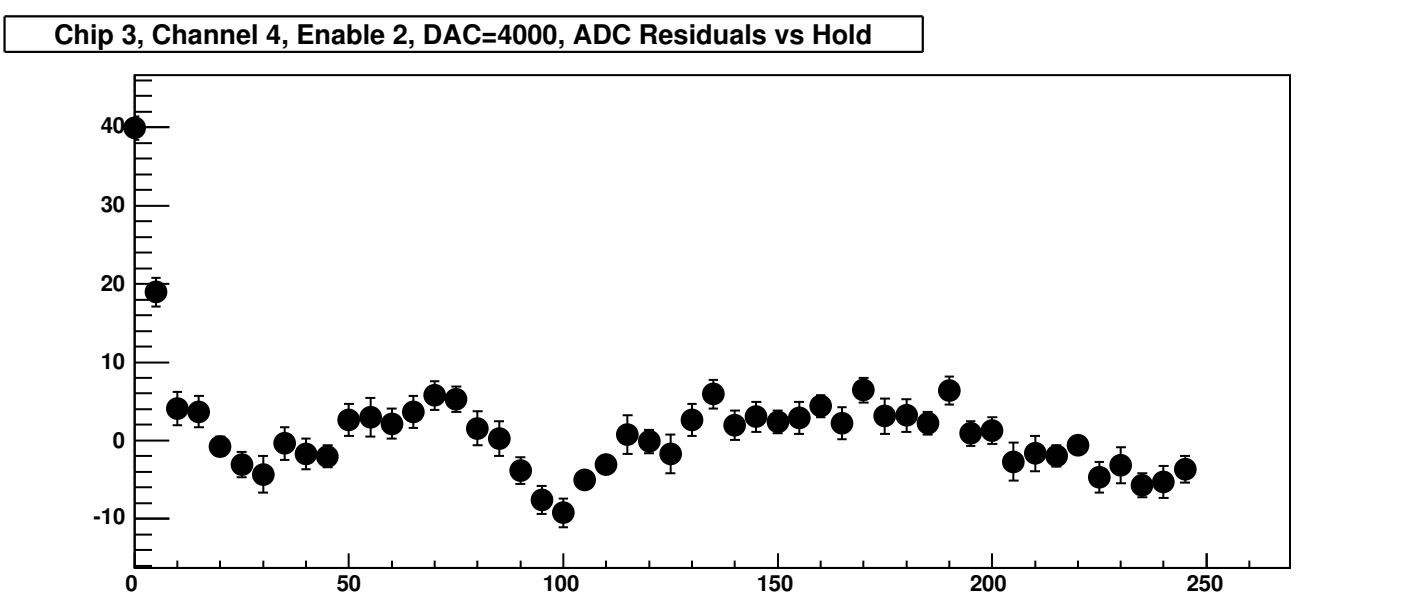

Chip 3, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 741.1 / 42
p0 -1214 ± 1.401
p1 3.718 ± 0.1085
p2 9318 ± 11.54
p3 30.28 ± 0.03538
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

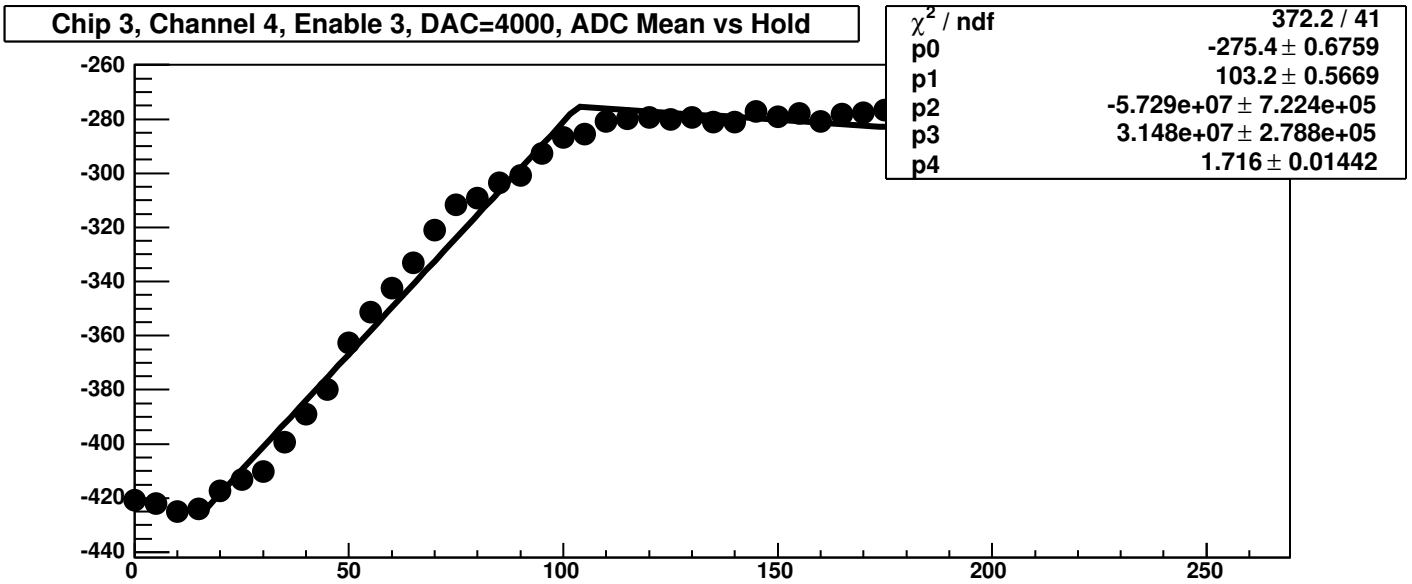

Chip 3, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold
350
300
250
200
150
100
50
0
0
50
100
150
200
250

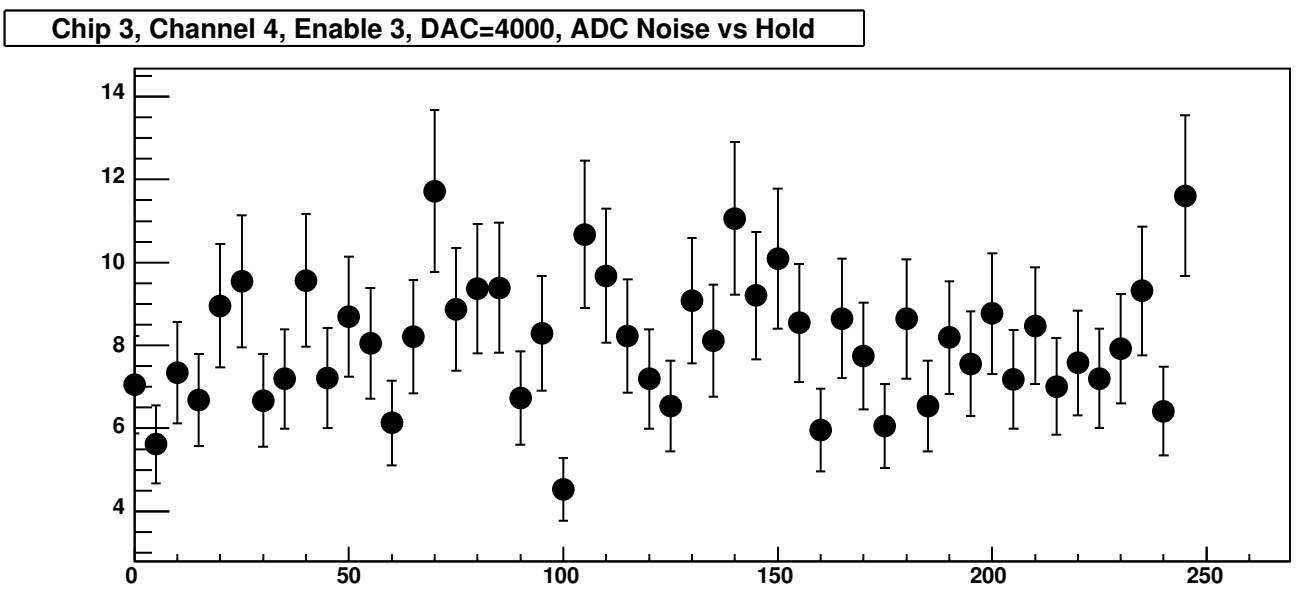

Chip 3, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

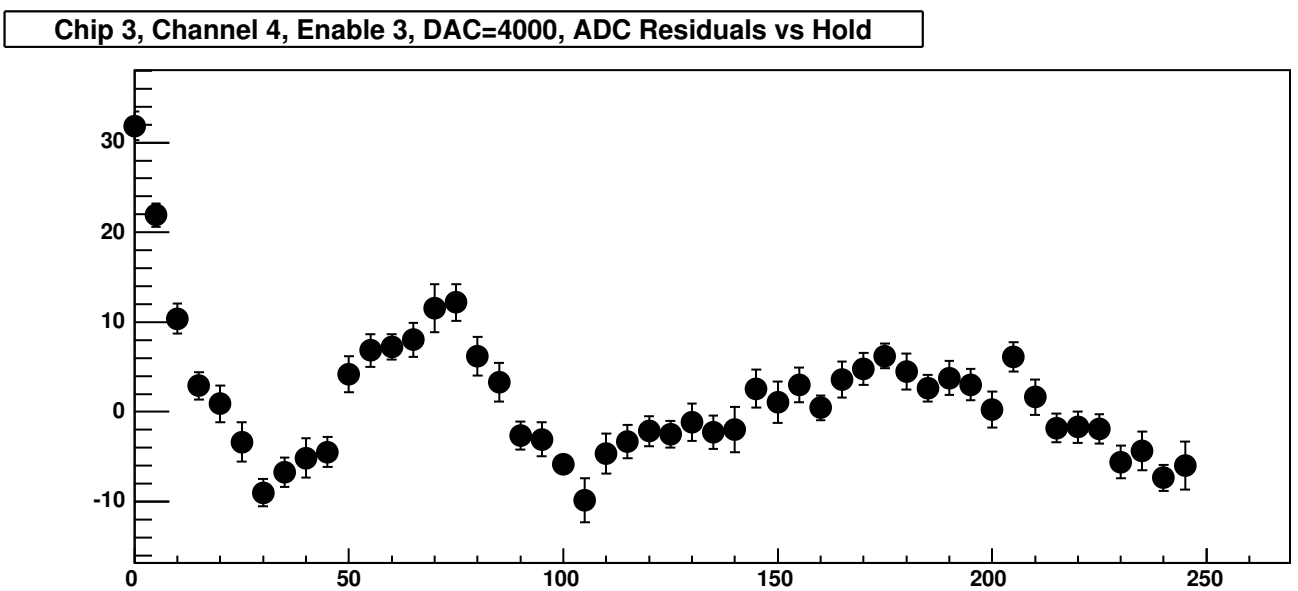

Chip 3, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 216.4 / 41
p0 -301.6 ± 0.6283
p1 96.5 ± 0.664
p2 -3.295e+07 ± 5.758e+05
p3 2.028e+07 ± 2.868e+05
p4 1.648 ± 0.01734
-280
-300
-320
-340
-360
-380
-400
-420
-440
0
50
100
150
200
250


Chip 3, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

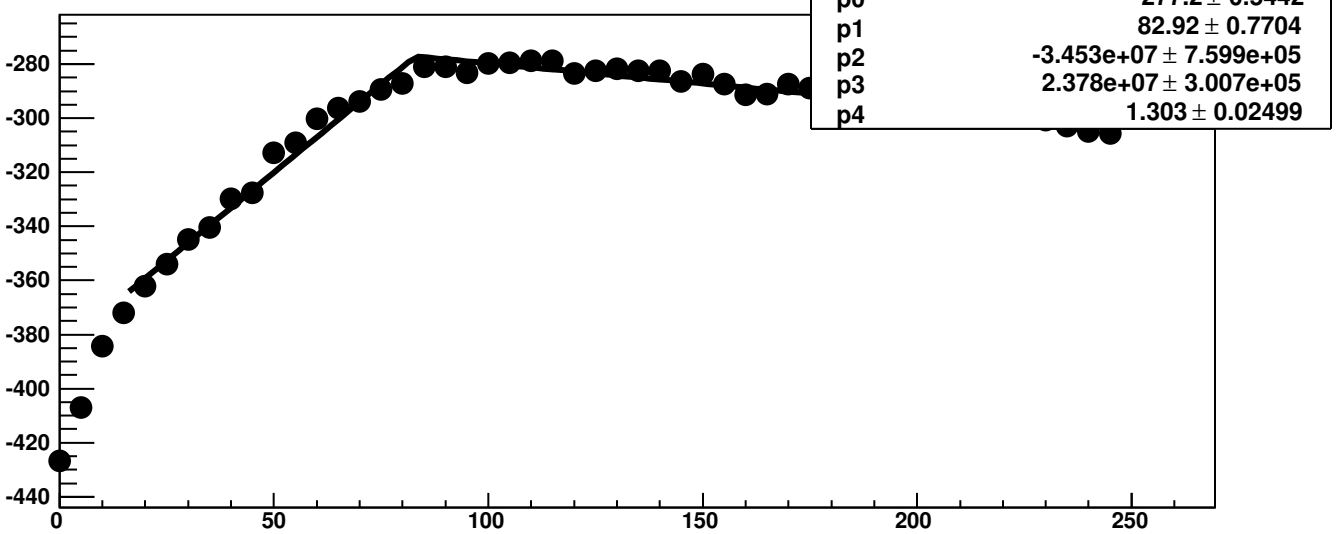

Chip 3, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250


Chip 3, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 372.2 / 41
p0 -275.4 ± 0.6759
p1 103.2 ± 0.5669
p2 -5.729e+07 ± 7.224e+05
p3 3.148e+07 ± 2.788e+05
p4 1.716 ± 0.01442
-260
-280
-300
-320
-340
-360
-380
-400
-420
-440
0
50
100
150
200
250

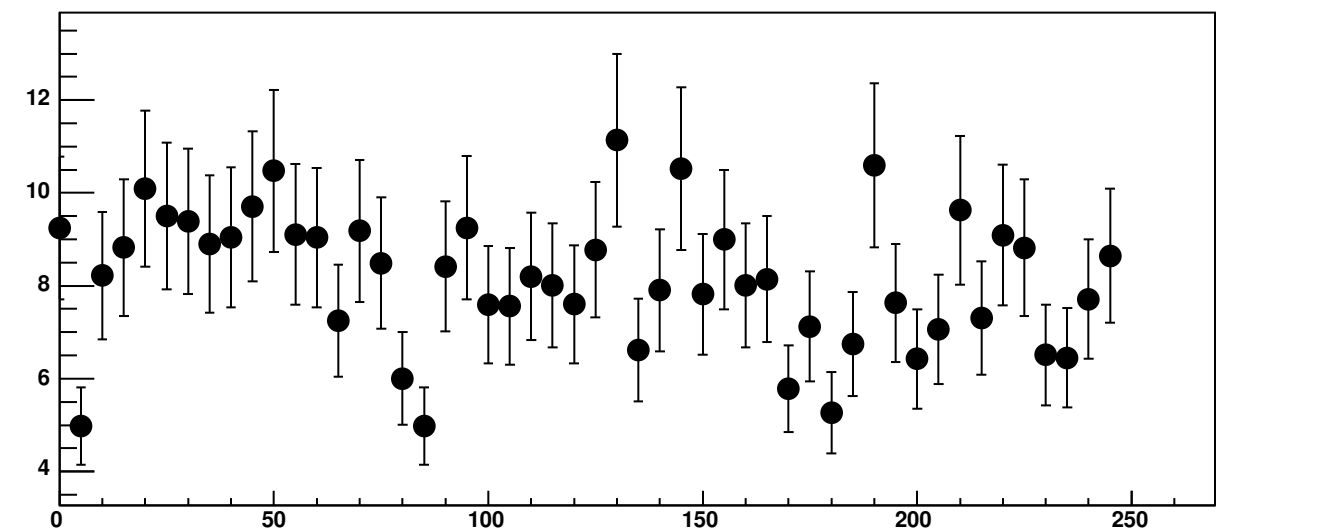

Chip 3, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 3, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
0
50
100
150
200
250

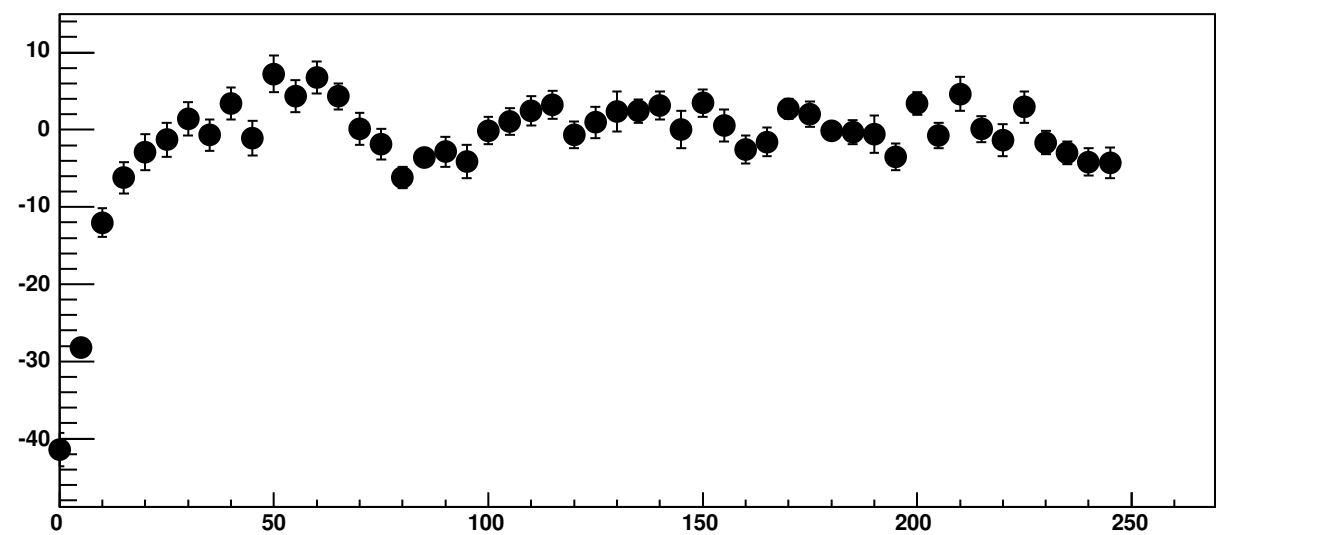
Chip 3, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold

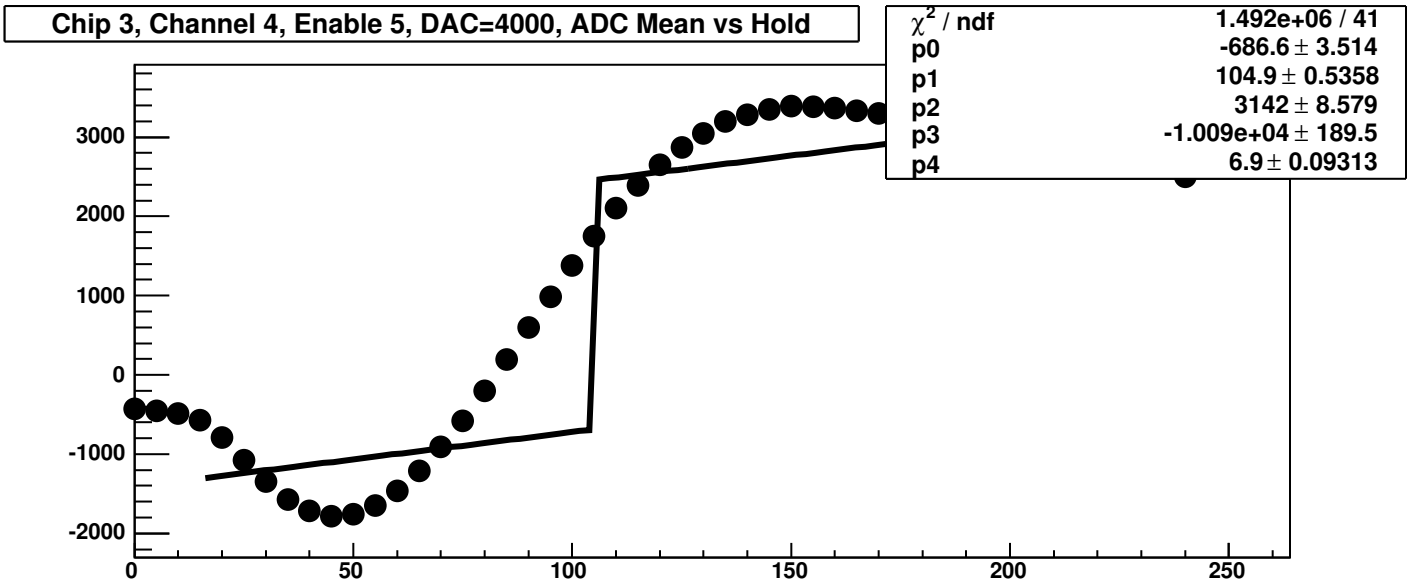


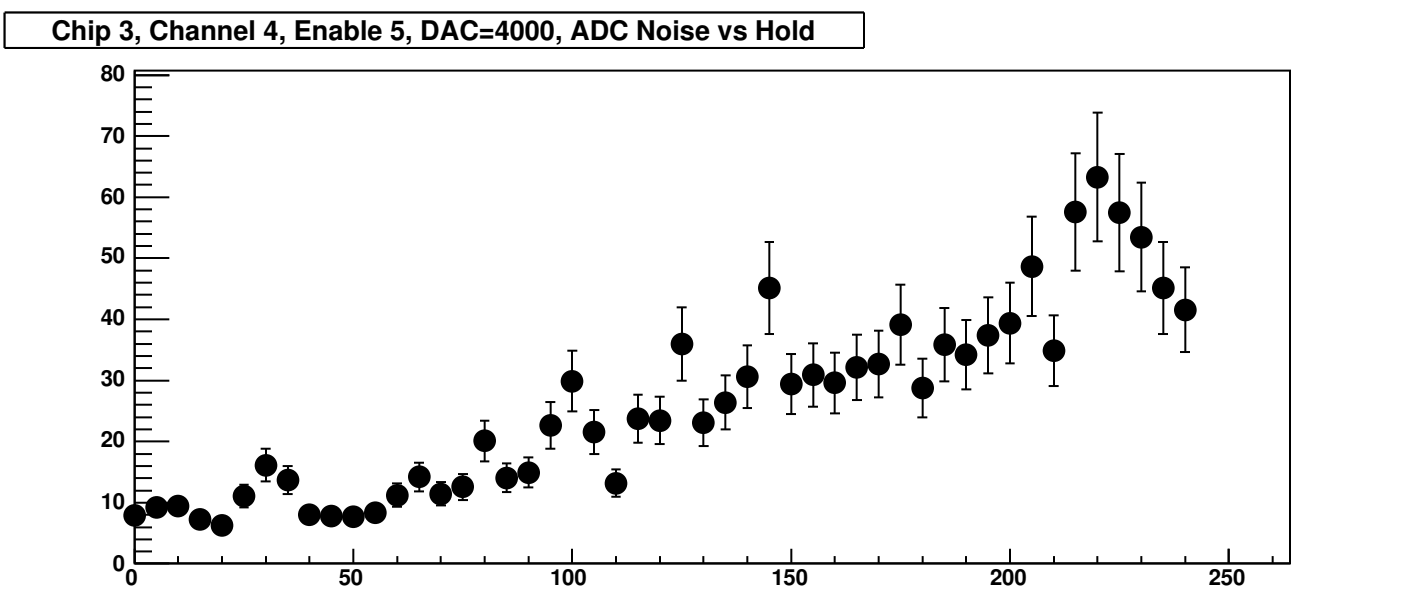
Chip 3, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold

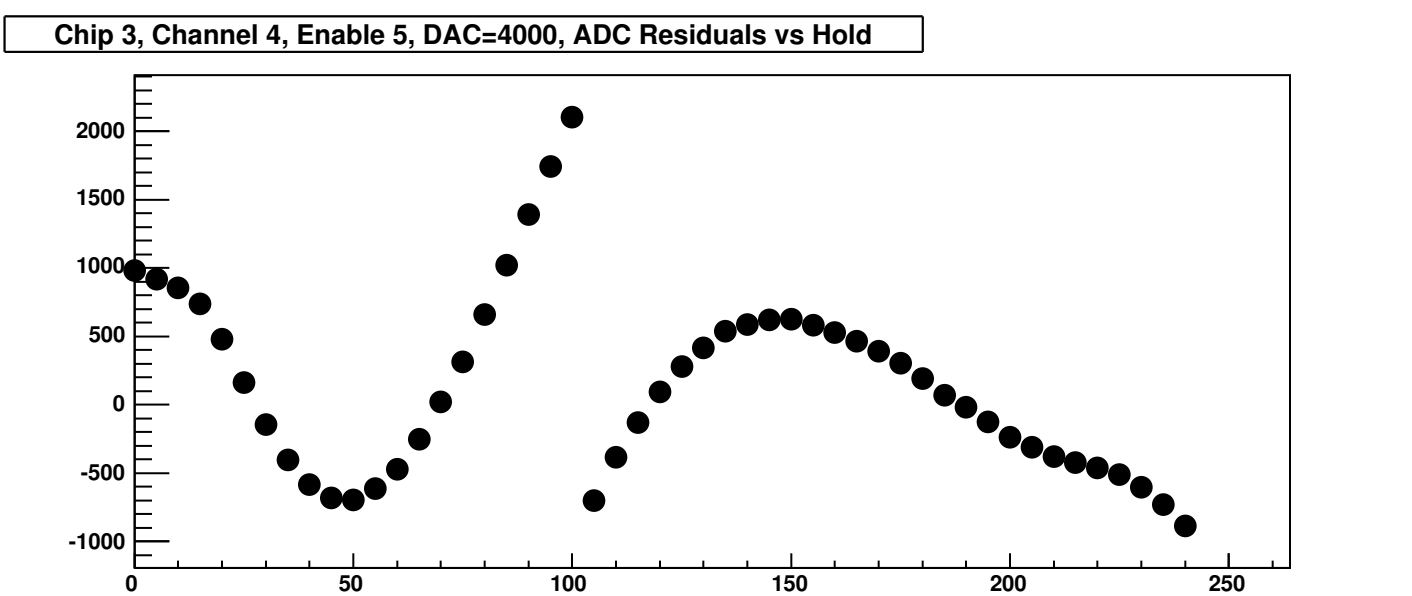


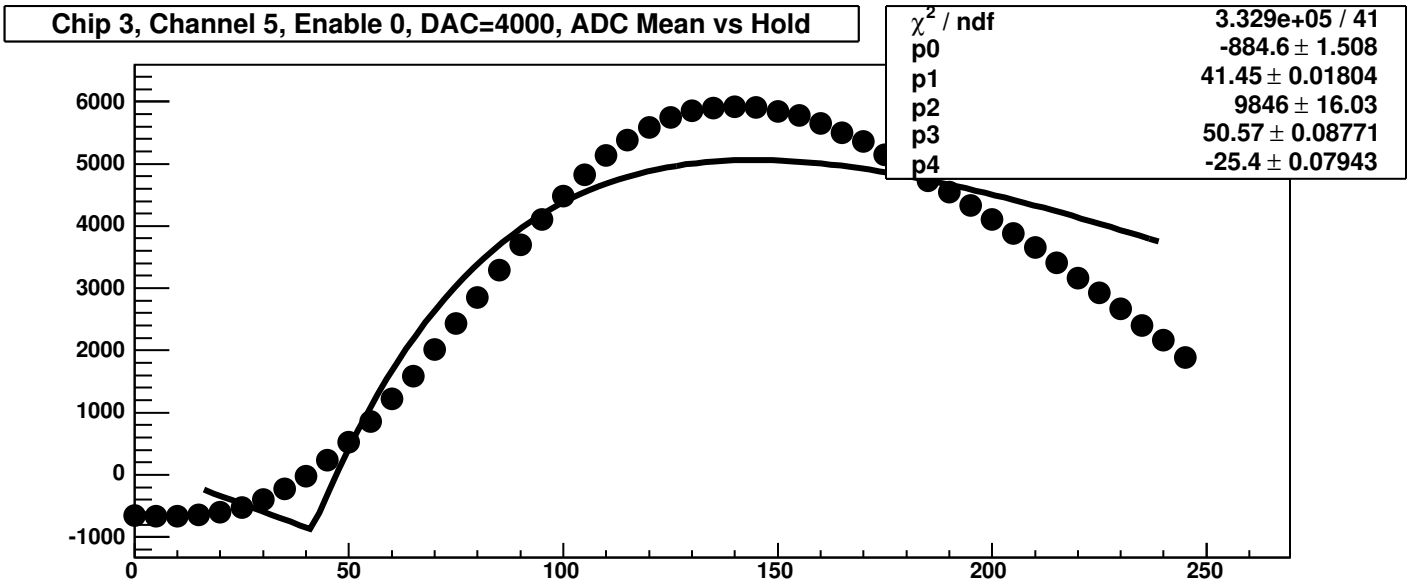
Chip 3, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold

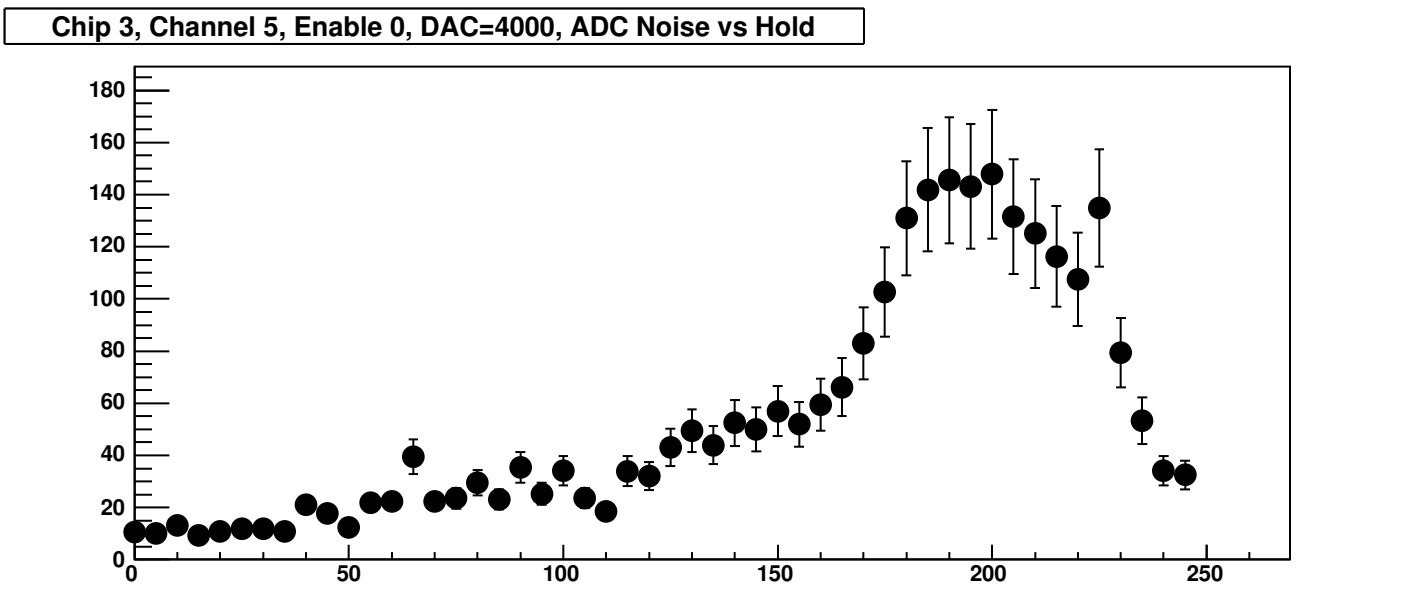


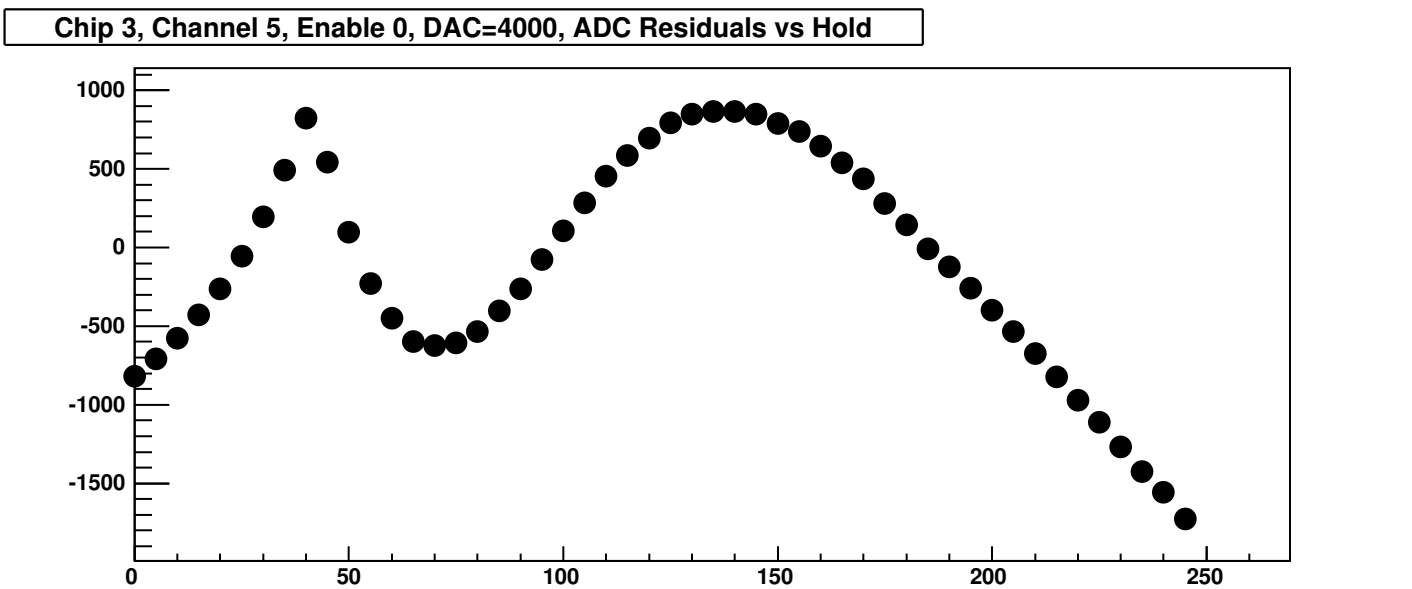

Chip 3, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.492e+06 / 41
p0 -686.6 ± 3.514
p1 104.9 ± 0.5358
p2 3142 ± 8.579
p3 -1.009e+04 ± 189.5
p4 6.9 ± 0.09313
3000
2000
1000
0
-1000
-2000
0
50
100
150
200
250


Chip 3, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250

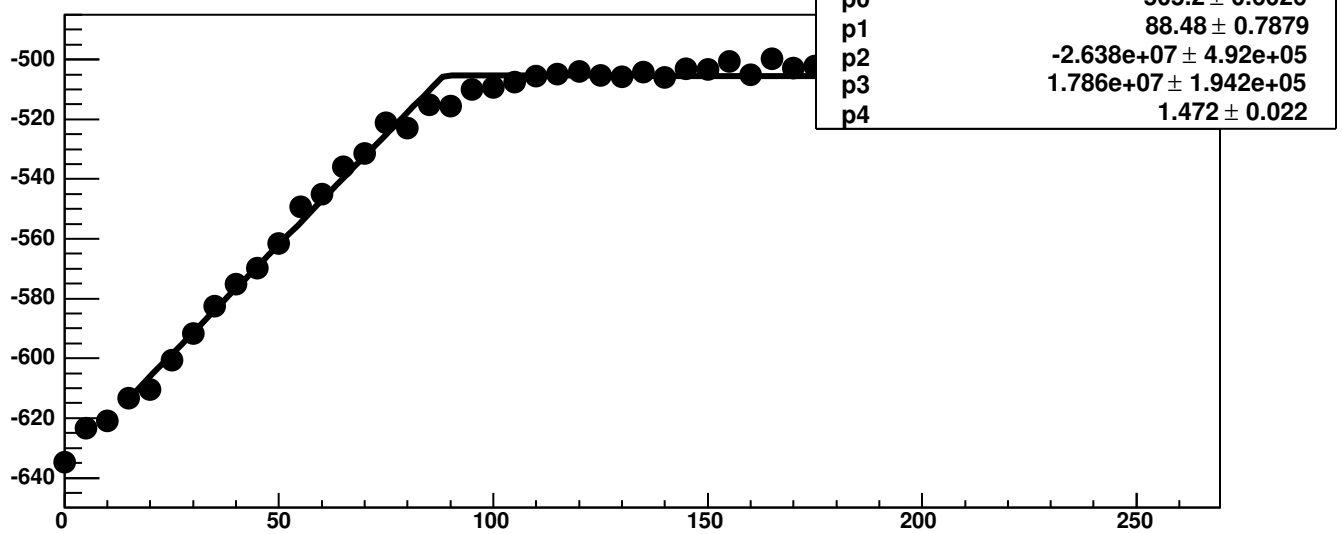

Chip 3, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
-500
-1000
0
50
100
150
200
250


Chip 3, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.329e+05 / 41
p0 -884.6 ± 1.508
p1 41.45 ± 0.01804
p2 9846 ± 16.03
p3 50.57 ± 0.08771
p4 -25.4 ± 0.07943
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

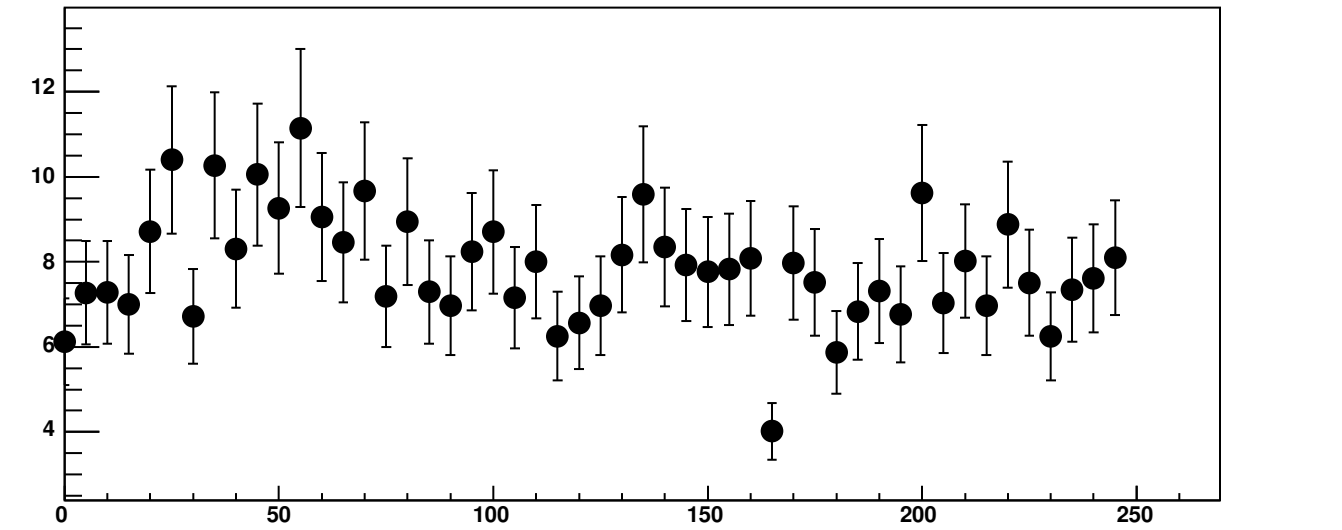

Chip 3, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 3, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

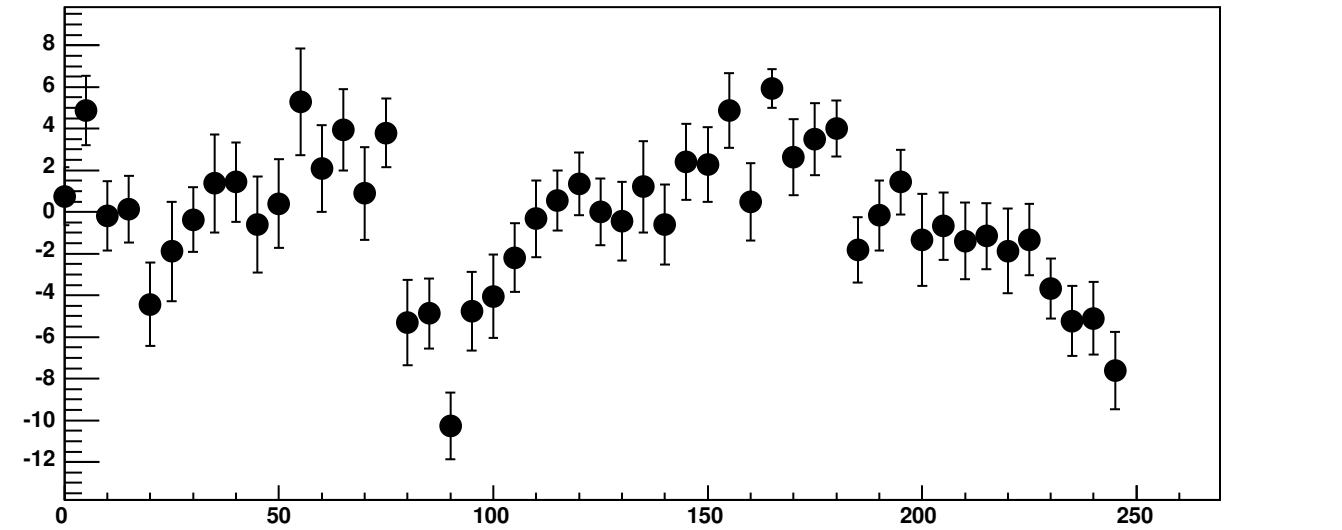
Chip 3, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold



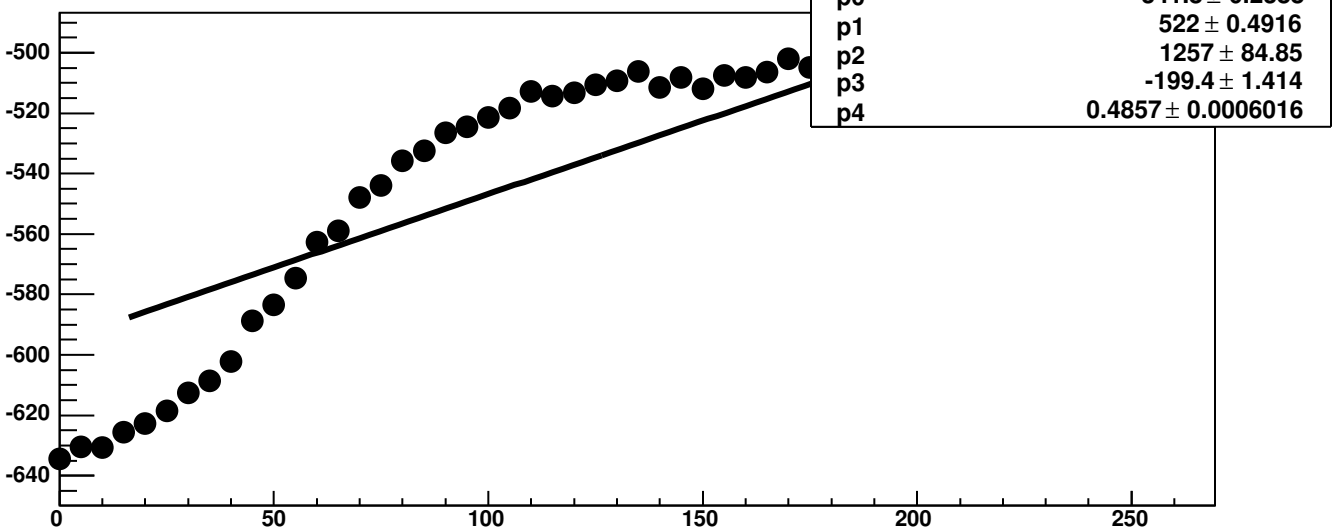
Chip 3, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold



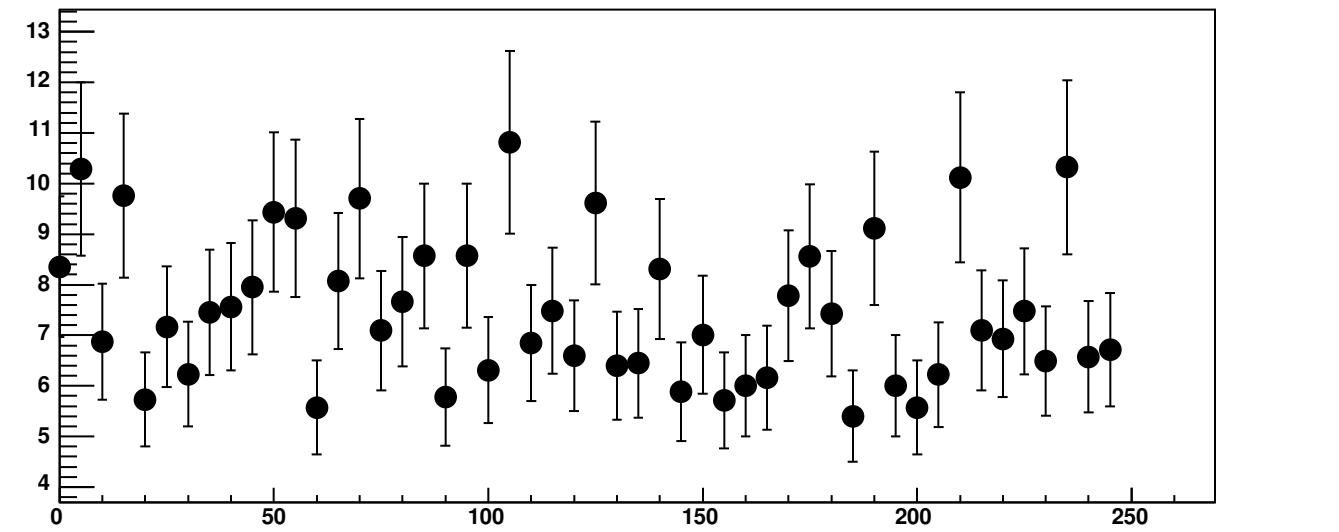
Chip 3, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold



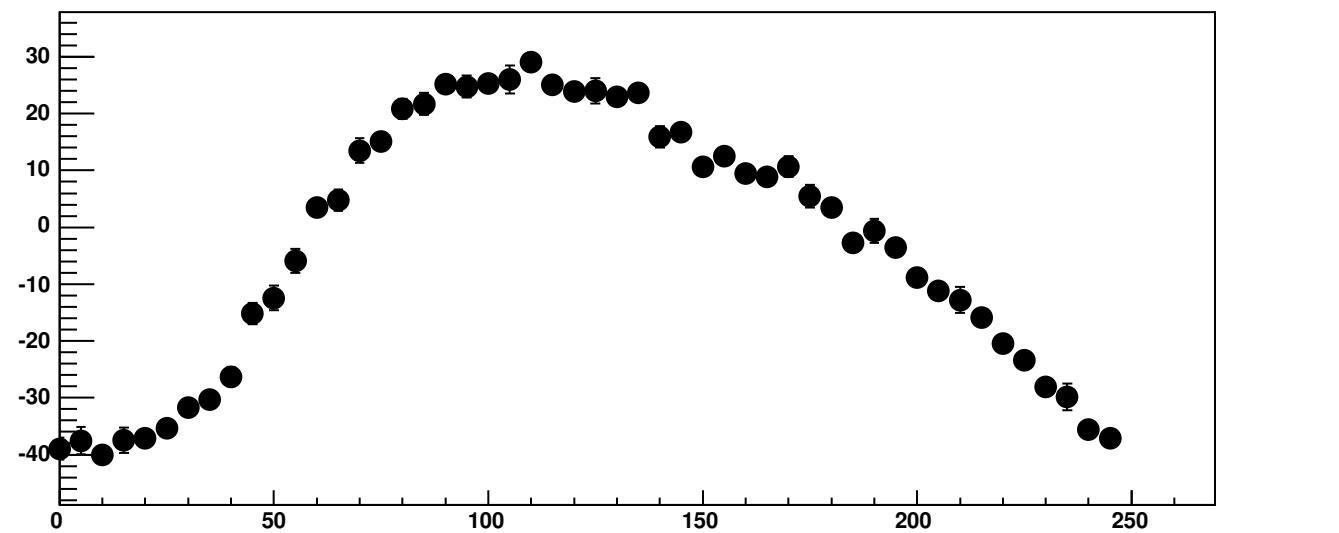
Chip 3, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

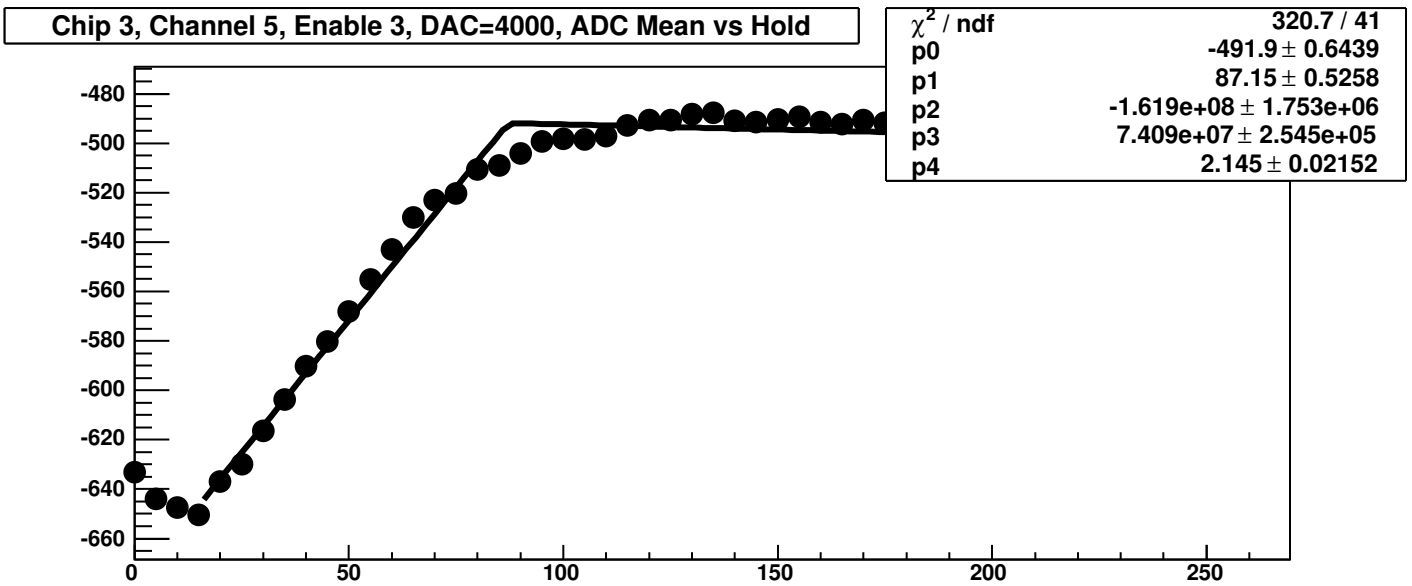


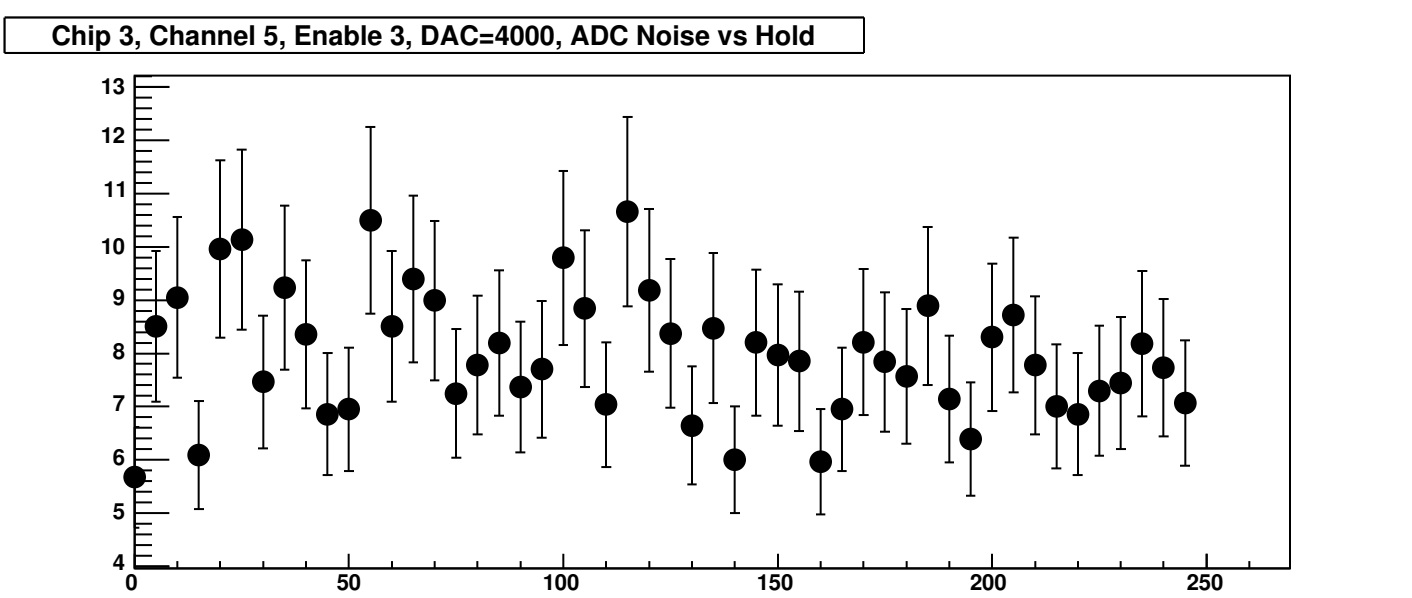
Chip 3, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold

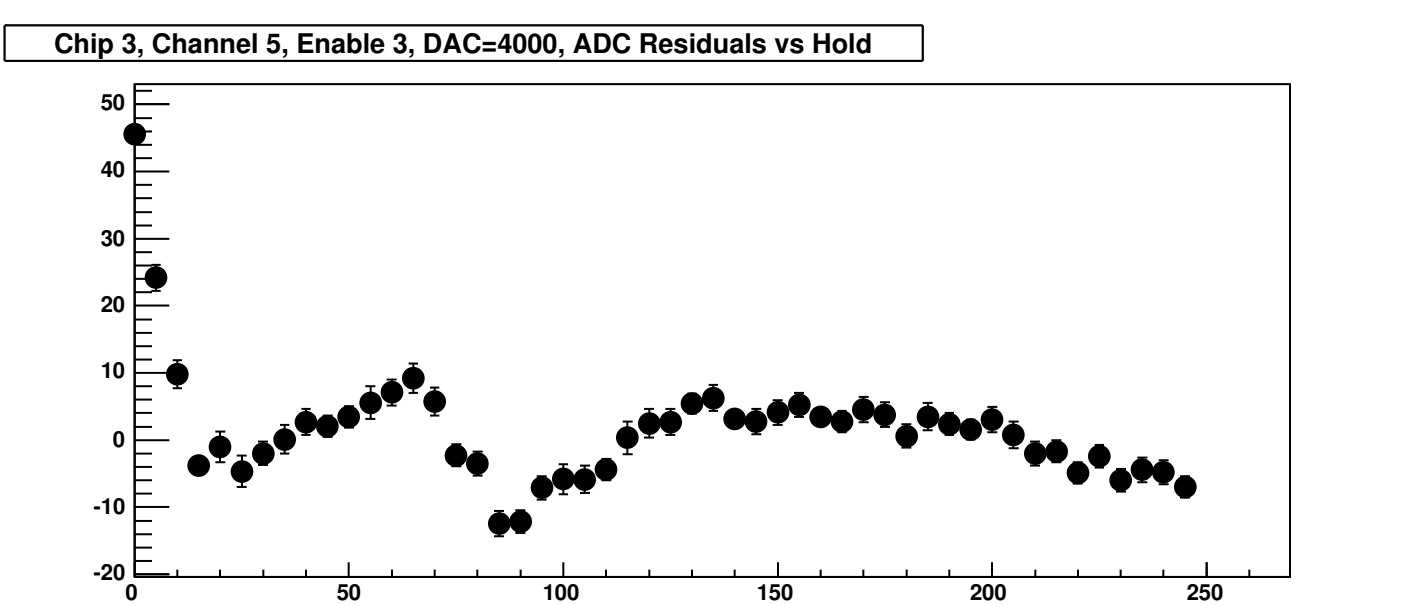


Chip 3, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold



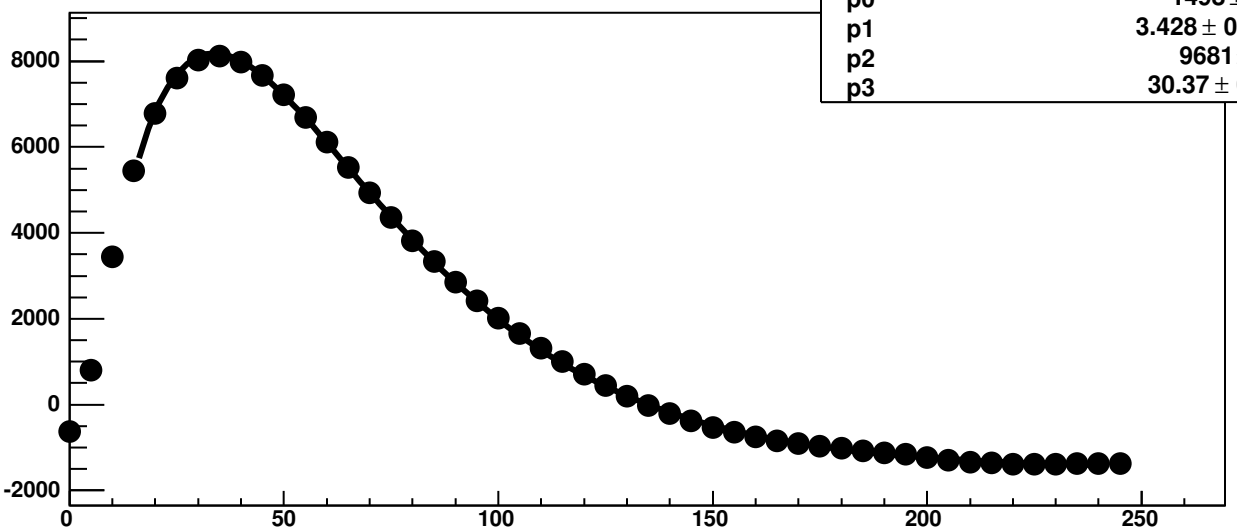

Chip 3, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 320.7 / 41
p0 -491.9 ± 0.6439
p1 87.15 ± 0.5258
p2 -1.619e+08 ± 1.753e+06
p3 7.409e+07 ± 2.545e+05
p4 2.145 ± 0.02152
-480
-500
-520
-540
-560
-580
-600
-620
-640
-660
0
50
100
150
200
250


Chip 3, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

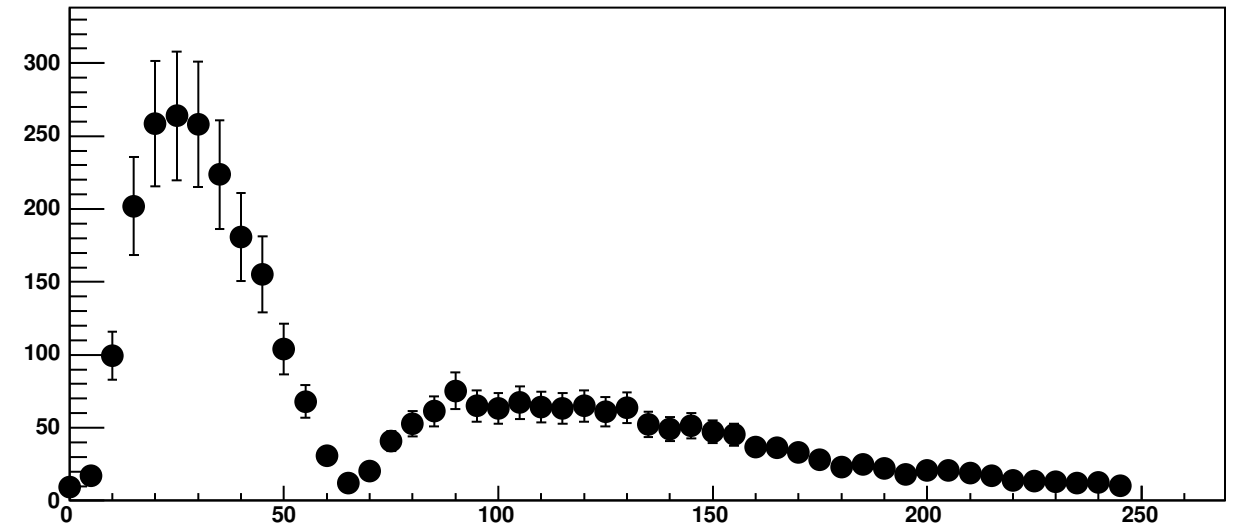

Chip 3, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
-20
0
50
100
150
200
250

Chip 3, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold

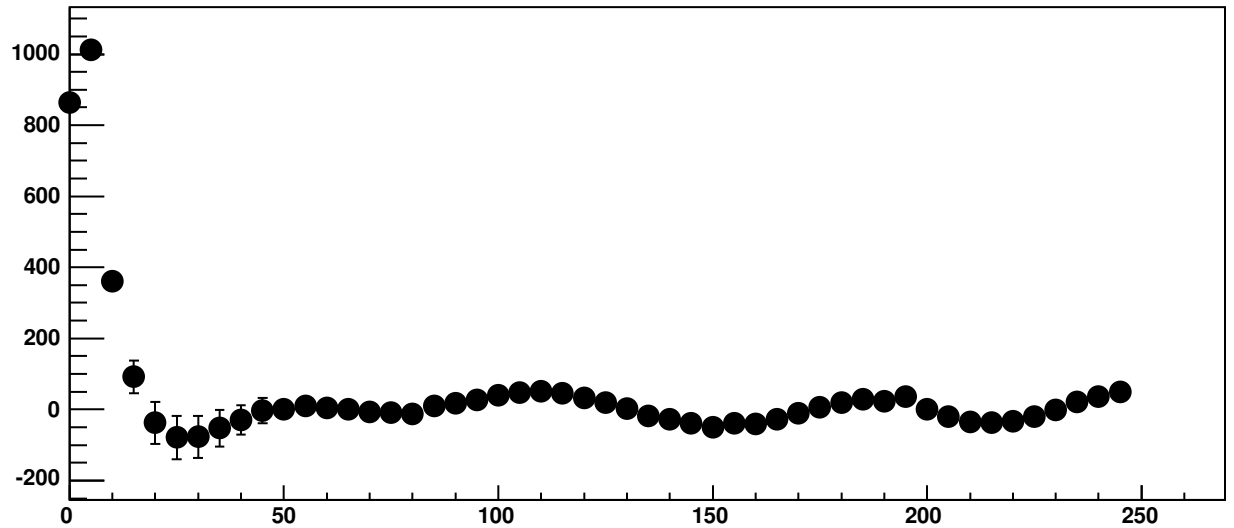
χ^2 / ndf	808.4 / 42
p0	-1498 ± 1.537
p1	3.428 ± 0.09238
p2	9681 ± 10.8
p3	30.37 ± 0.0366



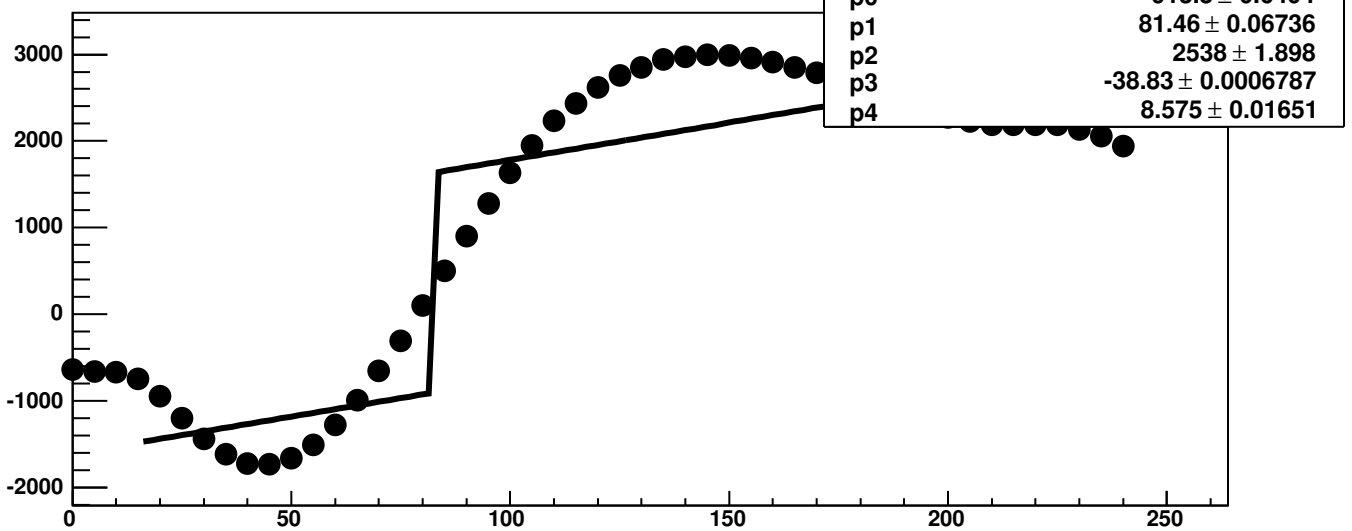
Chip 3, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold



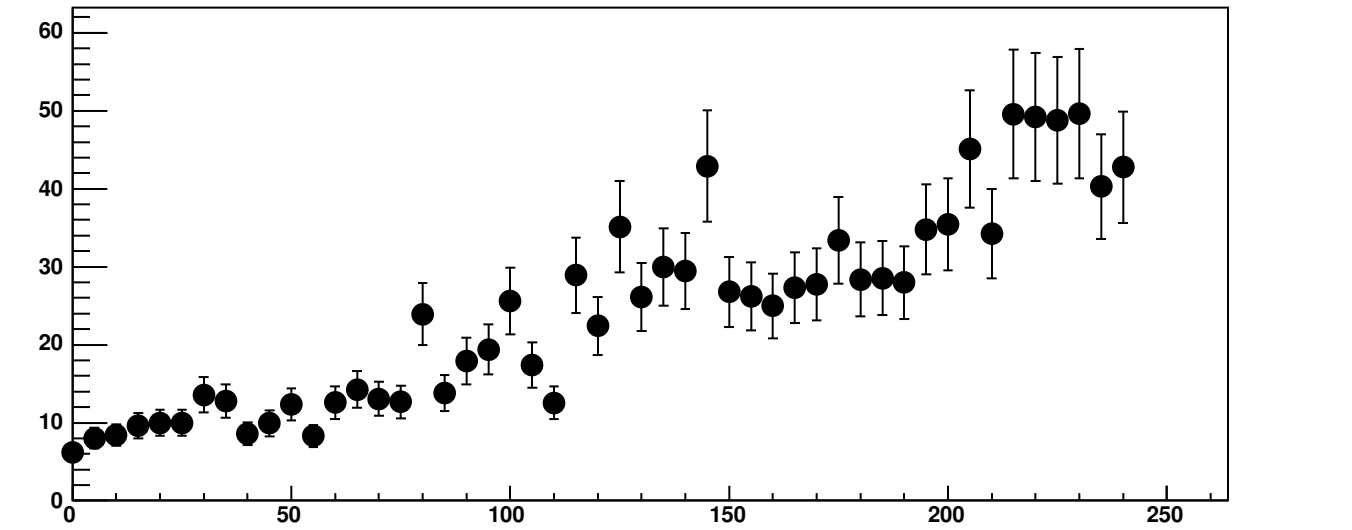
Chip 3, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold



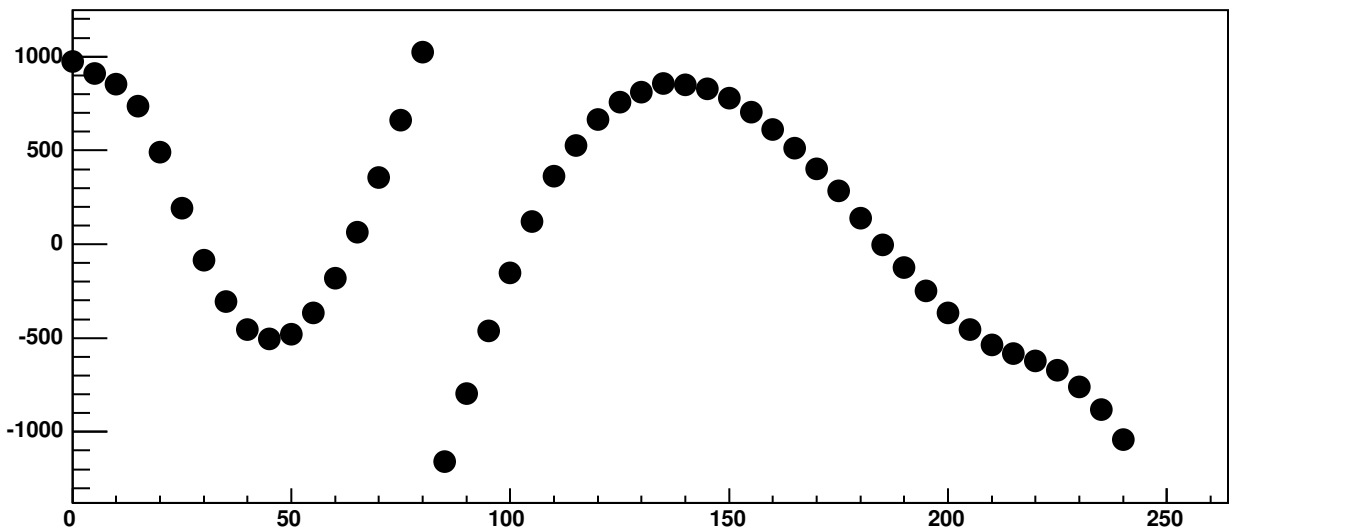
Chip 3, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

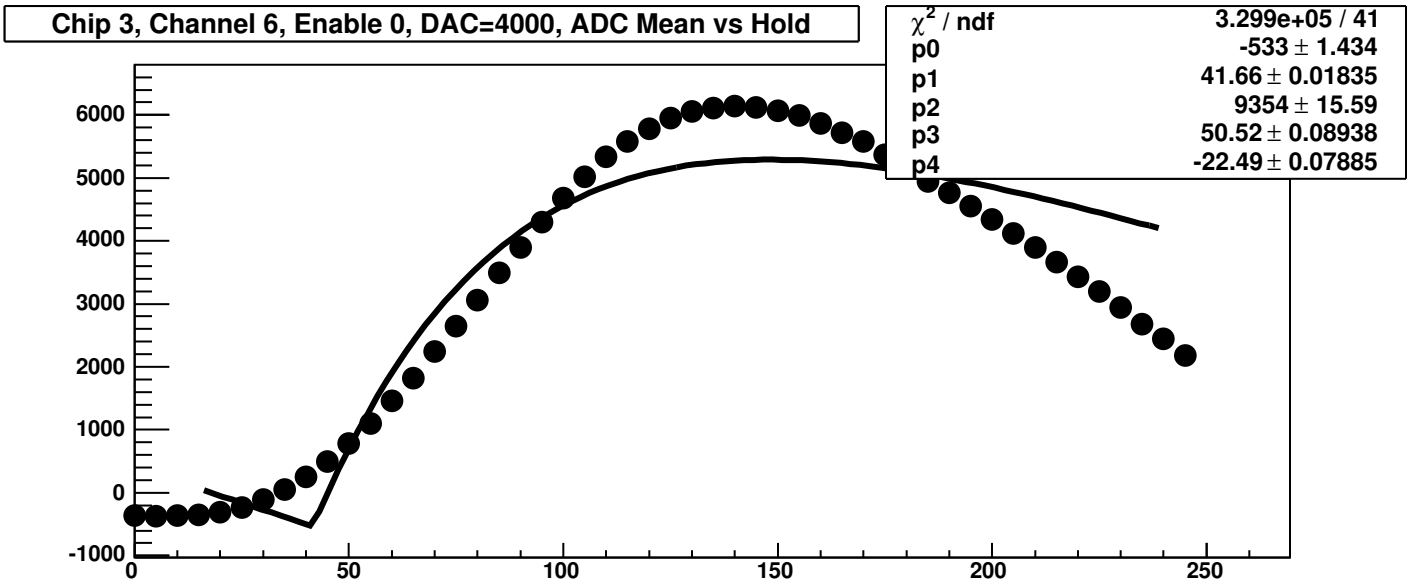


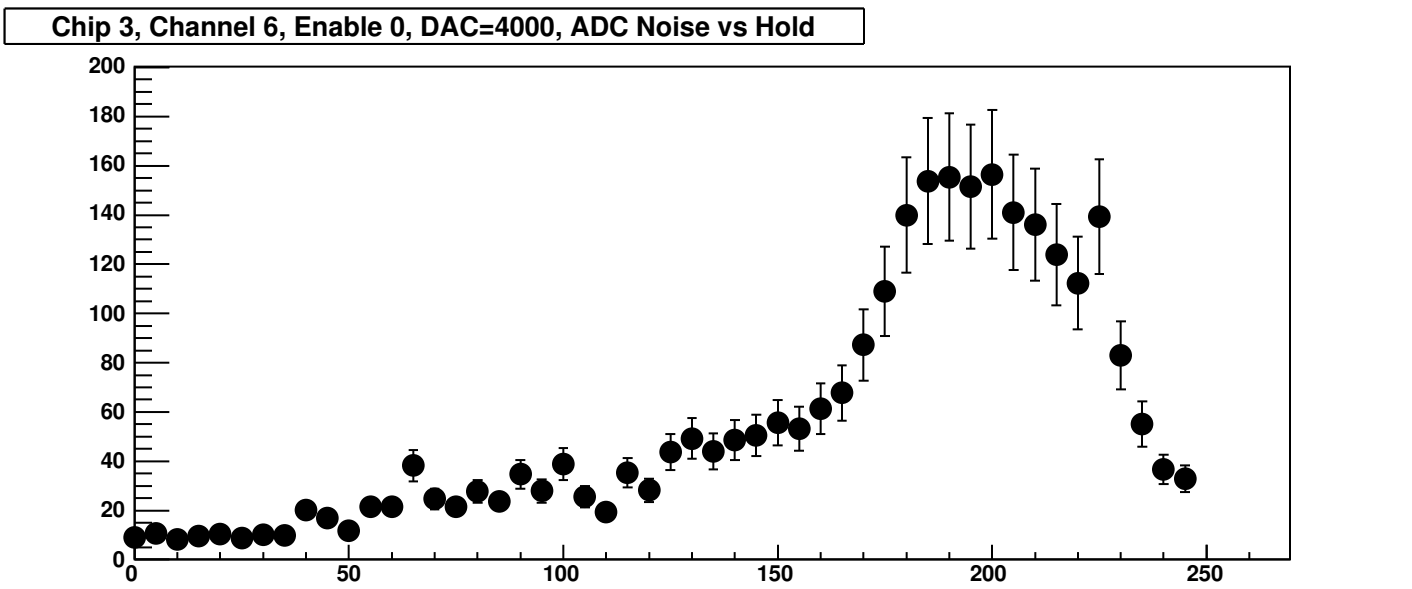
Chip 3, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold

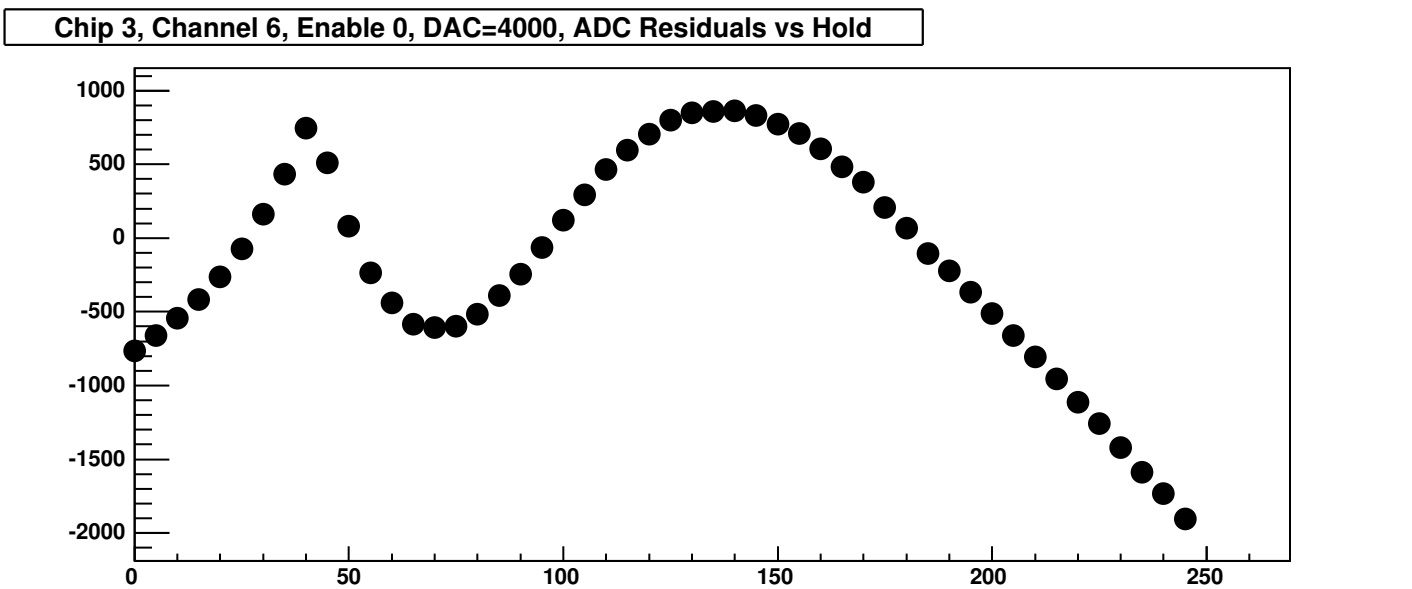


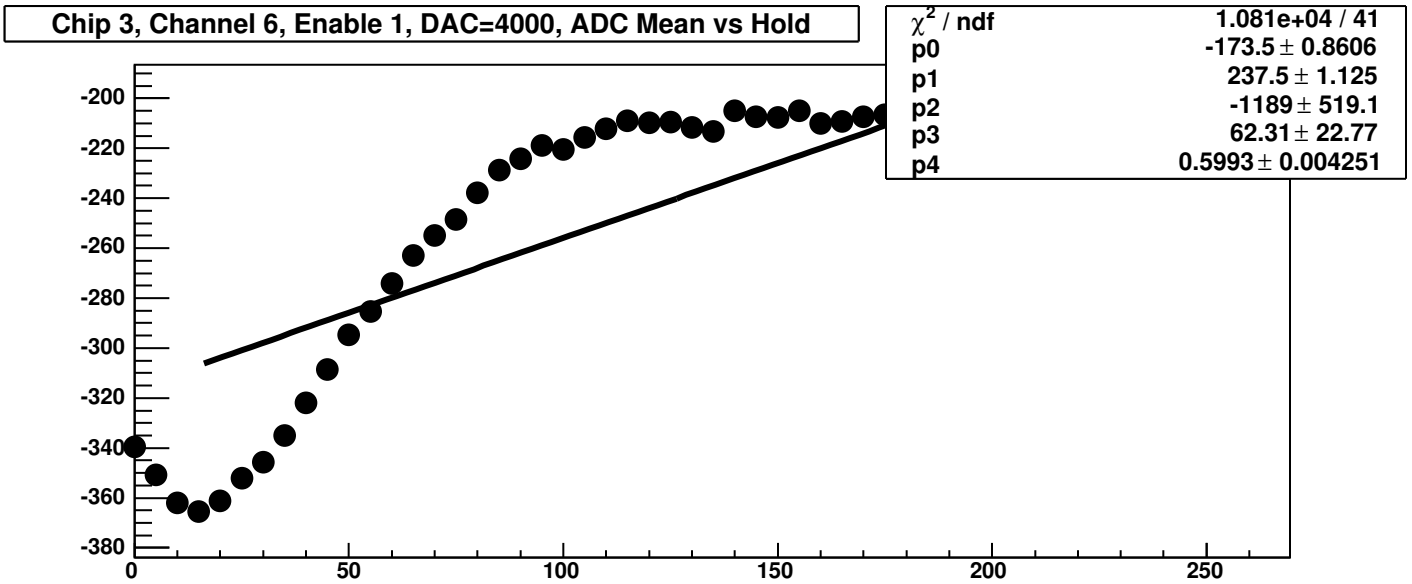
Chip 3, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

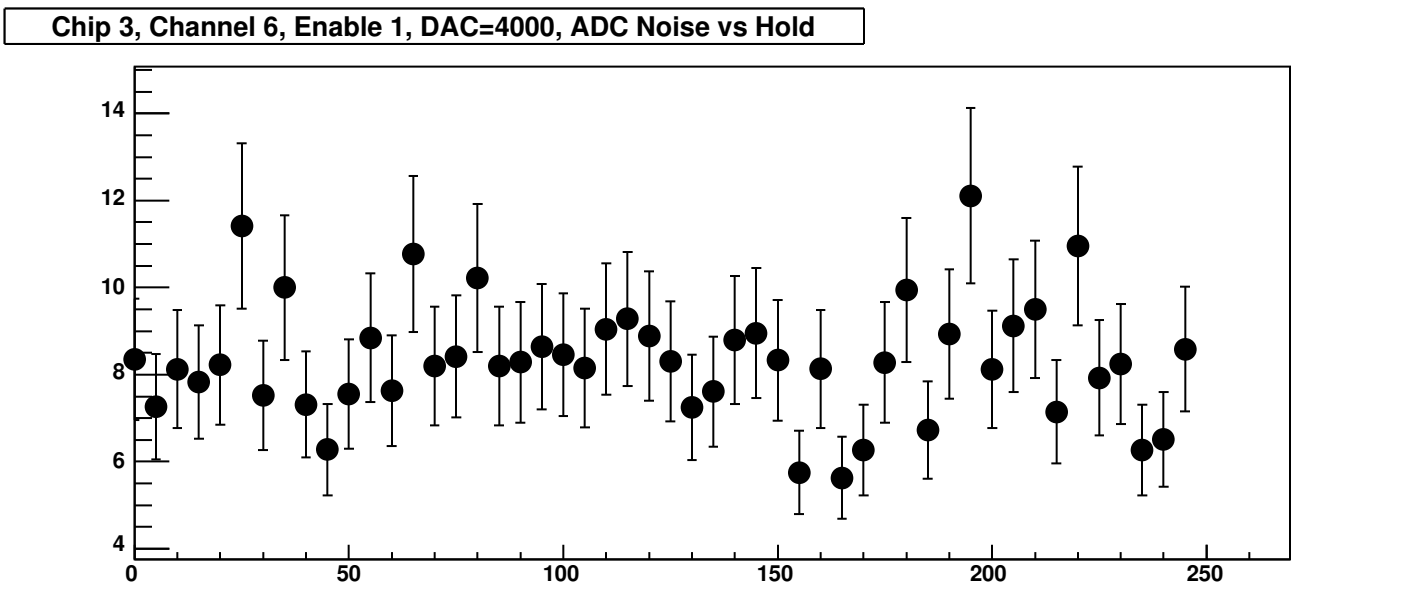


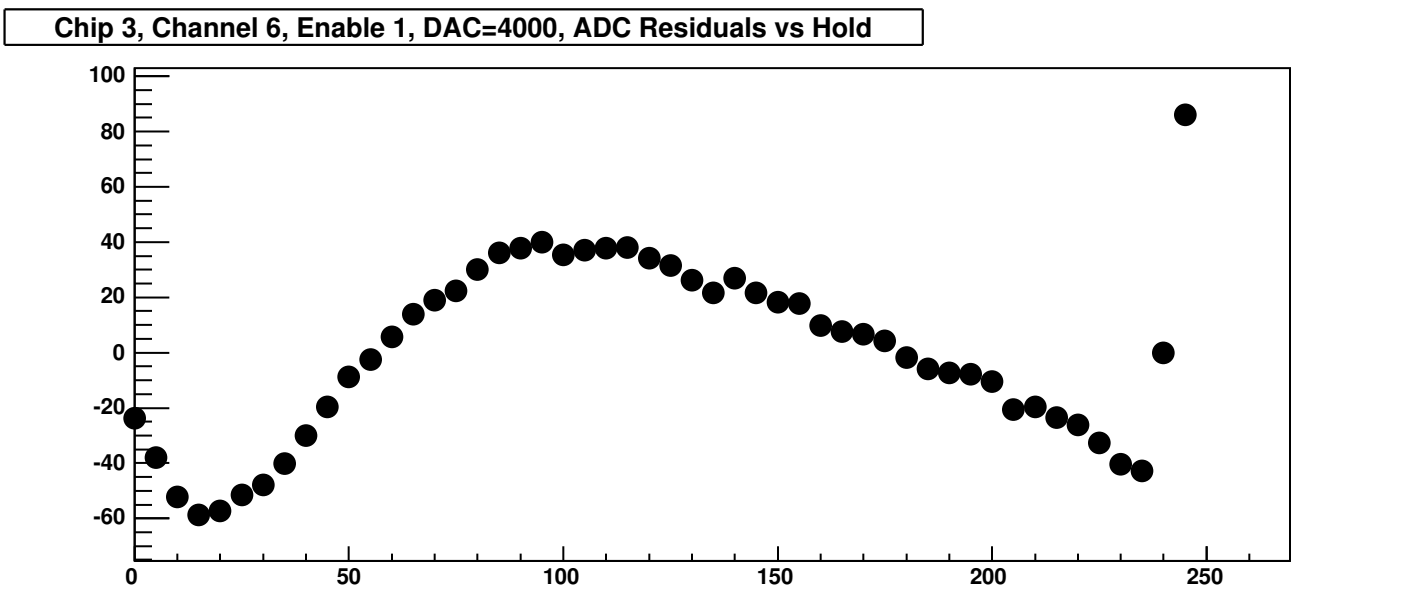

Chip 3, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.299e+05 / 41
p0 -533 ± 1.434
p1 41.66 ± 0.01835
p2 9354 ± 15.59
p3 50.52 ± 0.08938
p4 -22.49 ± 0.07885
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 3, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

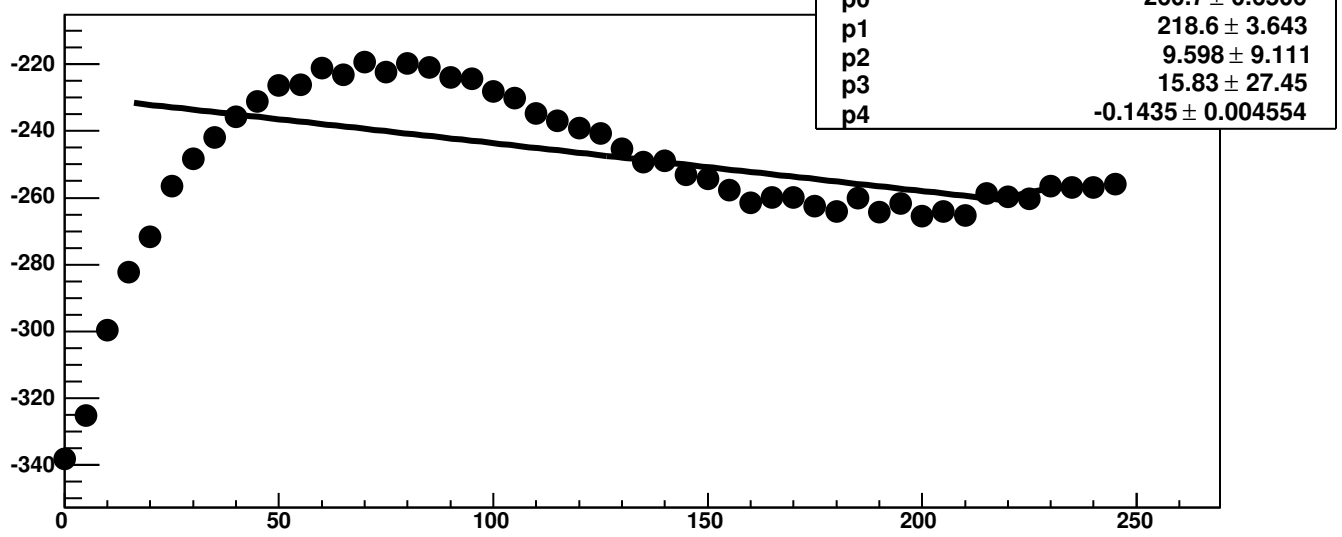

Chip 3, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250


Chip 3, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.081e+04 / 41
p0 -173.5 ± 0.8606
p1 237.5 ± 1.125
p2 -1189 ± 519.1
p3 62.31 ± 22.77
p4 0.5993 ± 0.004251
-200
-220
-240
-260
-280
-300
-320
-340
-360
-380
0
50
100
150
200
250

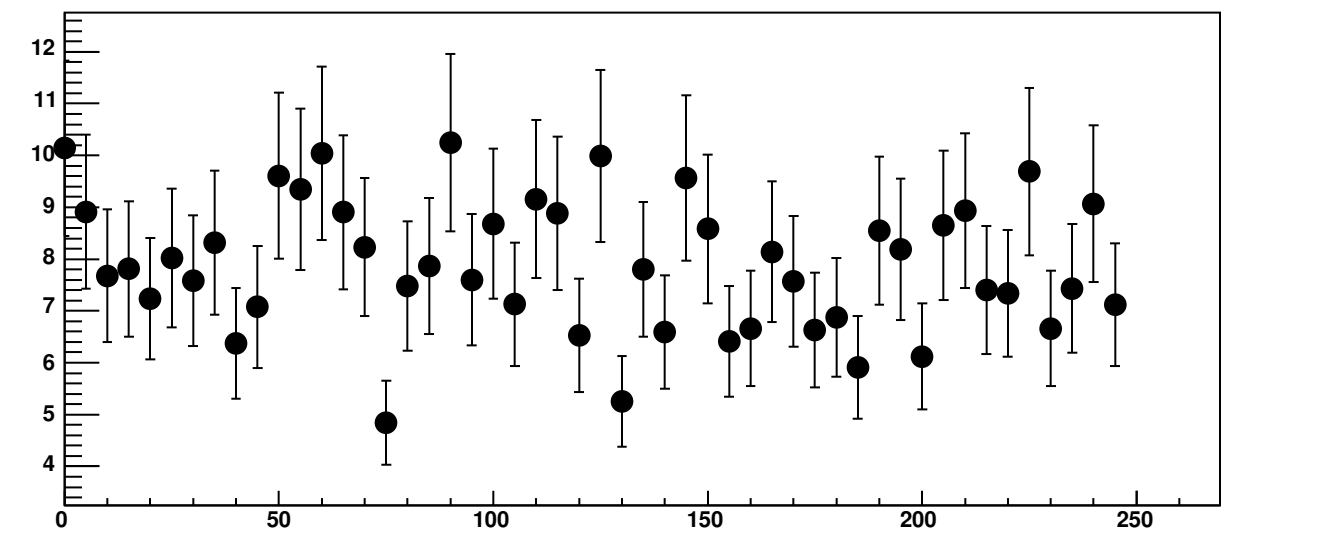

Chip 3, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 3, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold
100
80
60
40
20
0
-20
-40
-60
0
50
100
150
200
250

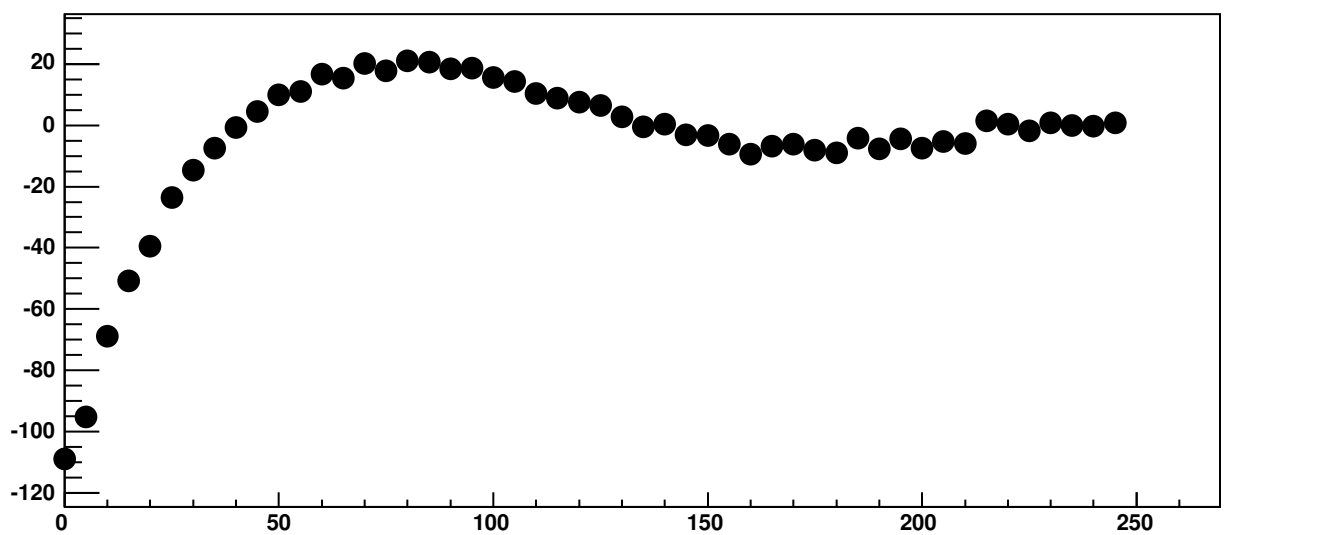
Chip 3, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold



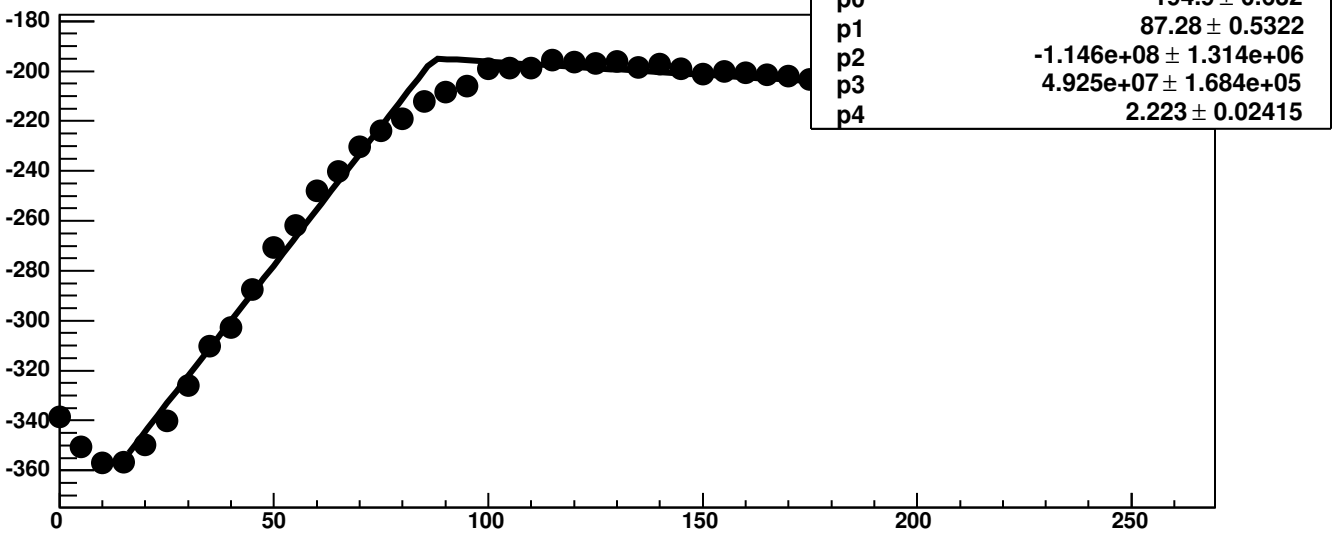
Chip 3, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold



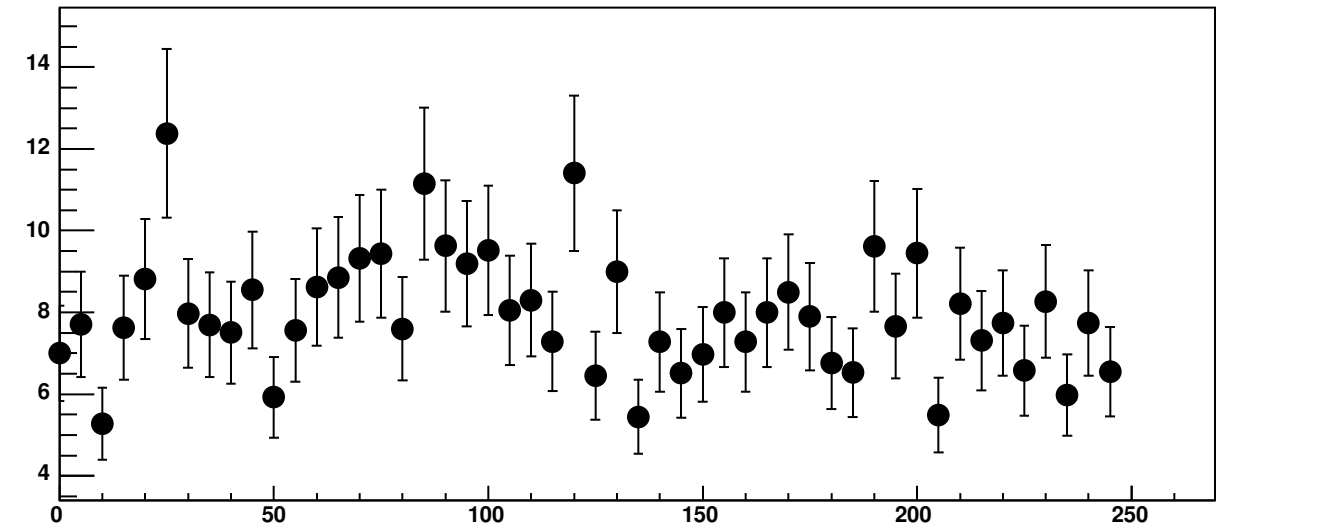
Chip 3, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold



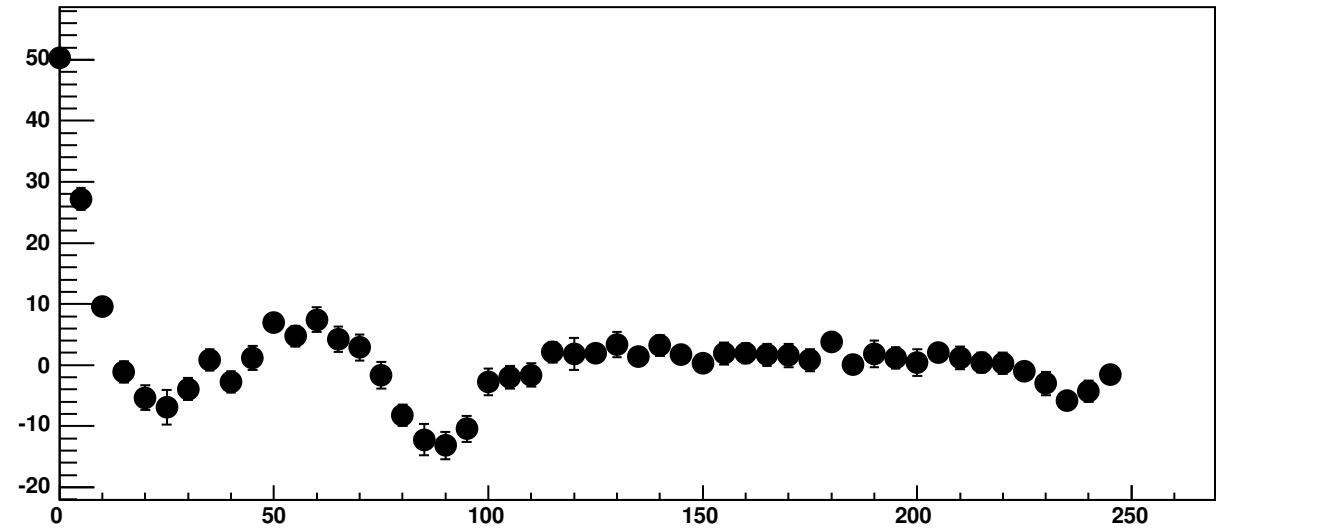
Chip 3, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold



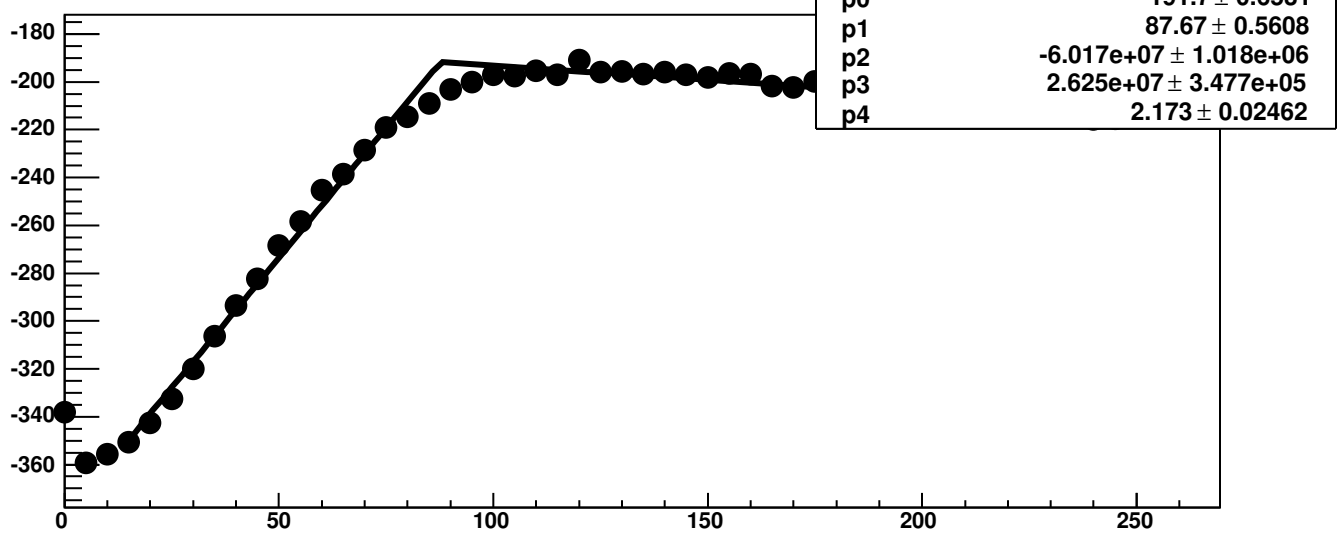
Chip 3, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



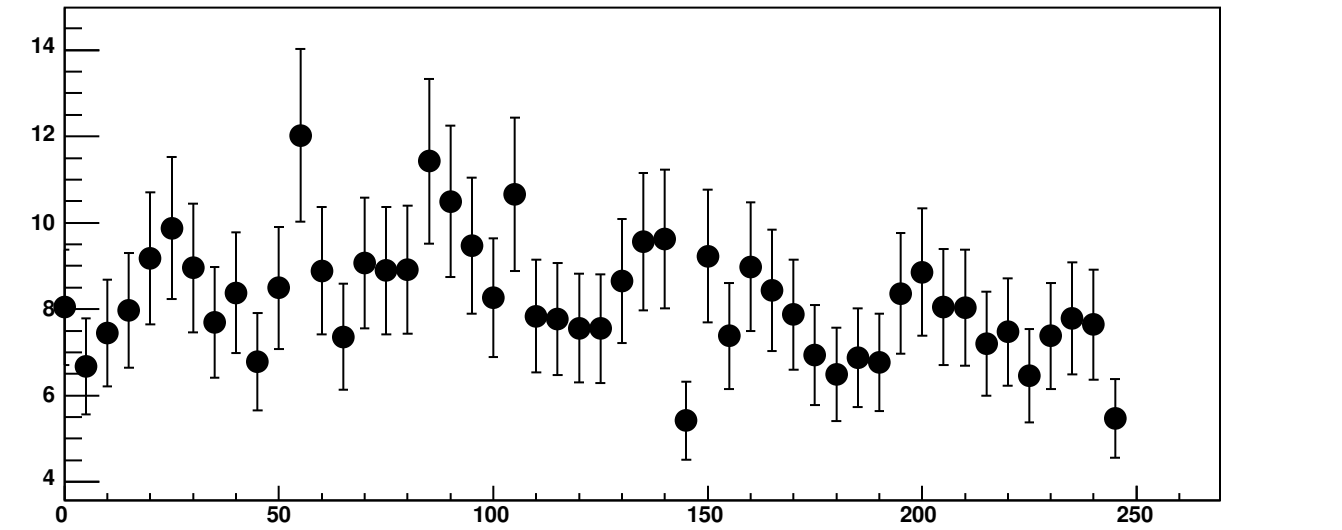
Chip 3, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold



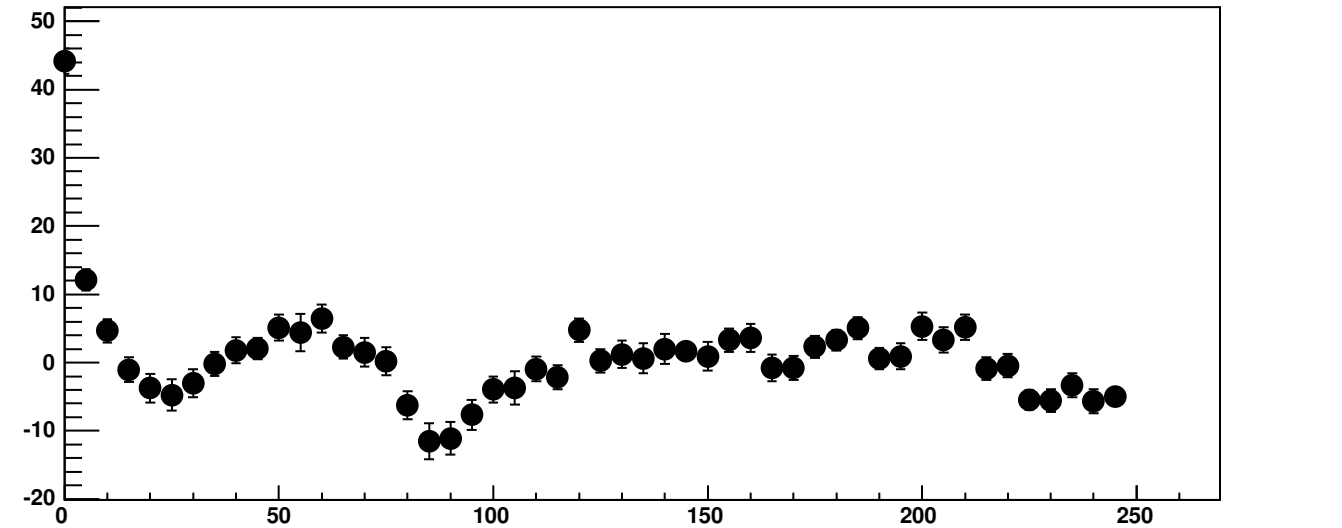
Chip 3, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold



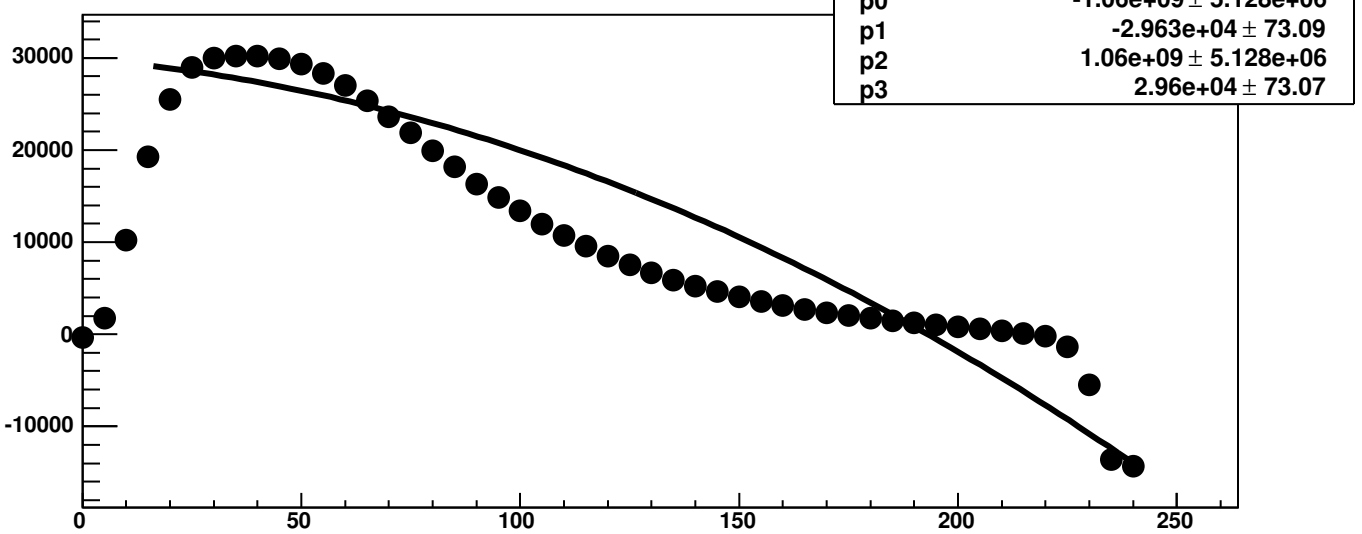
Chip 3, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



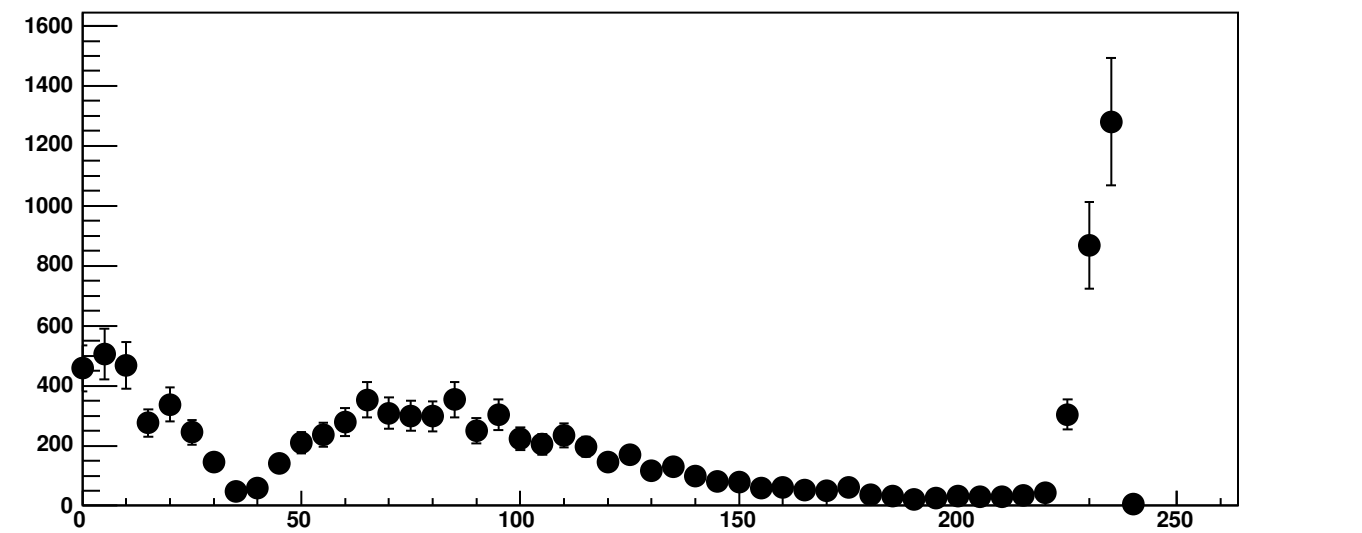
Chip 3, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold



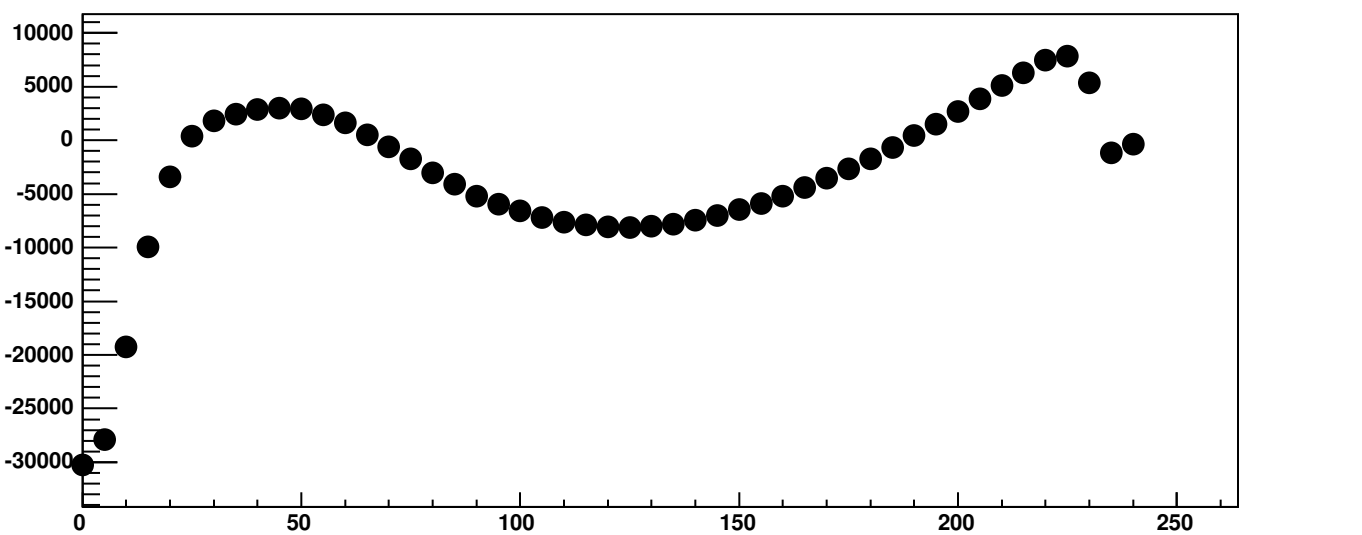
Chip 3, Channel 6, Enable 5!, DAC=4000, ADC Mean vs Hold

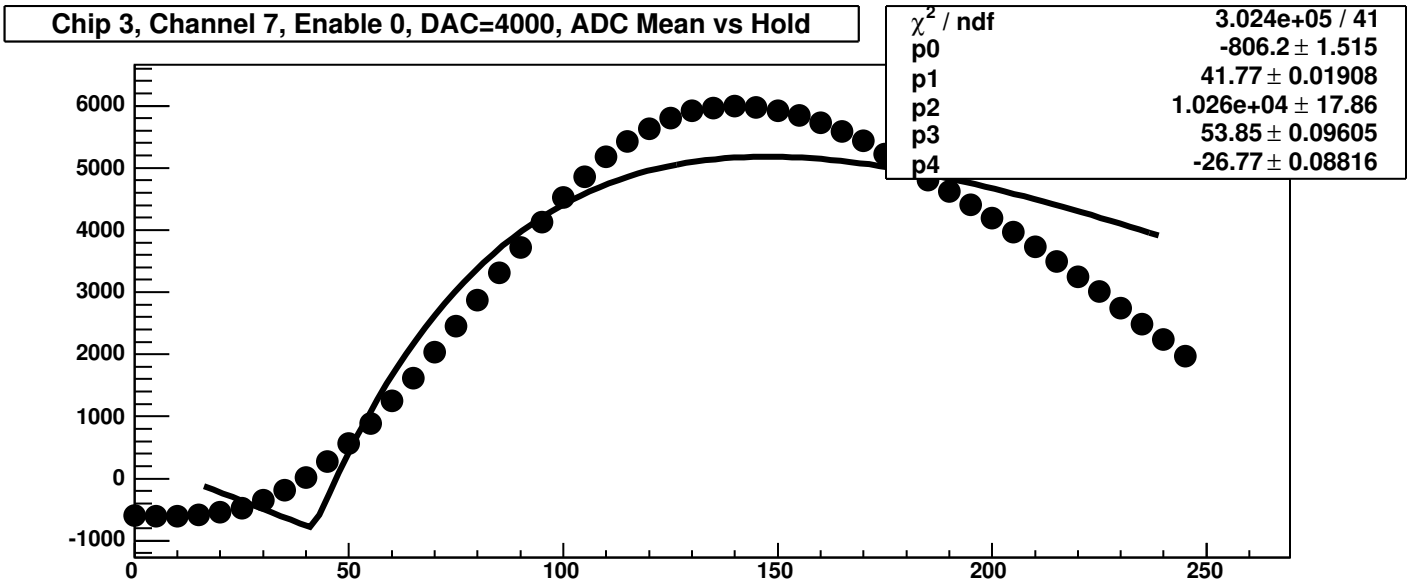


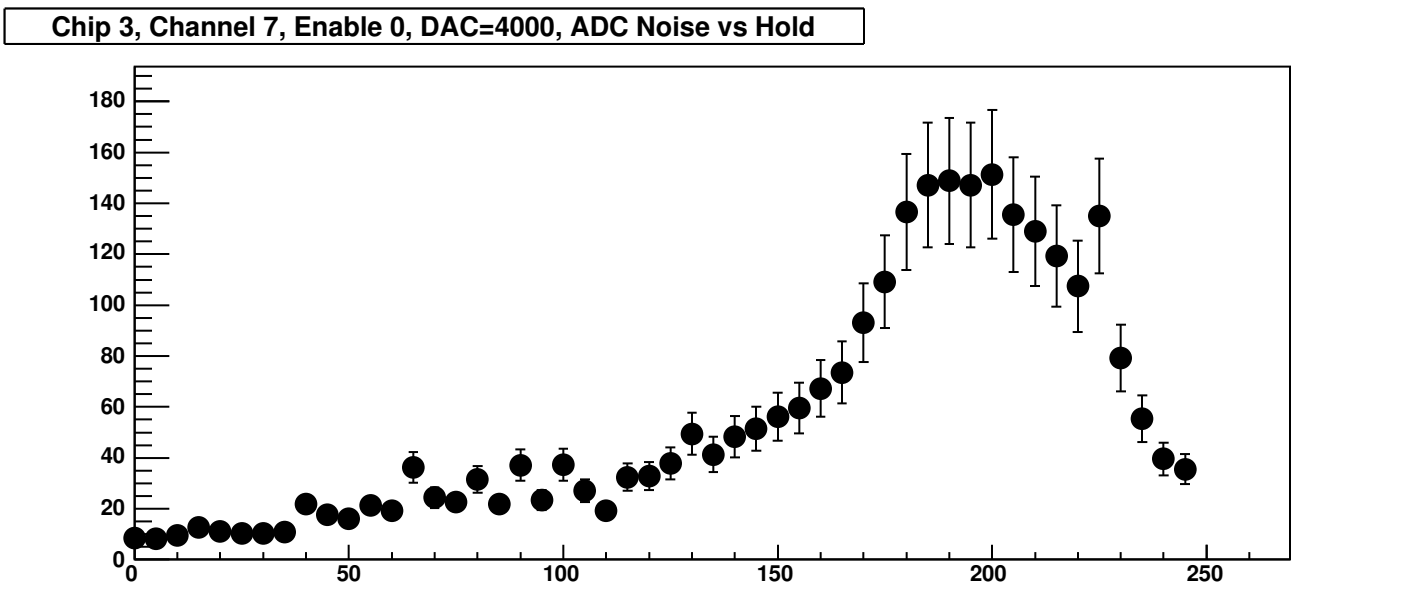
Chip 3, Channel 6, Enable 5!, DAC=4000, ADC Noise vs Hold

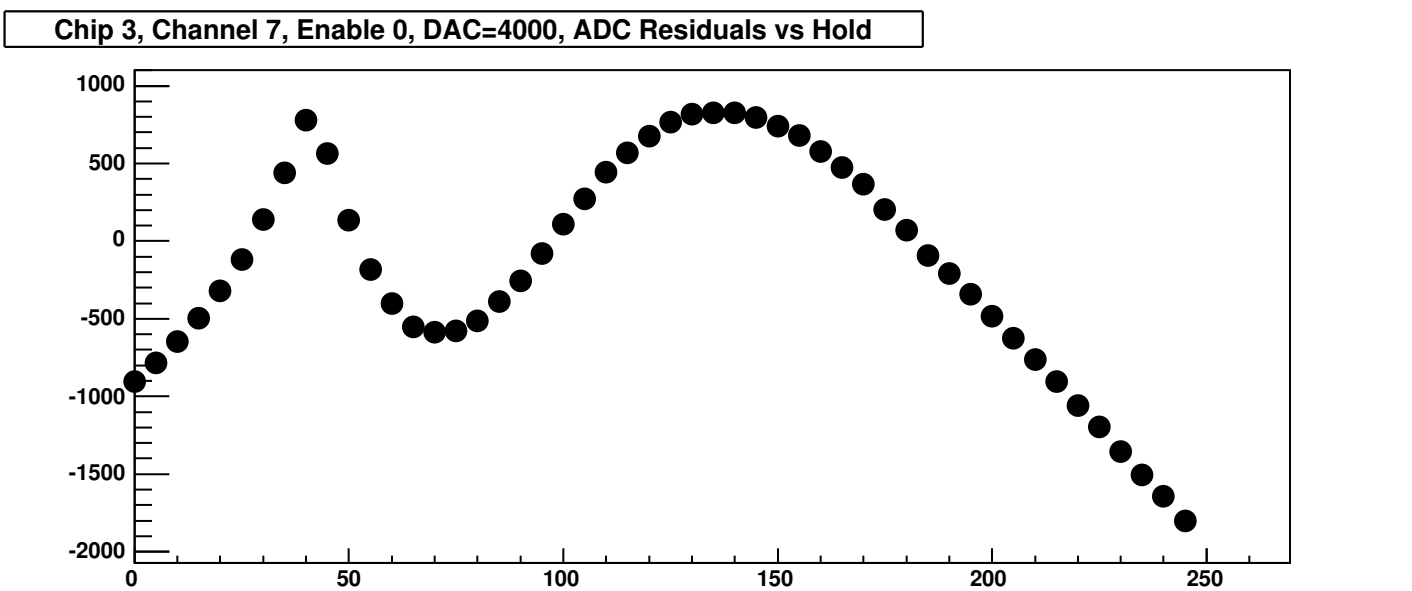


Chip 3, Channel 6, Enable 5!, DAC=4000, ADC Residuals vs Hold

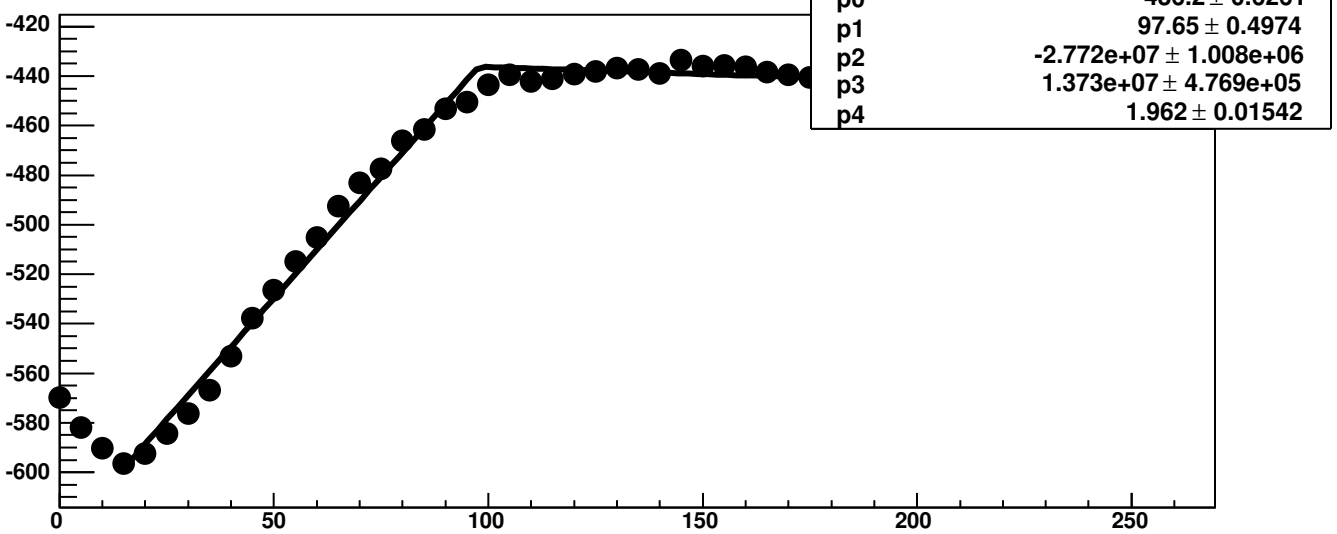



Chip 3, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.024e+05 / 41
p0 -806.2 ± 1.515
p1 41.77 ± 0.01908
p2 1.026e+04 ± 17.86
p3 53.85 ± 0.09605
p4 -26.77 ± 0.08816
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

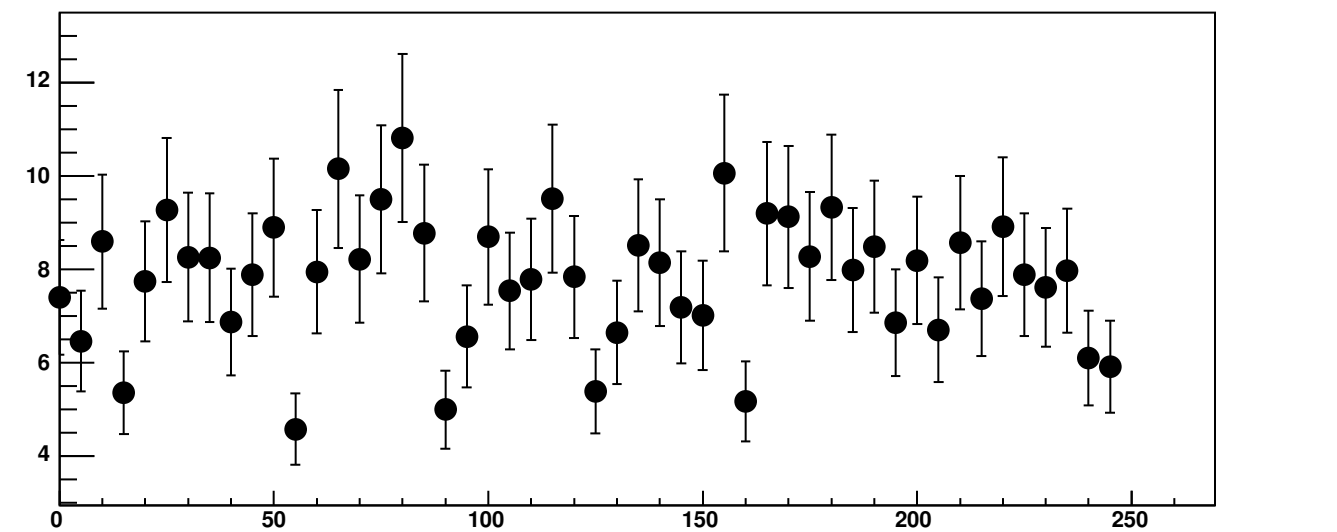

Chip 3, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 3, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

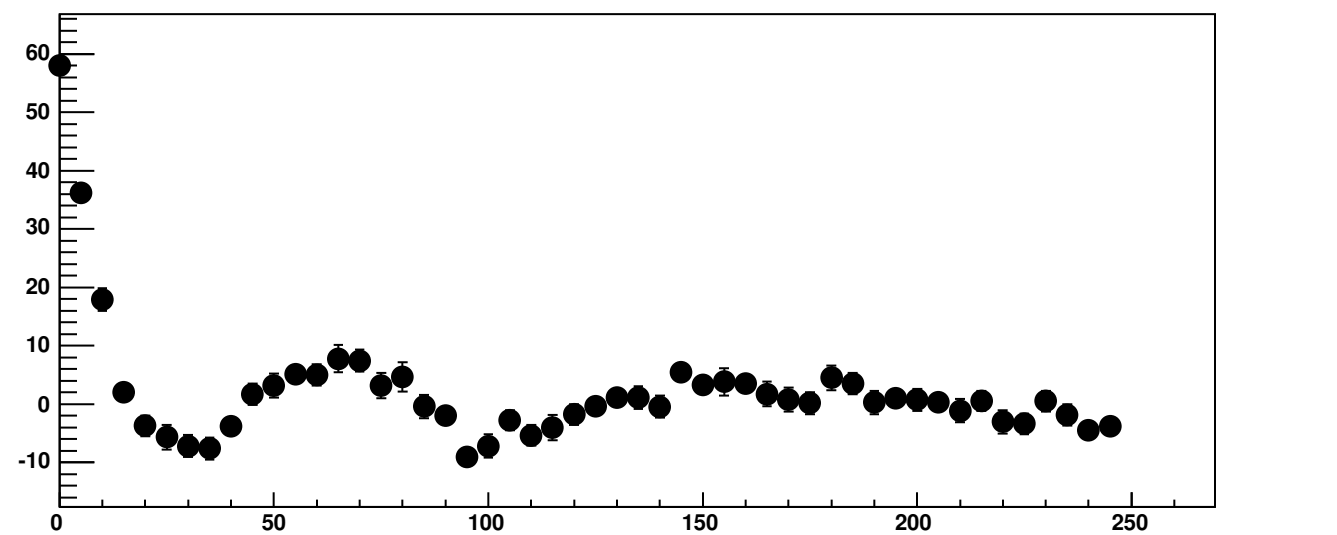
Chip 3, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold



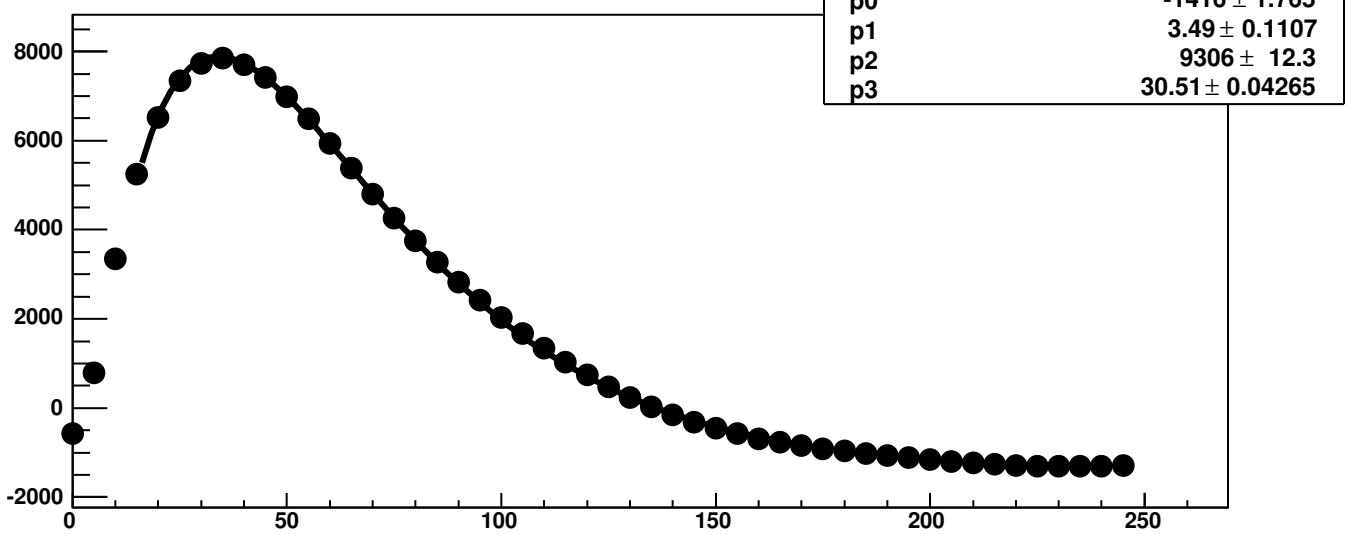
Chip 3, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



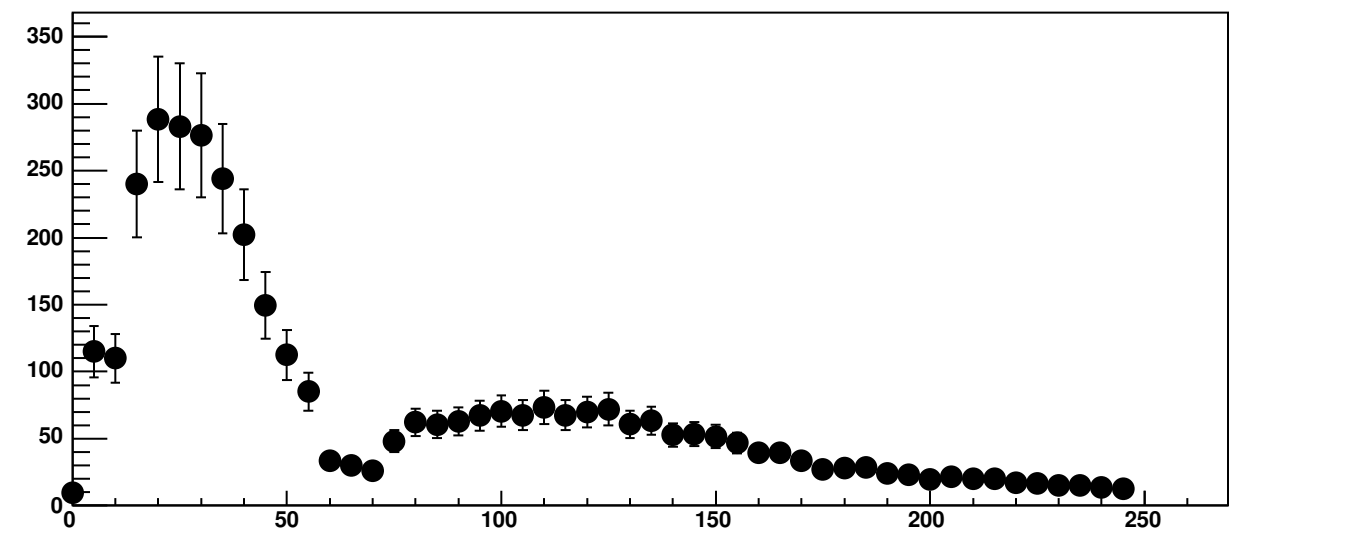
Chip 3, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold



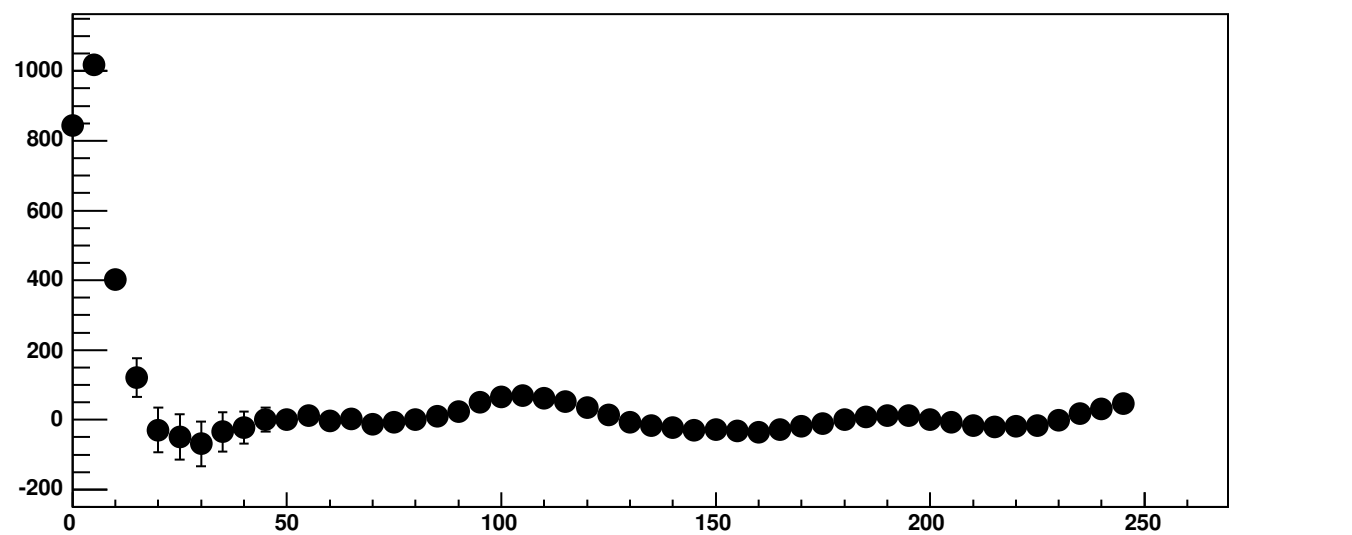
Chip 3, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold

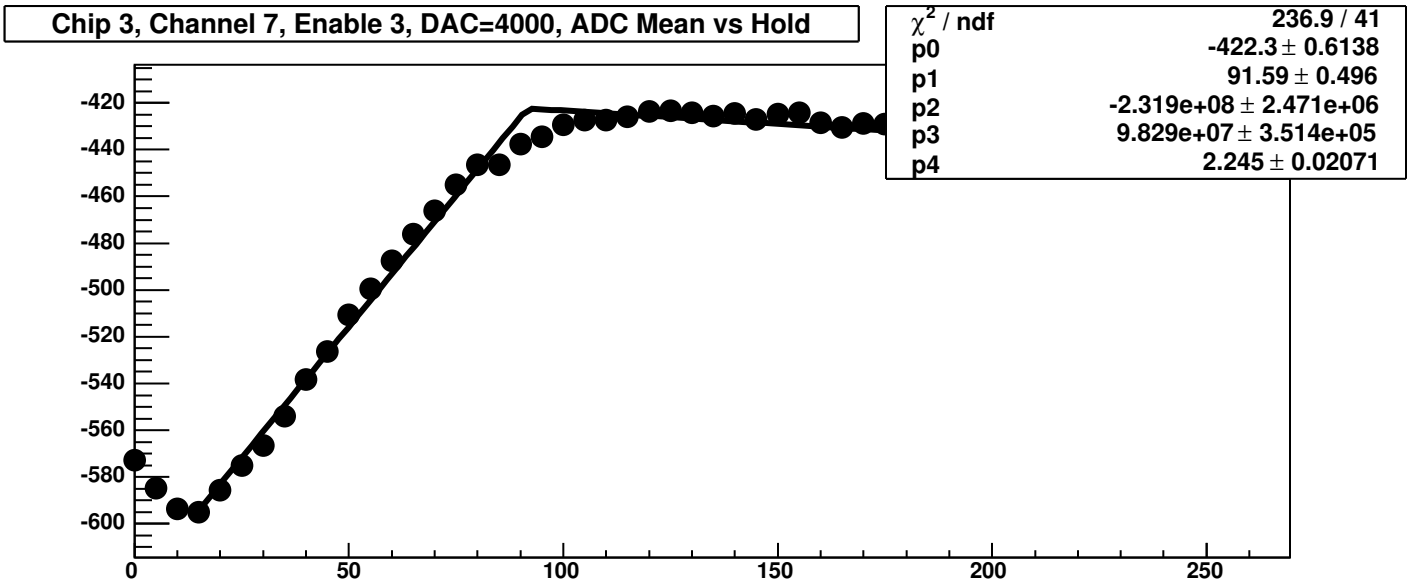


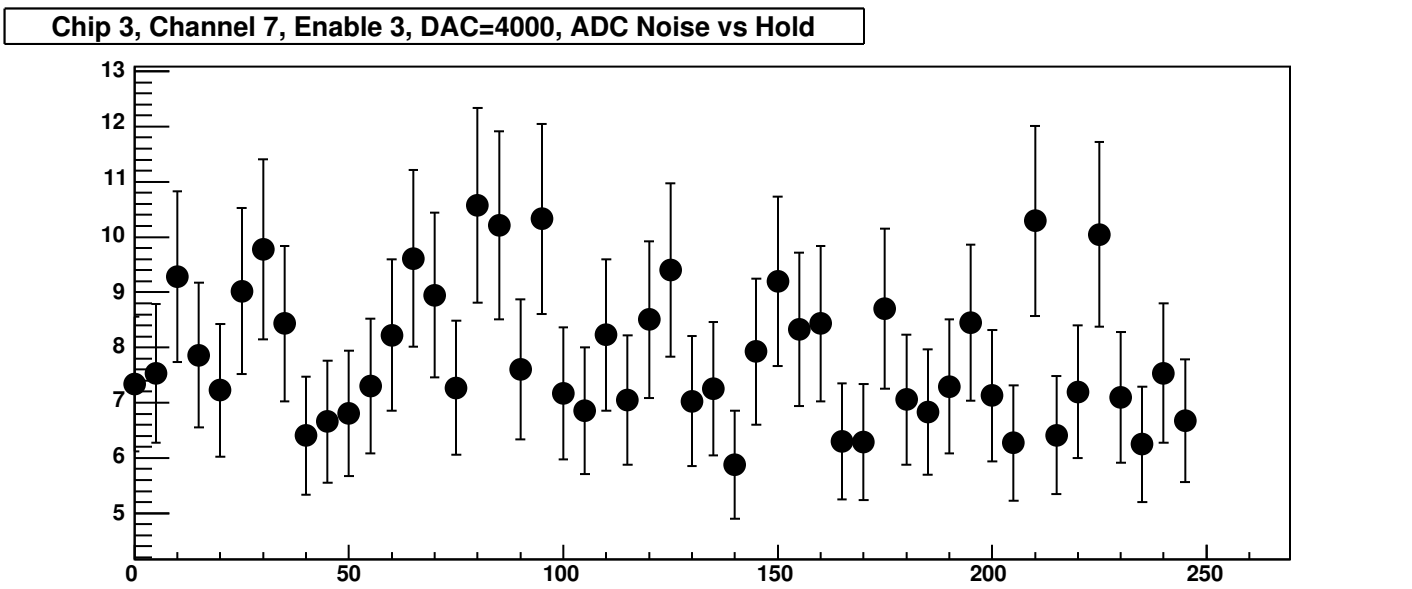
Chip 3, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold

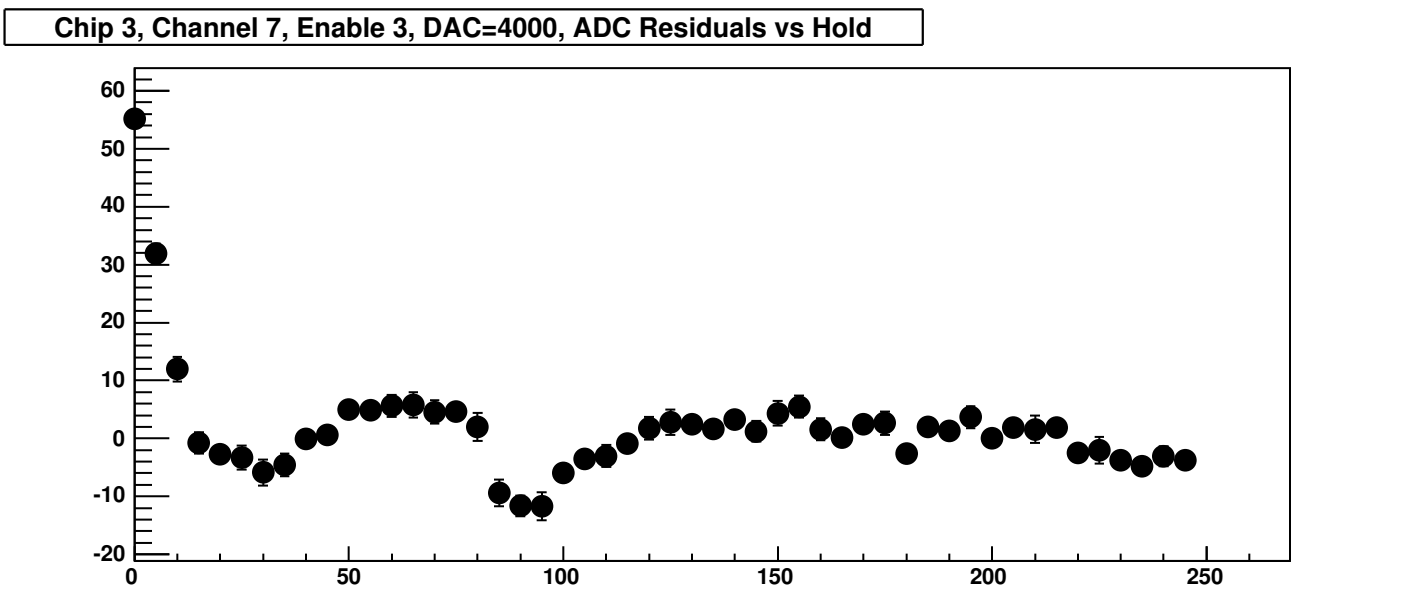


Chip 3, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold

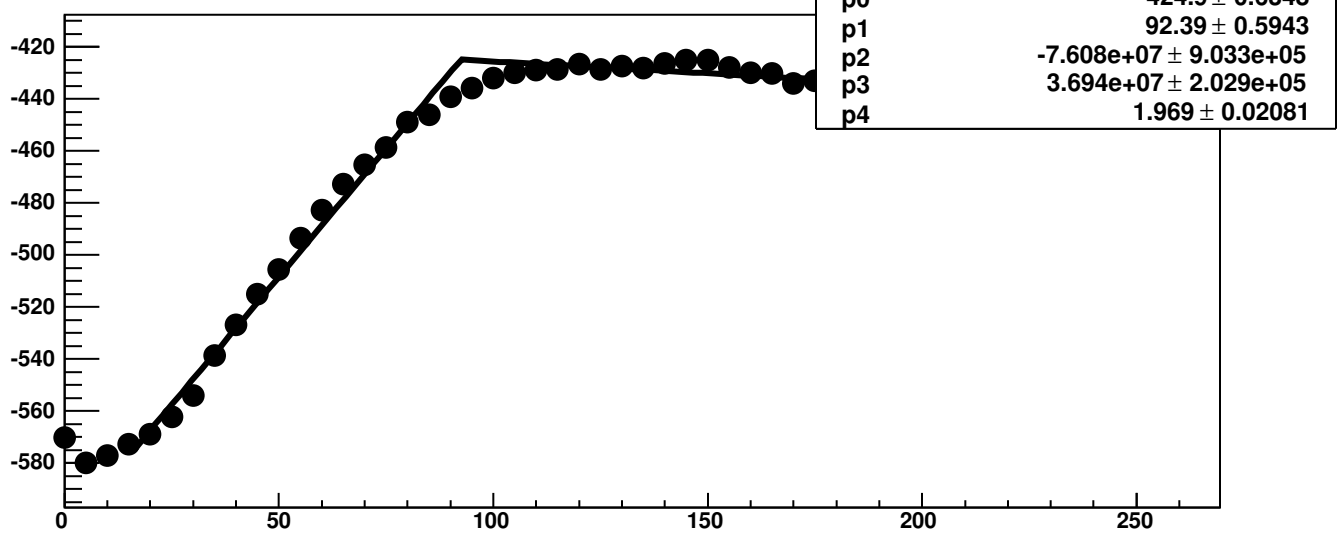



Chip 3, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 236.9 / 41
p0 -422.3 ± 0.6138
p1 91.59 ± 0.496
p2 -2.319e+08 ± 2.471e+06
p3 9.829e+07 ± 3.514e+05
p4 2.245 ± 0.02071
-420
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
50
100
150
200
250

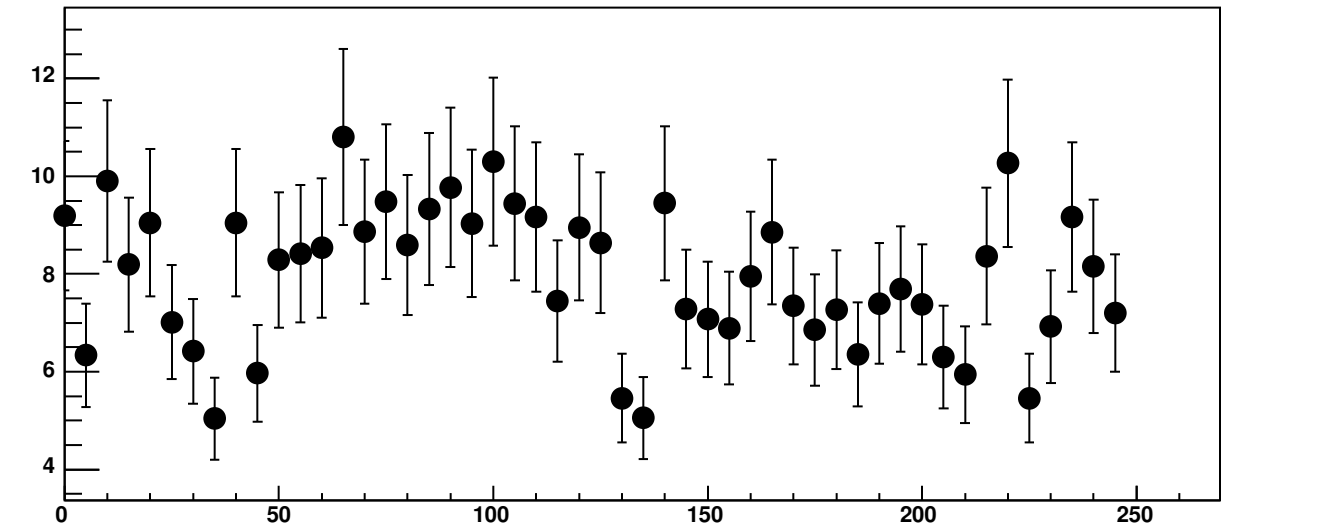

Chip 3, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
0
50
100
150
200
250


Chip 3, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold
60
50
40
30
20
10
0
-10
-20
0
50
100
150
200
250

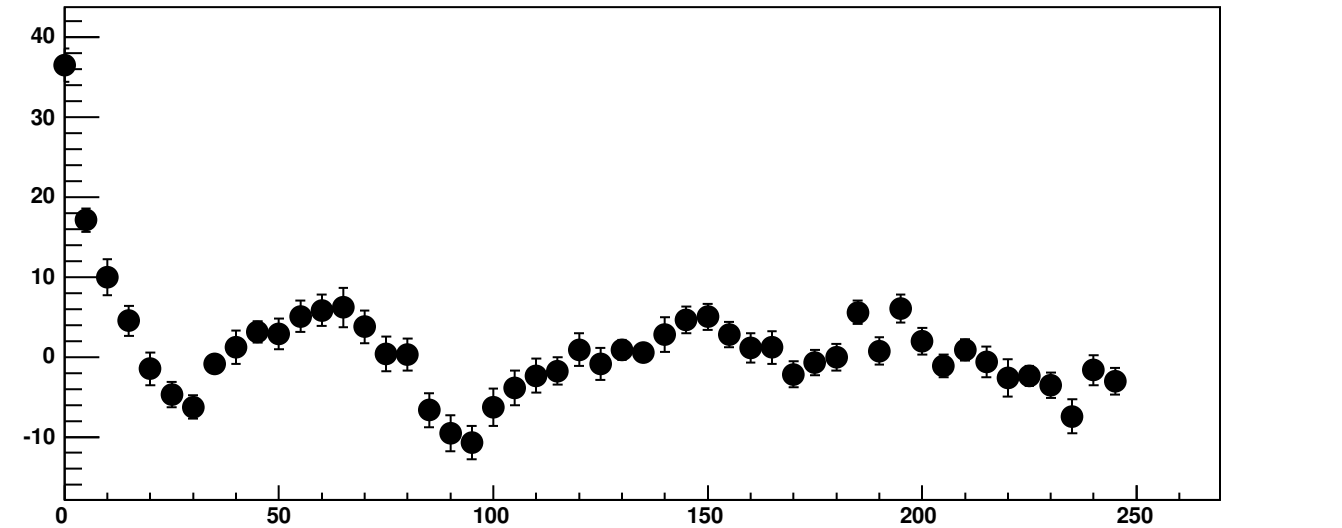
Chip 3, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

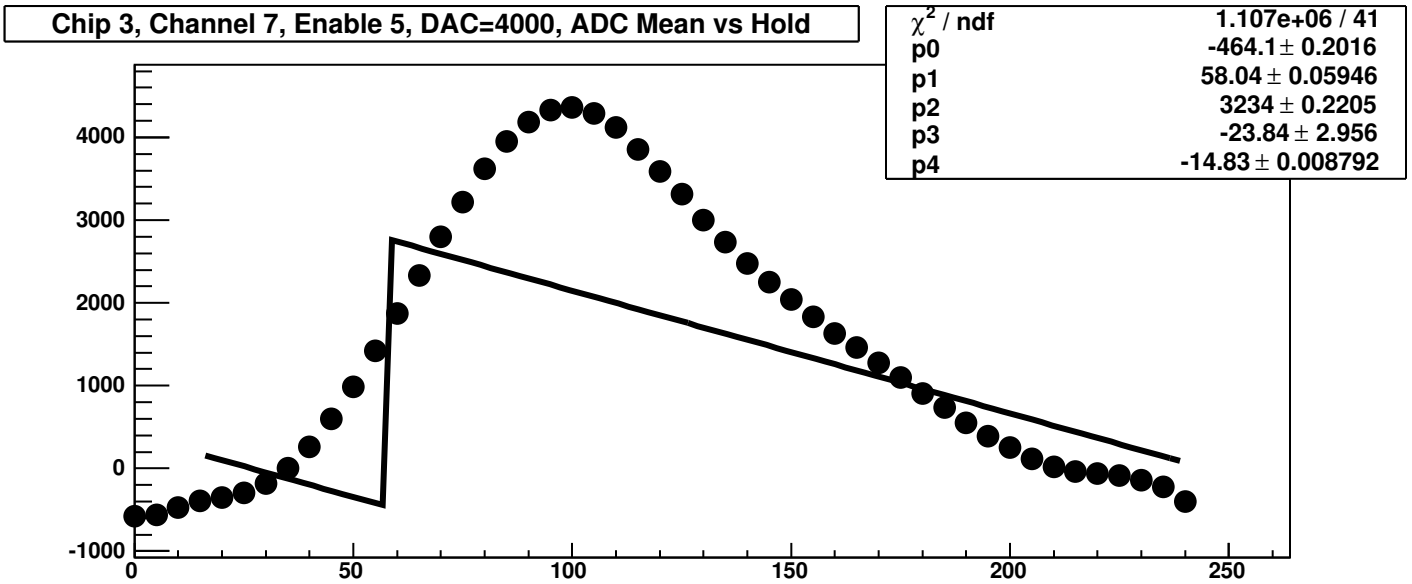


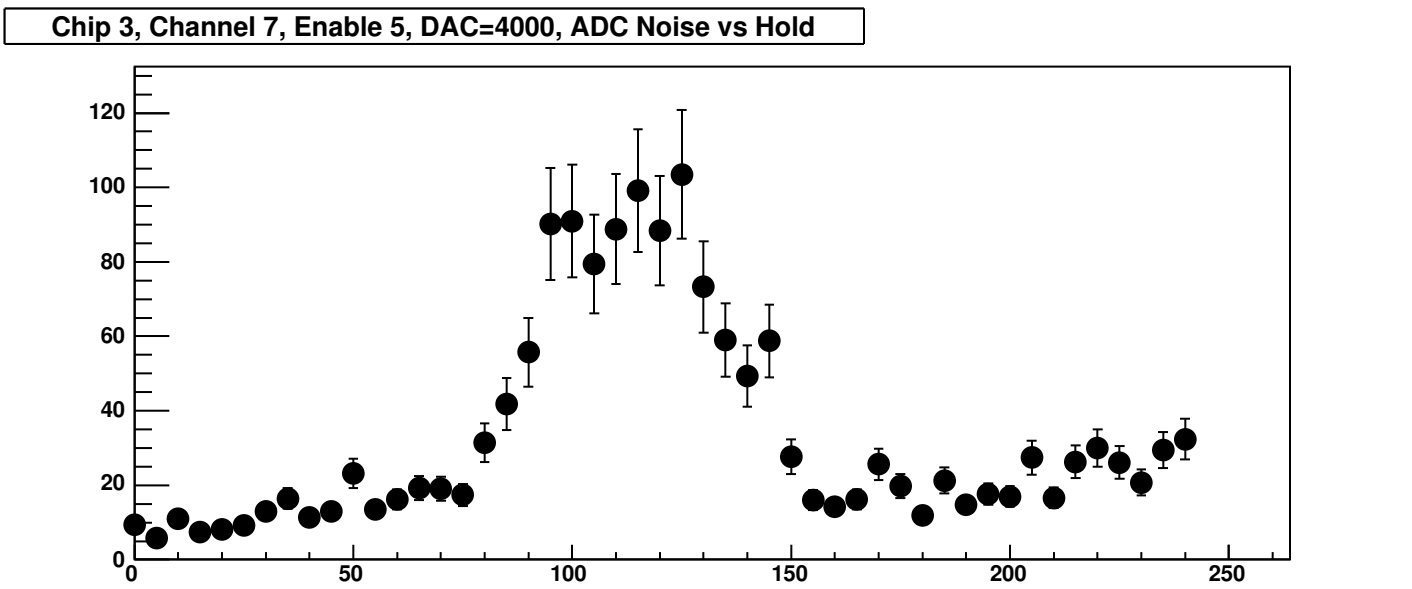
Chip 3, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold

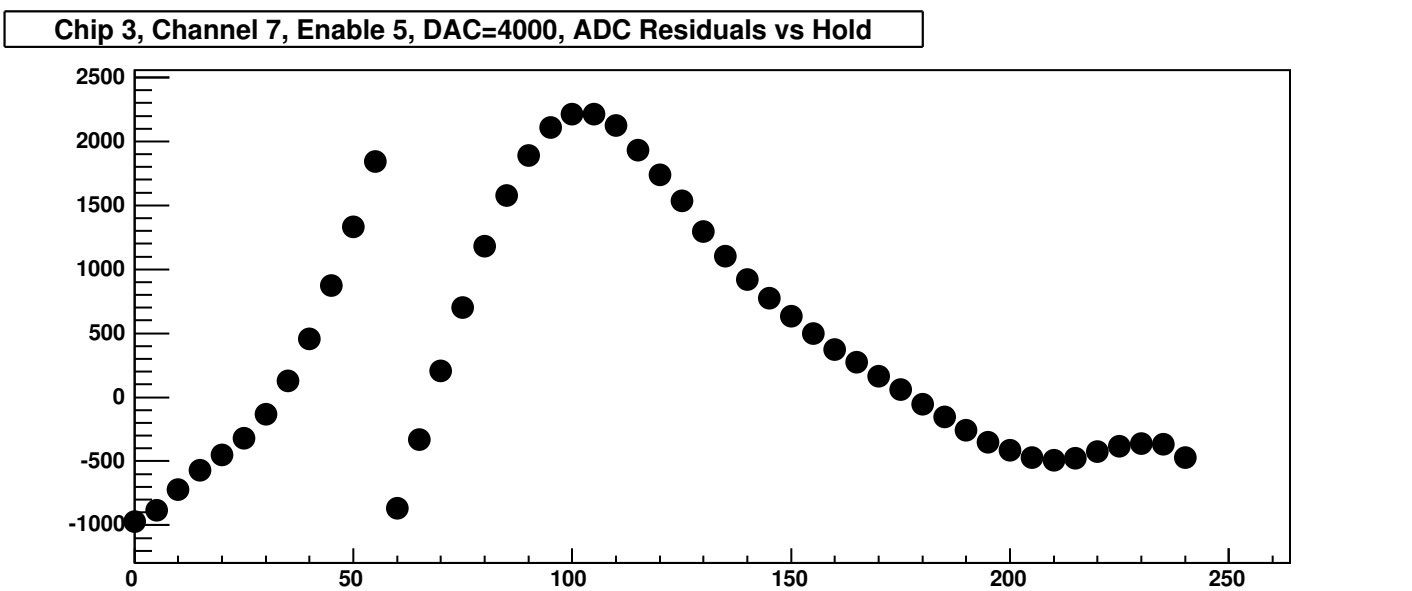


Chip 3, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

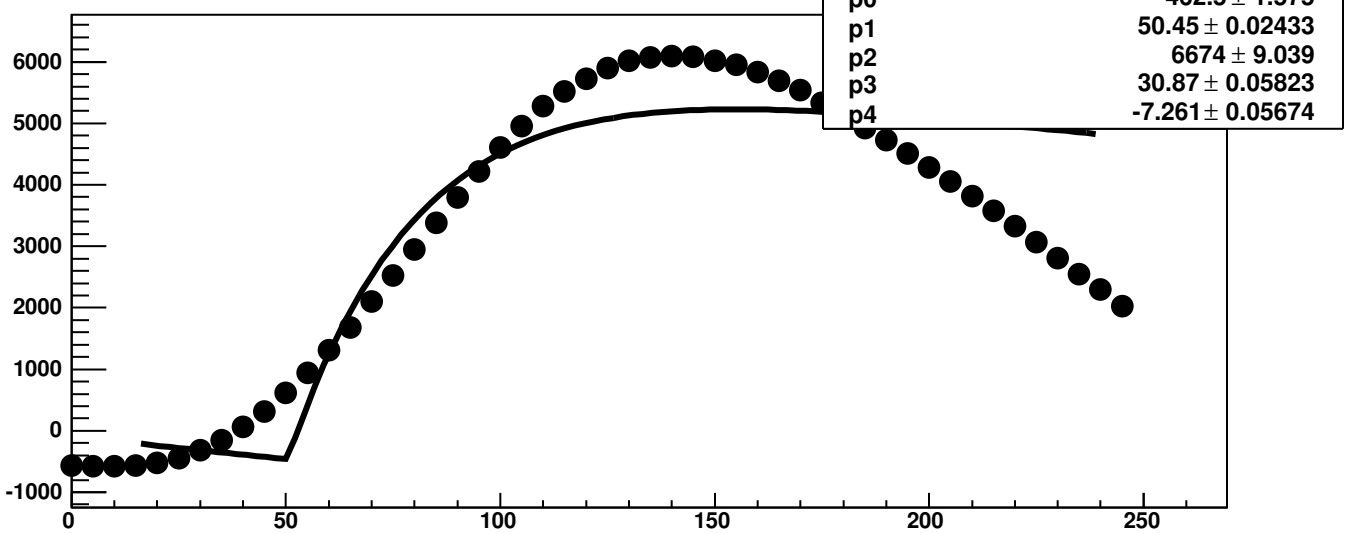



Chip 3, Channel 7, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.107e+06 / 41
p0 -464.1 ± 0.2016
p1 58.04 ± 0.05946
p2 3234 ± 0.2205
p3 -23.84 ± 2.956
p4 -14.83 ± 0.008792
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

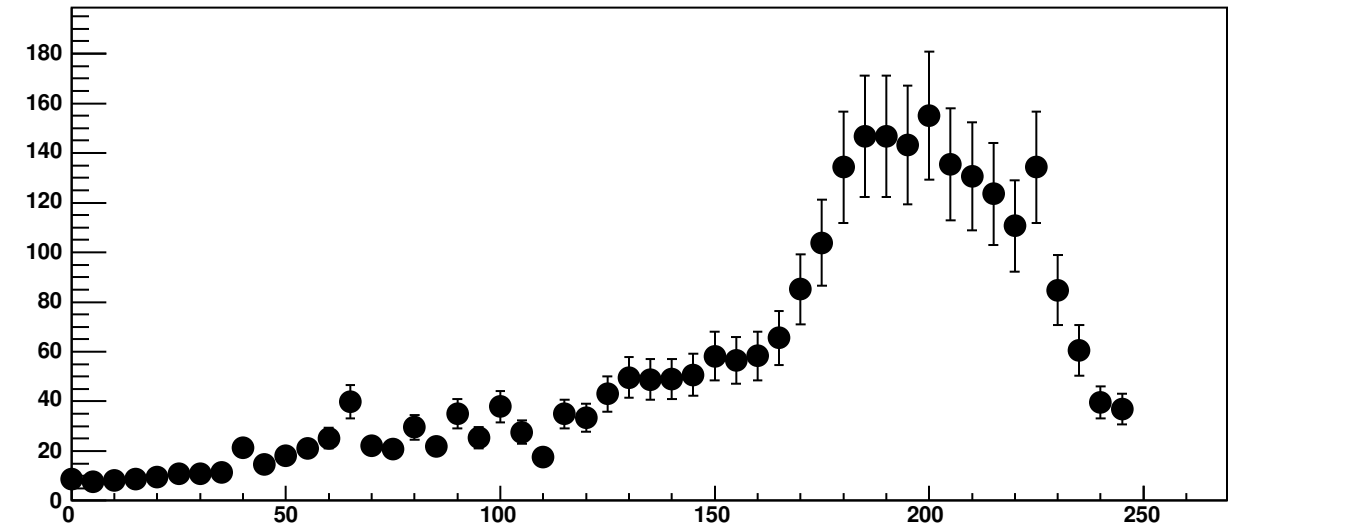

Chip 3, Channel 7, Enable 5, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 3, Channel 7, Enable 5, DAC=4000, ADC Residuals vs Hold
2500
2000
1500
1000
500
0
-500
-1000
0
50
100
150
200
250

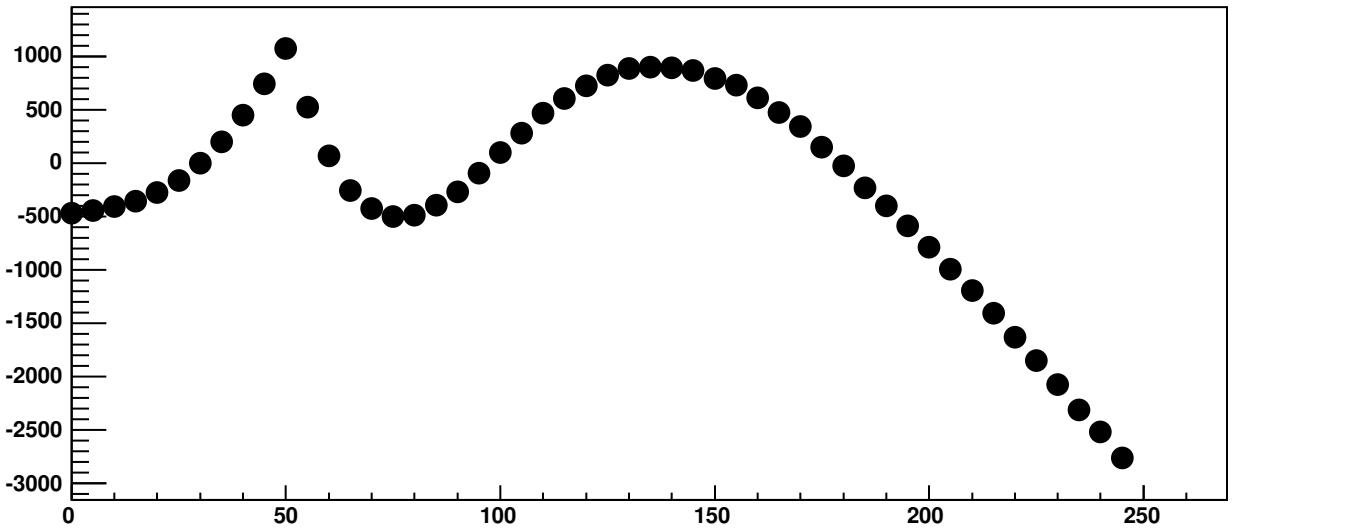
Chip 3, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

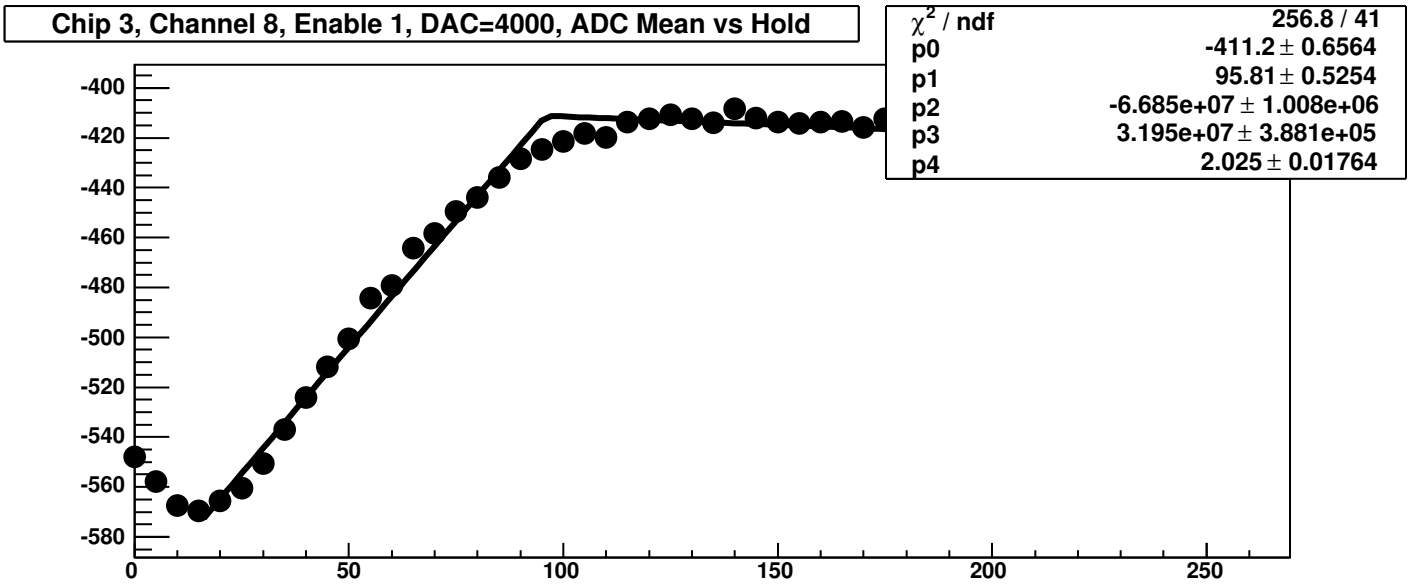


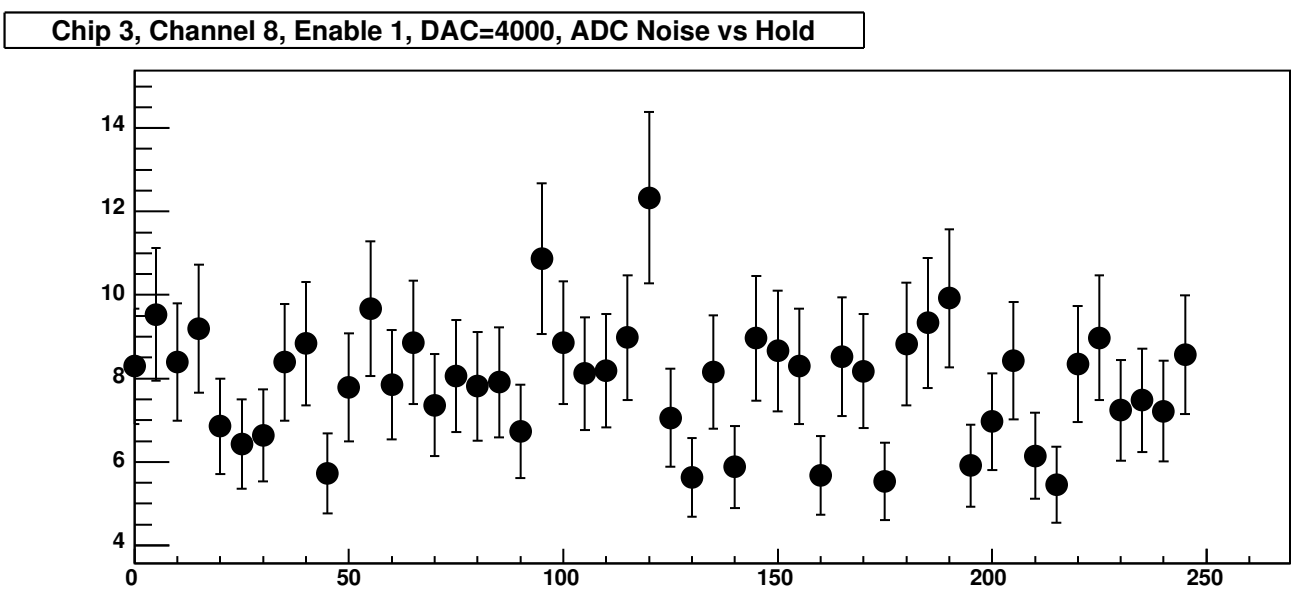
Chip 3, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold

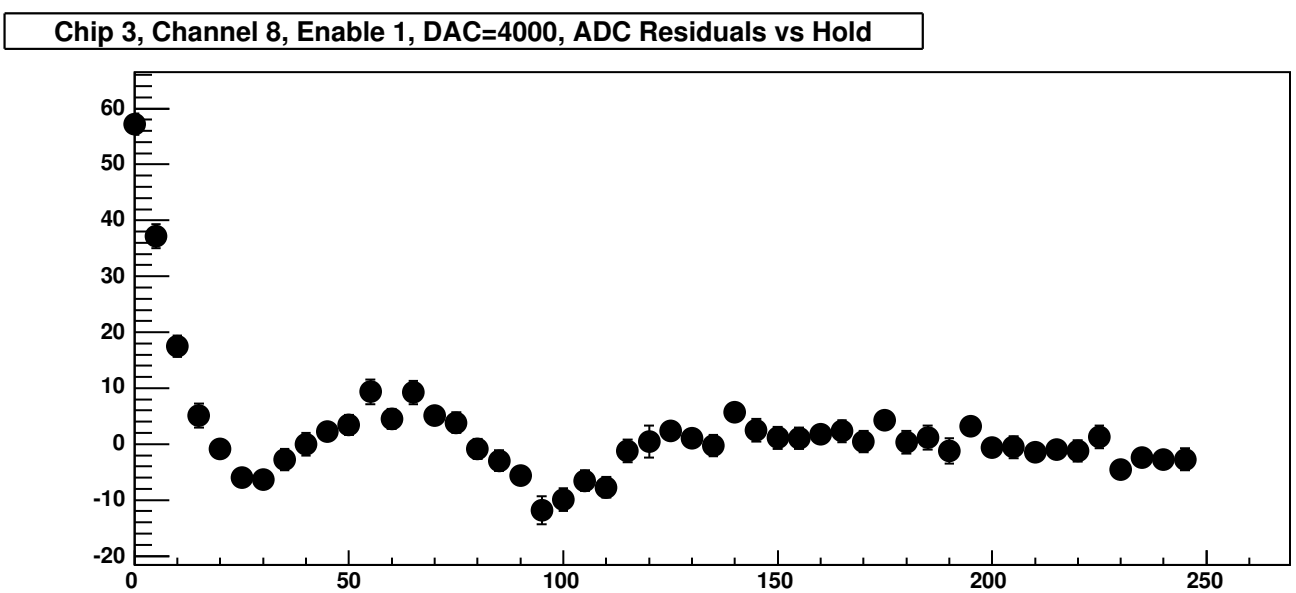


Chip 3, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

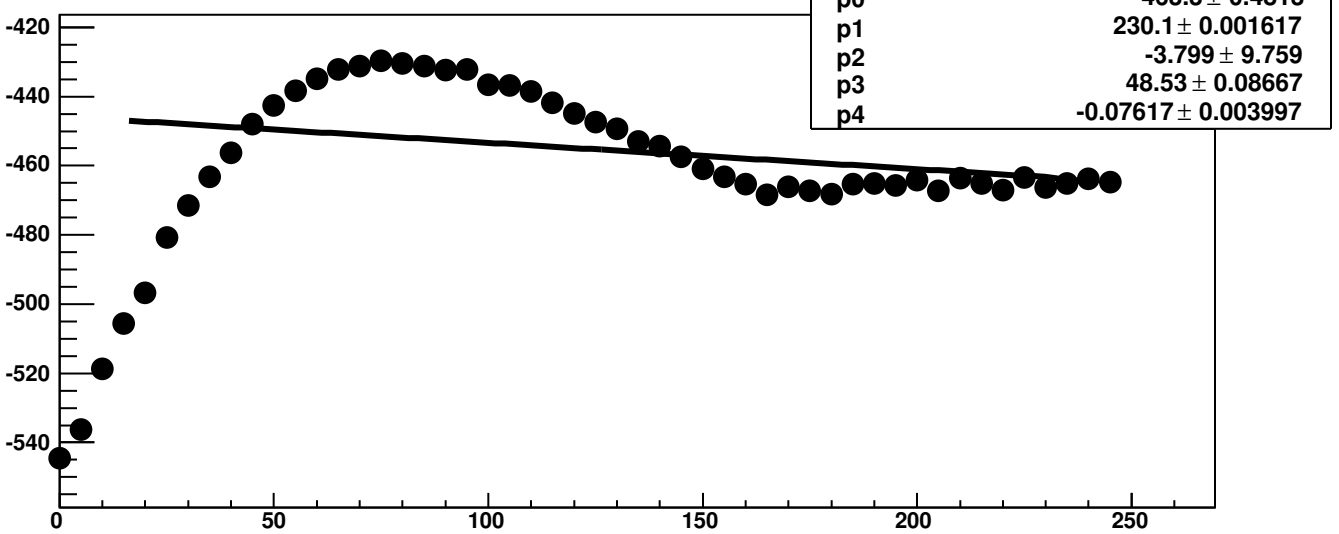



Chip 3, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 256.8 / 41
p0 -411.2 ± 0.6564
p1 95.81 ± 0.5254
p2 -6.685e+07 ± 1.008e+06
p3 3.195e+07 ± 3.881e+05
p4 2.025 ± 0.01764
-400
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
50
100
150
200
250

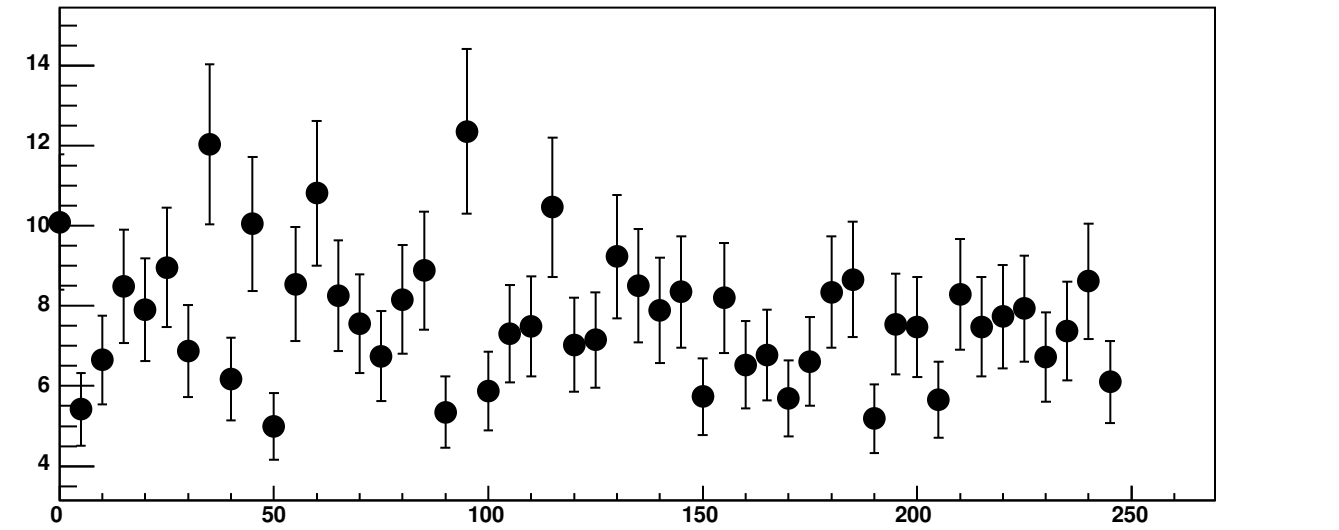

Chip 3, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 3, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold
60
50
40
30
20
10
0
-10
-20
0
50
100
150
200
250

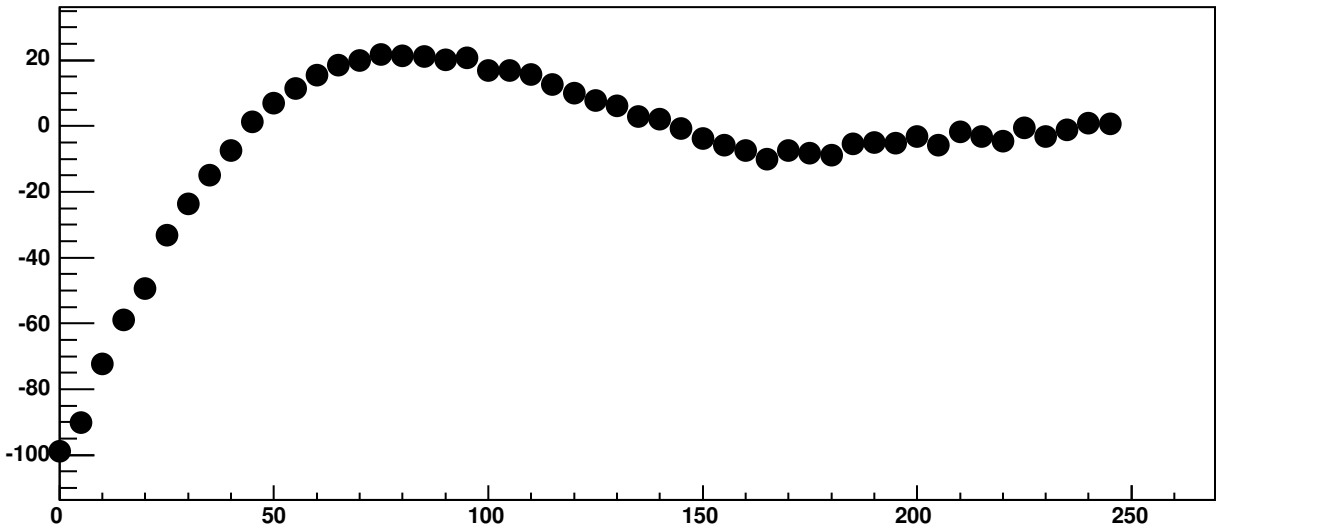
Chip 3, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold

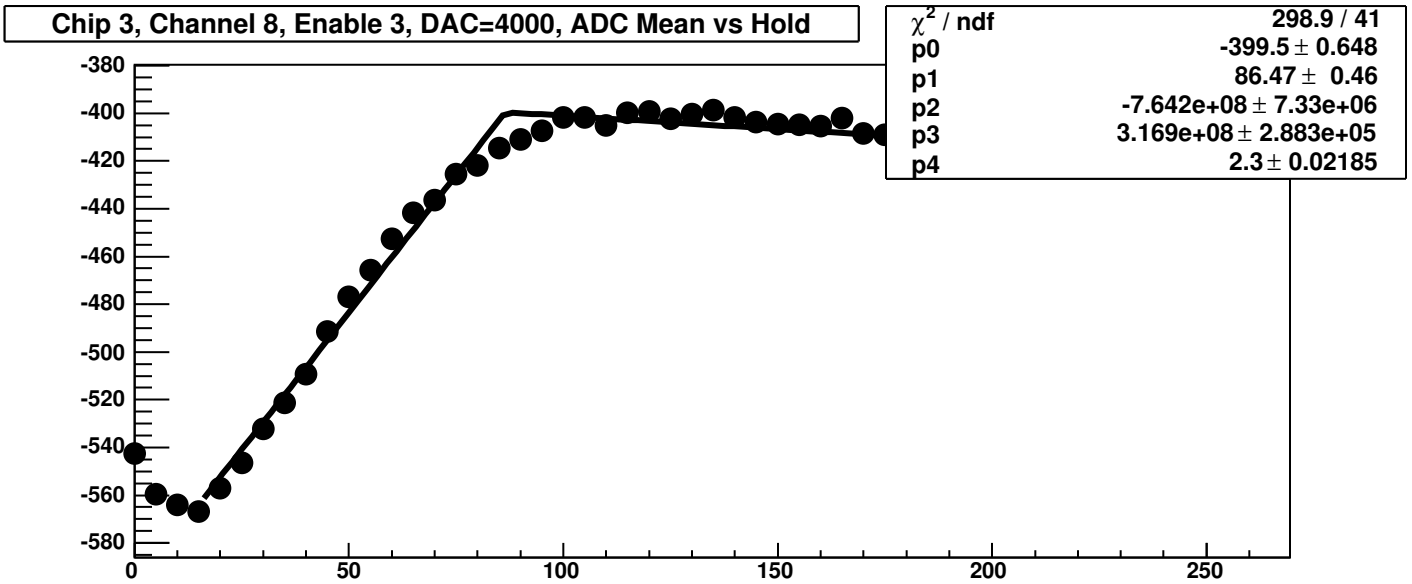


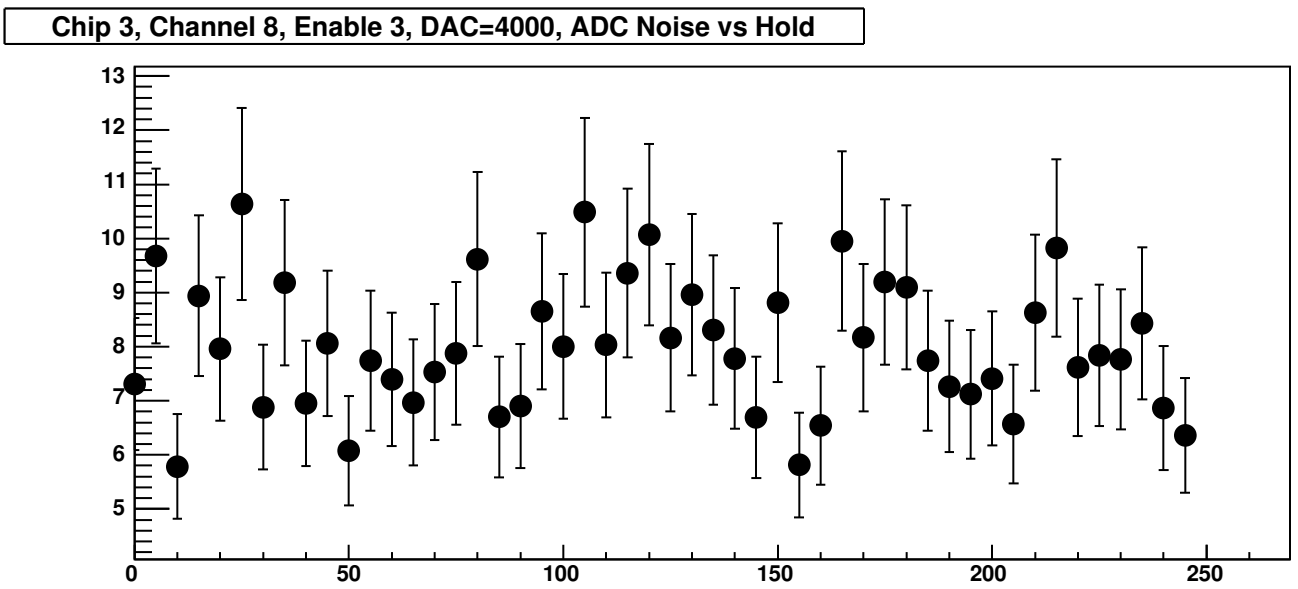
Chip 3, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold

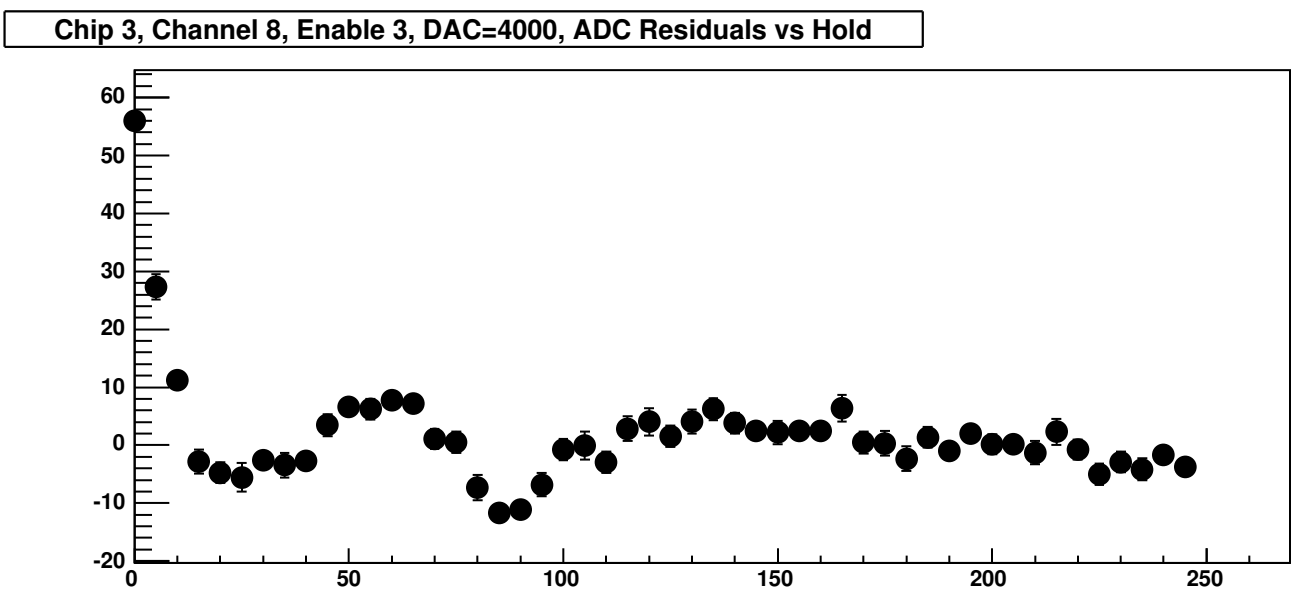


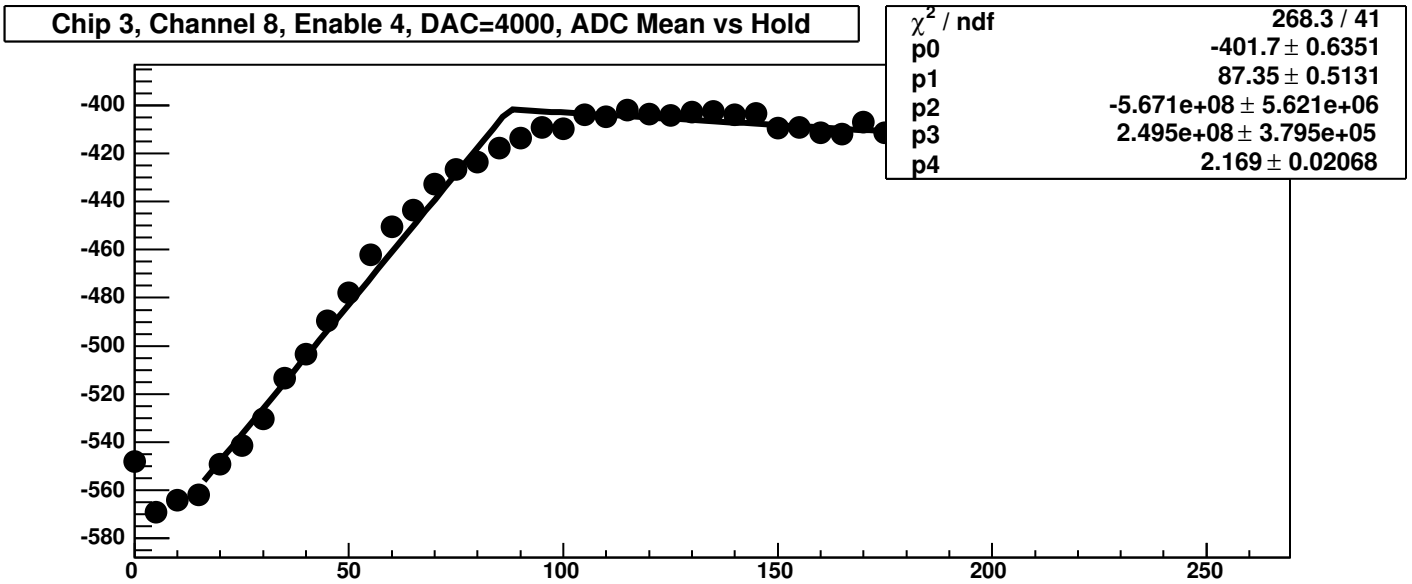
Chip 3, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold

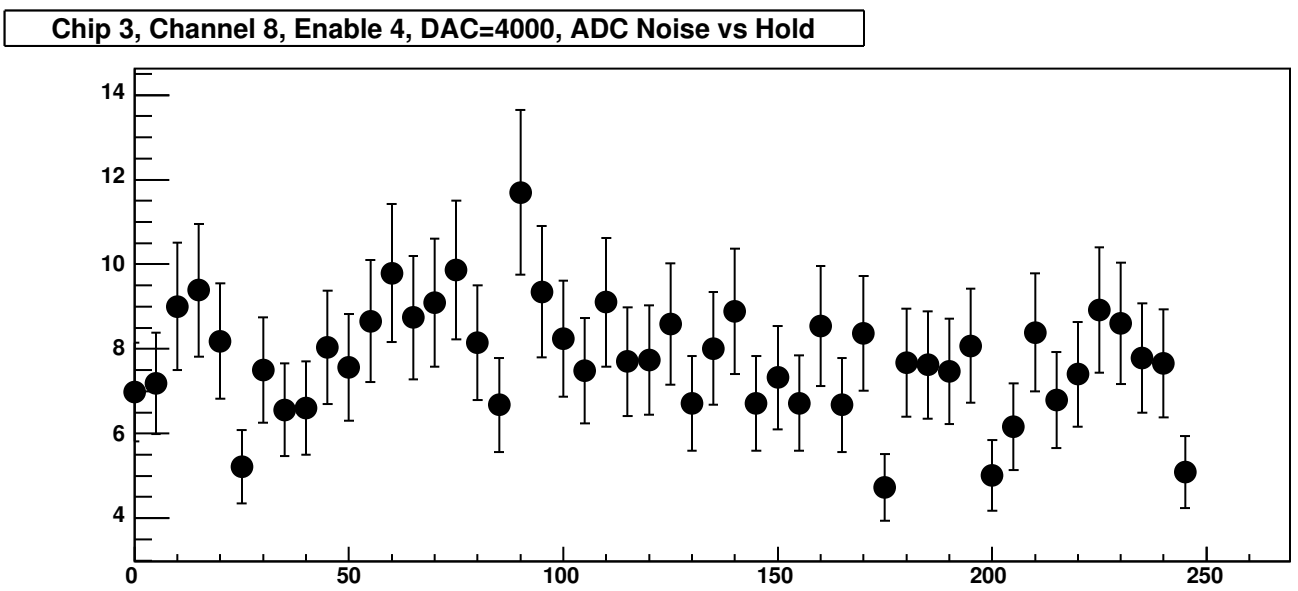


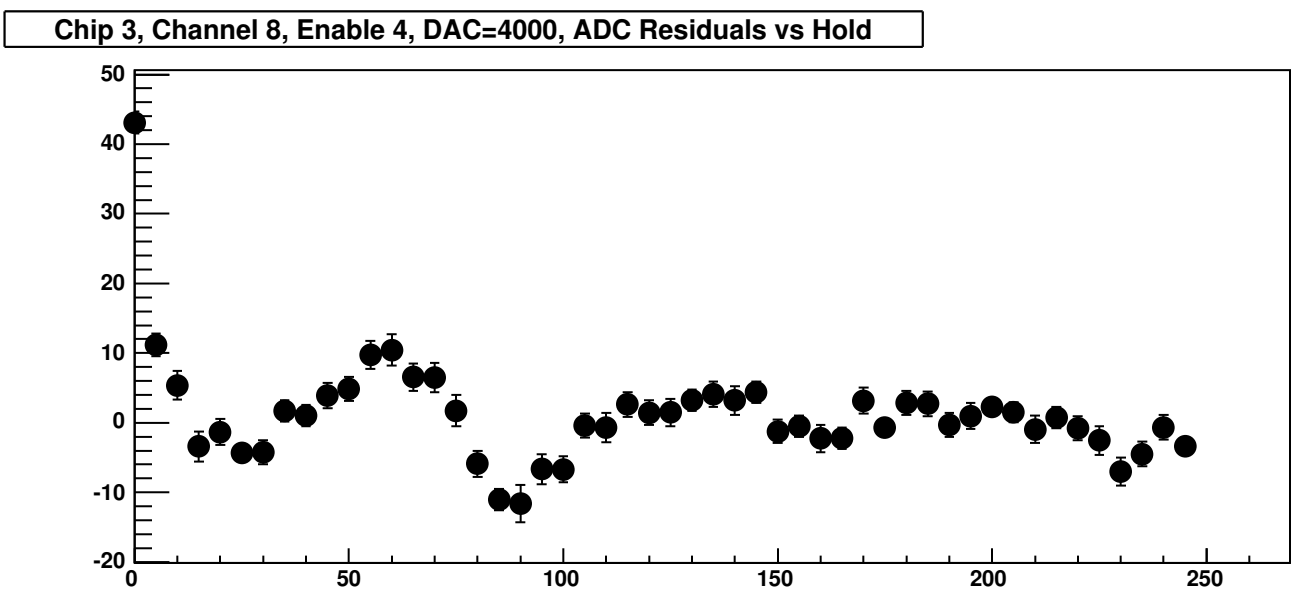

Chip 3, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 298.9 / 41
p0 -399.5 ± 0.648
p1 86.47 ± 0.46
p2 -7.642e+08 ± 7.33e+06
p3 3.169e+08 ± 2.883e+05
p4 2.3 ± 0.02185
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
50
100
150
200
250


Chip 3, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
0
50
100
150
200
250


Chip 3, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold
60
50
40
30
20
10
0
-10
-20
0
50
100
150
200
250

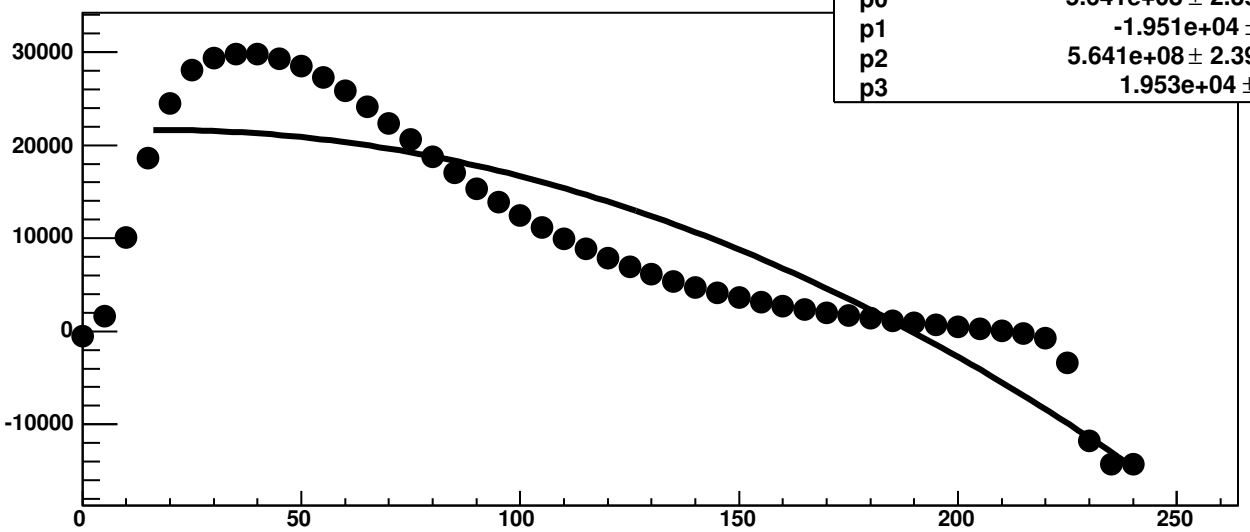

Chip 3, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 268.3 / 41
p0 -401.7 ± 0.6351
p1 87.35 ± 0.5131
p2 -5.671e+08 ± 5.621e+06
p3 2.495e+08 ± 3.795e+05
p4 2.169 ± 0.02068
-400
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
50
100
150
200
250


Chip 3, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

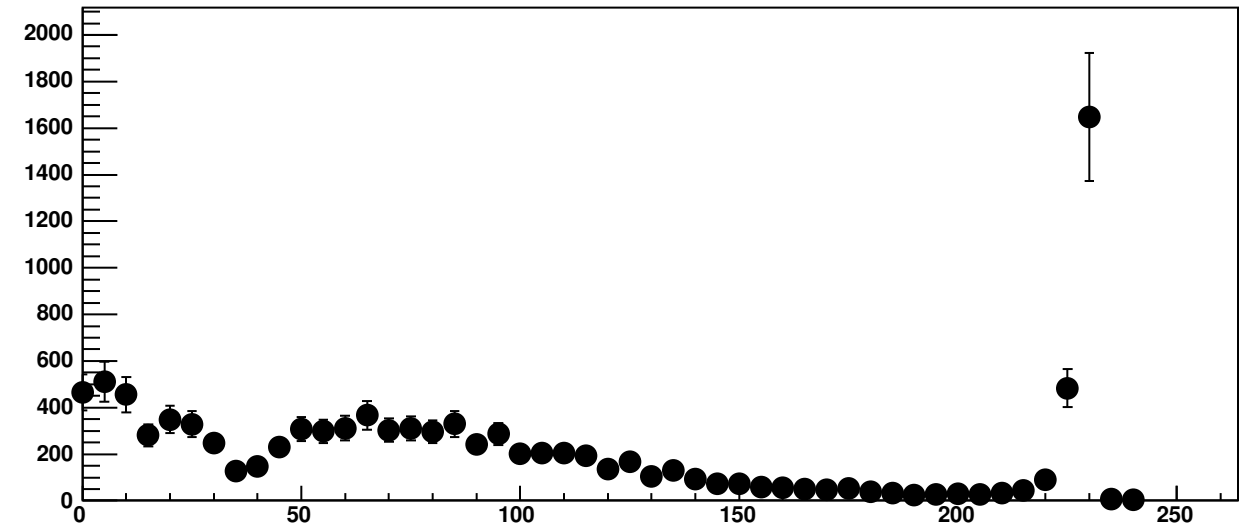

Chip 3, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
-20
0
50
100
150
200
250

Chip 3, Channel 8, Enable 5!, DAC=4000, ADC Mean vs Hold

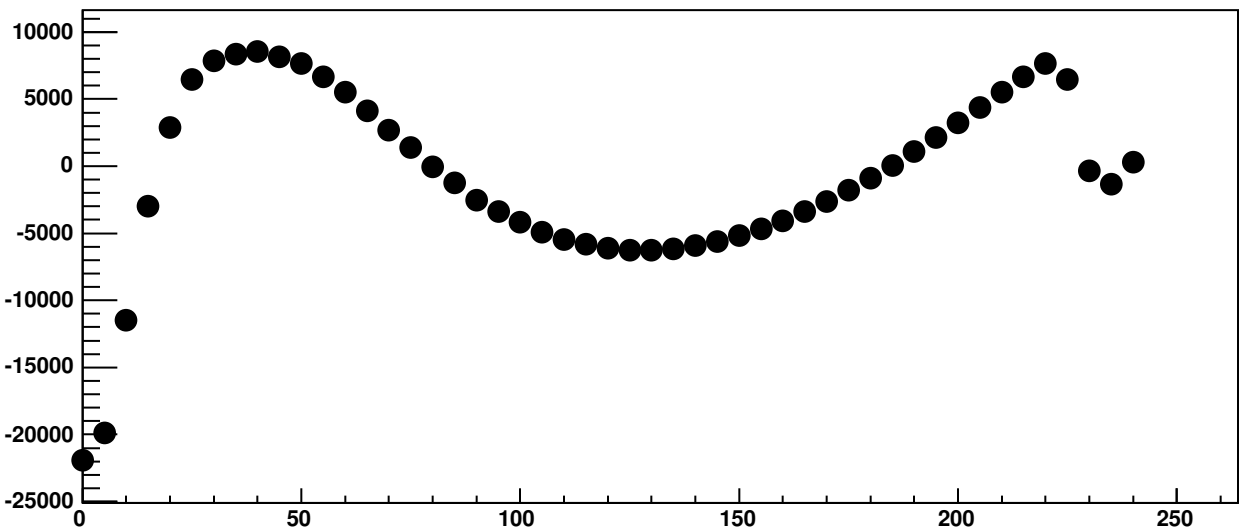
χ^2 / ndf	3.947e+06 / 42
p0	-5.641e+08 ± 2.396e+06
p1	-1.951e+04 ± 43.51
p2	5.641e+08 ± 2.396e+06
p3	1.953e+04 ± 43.47

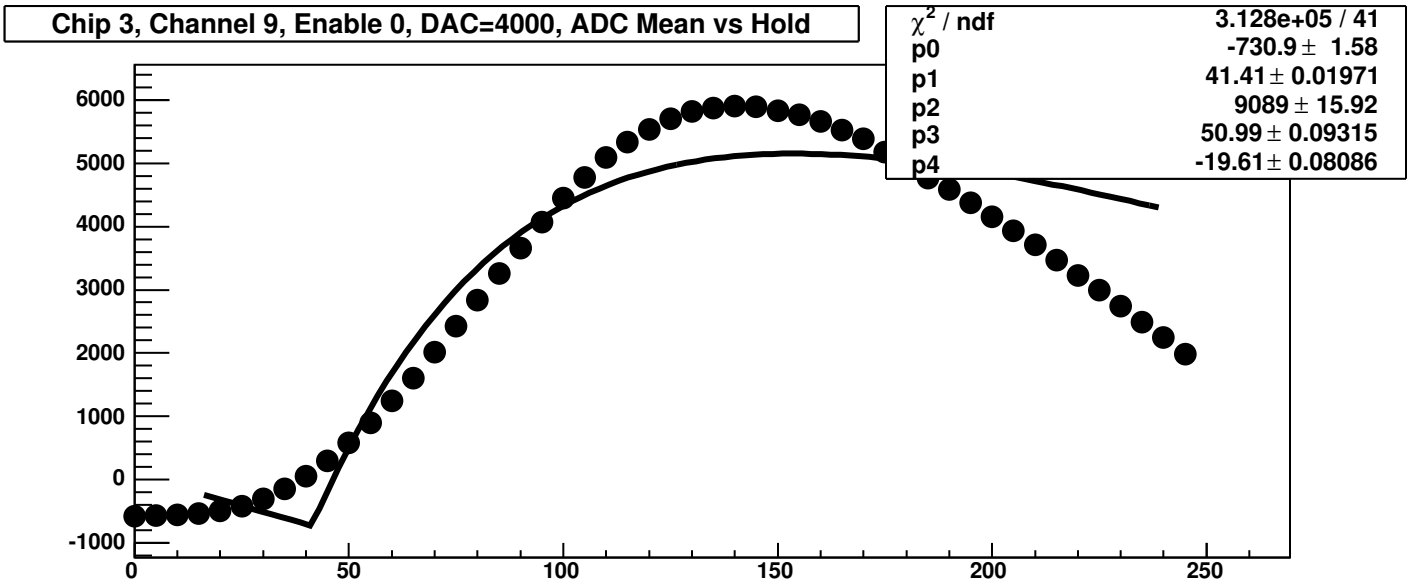


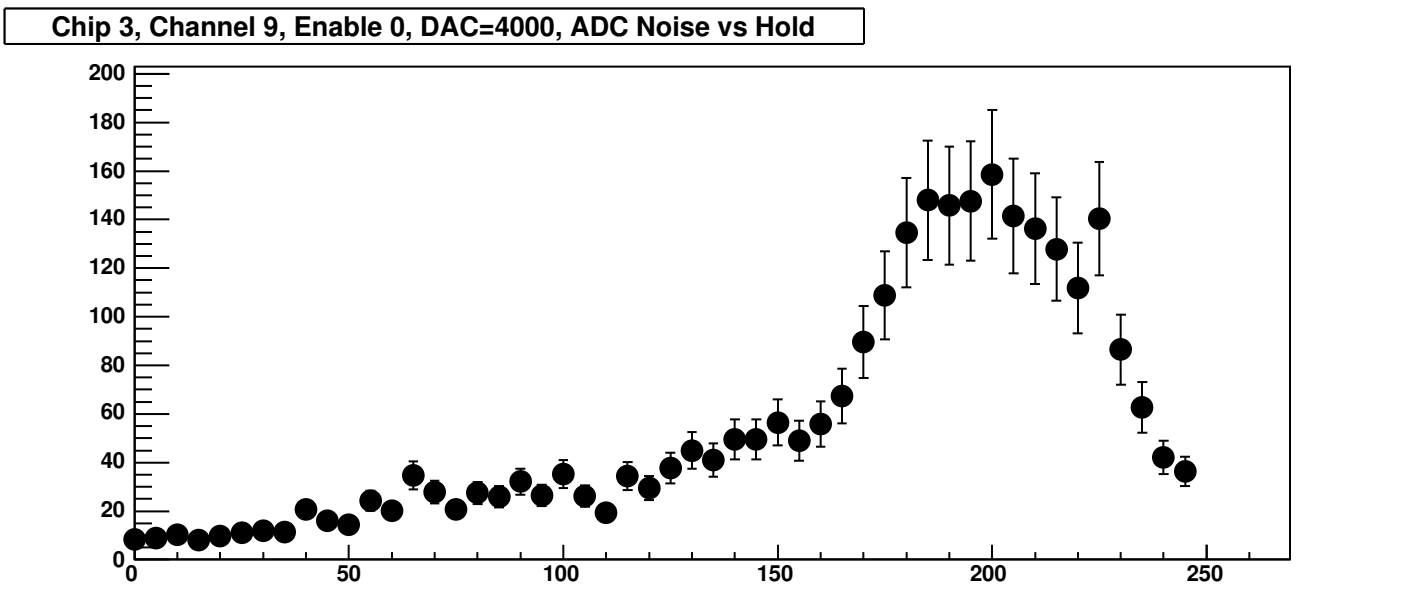
Chip 3, Channel 8, Enable 5!, DAC=4000, ADC Noise vs Hold

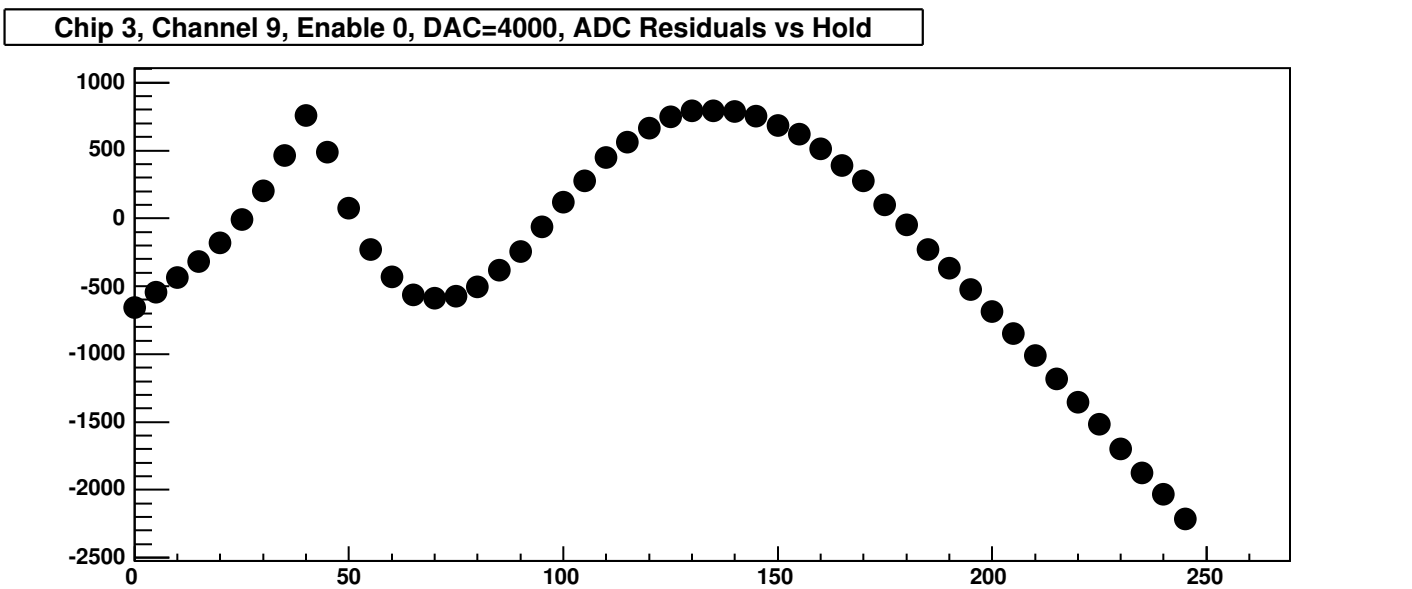


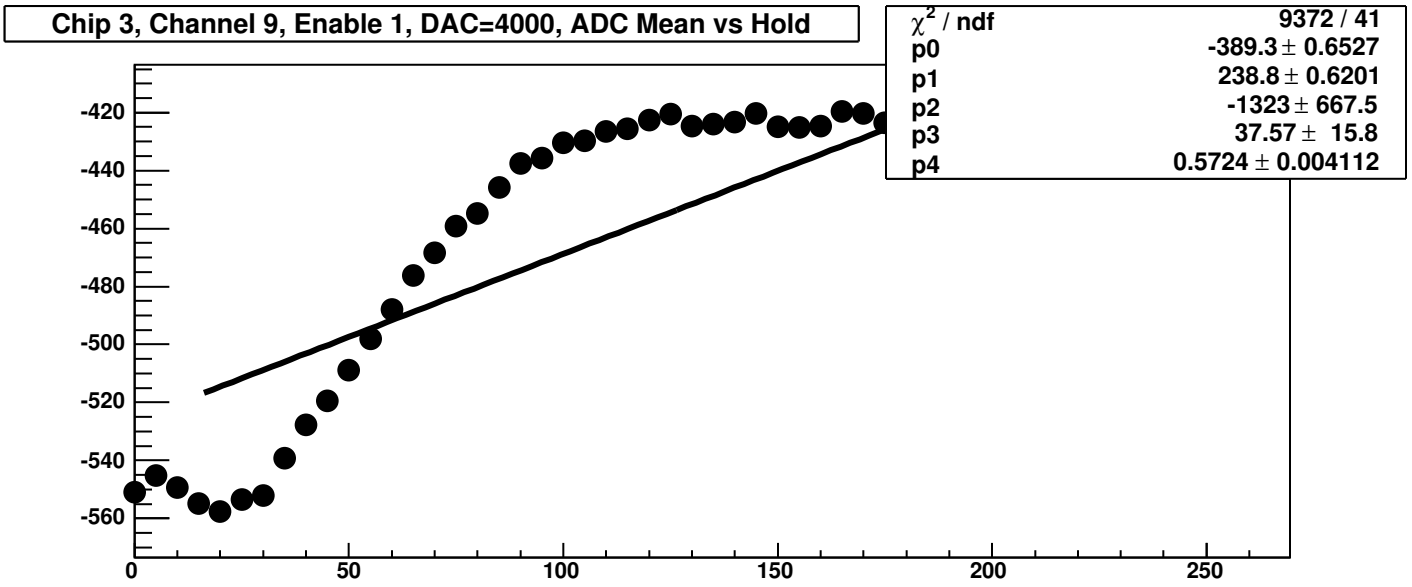
Chip 3, Channel 8, Enable 5!, DAC=4000, ADC Residuals vs Hold

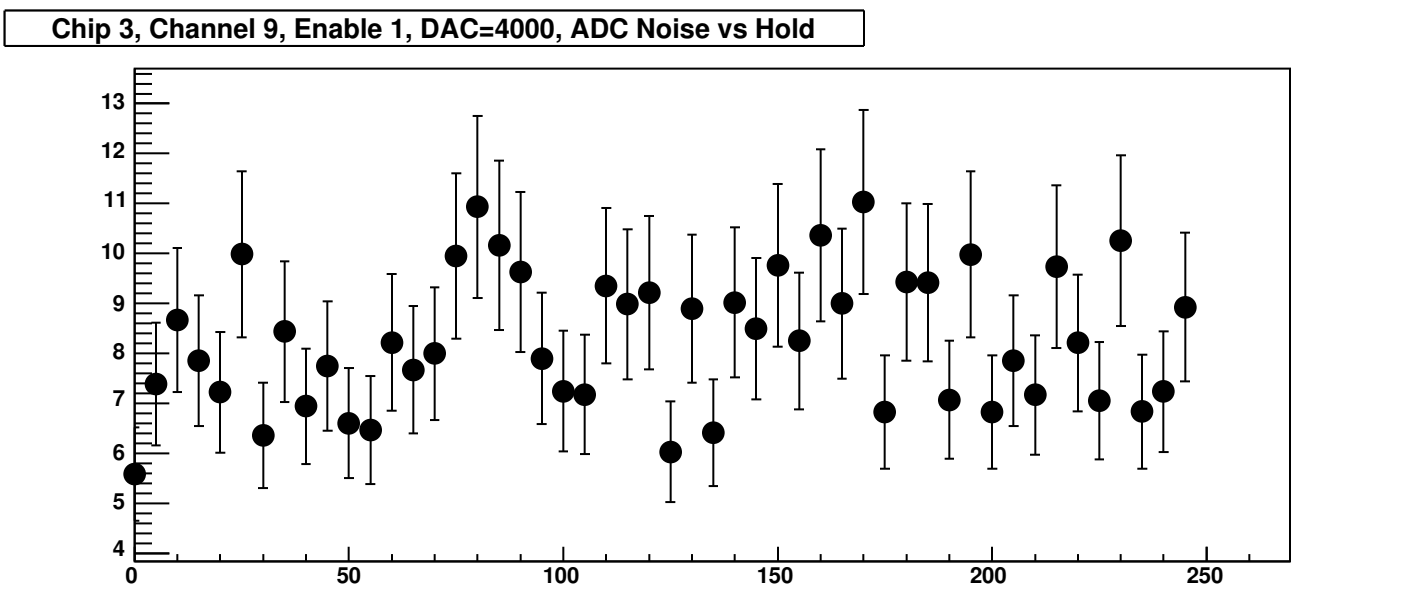


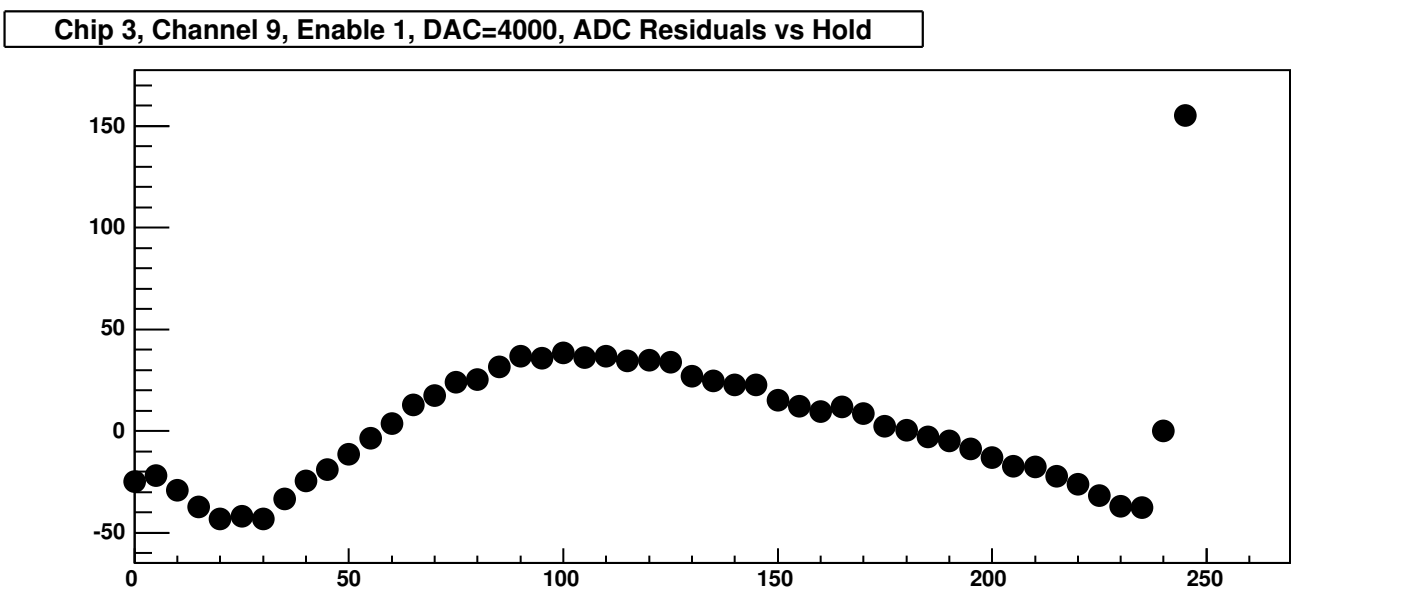

Chip 3, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.128e+05 / 41
p0 -730.9 ± 1.58
p1 41.41 ± 0.01971
p2 9089 ± 15.92
p3 50.99 ± 0.09315
p4 -19.61 ± 0.08086
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 3, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

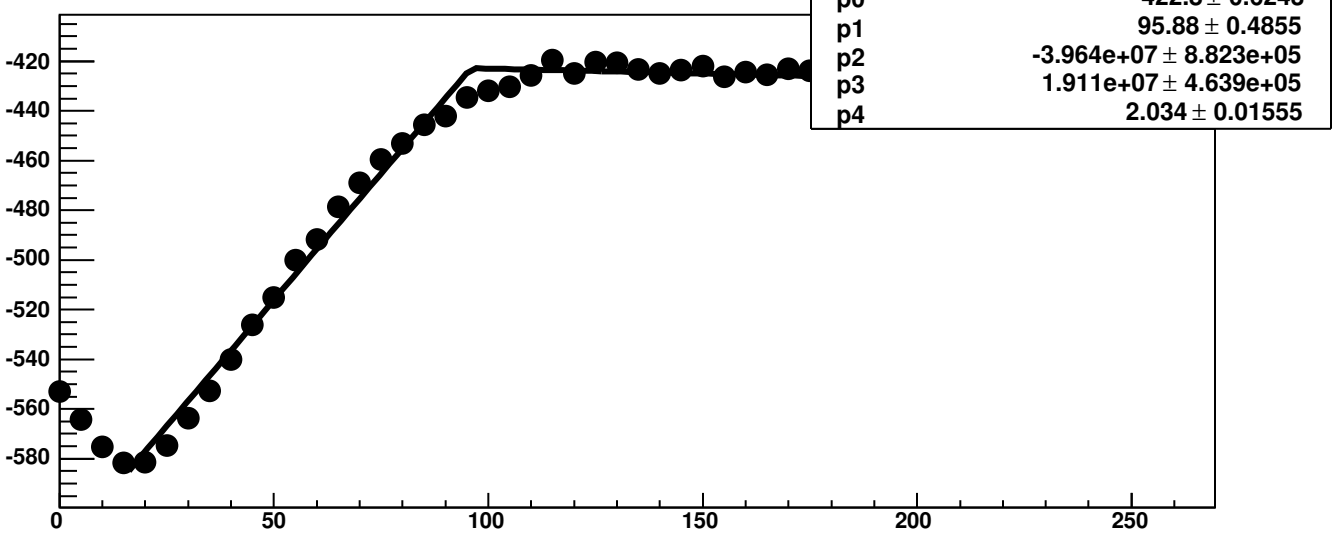

Chip 3, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
0
50
100
150
200
250


Chip 3, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 9372 / 41
p0 -389.3 ± 0.6527
p1 238.8 ± 0.6201
p2 -1323 ± 667.5
p3 37.57 ± 15.8
p4 0.5724 ± 0.004112
-420
-440
-460
-480
-500
-520
-540
-560
0
50
100
150
200
250

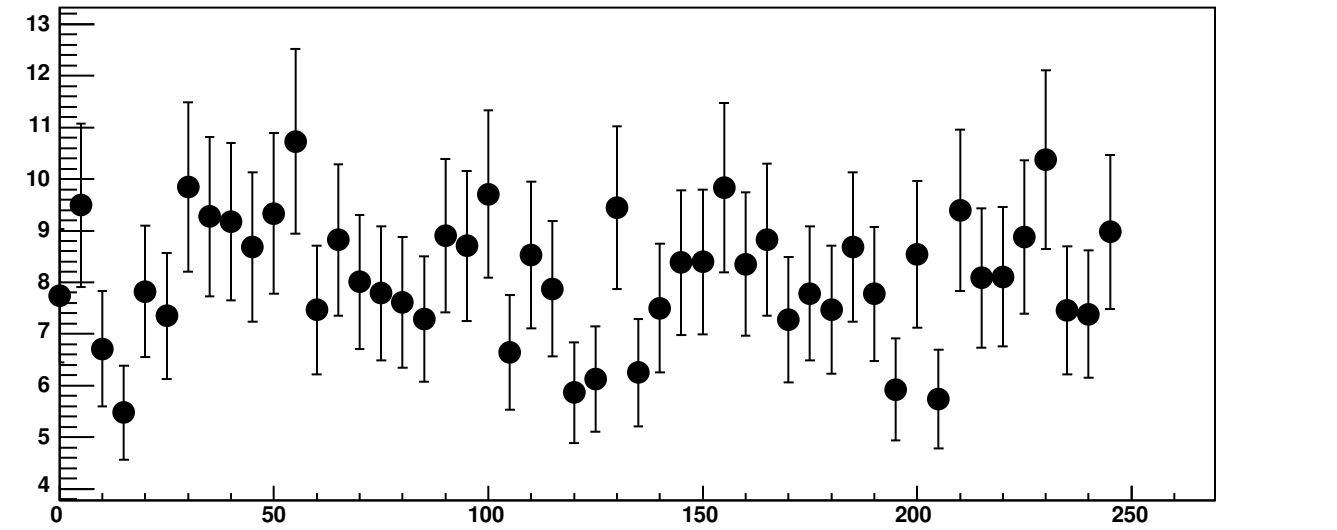

Chip 3, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 3, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold
150
100
50
0
-50
0
50
100
150
200
250

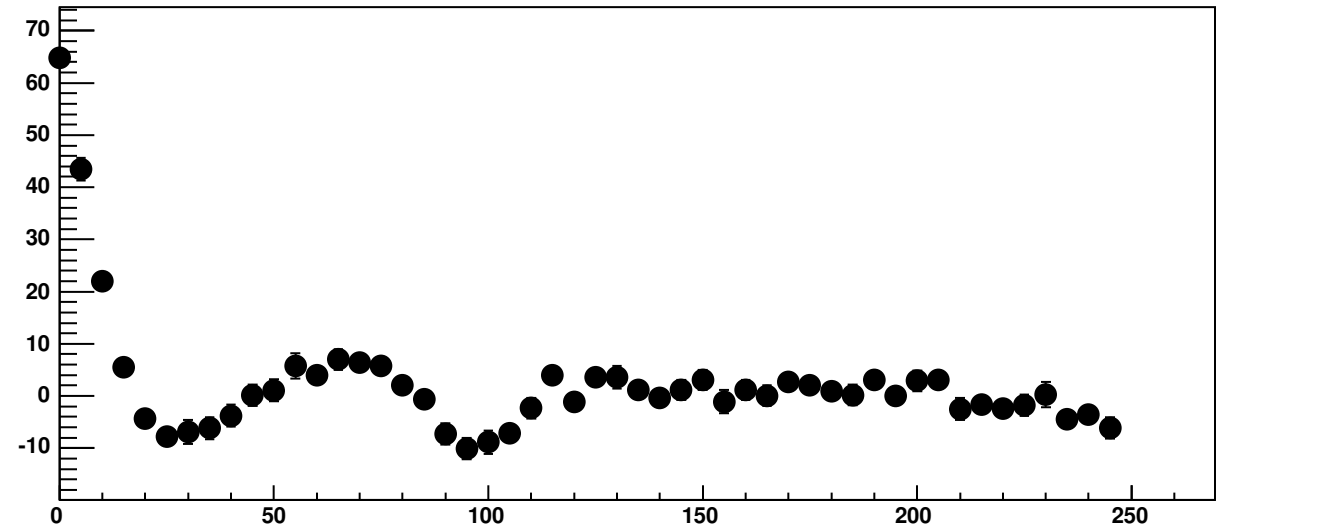
Chip 3, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold



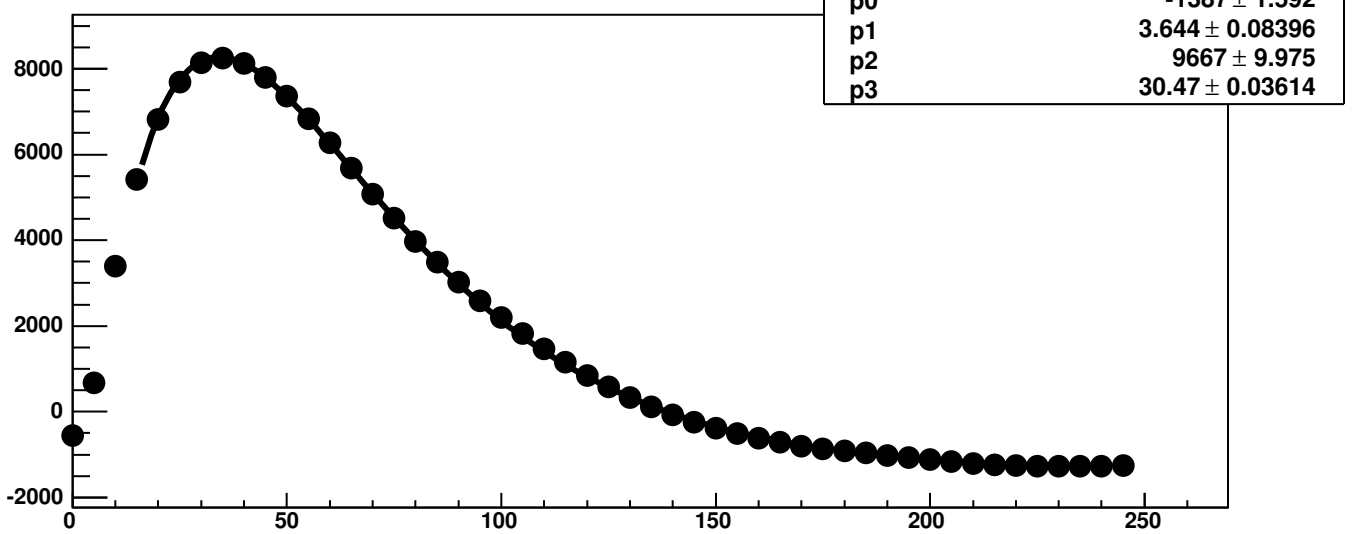
Chip 3, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



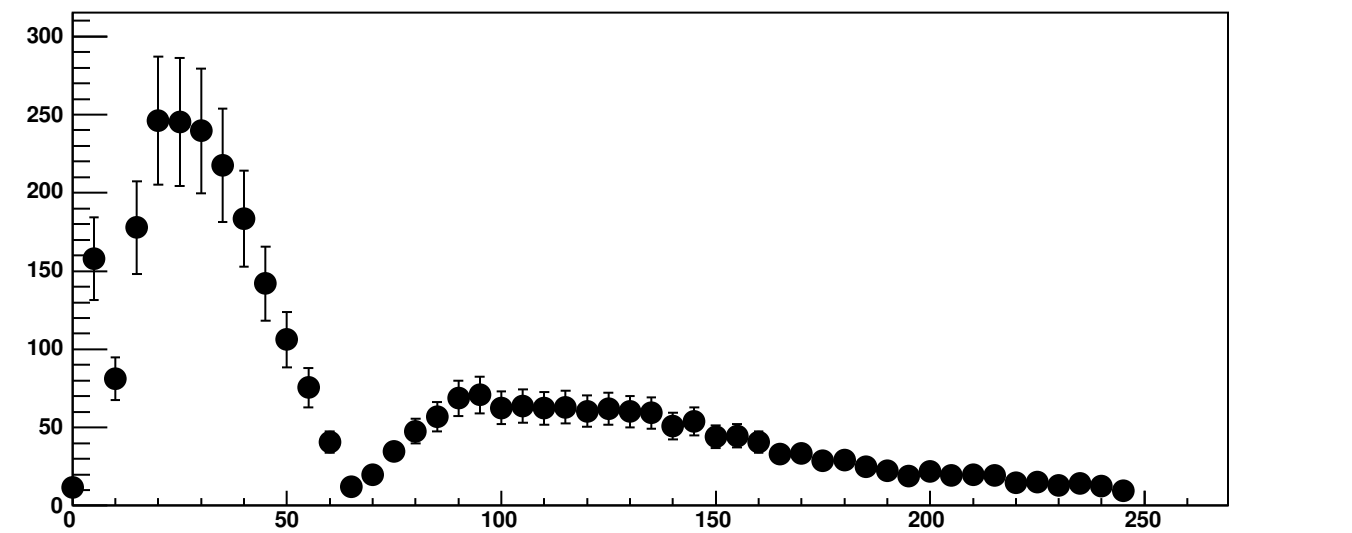
Chip 3, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold



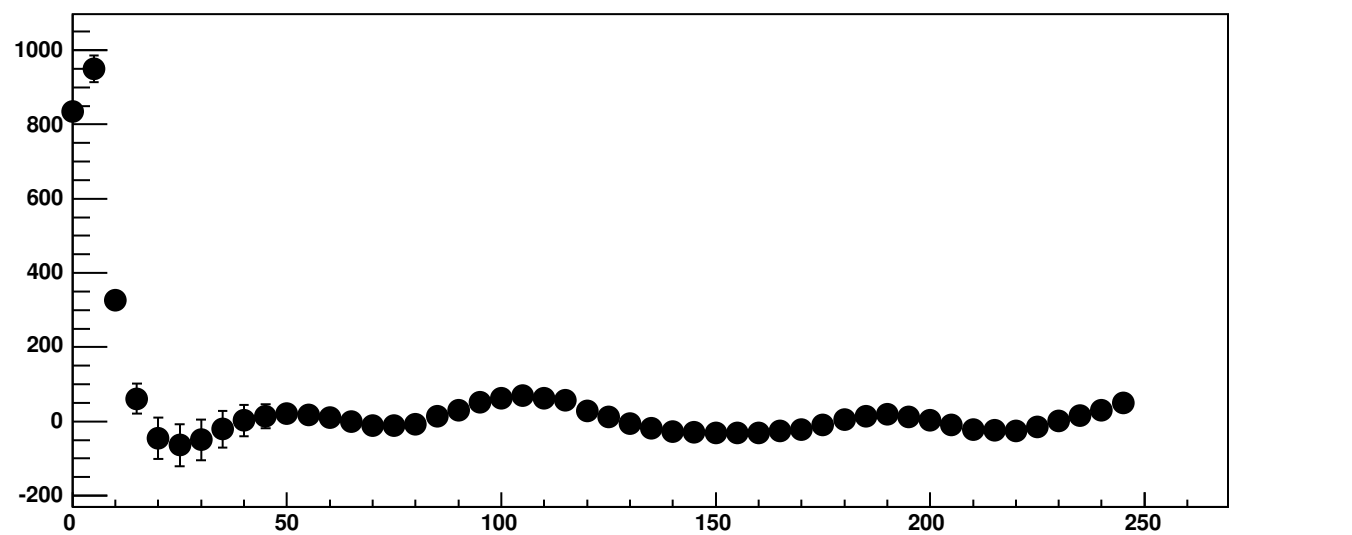
Chip 3, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold



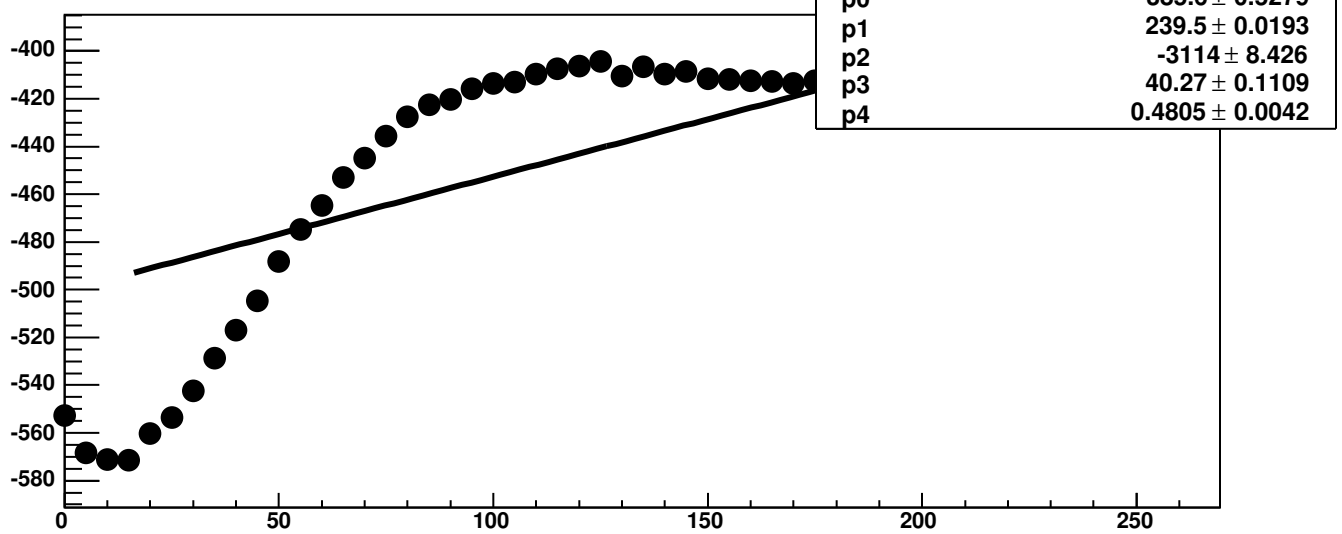
Chip 3, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold



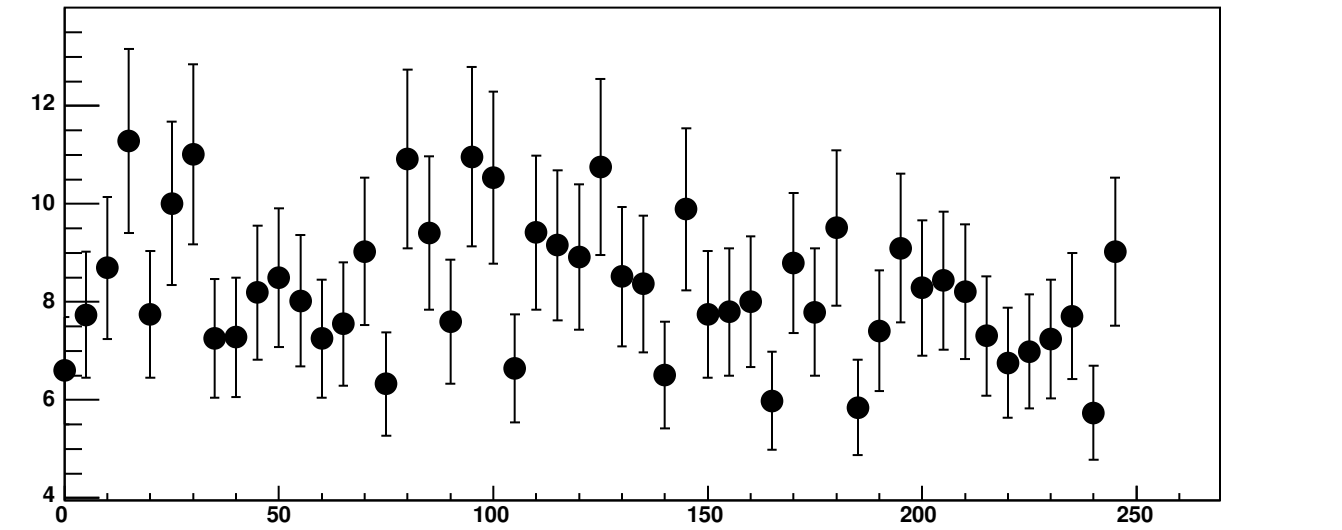
Chip 3, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold



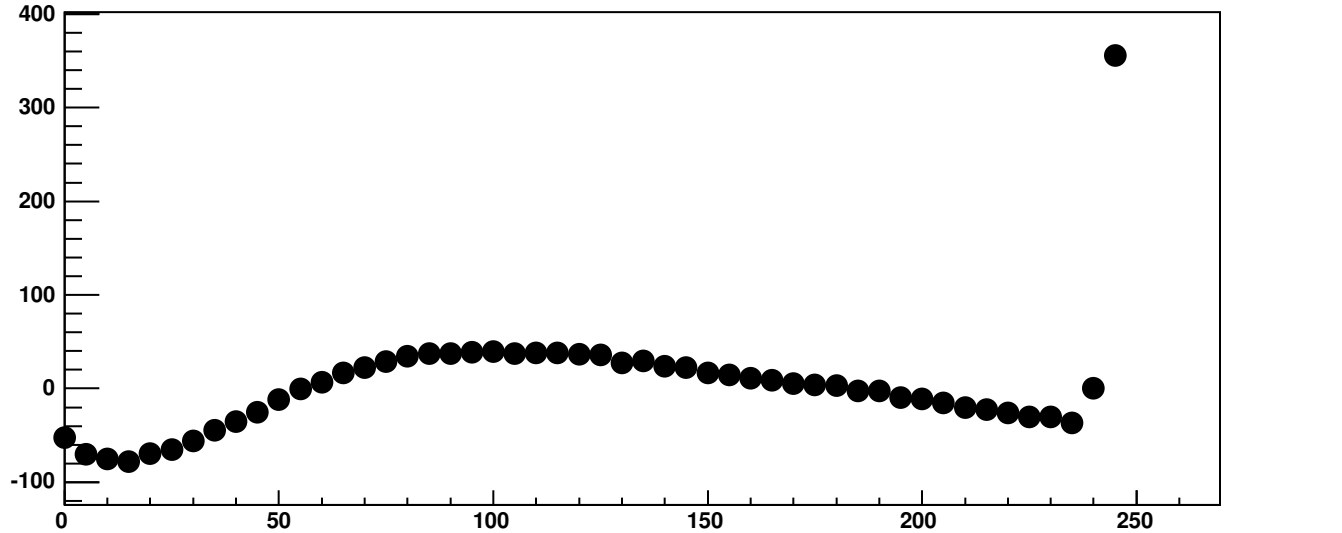
Chip 3, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

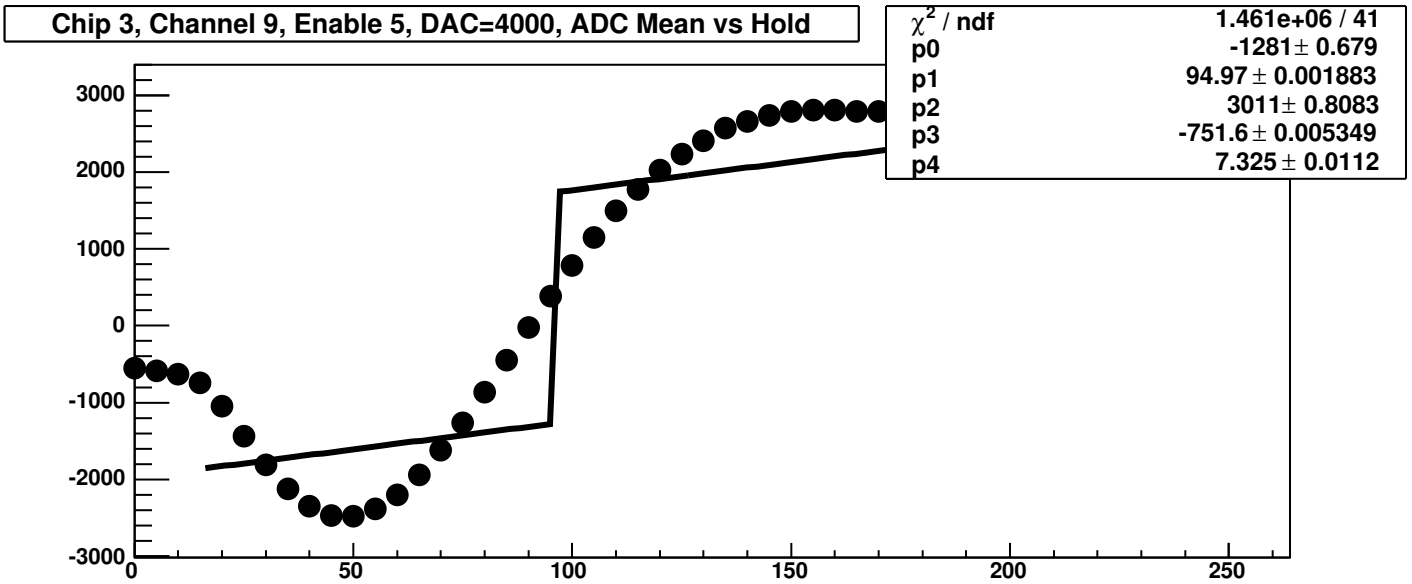


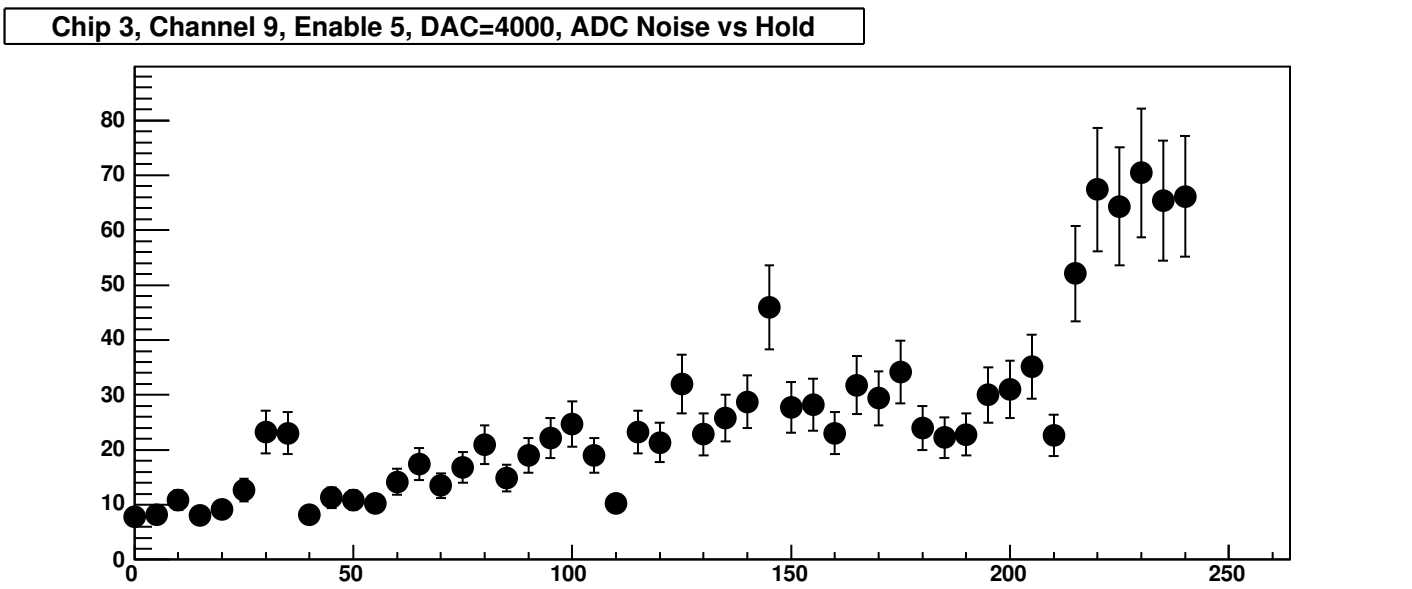
Chip 3, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold

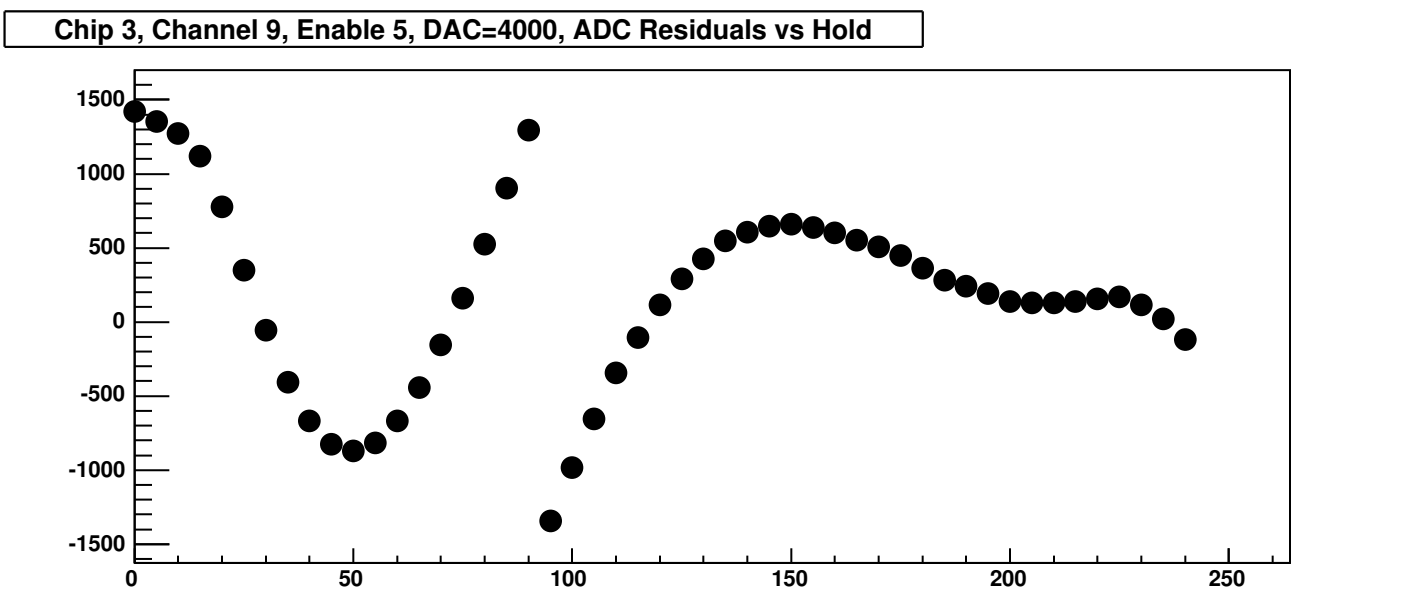


Chip 3, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

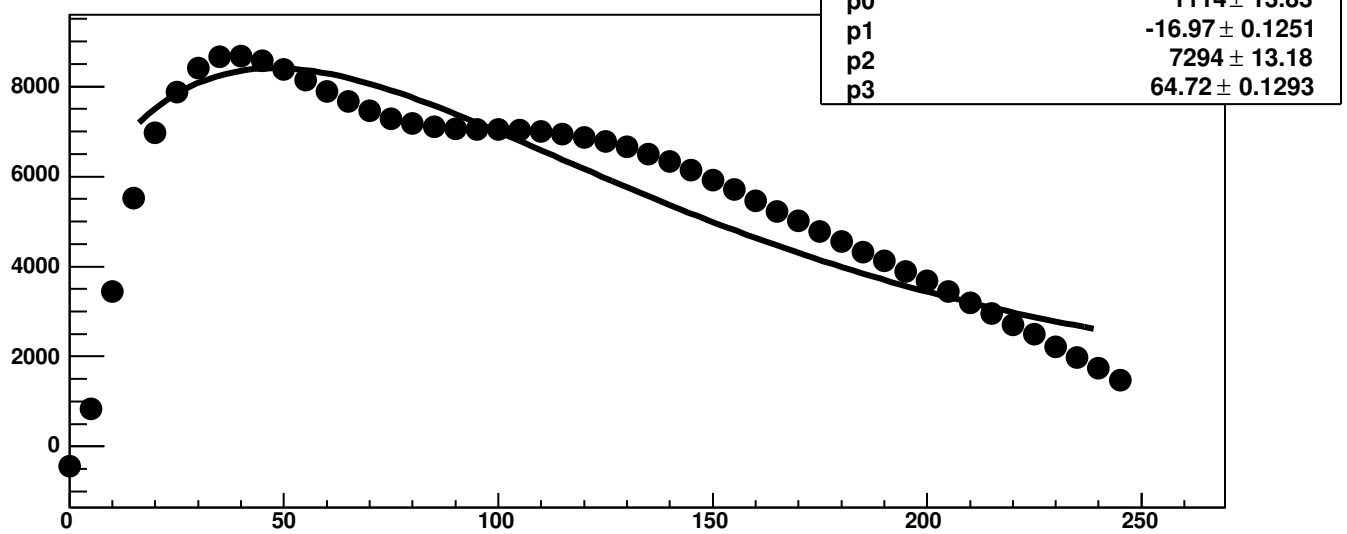



Chip 3, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.461e+06 / 41
p0 -1281 ± 0.679
p1 94.97 ± 0.001883
p2 3011 ± 0.8083
p3 -751.6 ± 0.005349
p4 7.325 ± 0.0112
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

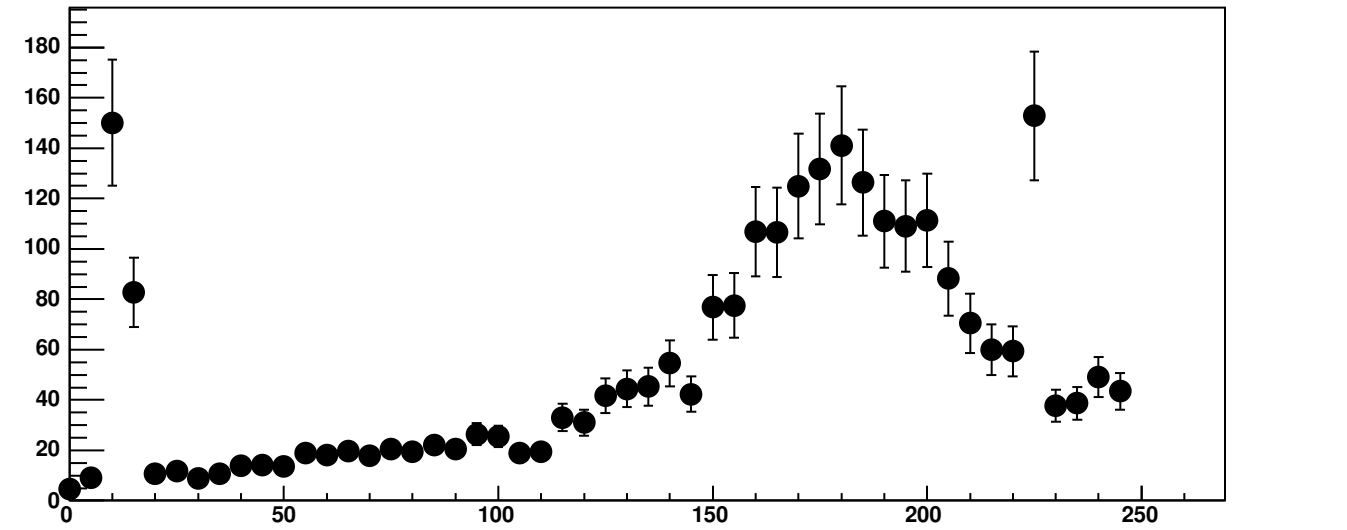

Chip 3, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250


Chip 3, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold
1500
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

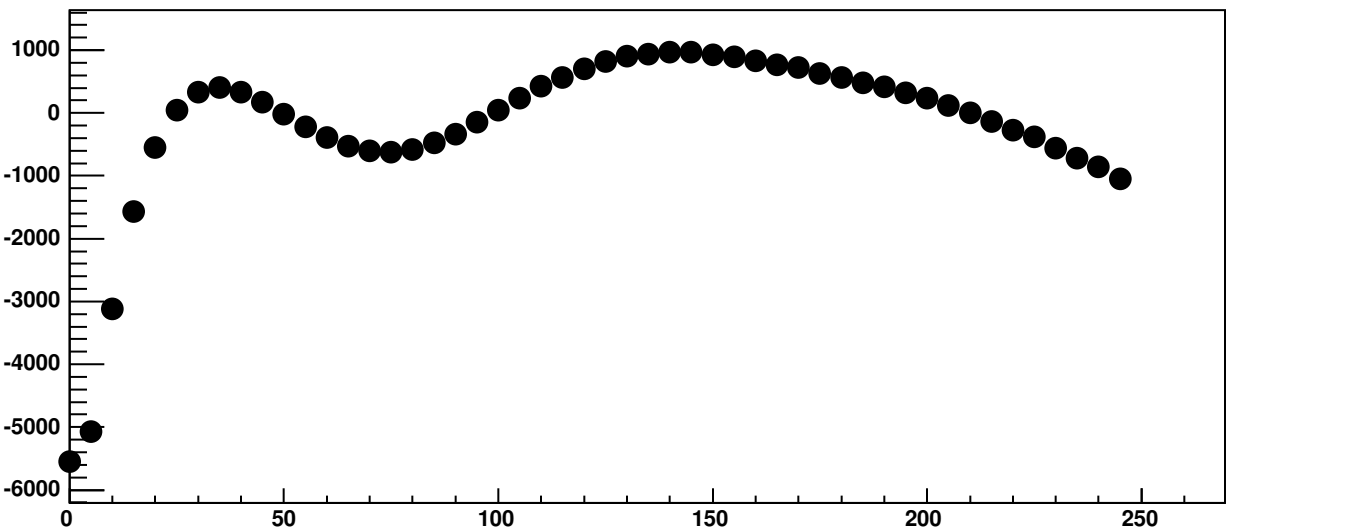
Chip 3, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold

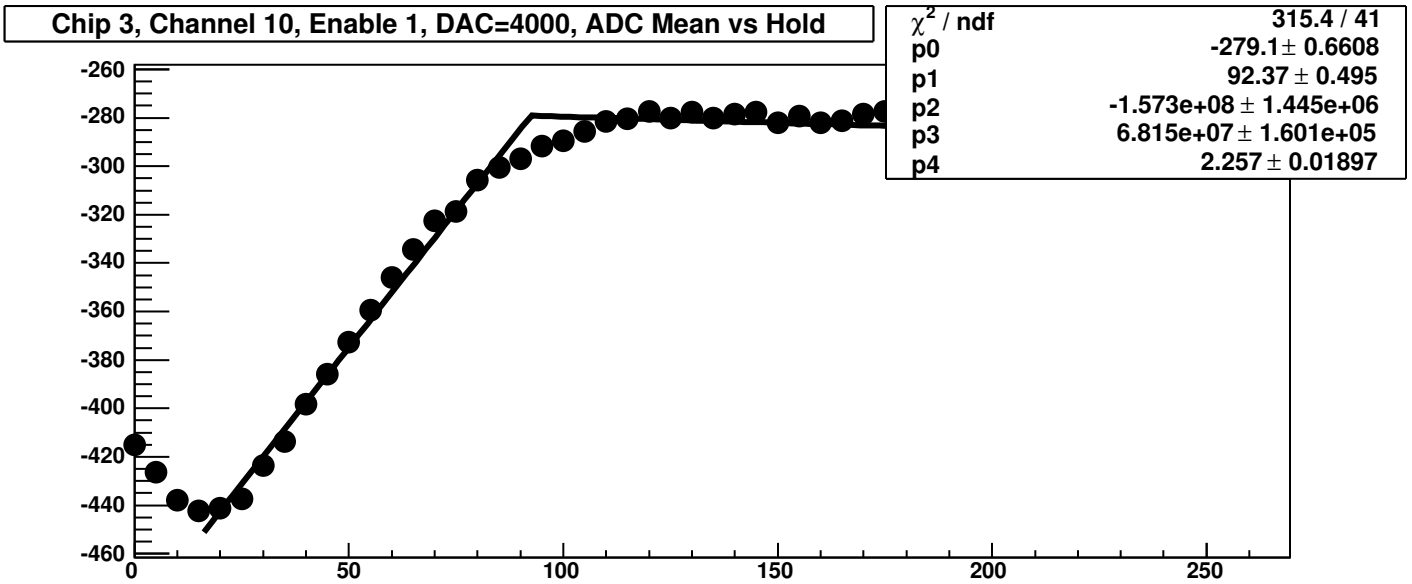


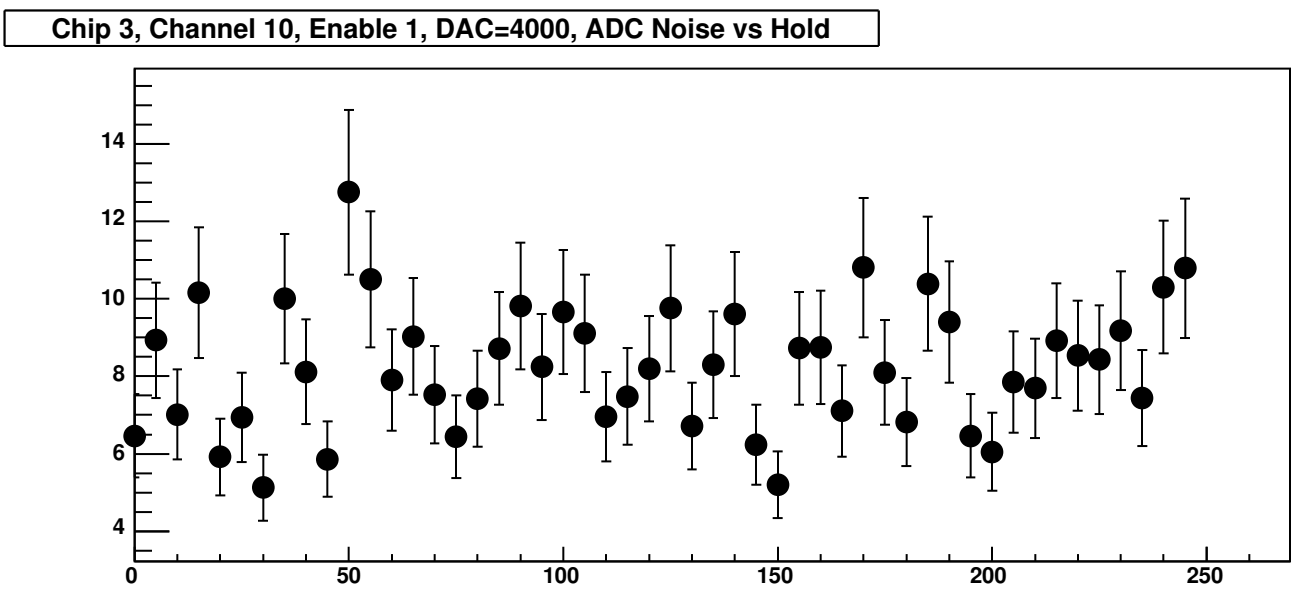
Chip 3, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold

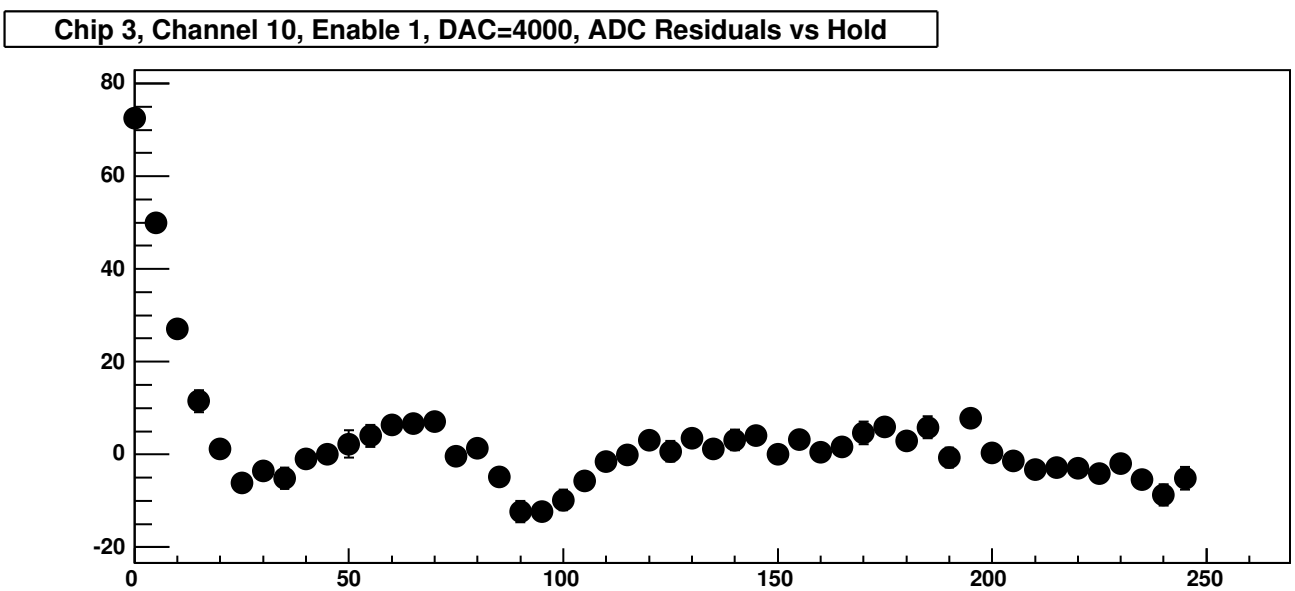


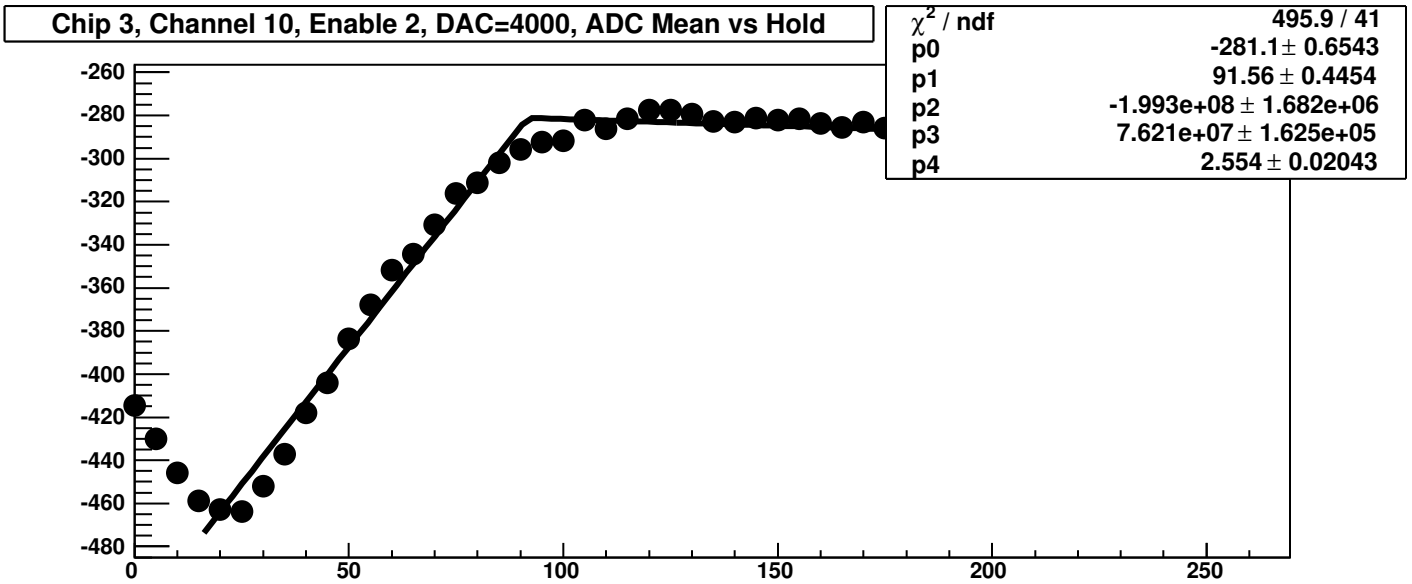
Chip 3, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold

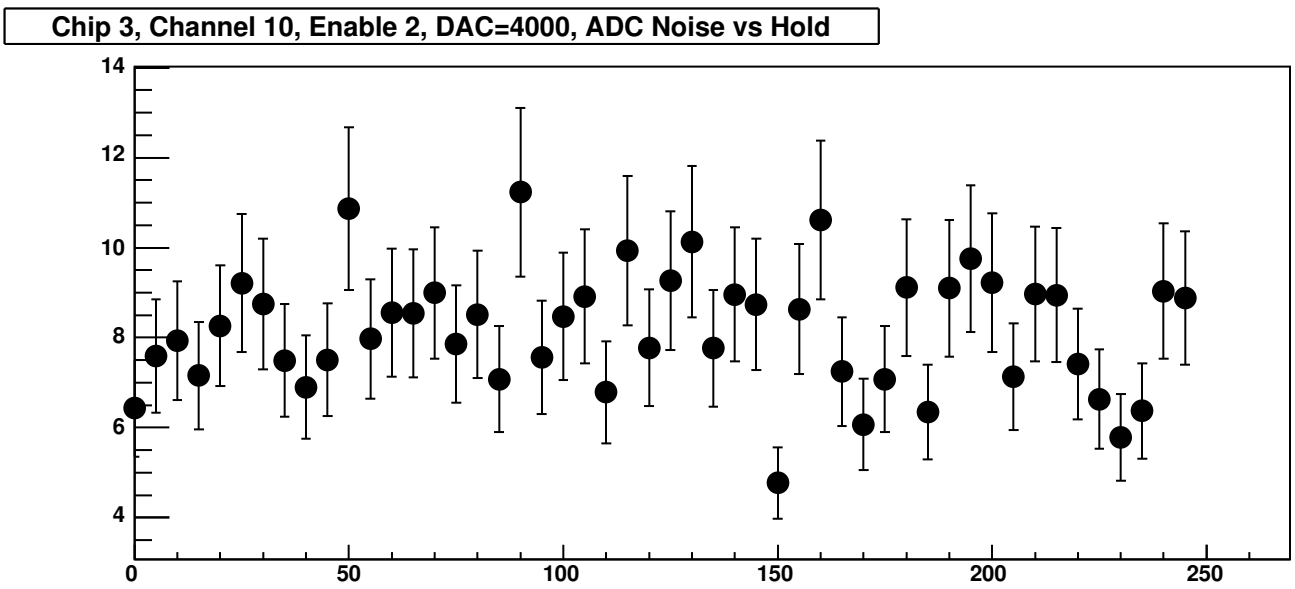


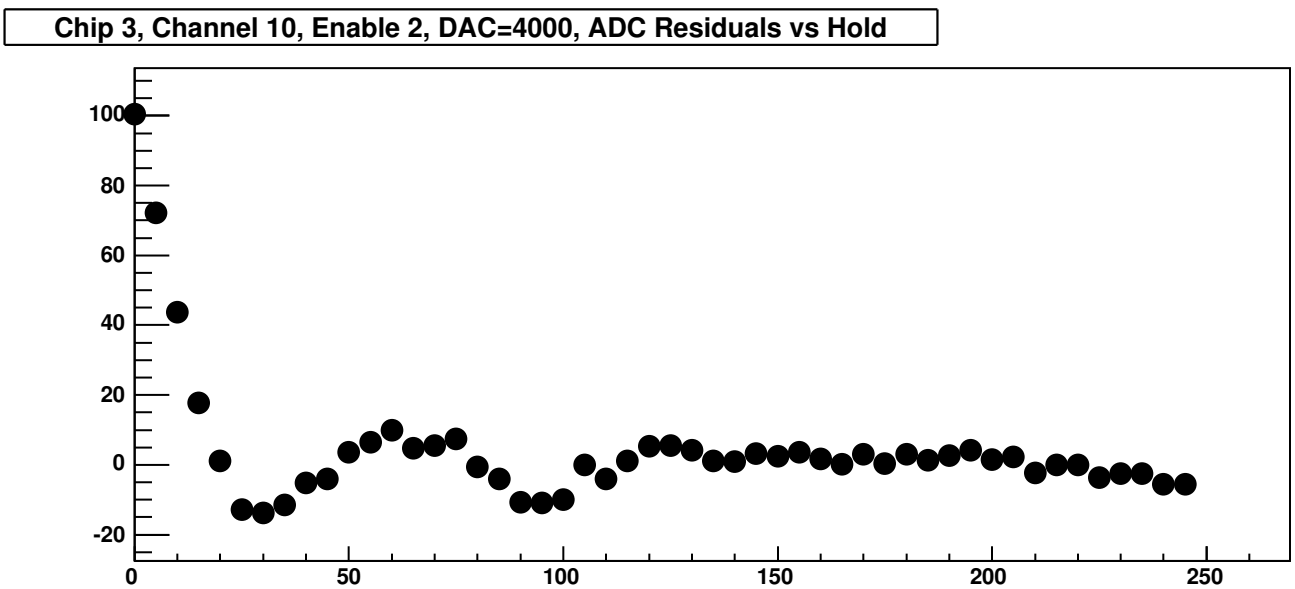

Chip 3, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 315.4 / 41
p0 -279.1 ± 0.6608
p1 92.37 ± 0.495
p2 -1.573e+08 ± 1.445e+06
p3 6.815e+07 ± 1.601e+05
p4 2.257 ± 0.01897
-260
-280
-300
-320
-340
-360
-380
-400
-420
-440
-460
0
50
100
150
200
250

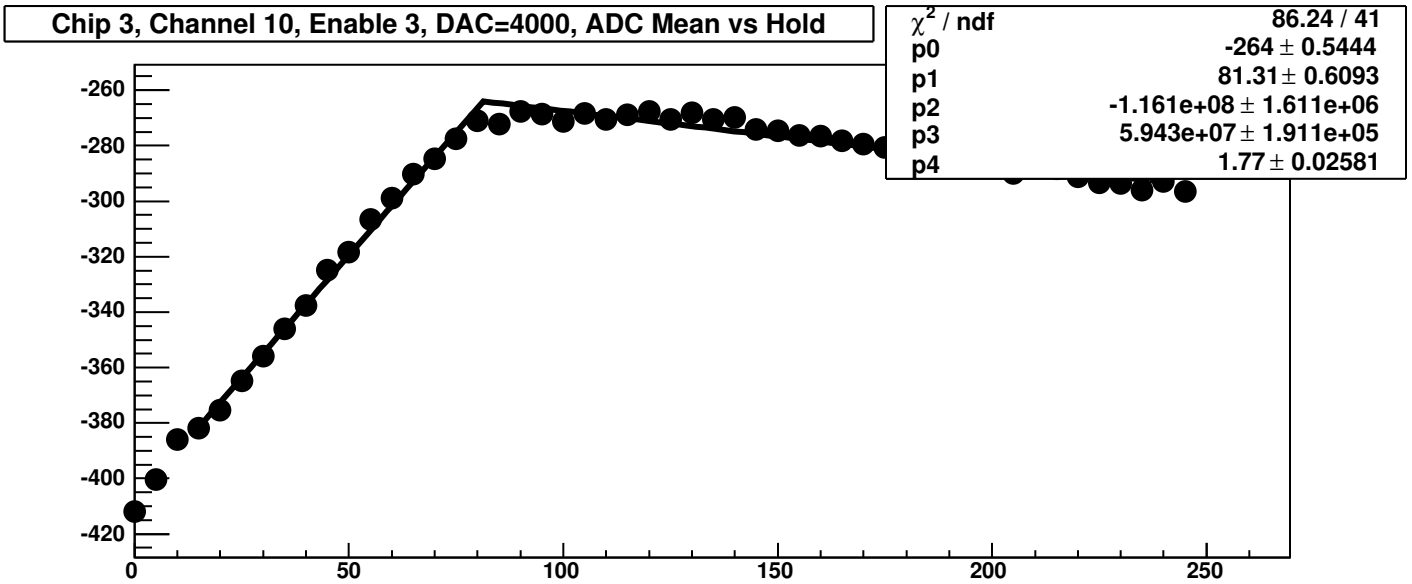

Chip 3, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

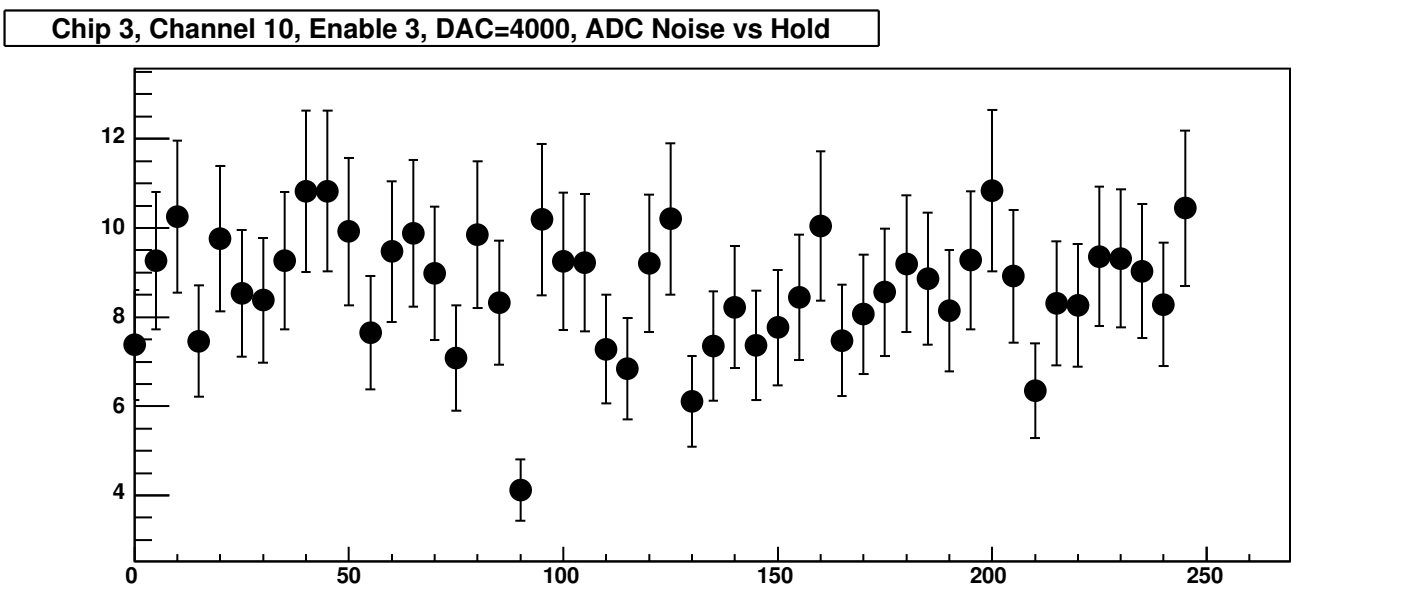

Chip 3, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold
80
60
40
20
0
-20
0
50
100
150
200
250

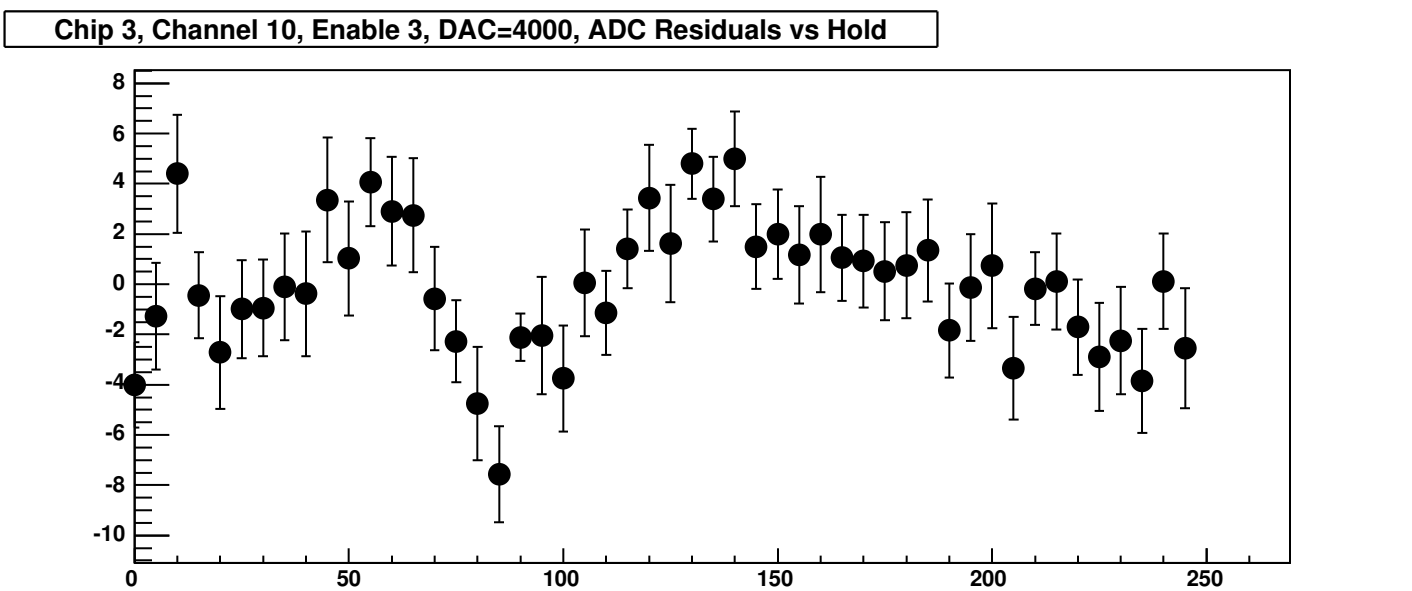

Chip 3, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 495.9 / 41
p0 -281.1 ± 0.6543
p1 91.56 ± 0.4454
p2 -1.993e+08 ± 1.682e+06
p3 7.621e+07 ± 1.625e+05
p4 2.554 ± 0.02043
-260
-280
-300
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
50
100
150
200
250


Chip 3, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

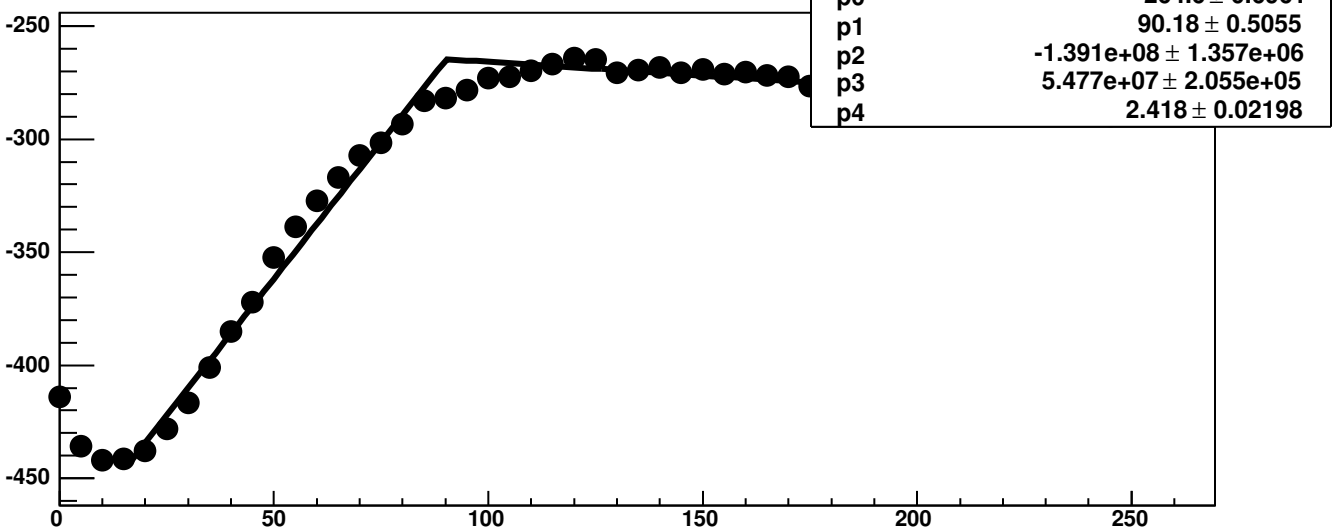

Chip 3, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold
100
80
60
40
20
0
-20
0
50
100
150
200
250


Chip 3, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 86.24 / 41
p0 -264 ± 0.5444
p1 81.31 ± 0.6093
p2 -1.161e+08 ± 1.611e+06
p3 5.943e+07 ± 1.911e+05
p4 1.77 ± 0.02581
-260
-280
-300
-320
-340
-360
-380
-400
-420
0
50
100
150
200
250

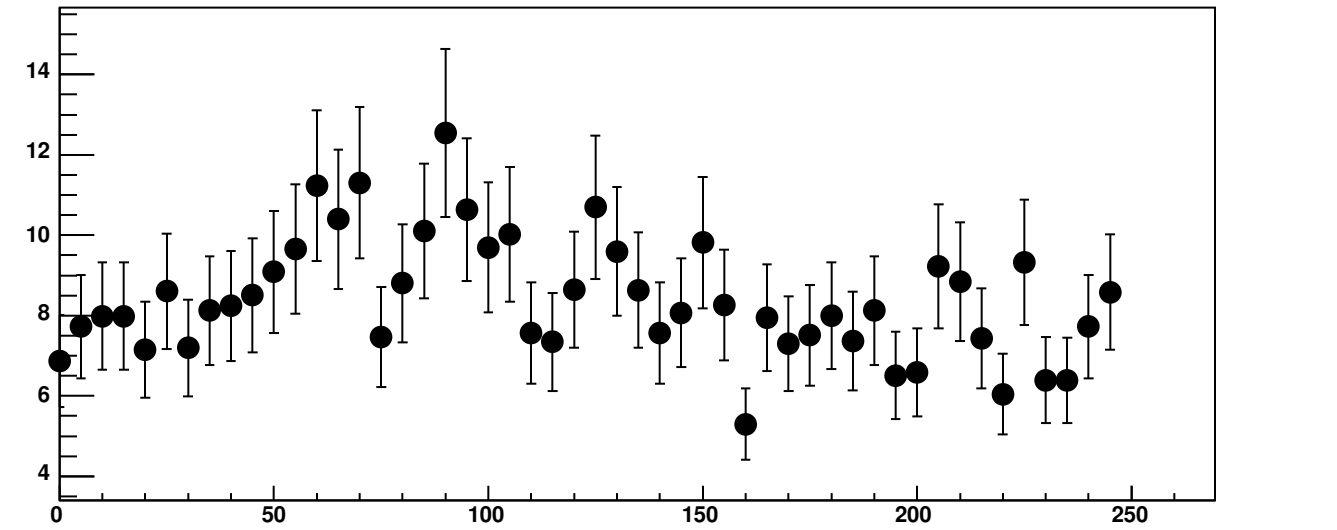

Chip 3, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 3, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
0
50
100
150
200
250

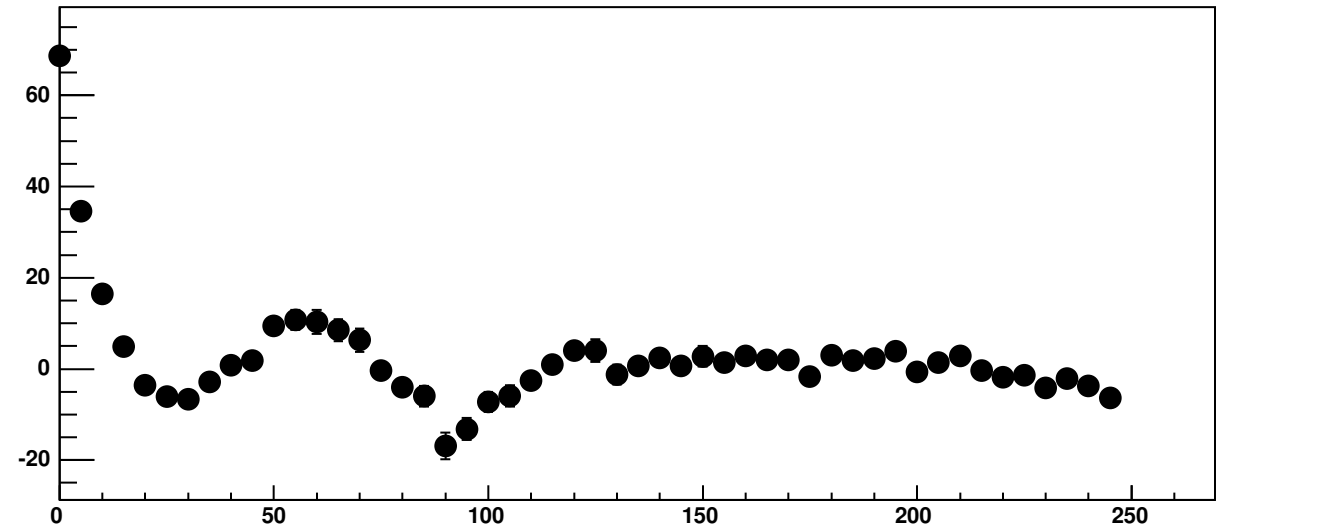
Chip 3, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold



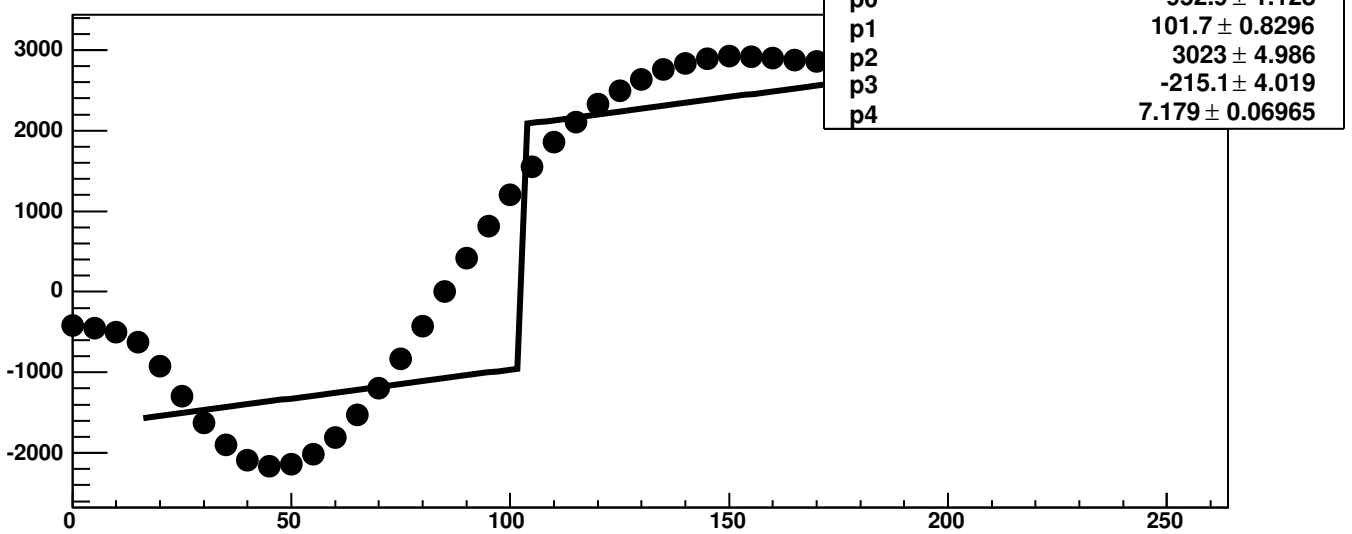
Chip 3, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



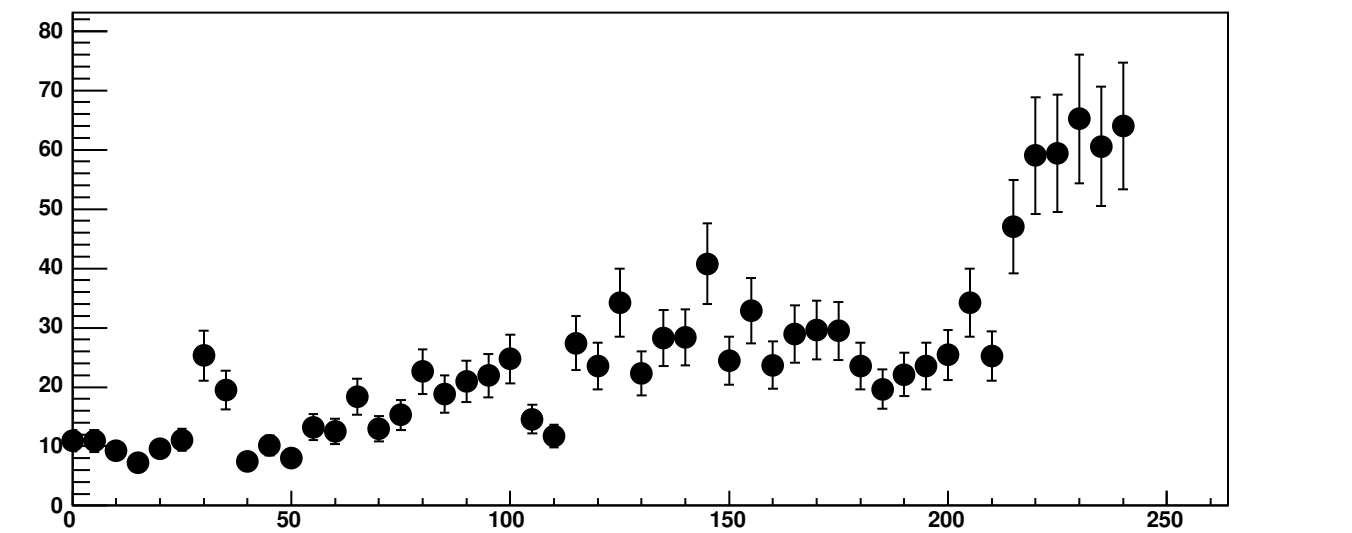
Chip 3, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold



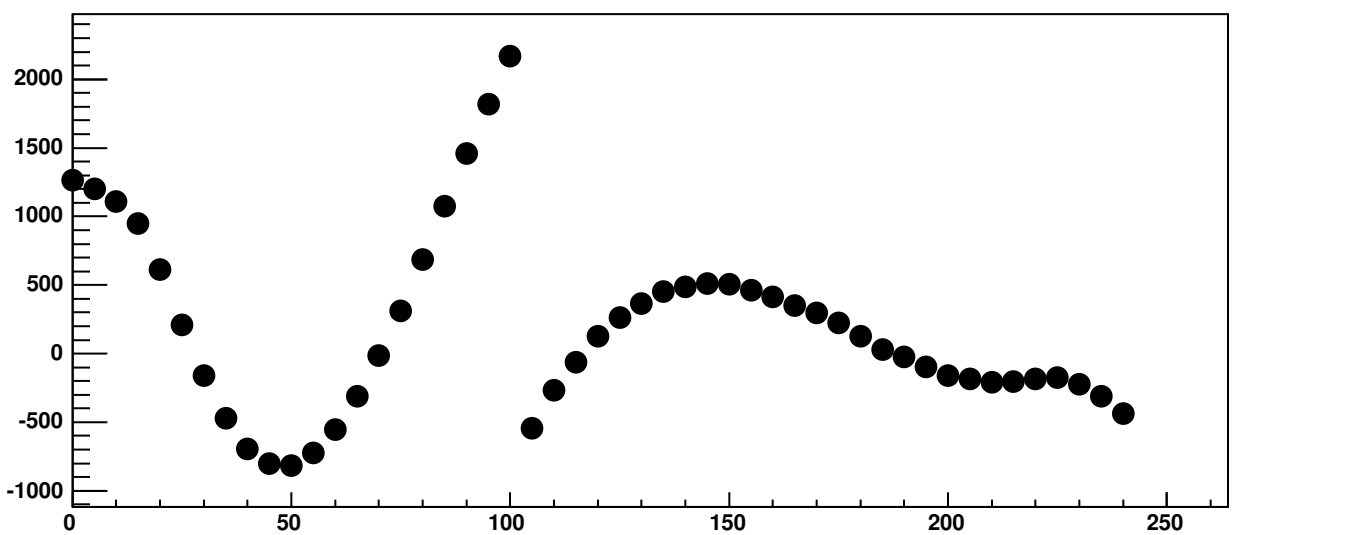
Chip 3, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold

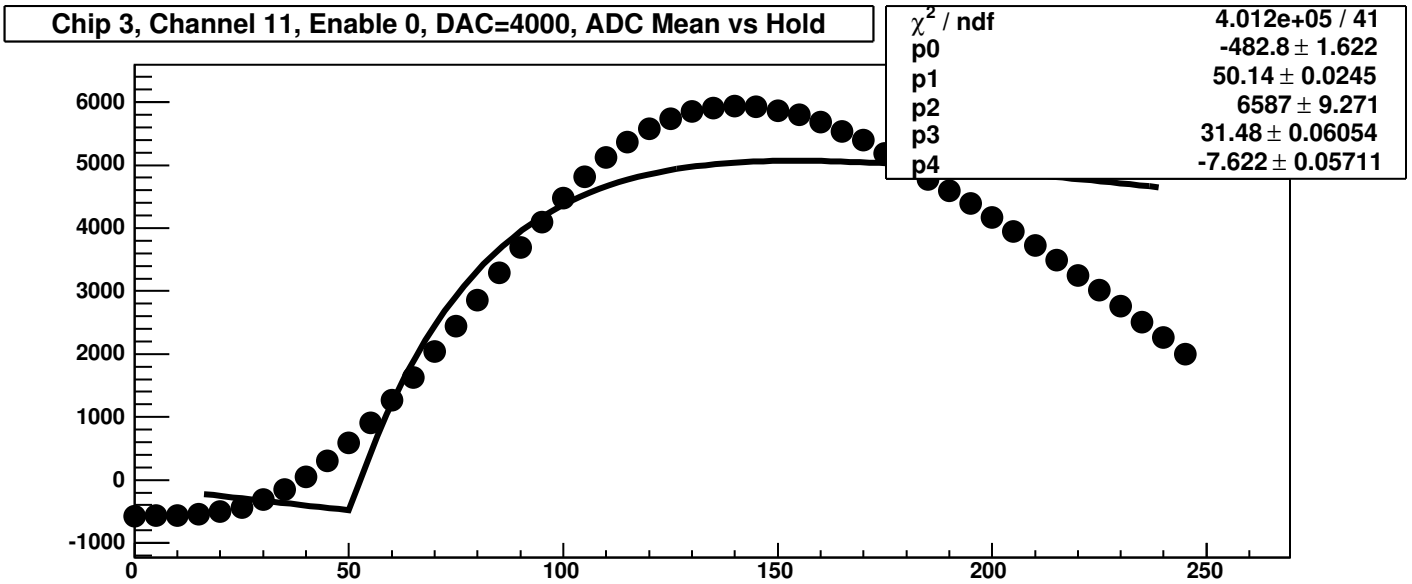


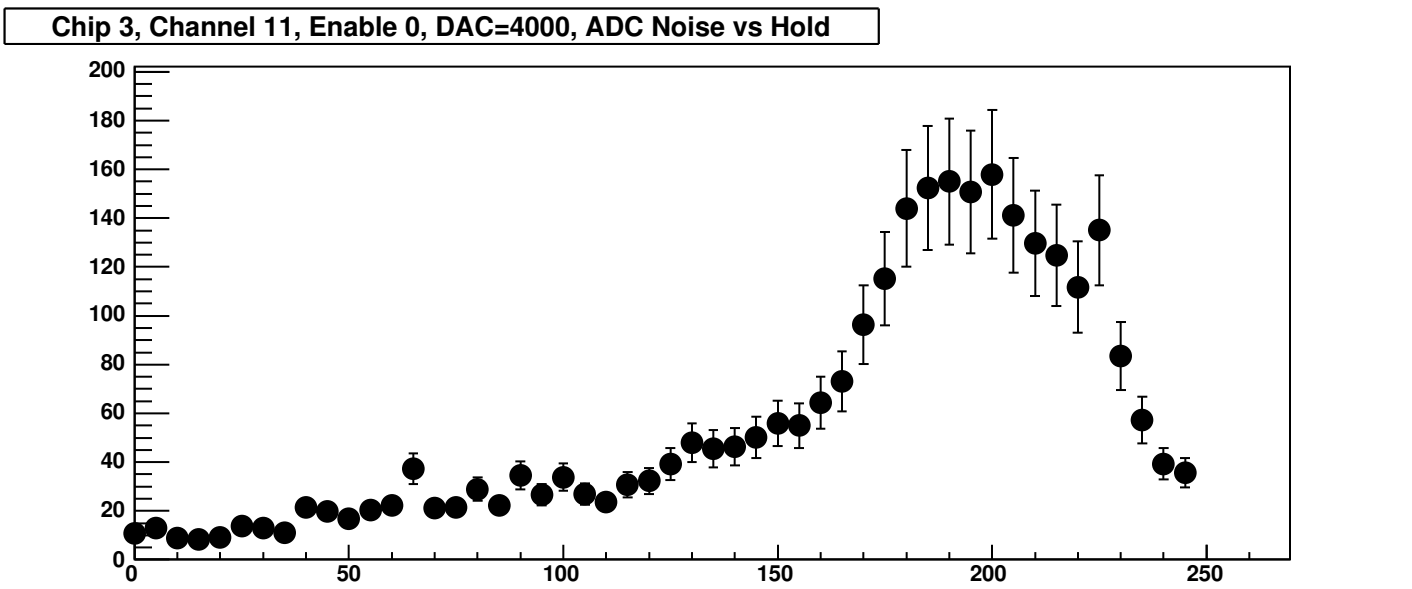
Chip 3, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold

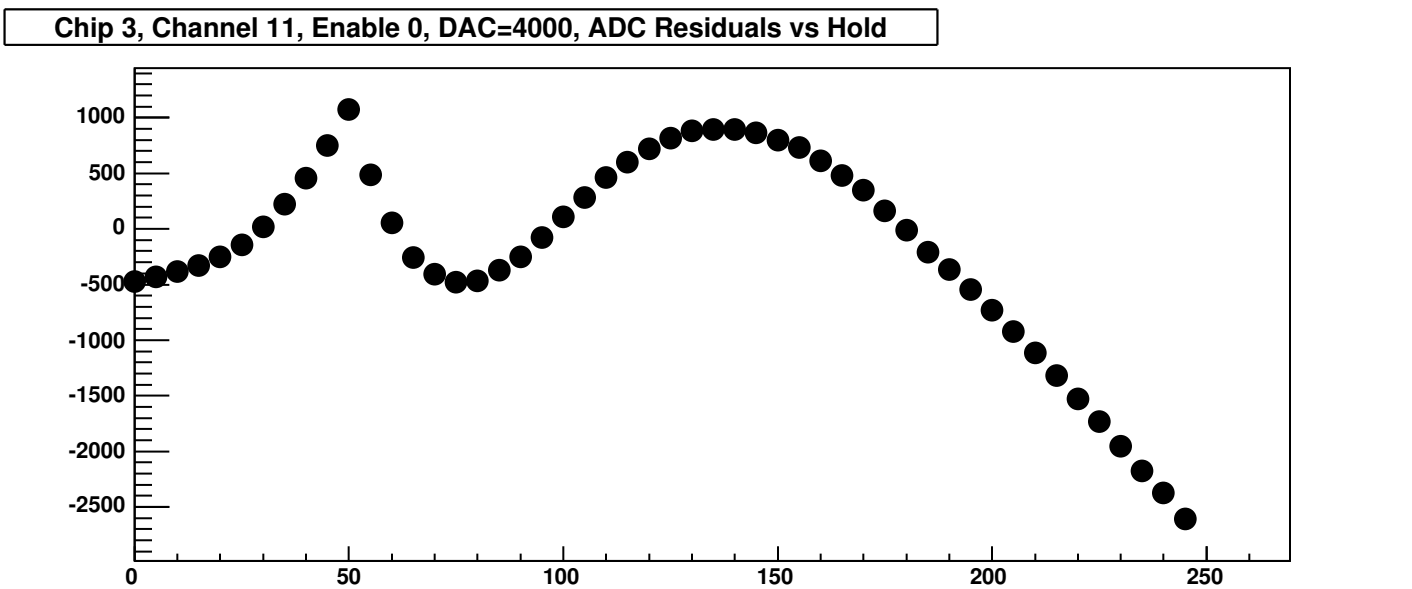


Chip 3, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

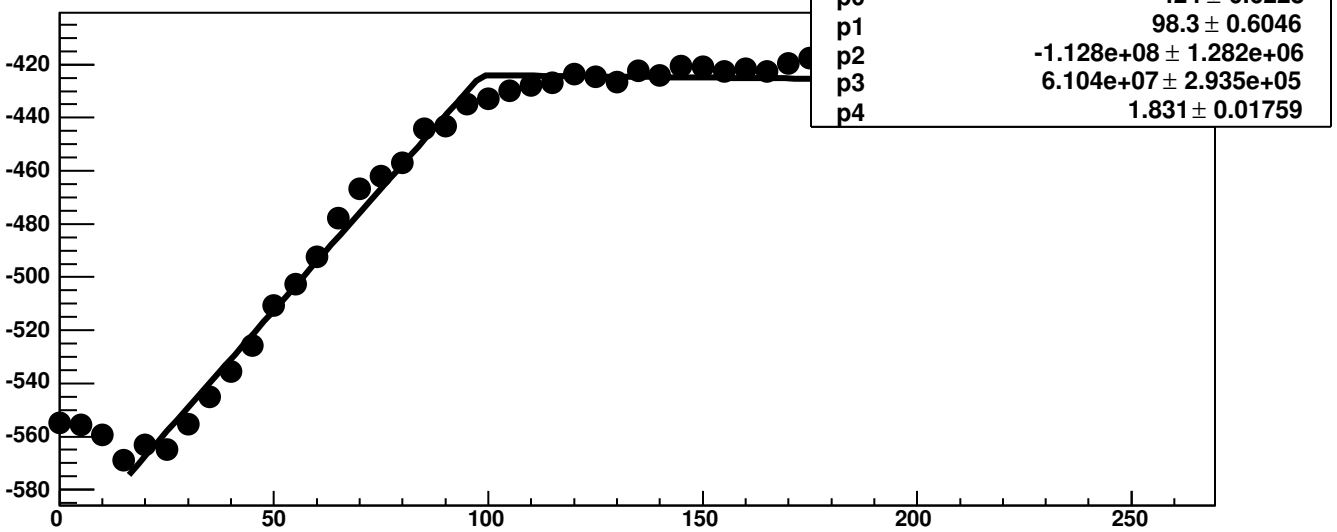



Chip 3, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.012e+05 / 41
p0 -482.8 ± 1.622
p1 50.14 ± 0.0245
p2 6587 ± 9.271
p3 31.48 ± 0.06054
p4 -7.622 ± 0.05711
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

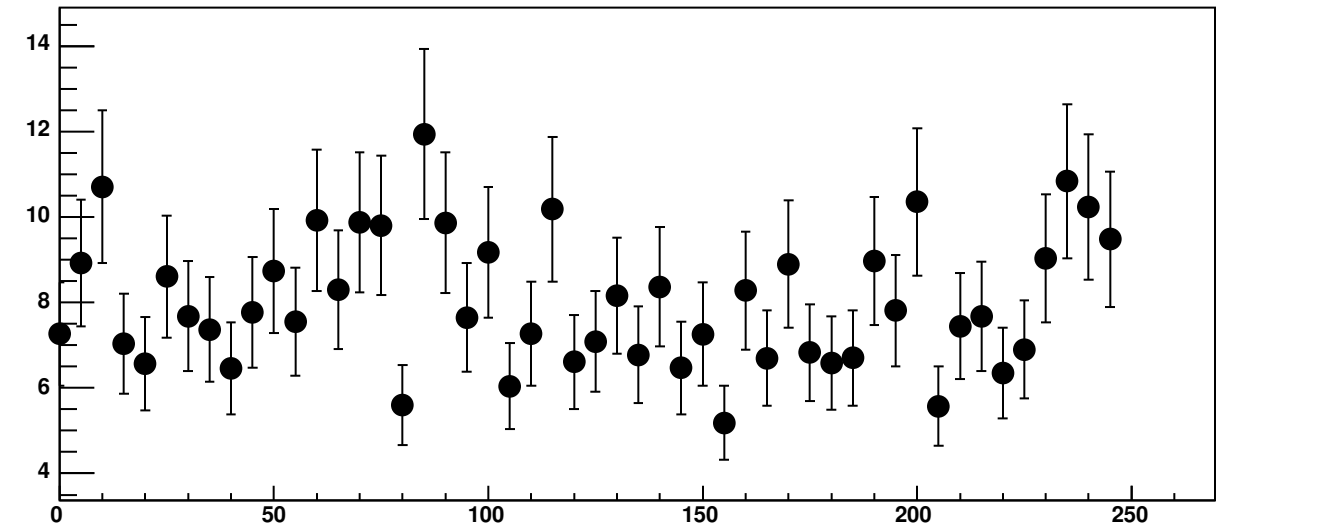

Chip 3, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 3, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
0
50
100
150
200
250

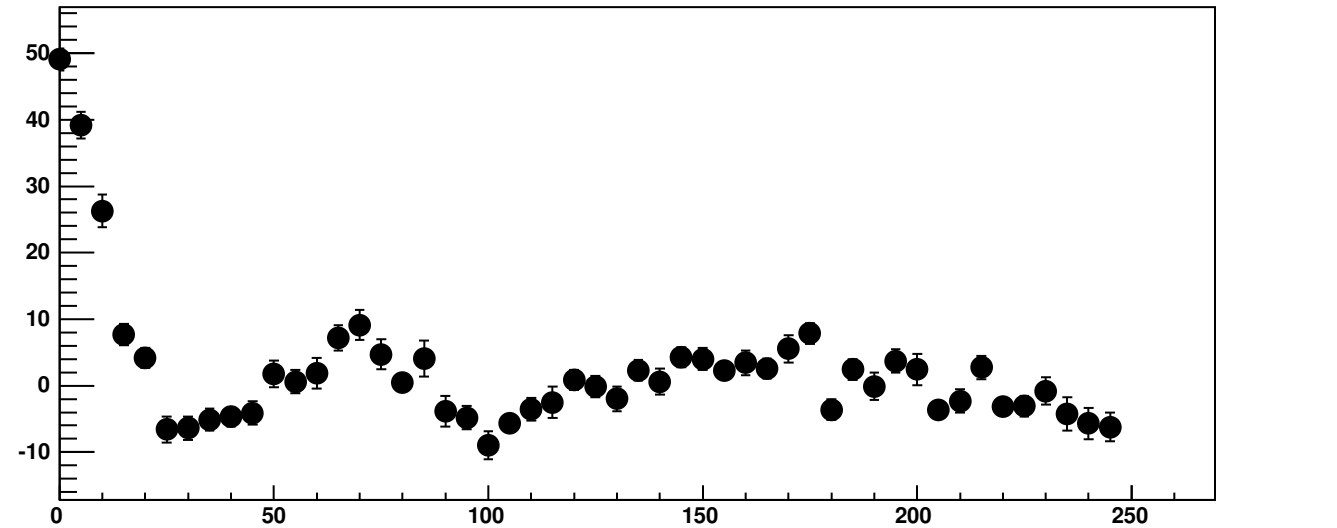
Chip 3, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

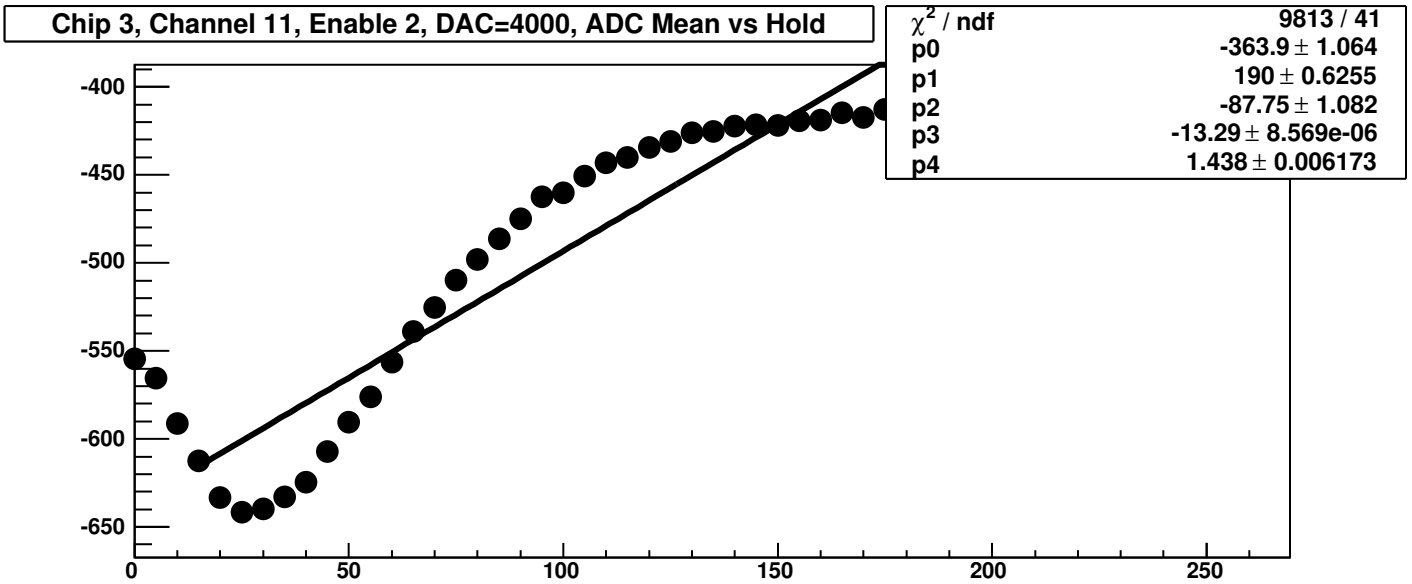


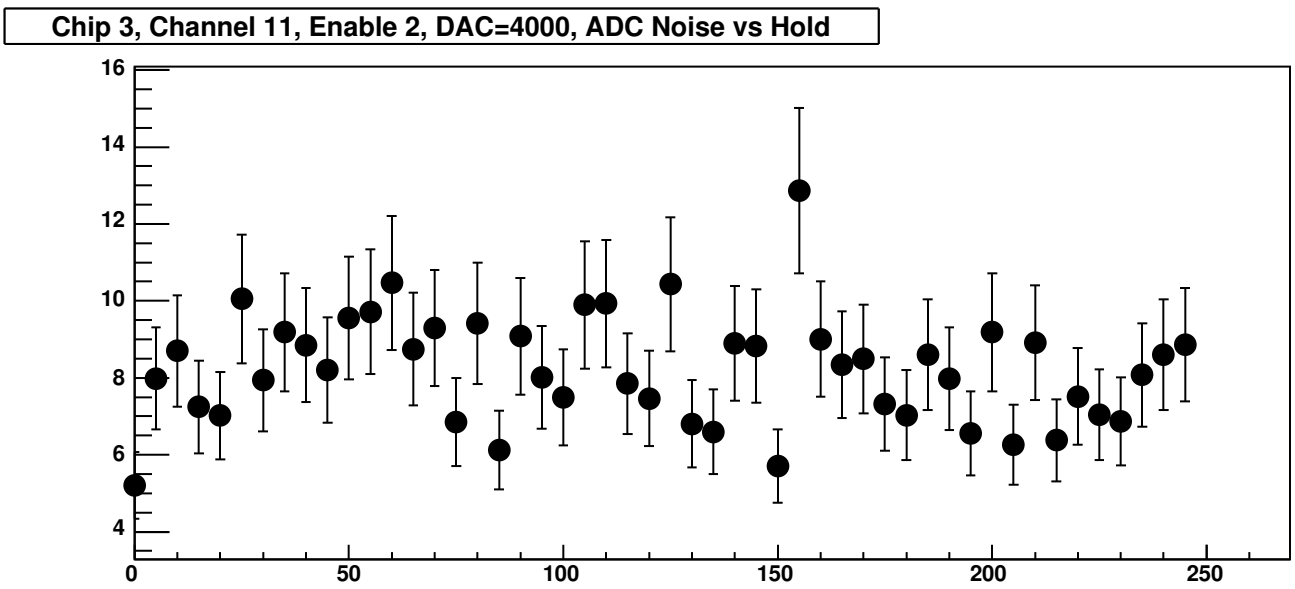
Chip 3, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold

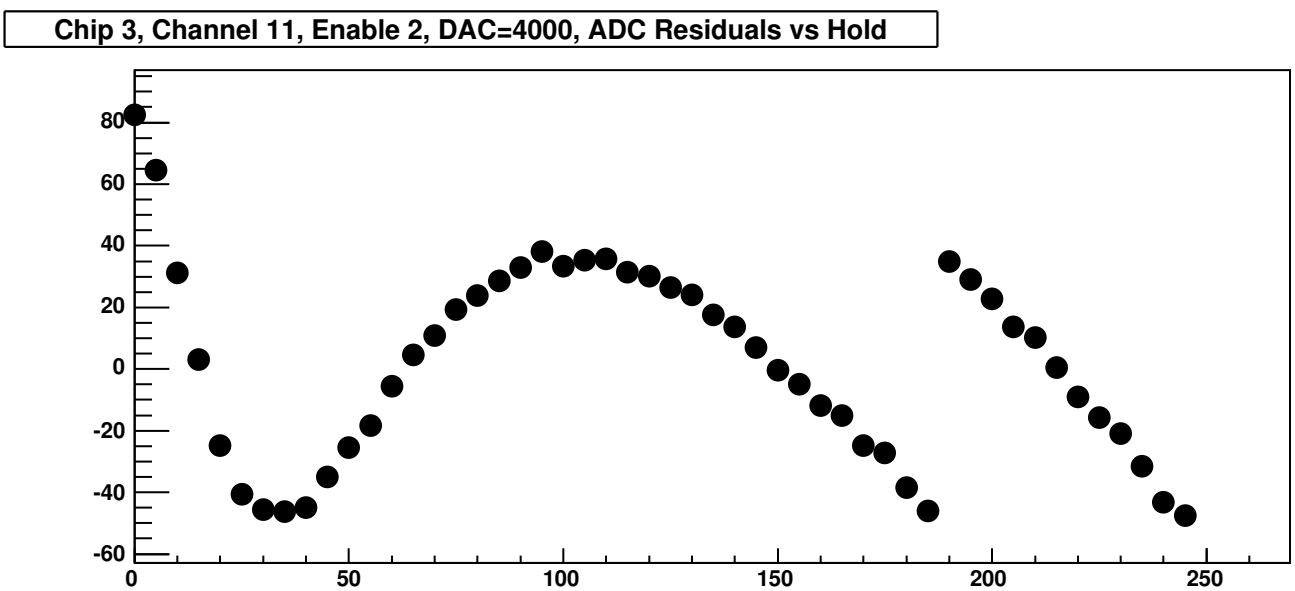


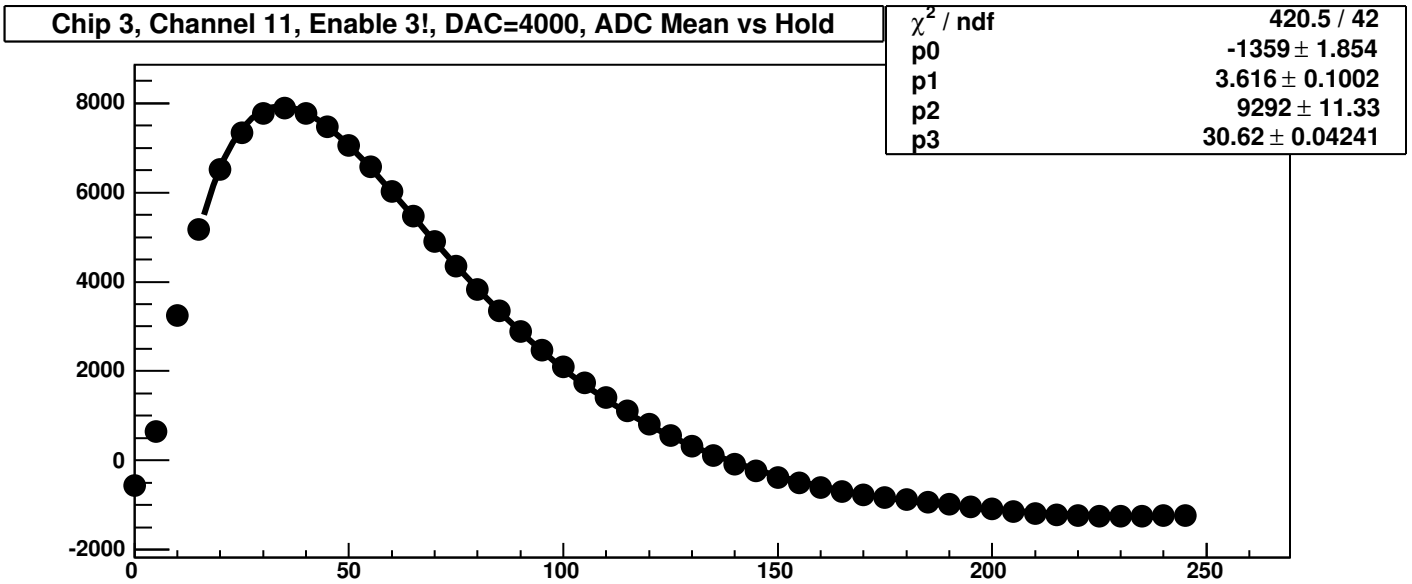
Chip 3, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold

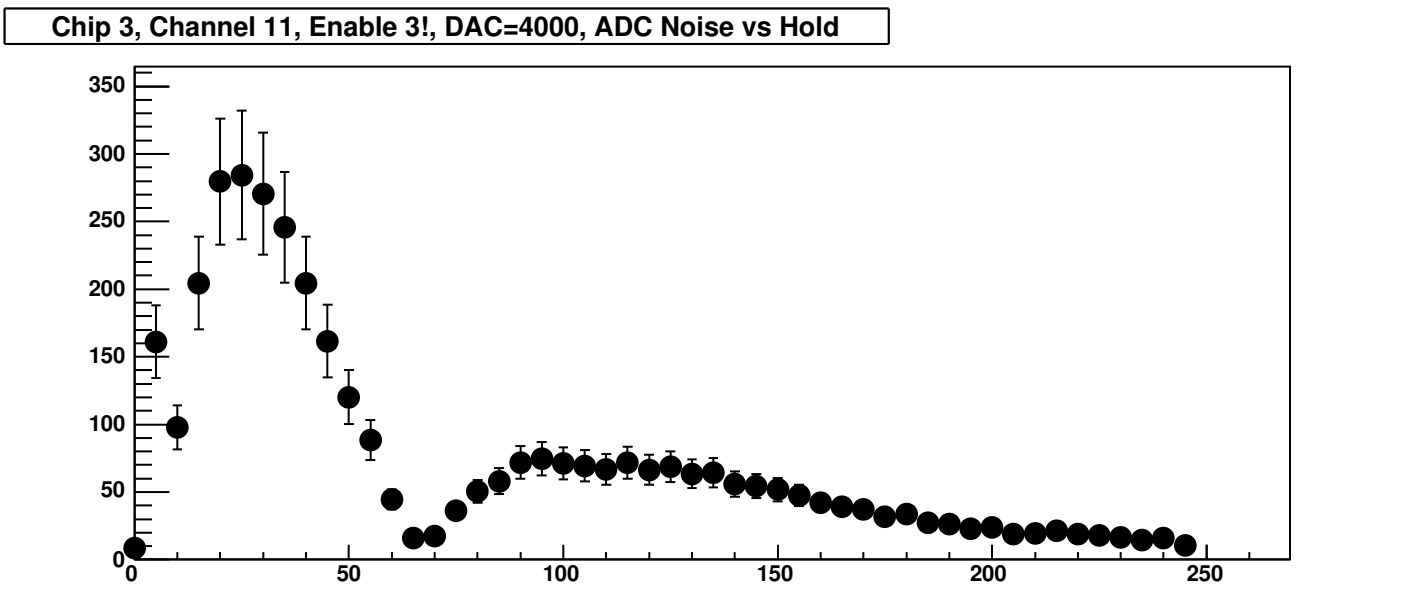


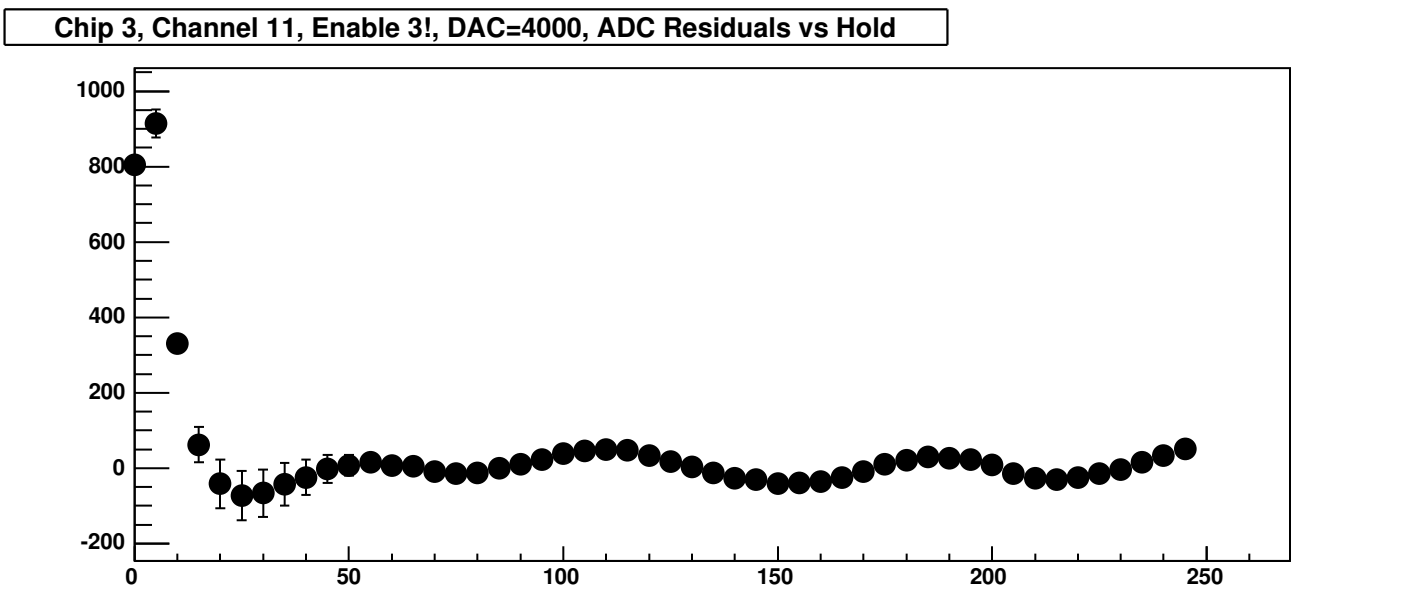

Chip 3, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 9813 / 41
p0 -363.9 ± 1.064
p1 190 ± 0.6255
p2 -87.75 ± 1.082
p3 -13.29 ± 8.569e-06
p4 1.438 ± 0.006173
-400
-450
-500
-550
-600
-650
0
50
100
150
200
250


Chip 3, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold
16
14
12
10
8
6
4
0
50
100
150
200
250

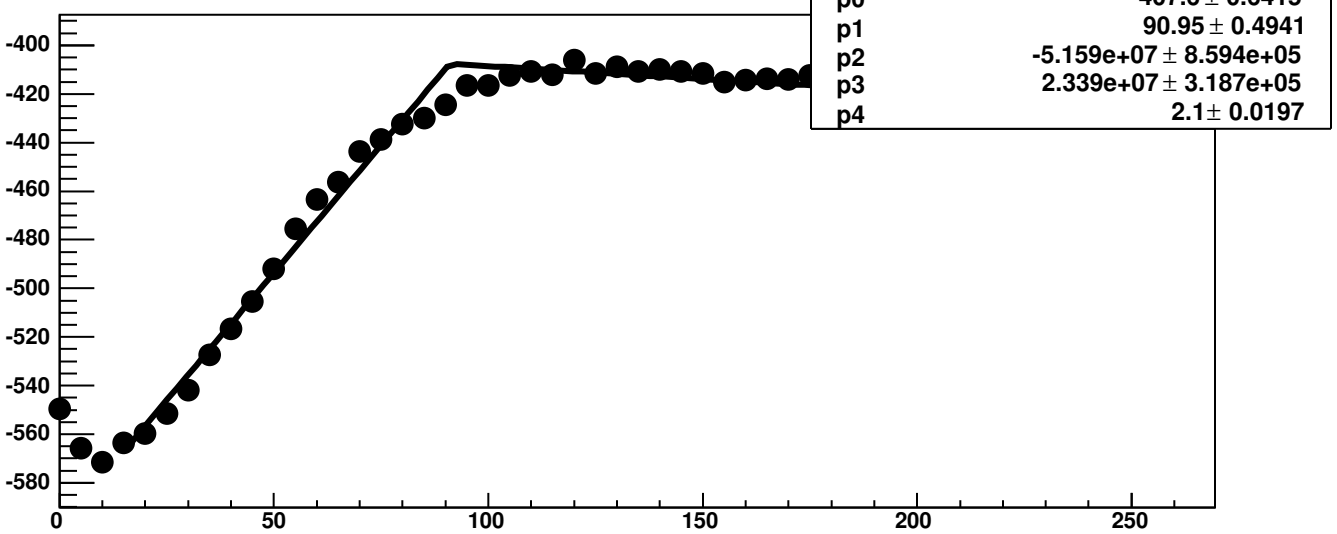

Chip 3, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold
80
60
40
20
0
-20
-40
-60
0
50
100
150
200
250


Chip 3, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 420.5 / 42
p0 -1359 ± 1.854
p1 3.616 ± 0.1002
p2 9292 ± 11.33
p3 30.62 ± 0.04241
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

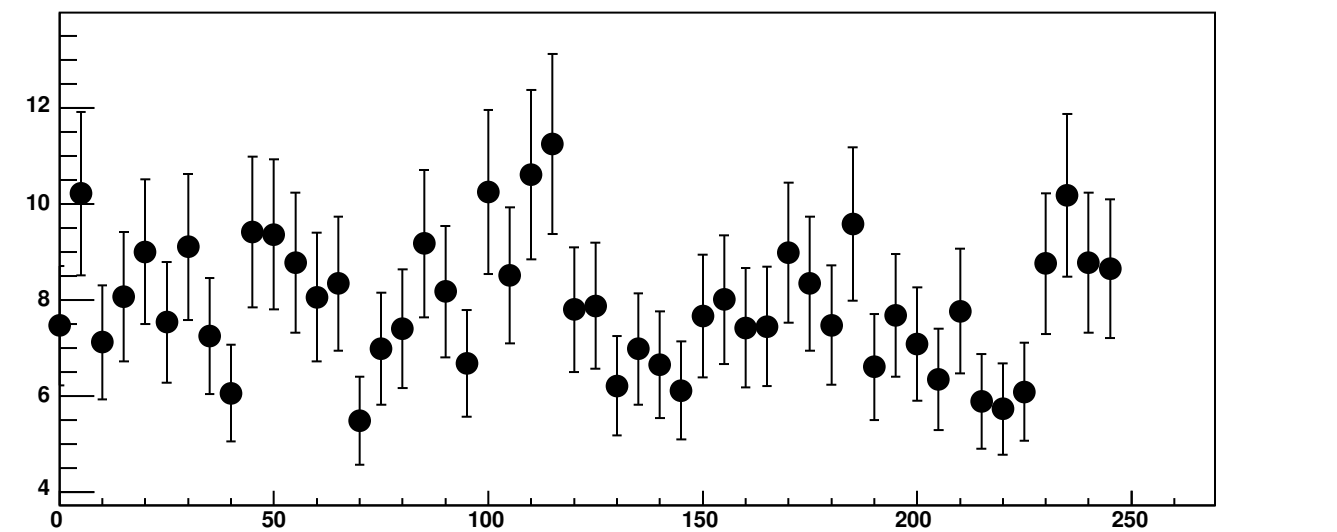

Chip 3, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold
350
300
250
200
150
100
50
0
0
50
100
150
200
250


Chip 3, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold
1000
800
600
400
200
0
-200
0
50
100
150
200
250

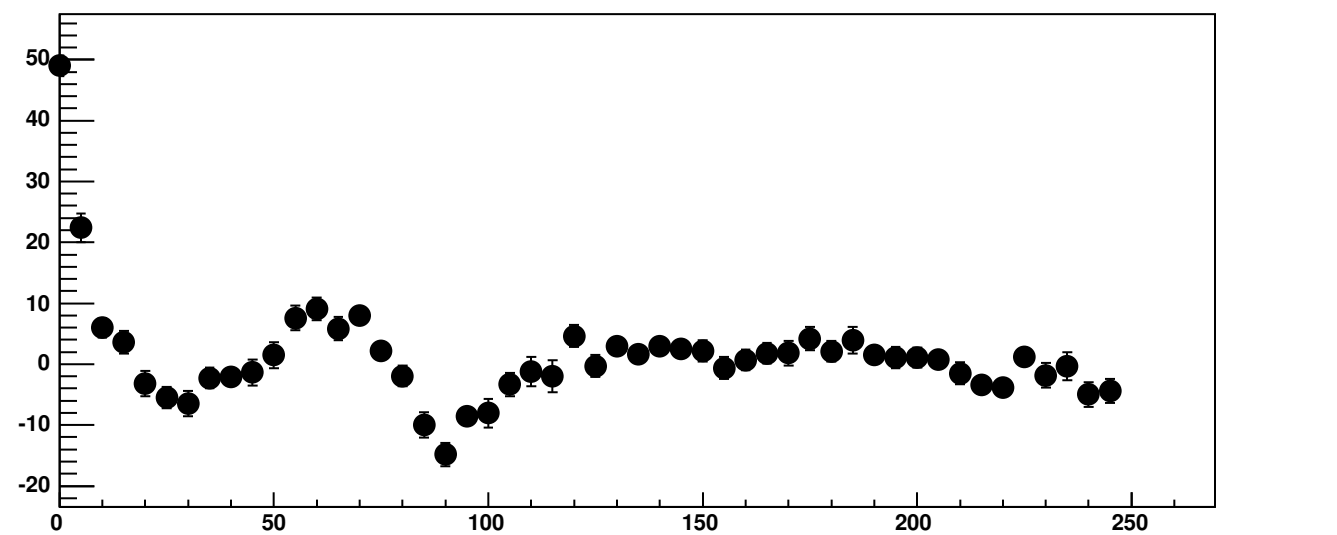
Chip 3, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

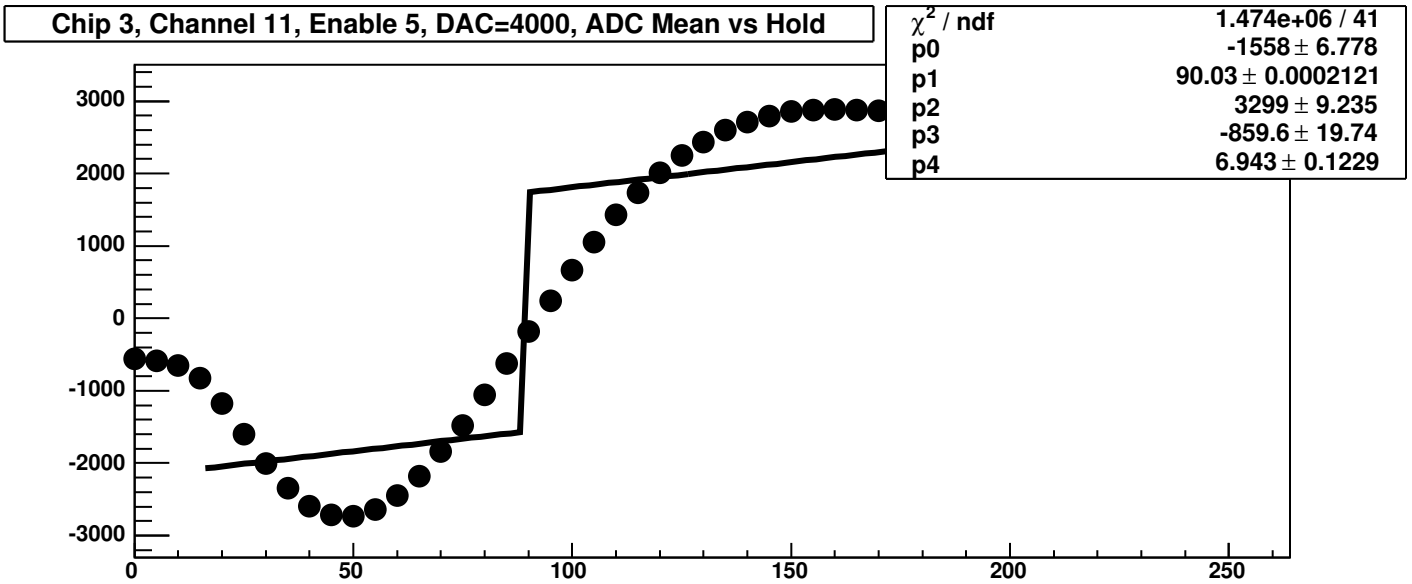


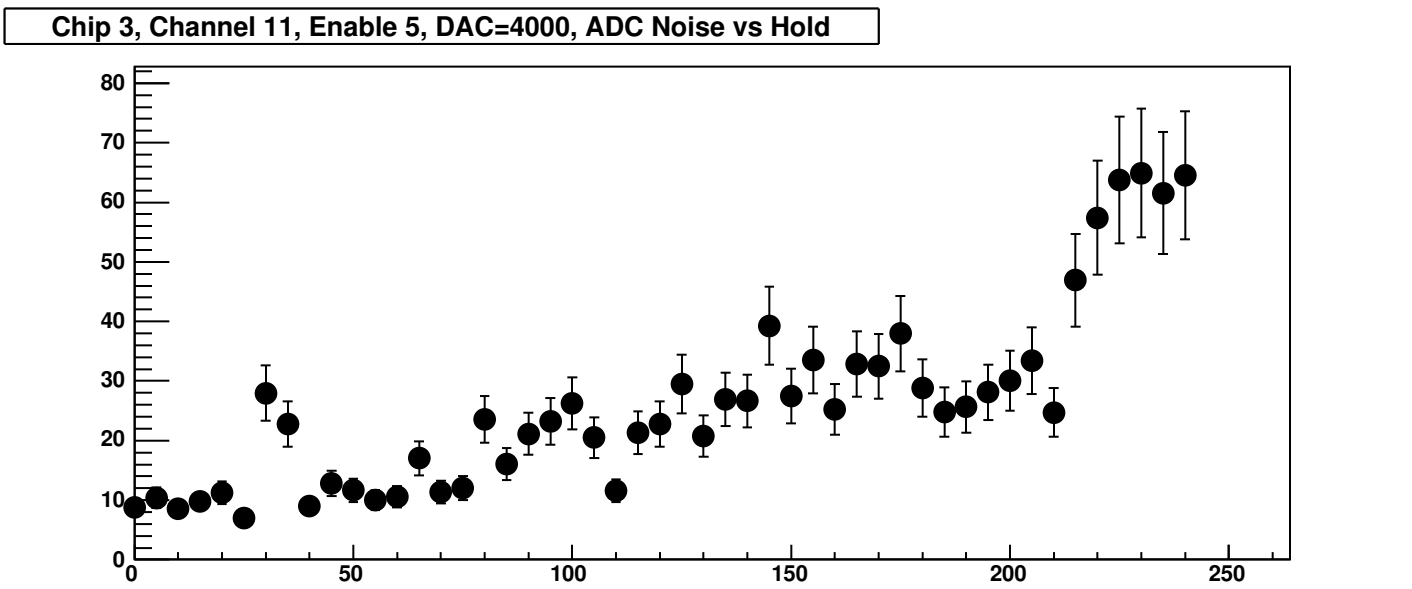
Chip 3, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold

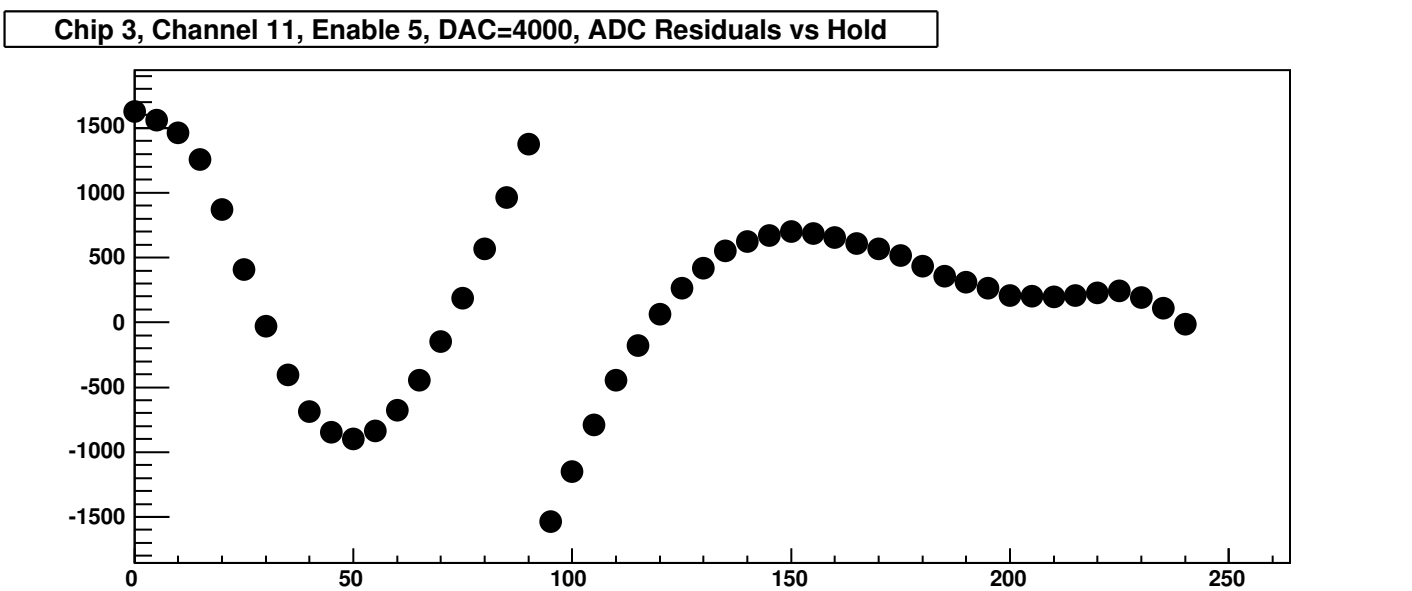


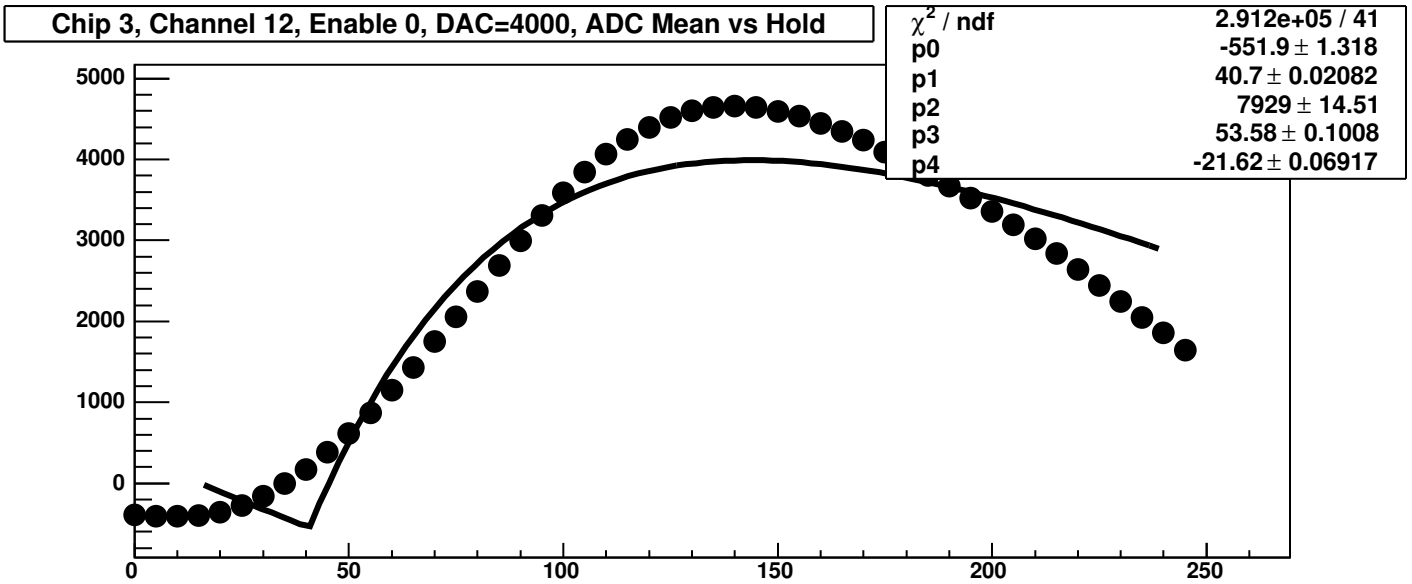
Chip 3, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

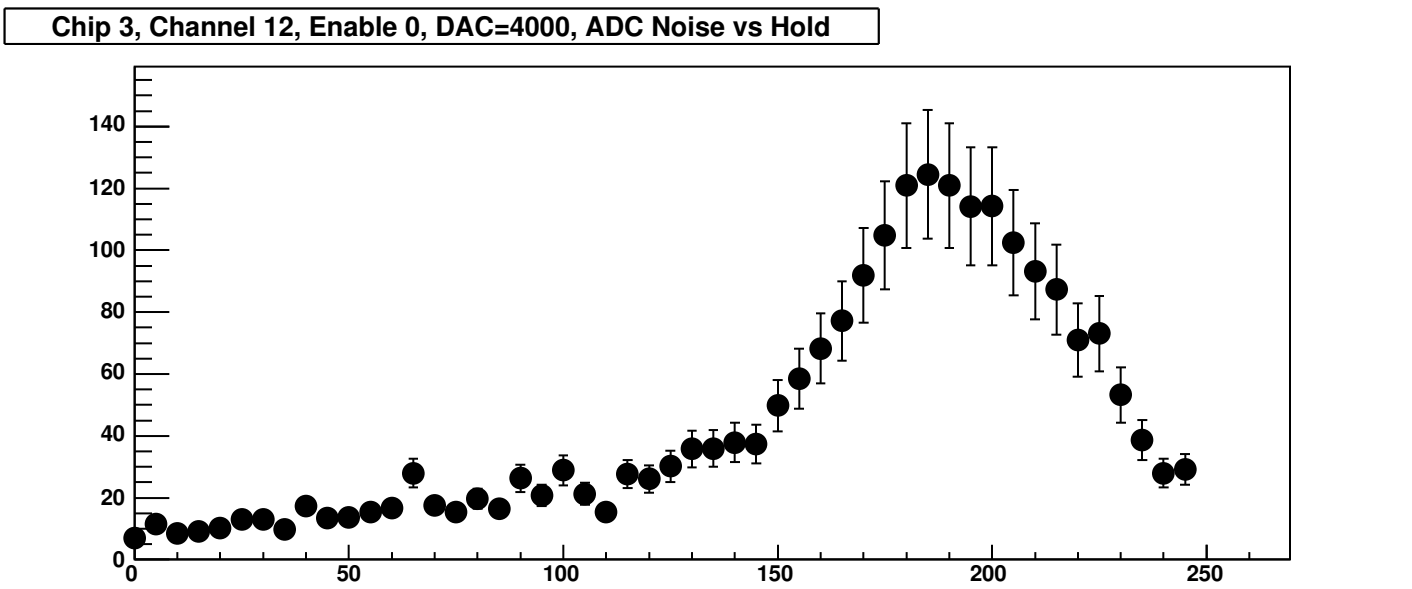


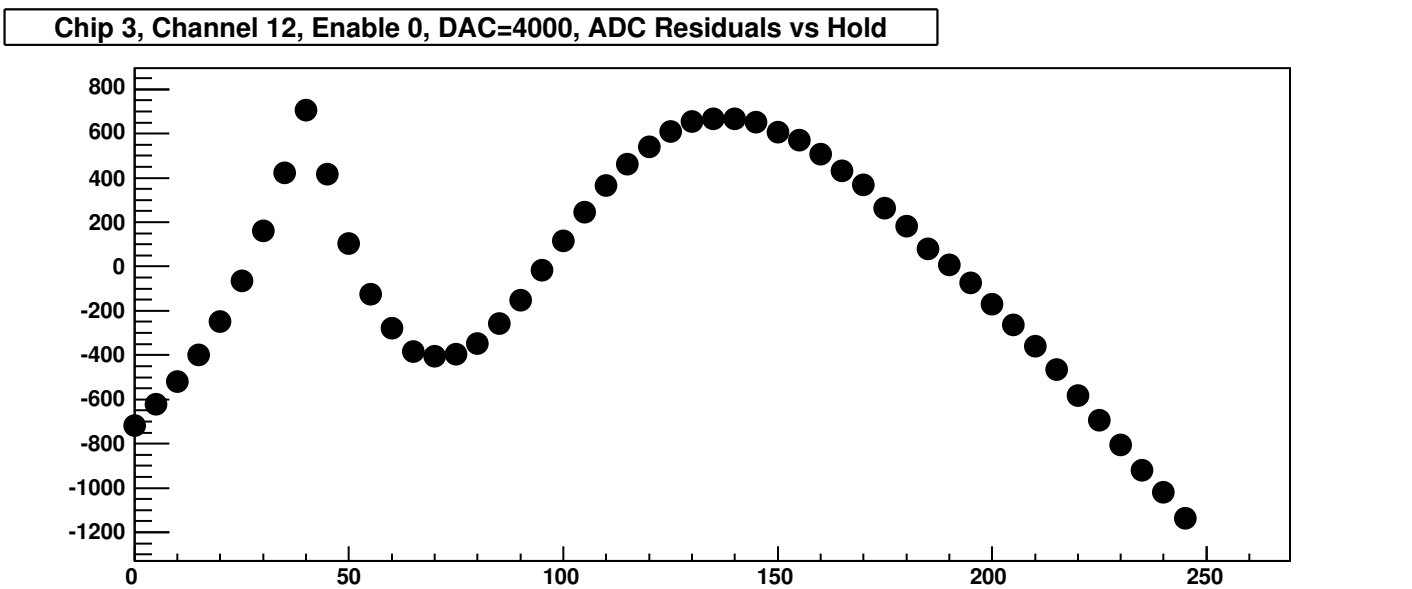

Chip 3, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.474e+06 / 41
p0 -1558 ± 6.778
p1 90.03 ± 0.0002121
p2 3299 ± 9.235
p3 -859.6 ± 19.74
p4 6.943 ± 0.1229
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250


Chip 3, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250

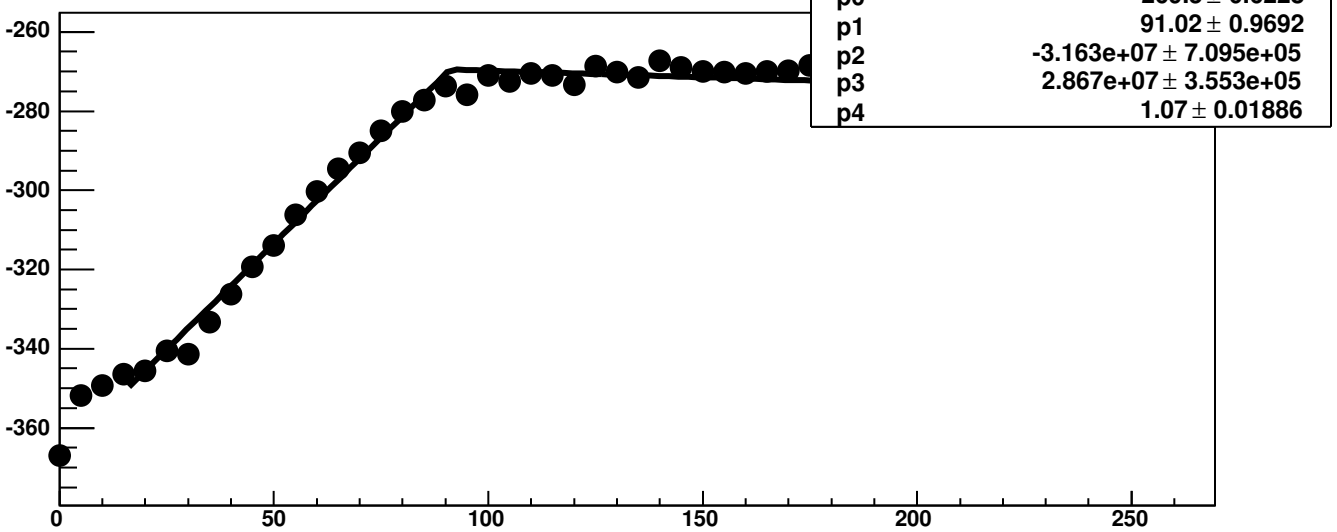

Chip 3, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold
1500
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250


Chip 3, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.912e+05 / 41
p0 -551.9 ± 1.318
p1 40.7 ± 0.02082
p2 7929 ± 14.51
p3 53.58 ± 0.1008
p4 -21.62 ± 0.06917
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

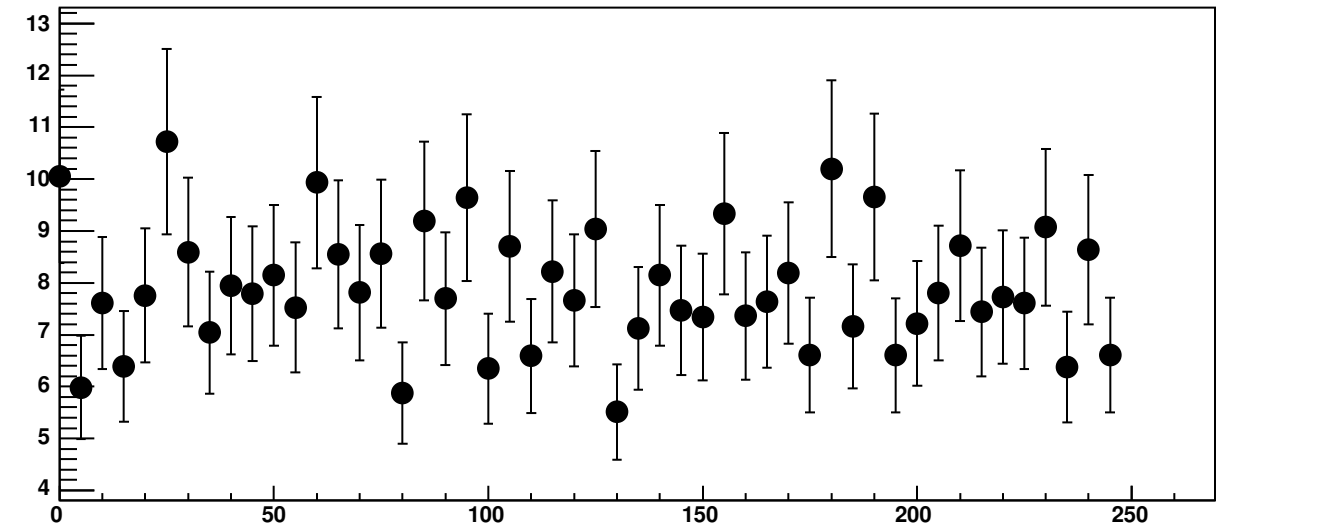

Chip 3, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 3, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold
800
600
400
200
0
-200
-400
-600
-800
-1000
-1200
0
50
100
150
200
250

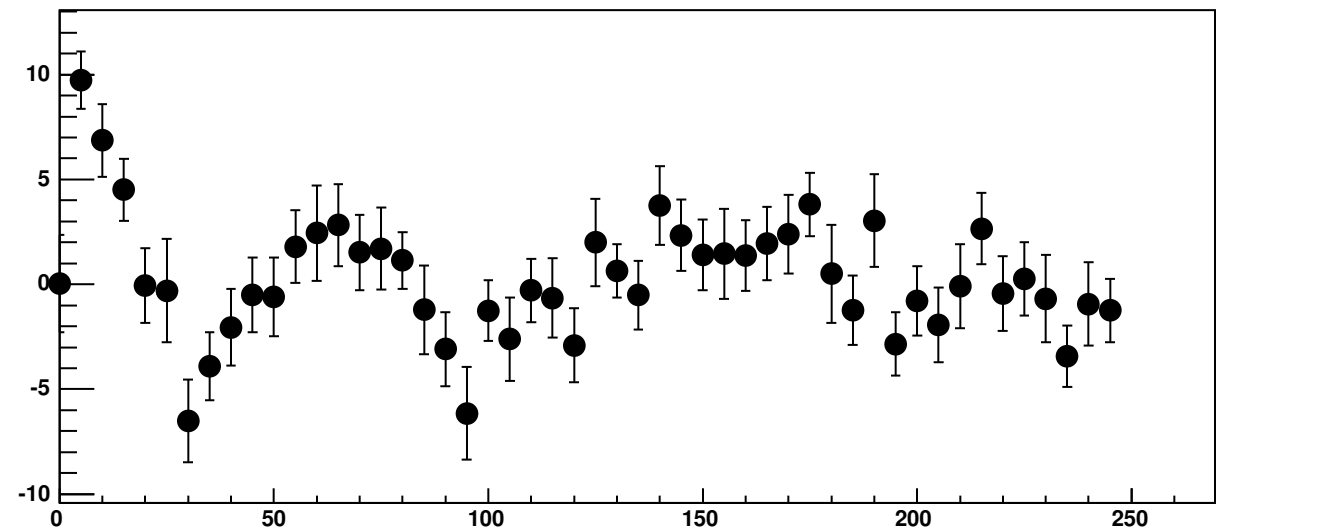
Chip 3, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold

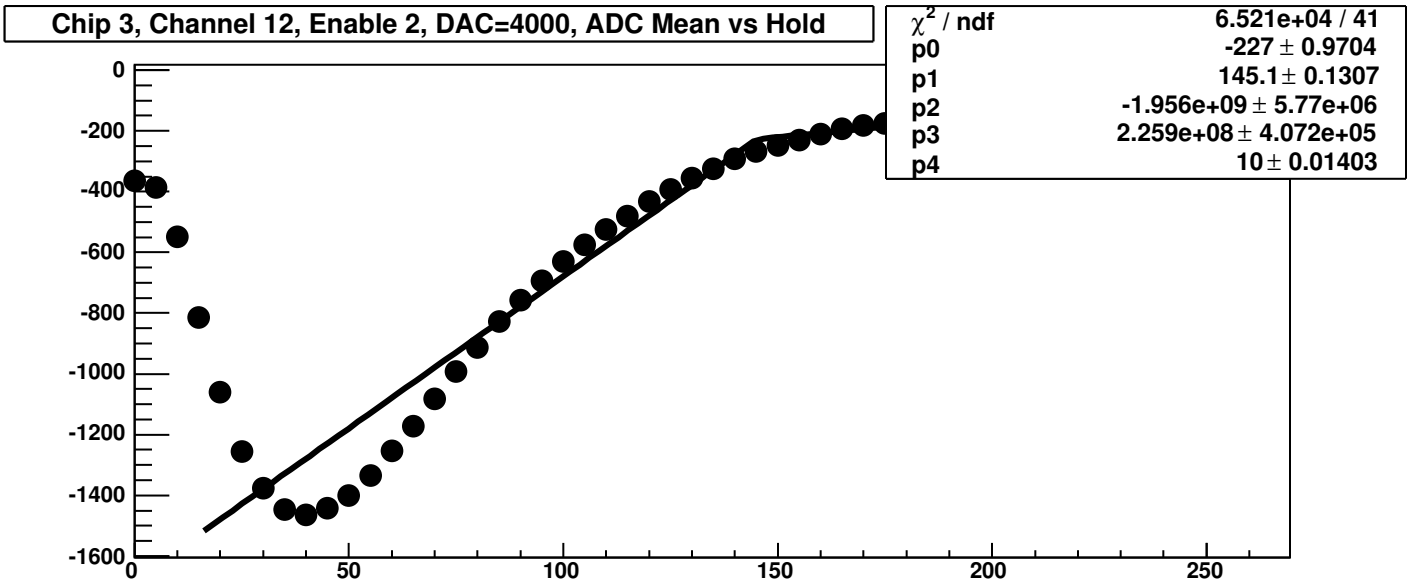


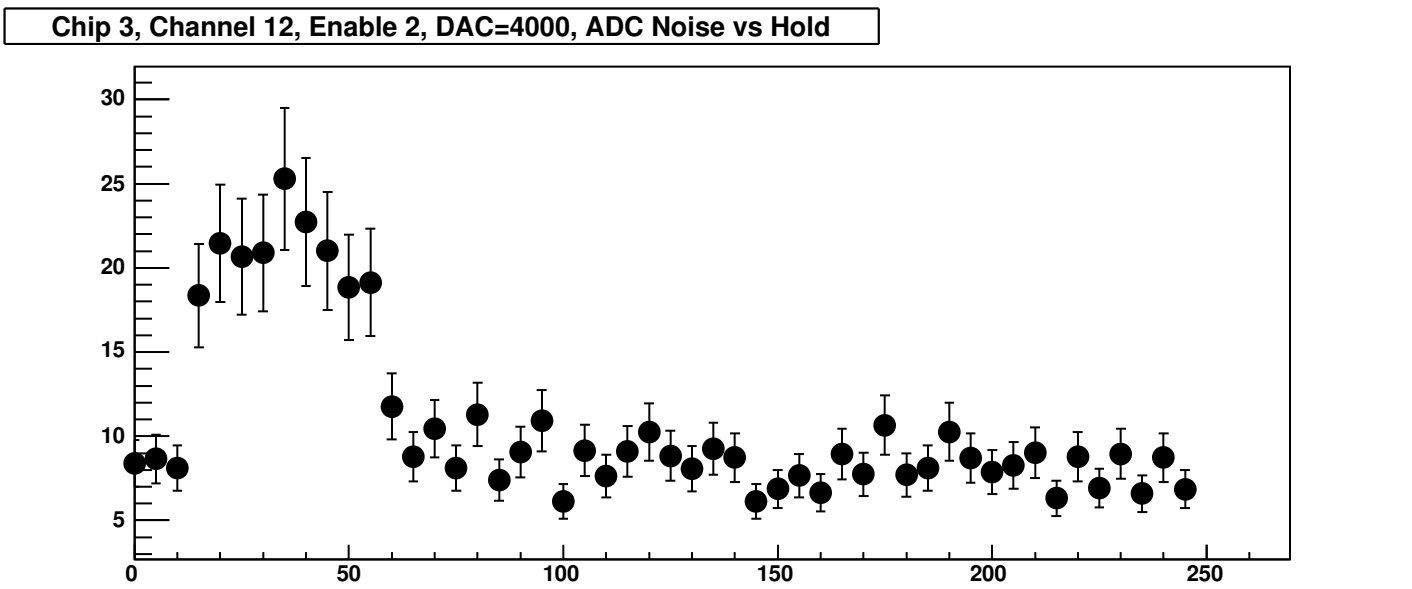
Chip 3, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold

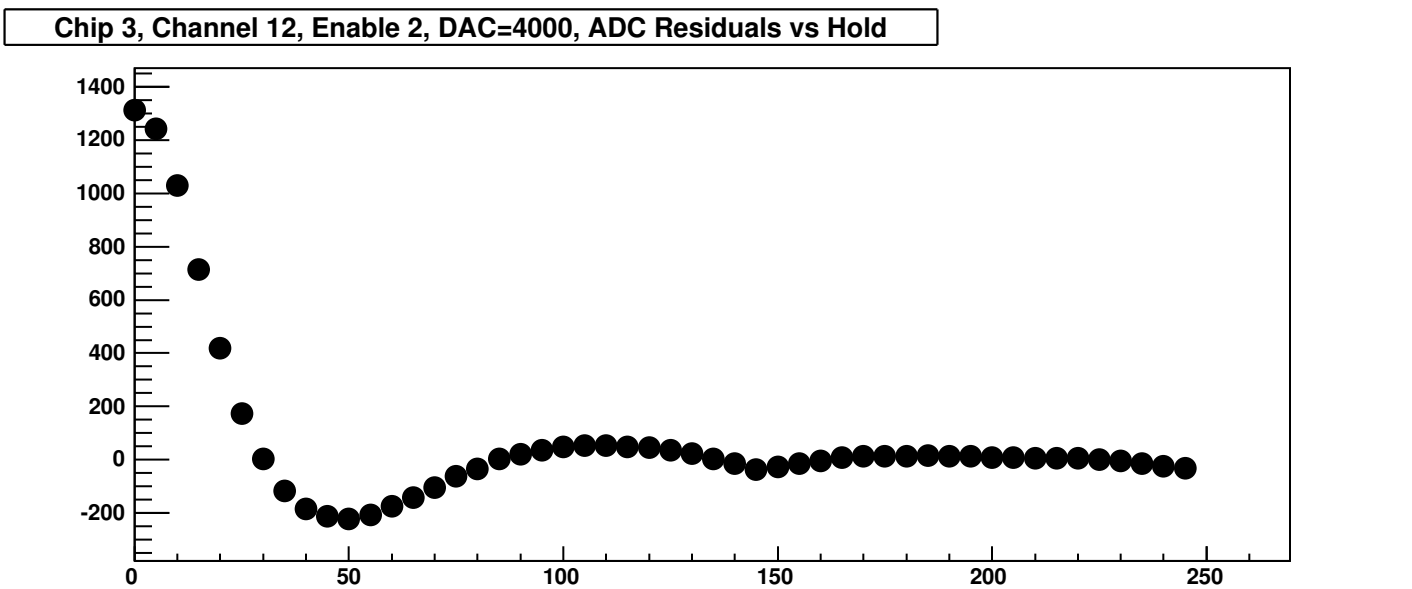


Chip 3, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold

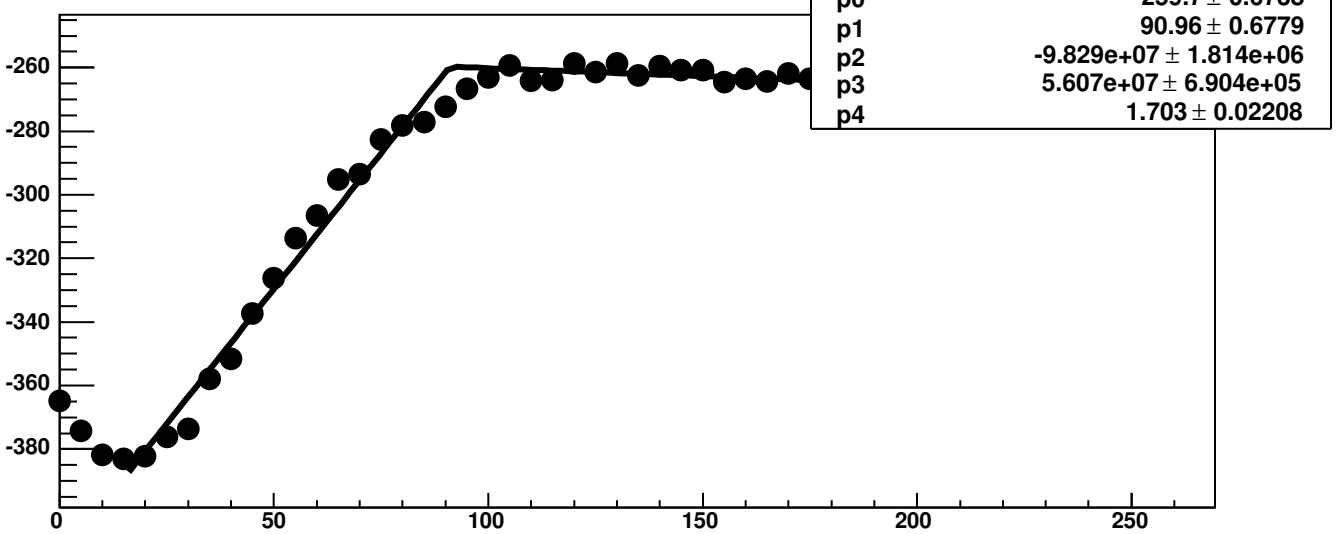



Chip 3, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 6.521e+04 / 41
p0 -227 ± 0.9704
p1 145.1 ± 0.1307
p2 -1.956e+09 ± 5.77e+06
p3 2.259e+08 ± 4.072e+05
p4 10 ± 0.01403
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
0
50
100
150
200
250

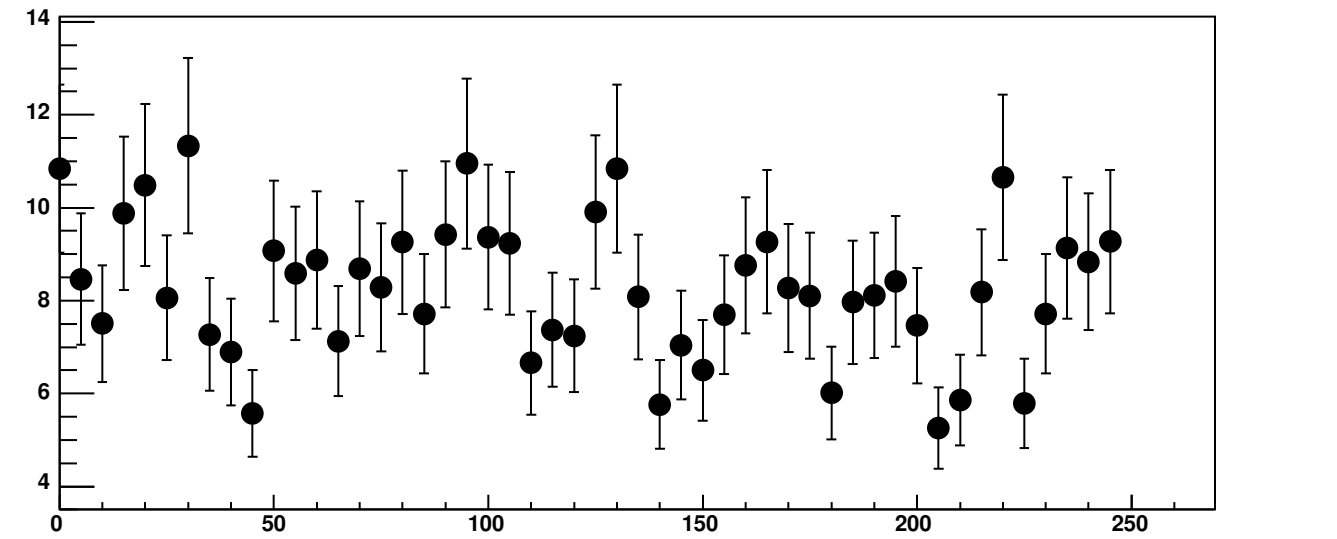

Chip 3, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold
30
25
20
15
10
5
0
50
100
150
200
250


Chip 3, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

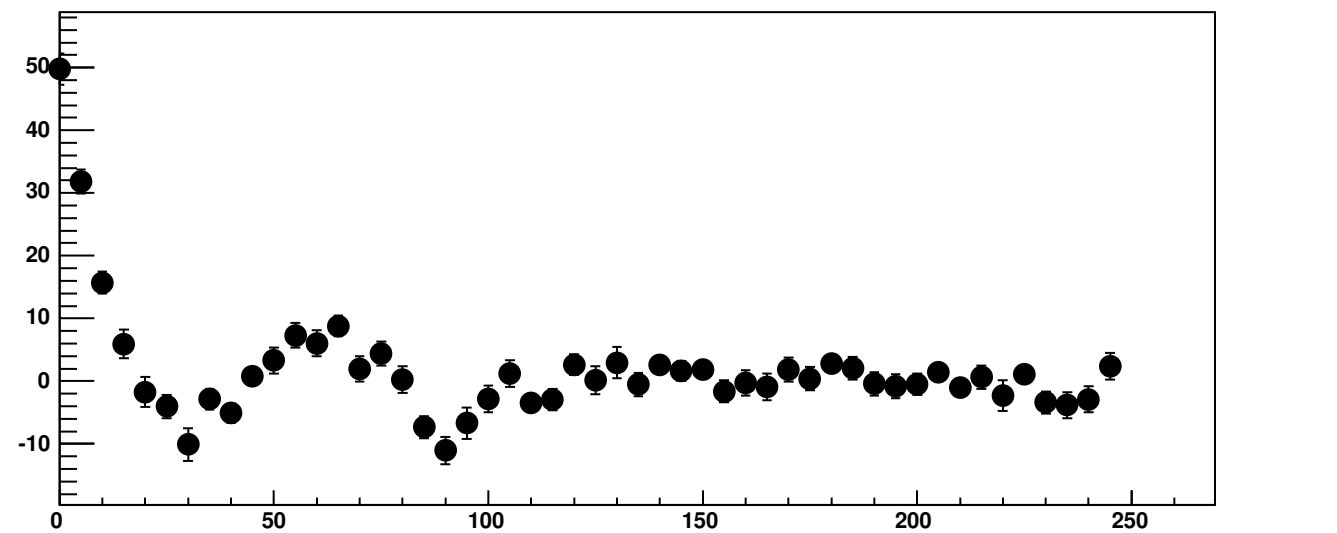
Chip 3, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold



Chip 3, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold

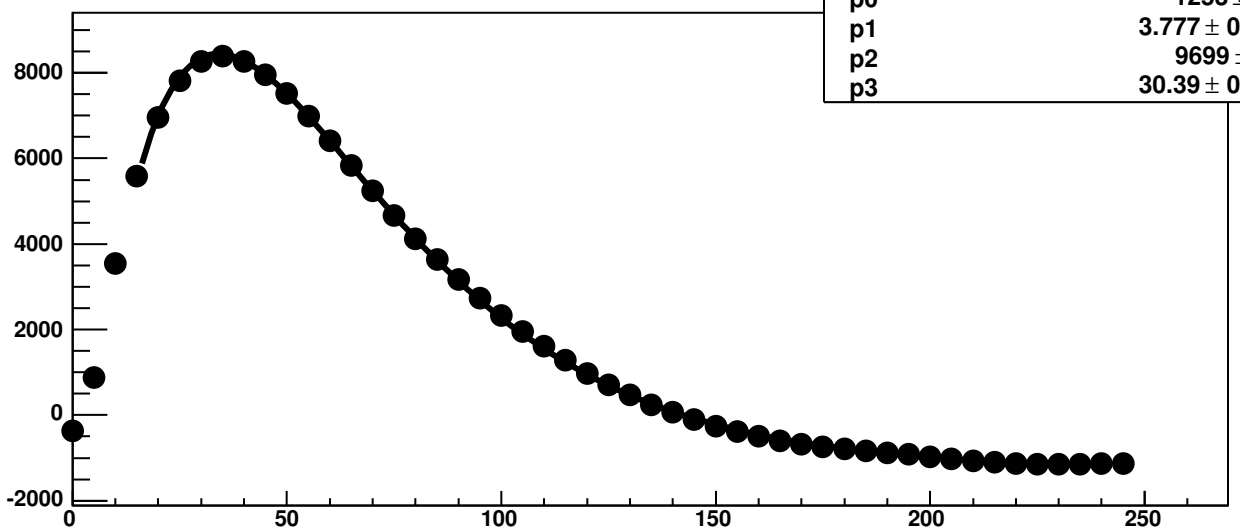


Chip 3, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

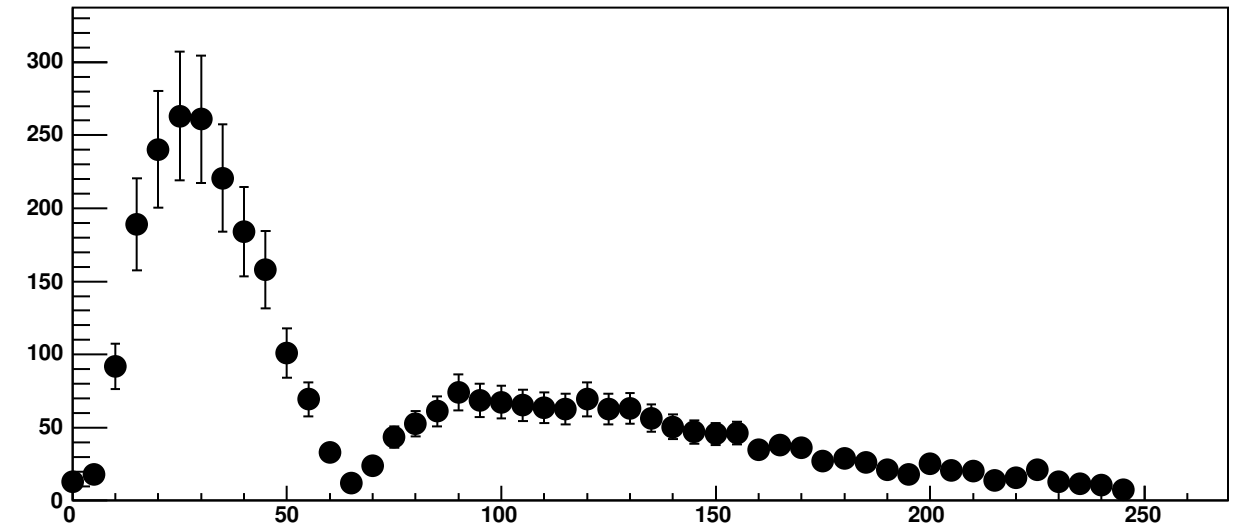


Chip 3, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold

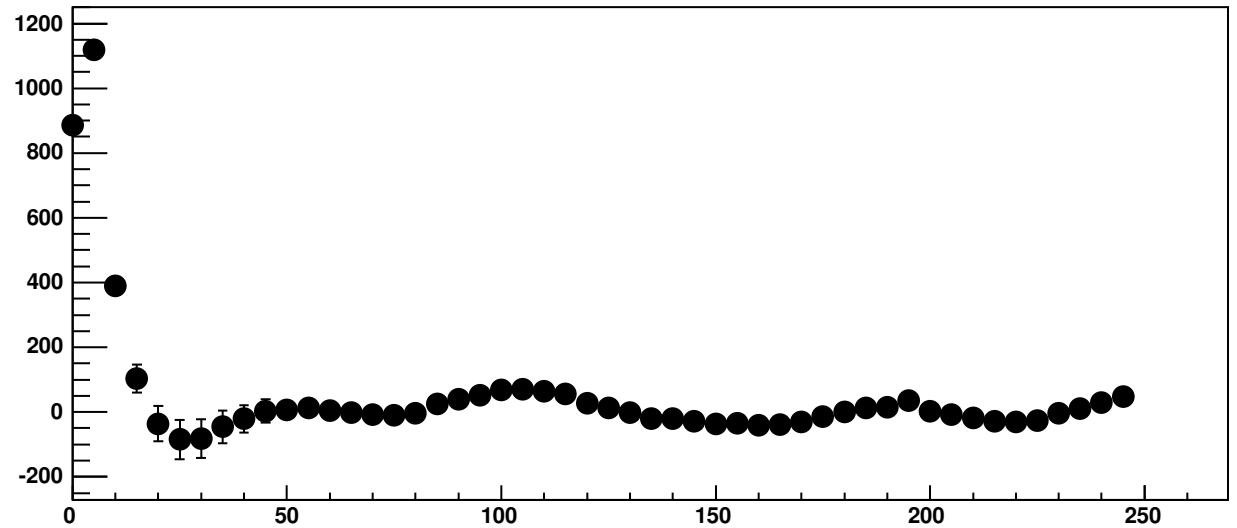
χ^2 / ndf	672.8 / 42
p0	-1253 ± 1.526
p1	3.777 ± 0.08577
p2	9699 ± 10.21
p3	30.39 ± 0.03585

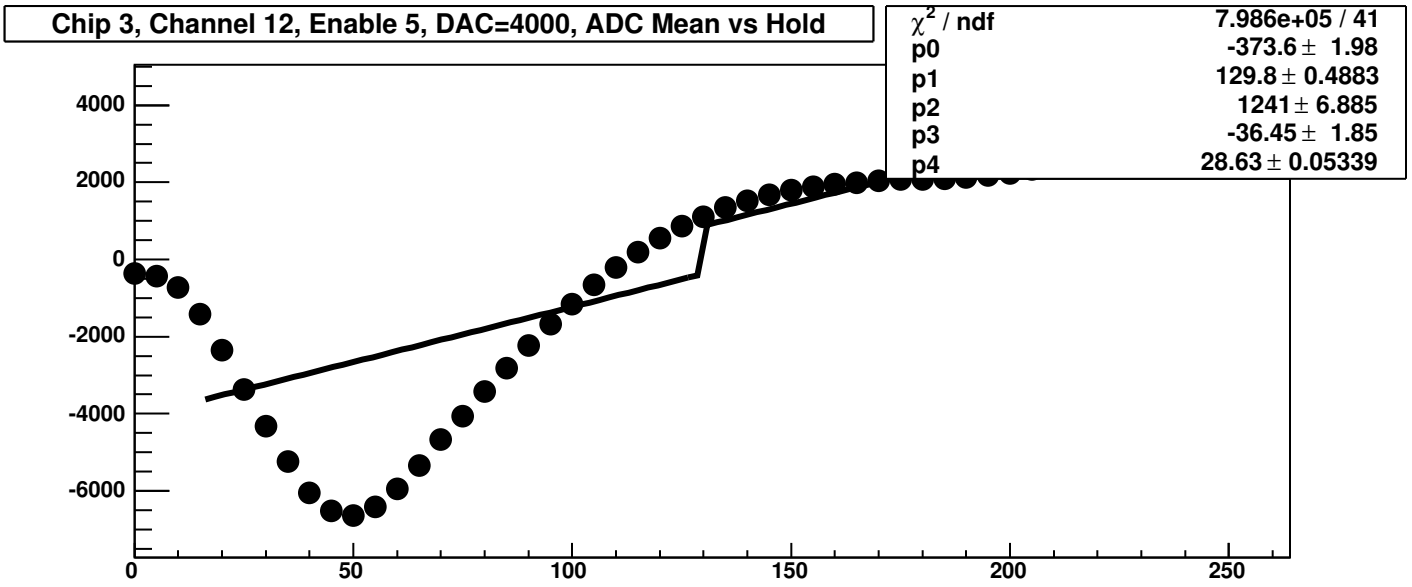


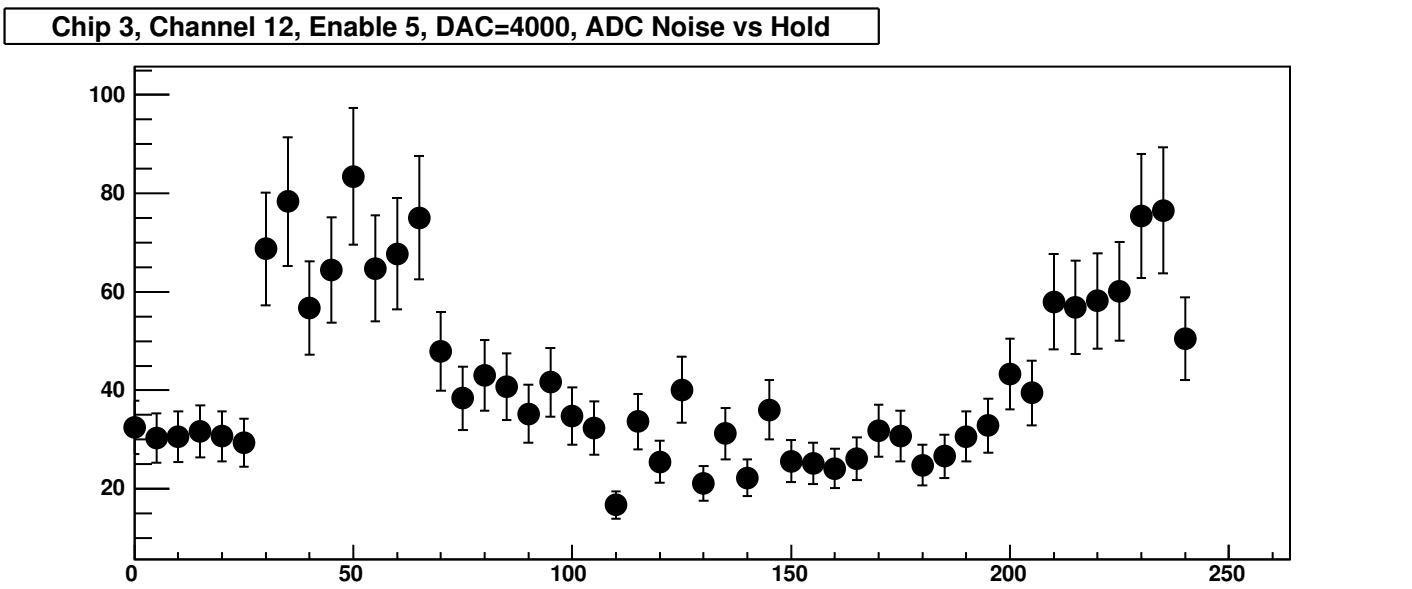
Chip 3, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold

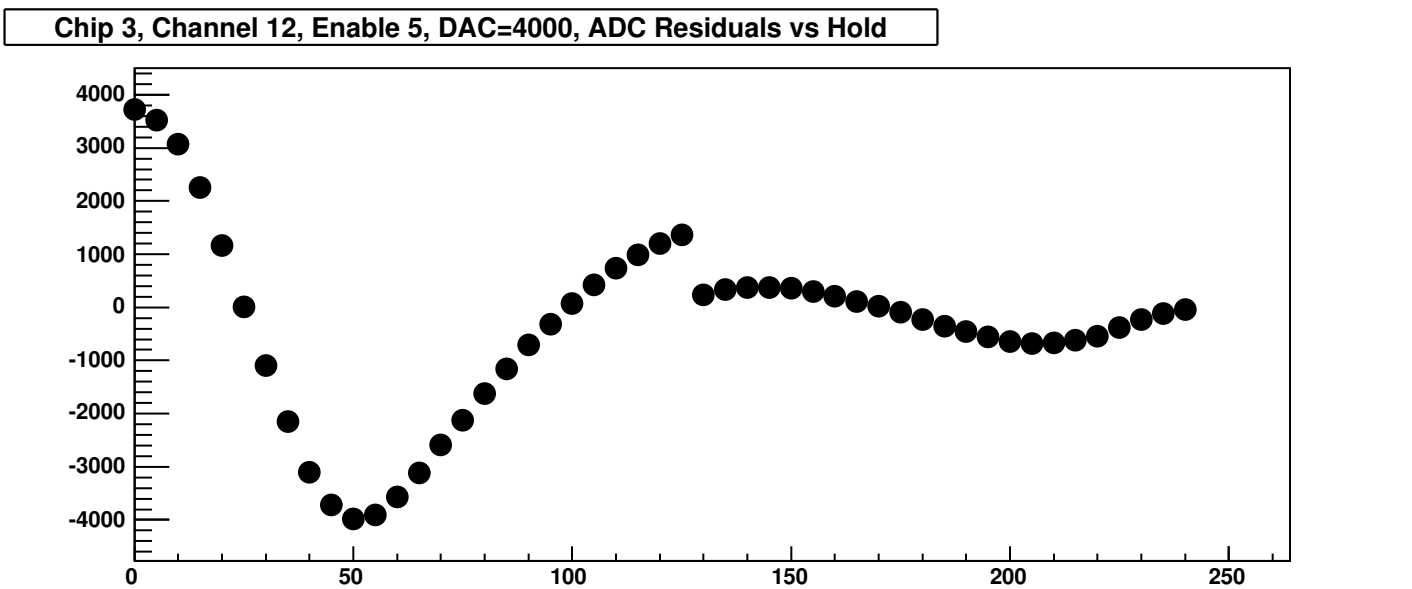


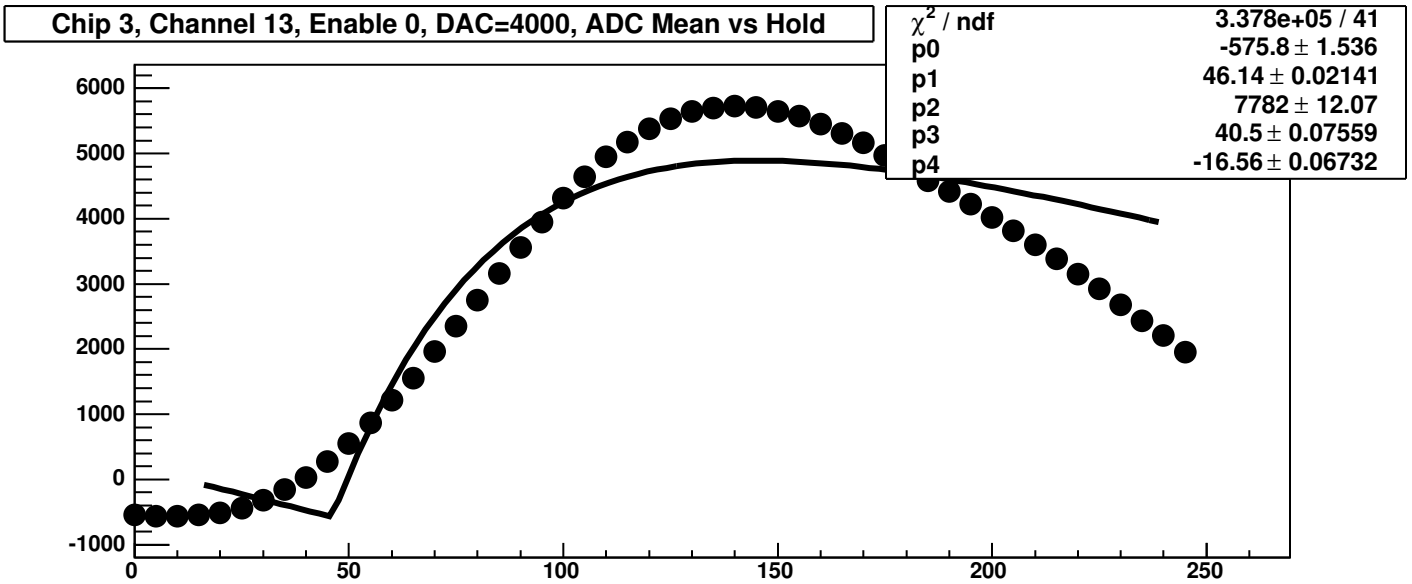
Chip 3, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold

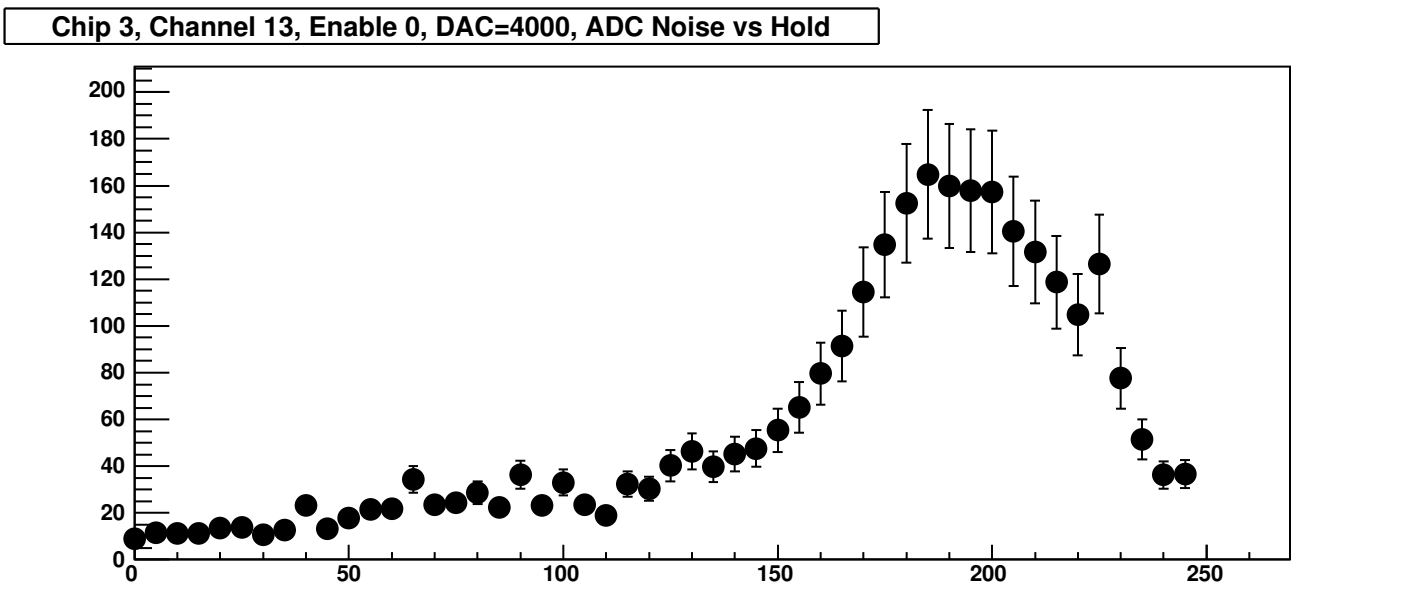


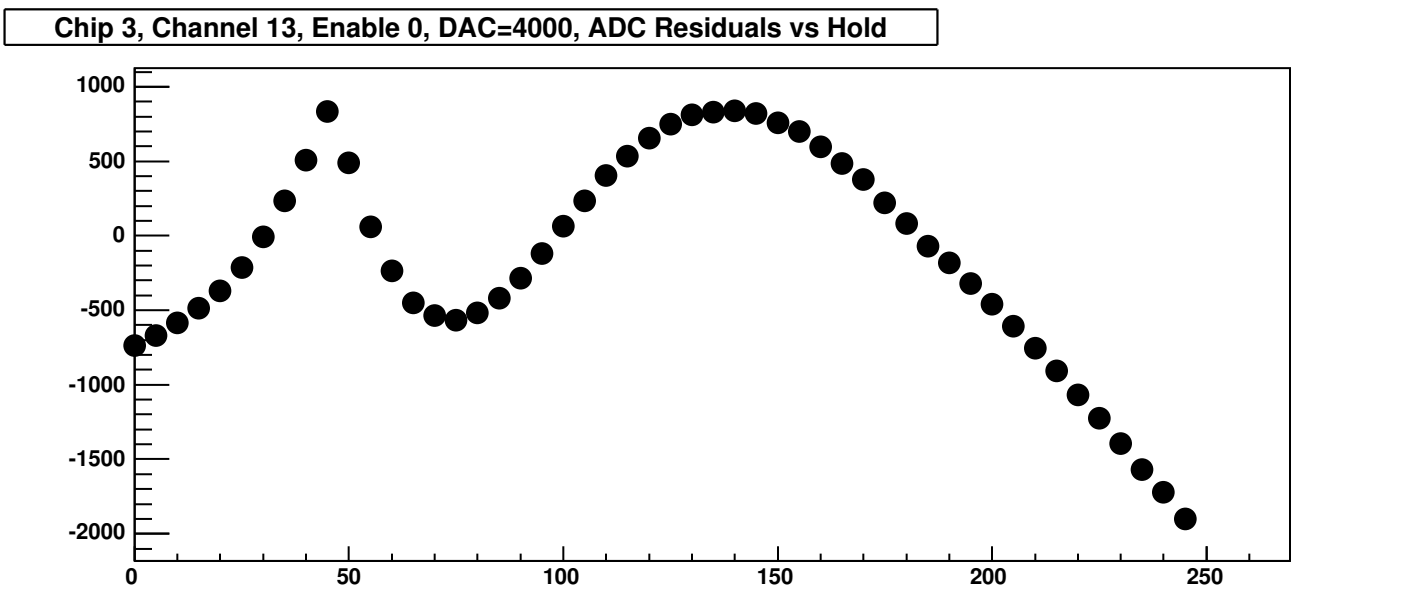

Chip 3, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 7.986e+05 / 41
p0 -373.6 ± 1.98
p1 129.8 ± 0.4883
p2 1241 ± 6.885
p3 -36.45 ± 1.85
p4 28.63 ± 0.05339
4000
2000
0
-2000
-4000
-6000
0
50
100
150
200
250

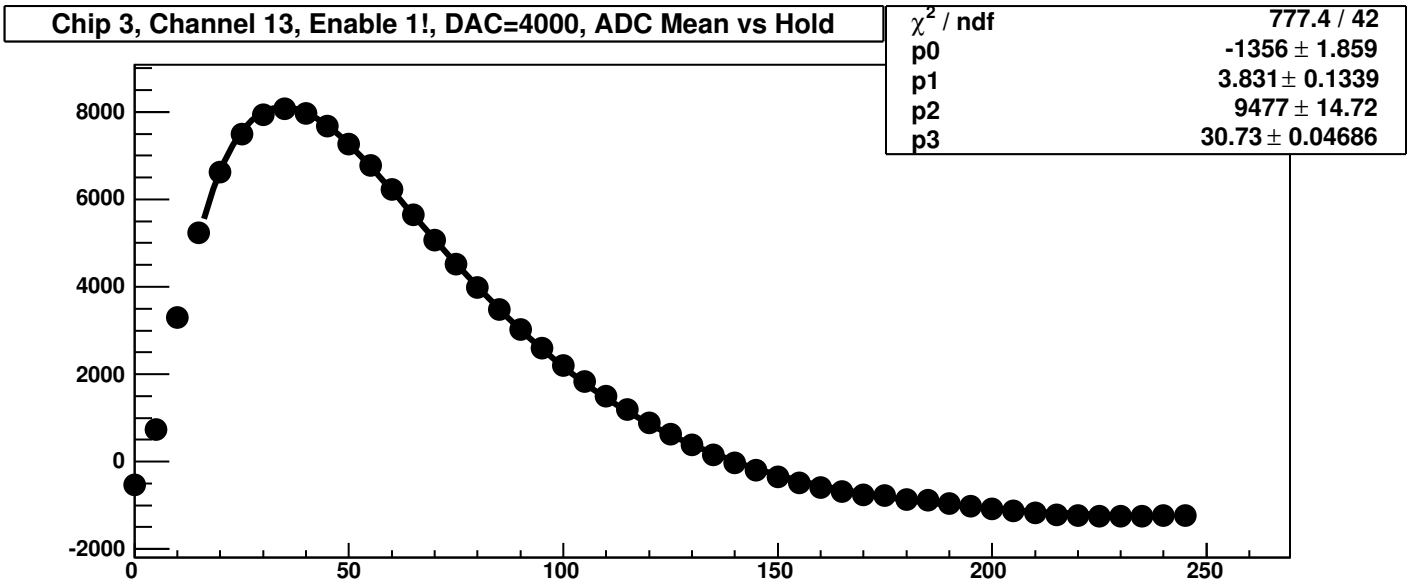

Chip 3, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
50
100
150
200
250

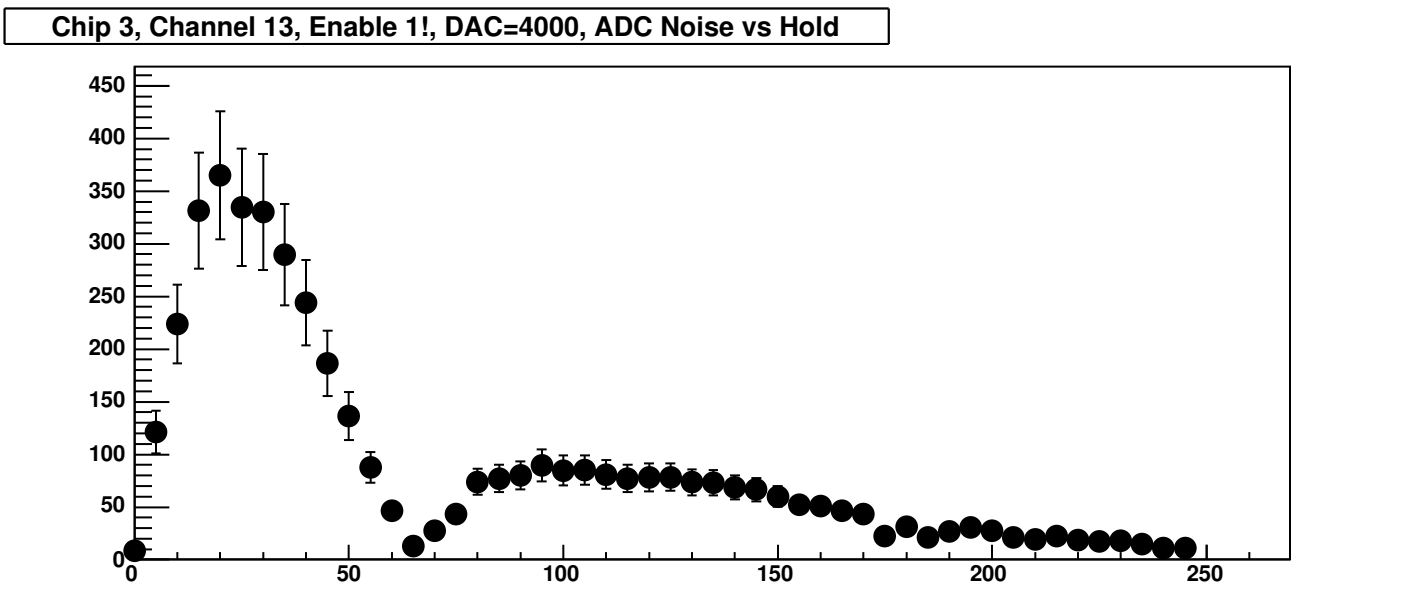

Chip 3, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold
4000
3000
2000
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

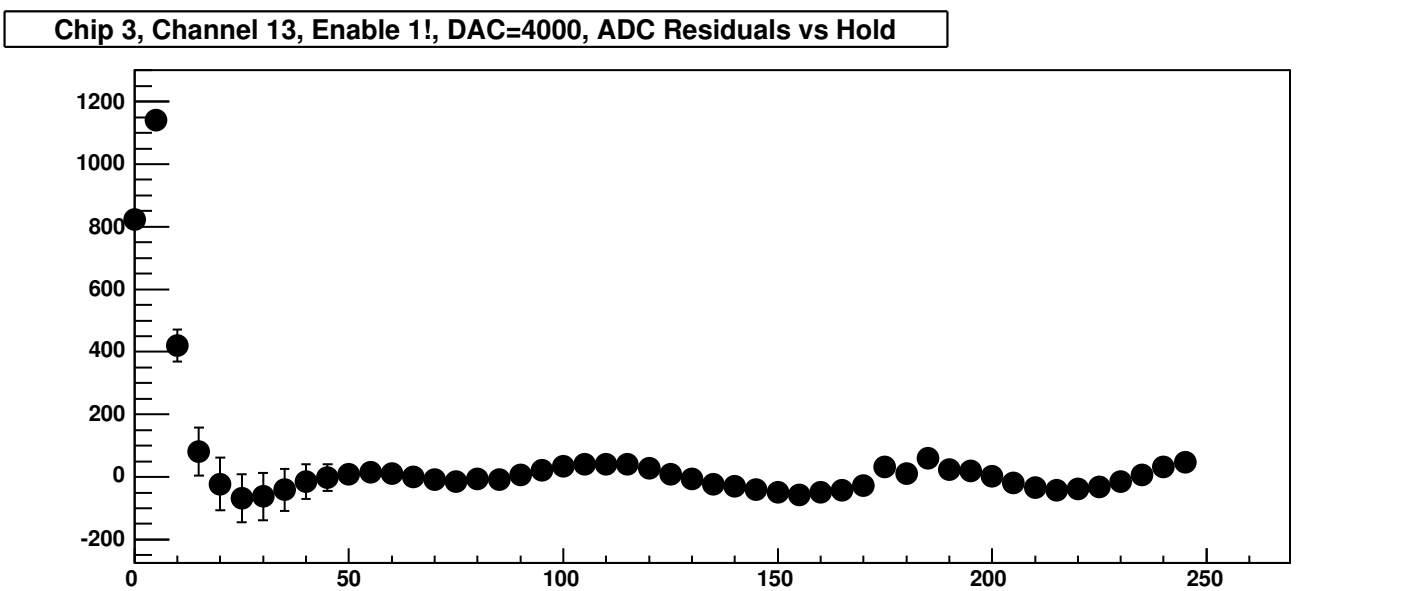

Chip 3, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.378e+05 / 41
p0 -575.8 ± 1.536
p1 46.14 ± 0.02141
p2 7782 ± 12.07
p3 40.5 ± 0.07559
p4 -16.56 ± 0.06732
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 3, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

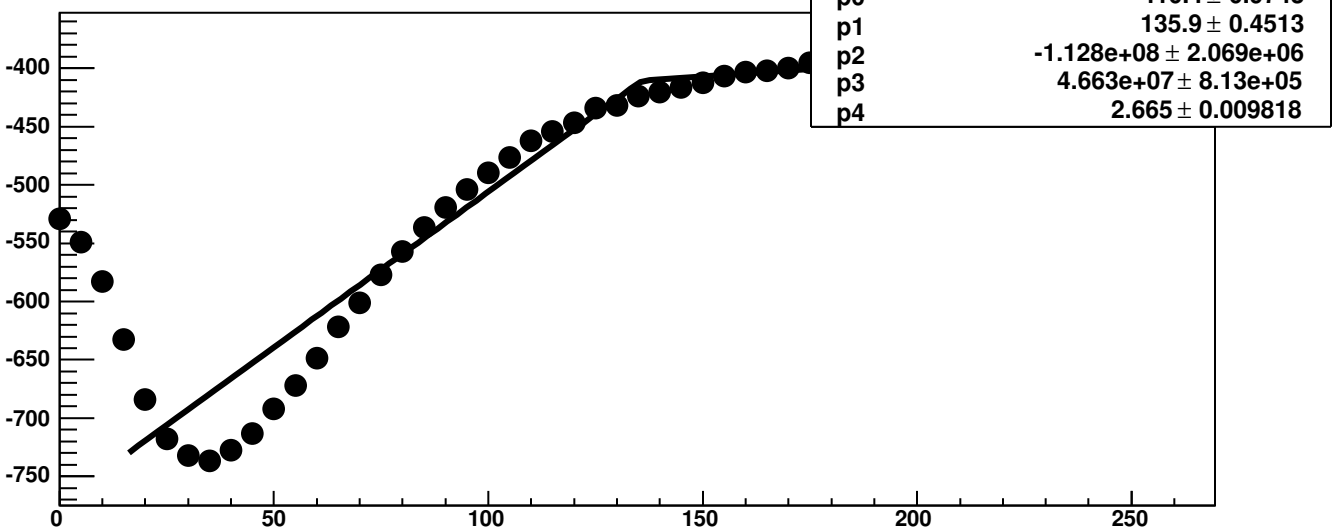

Chip 3, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250


Chip 3, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 777.4 / 42
p0 -1356 ± 1.859
p1 3.831 ± 0.1339
p2 9477 ± 14.72
p3 30.73 ± 0.04686
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

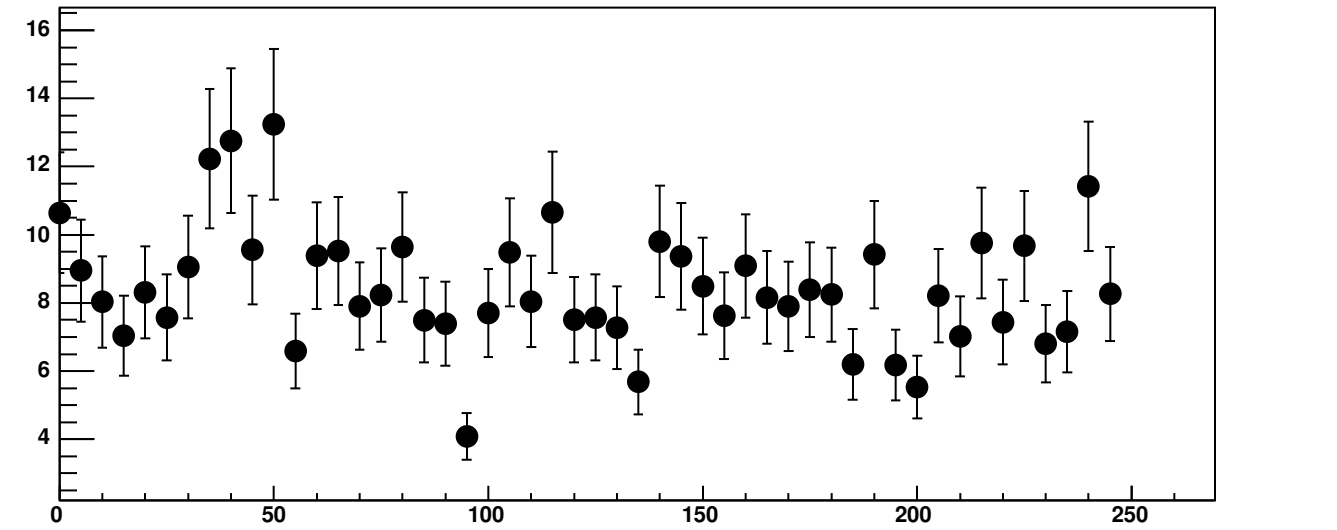

Chip 3, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold
450
400
350
300
250
200
150
100
50
0
0
50
100
150
200
250


Chip 3, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

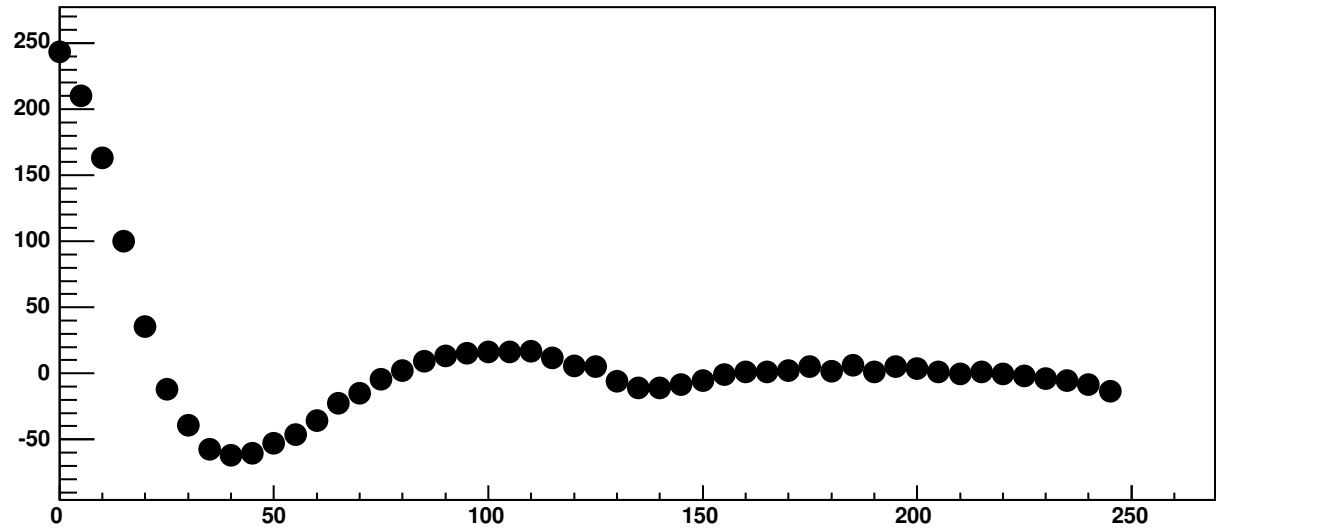
Chip 3, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold



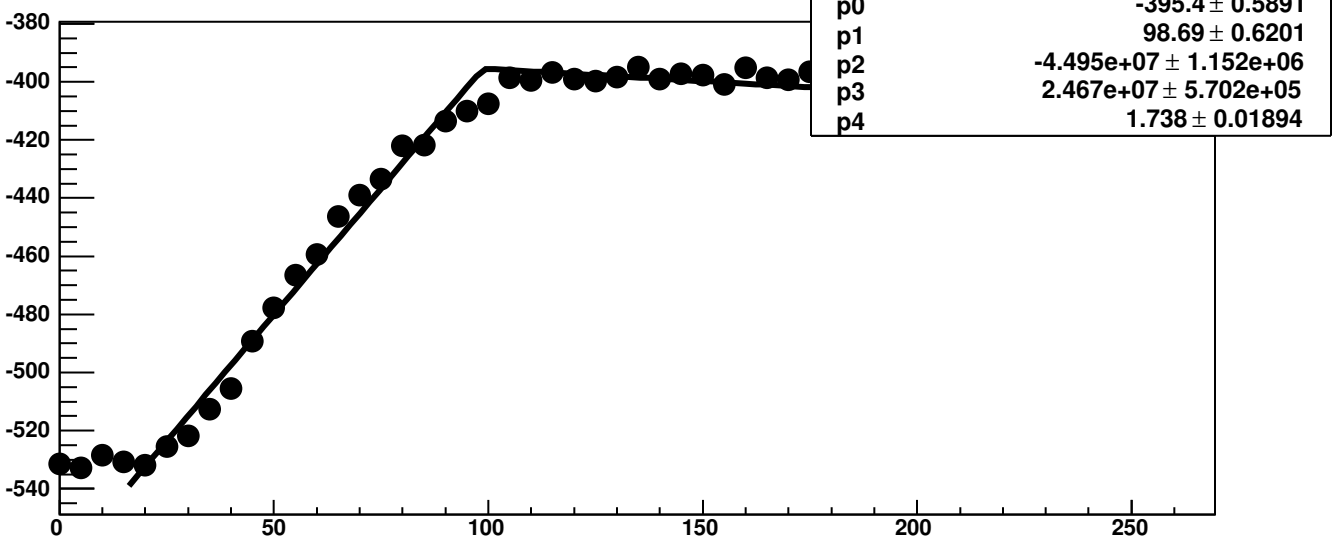
Chip 3, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



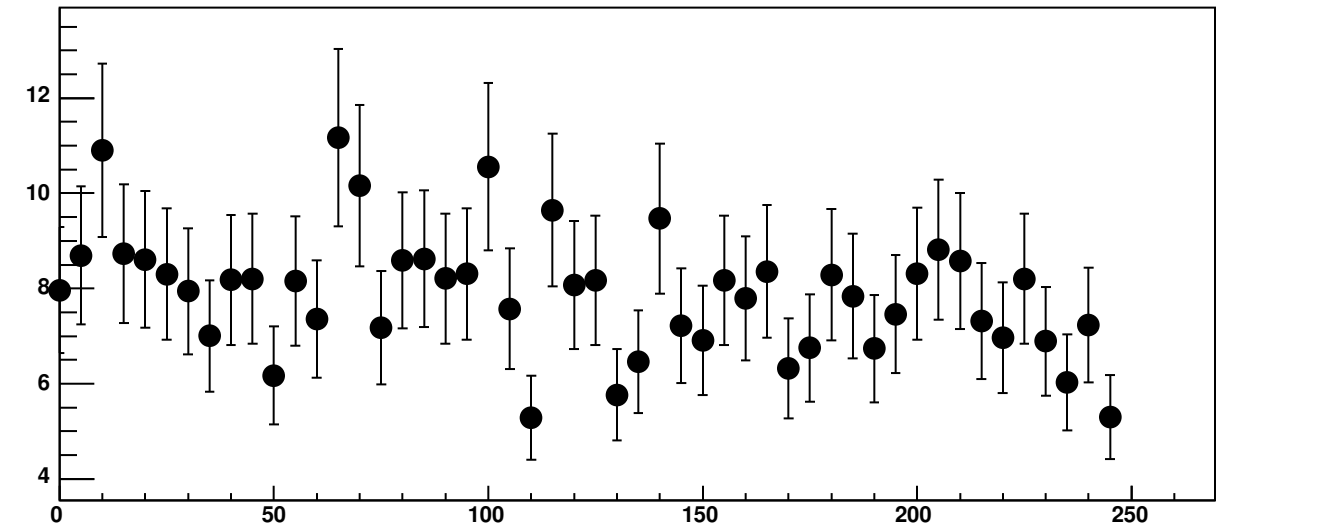
Chip 3, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold



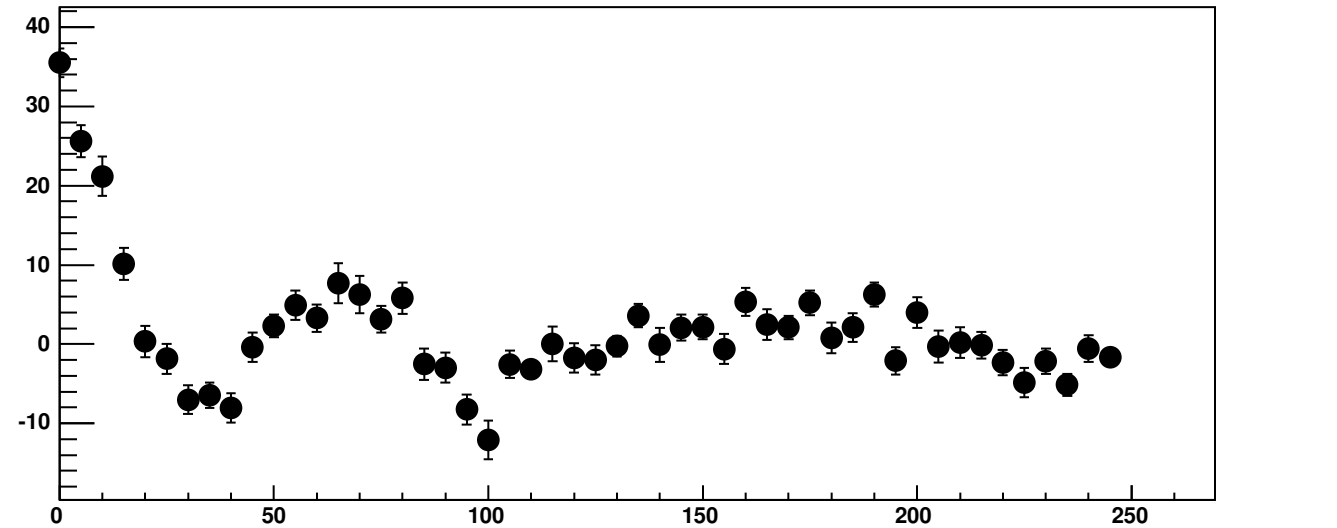
Chip 3, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold



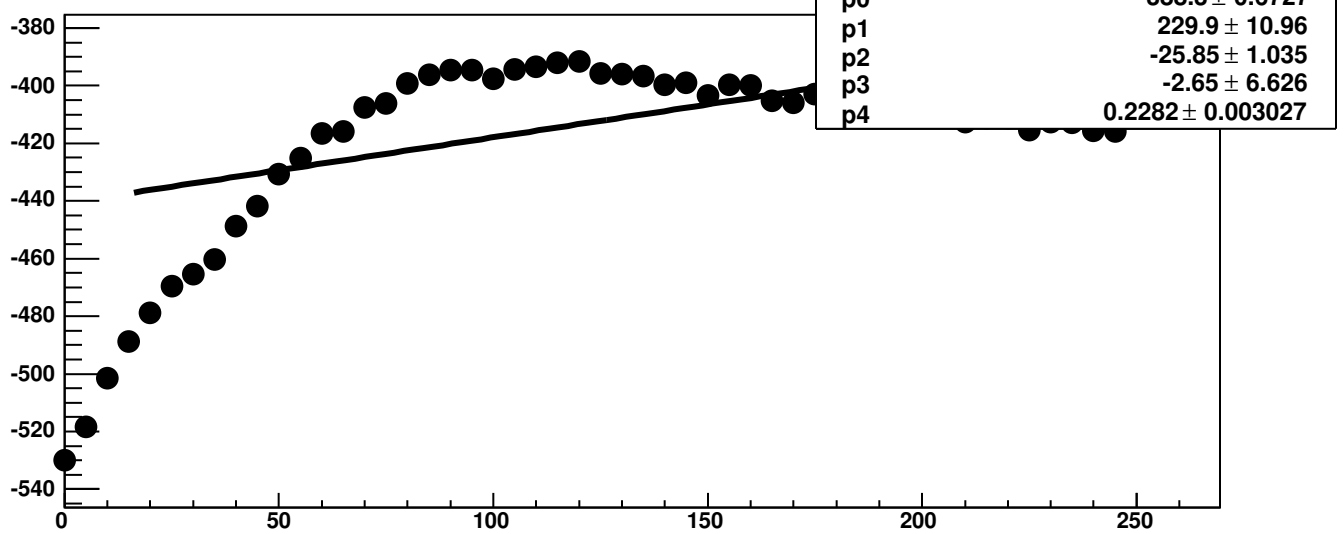
Chip 3, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



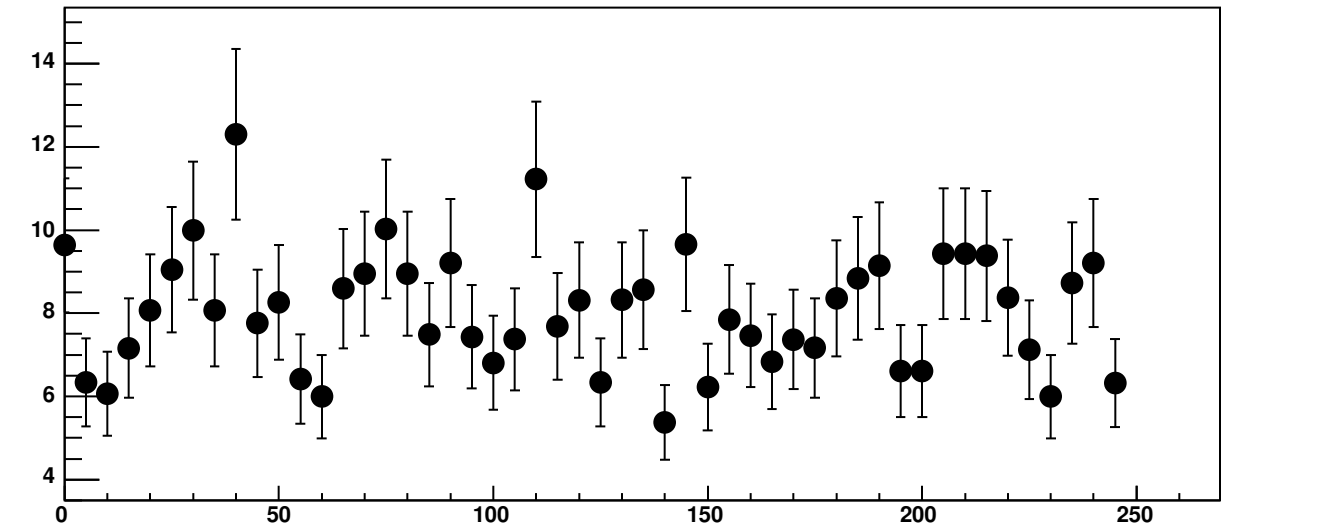
Chip 3, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold



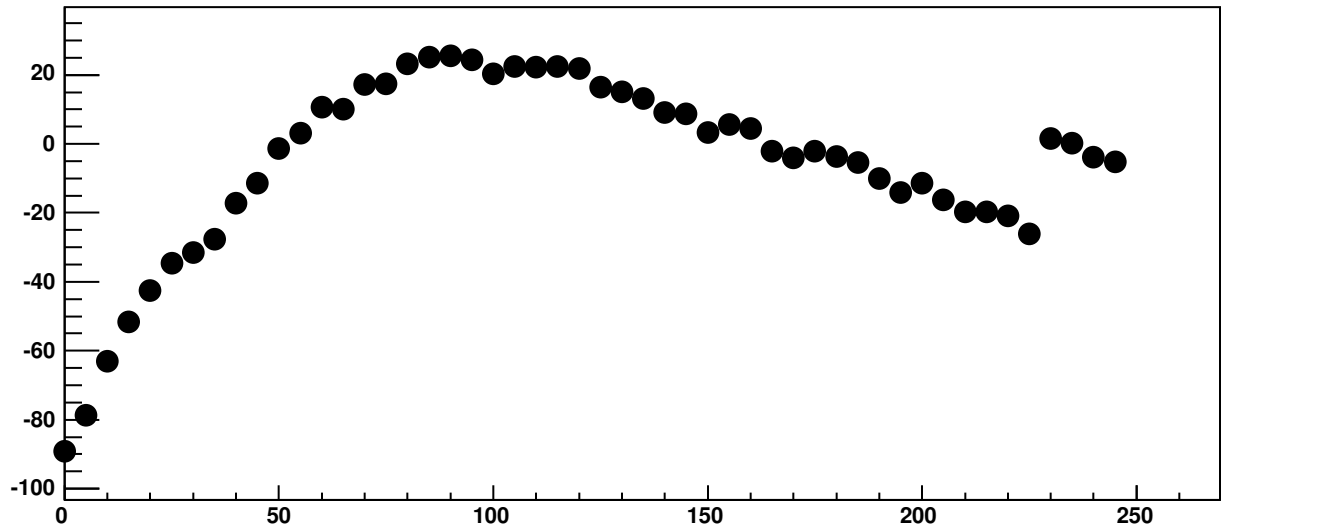
Chip 3, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold



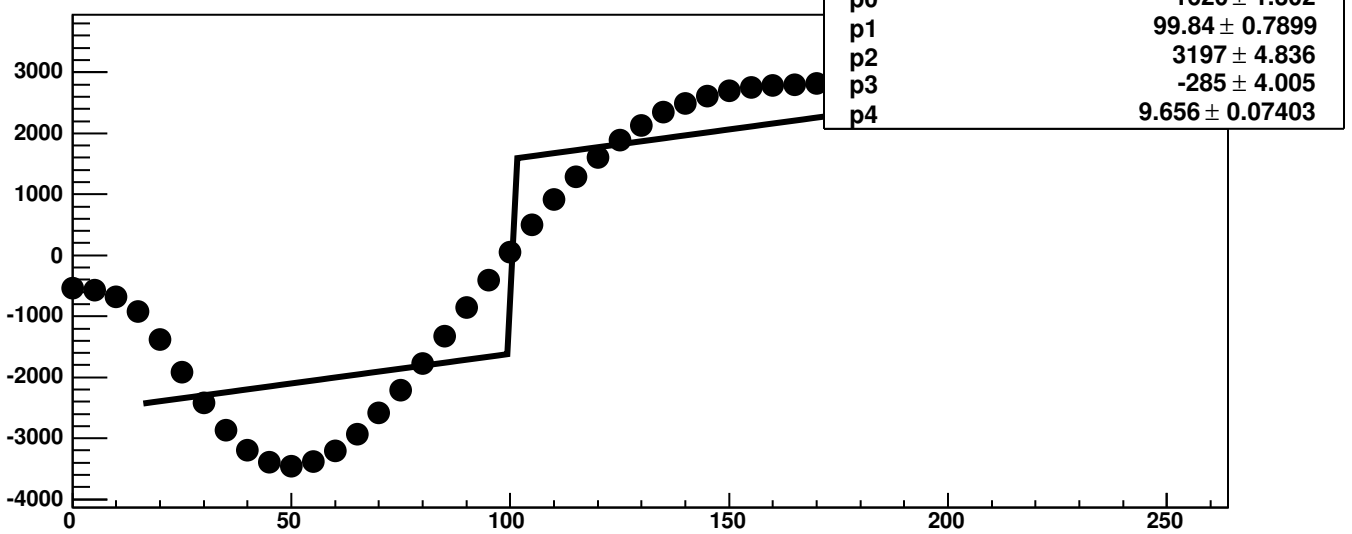
Chip 3, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold



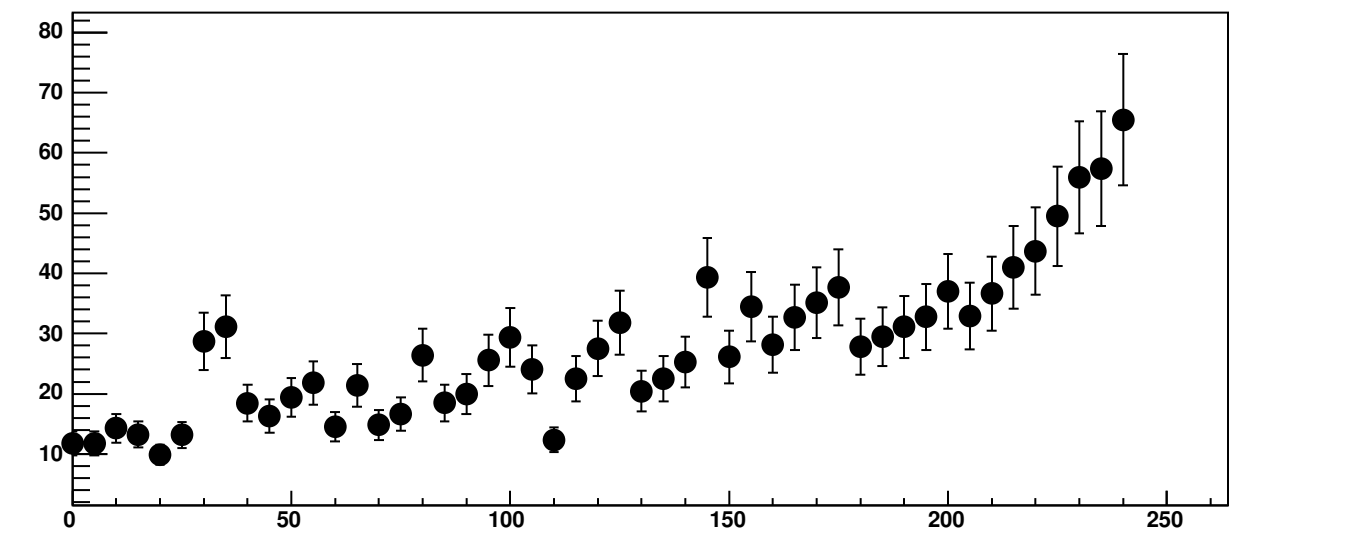
Chip 3, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold



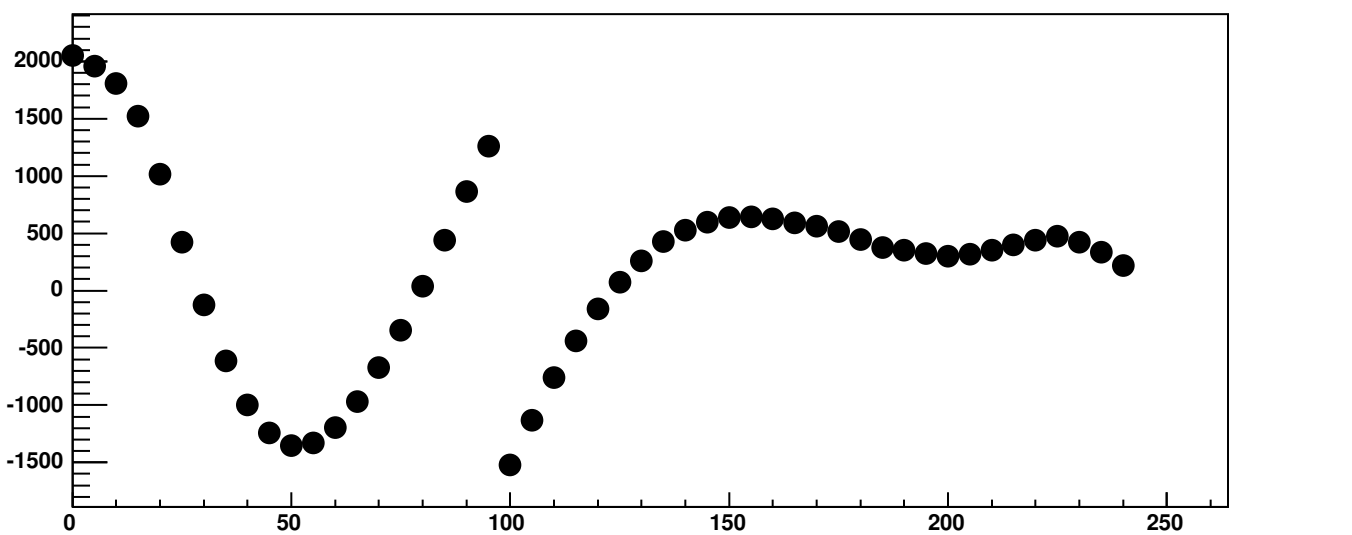
Chip 3, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold



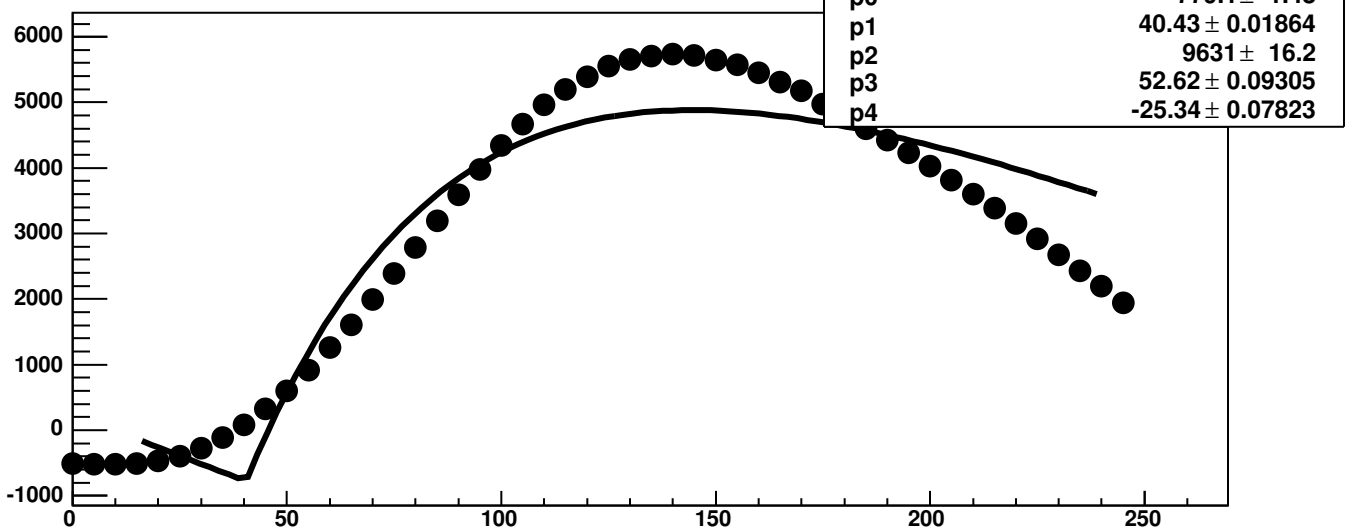
Chip 3, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



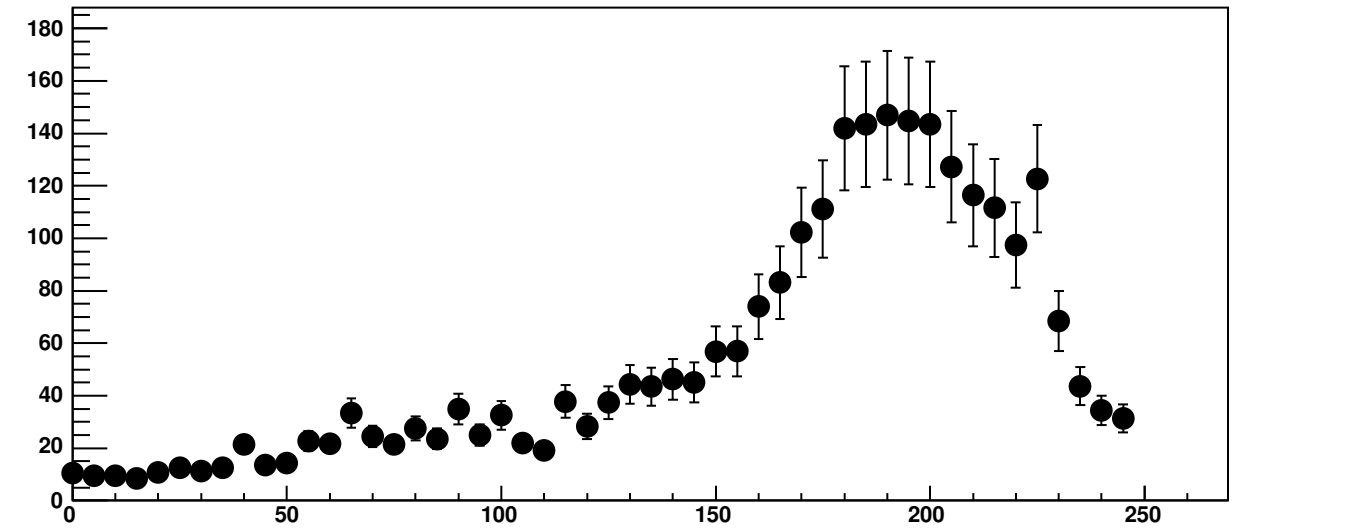
Chip 3, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold



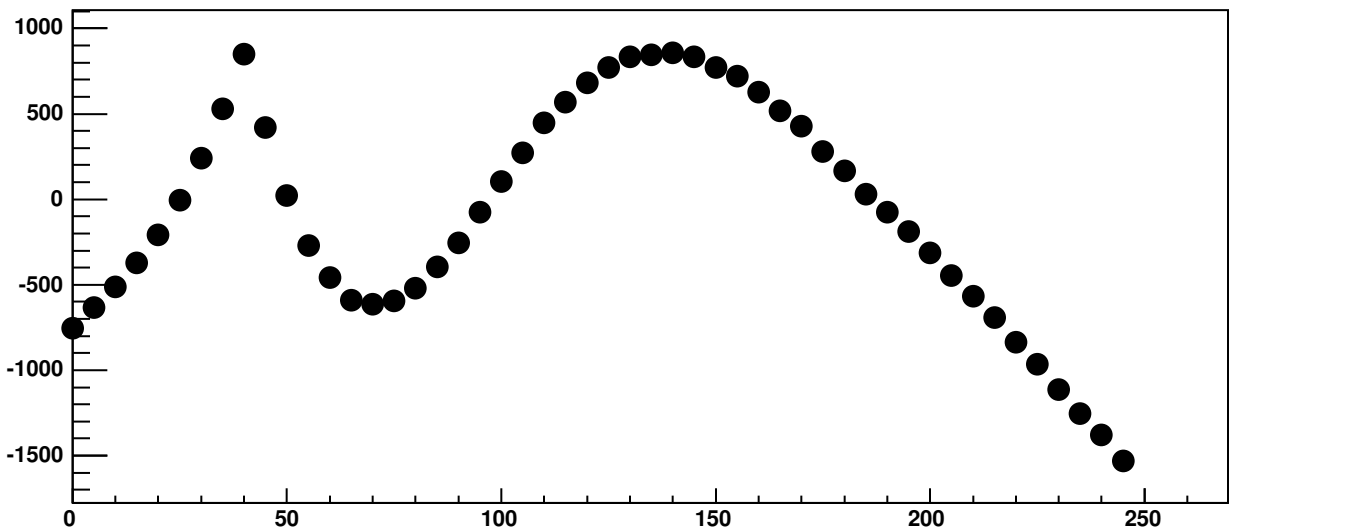
Chip 3, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold



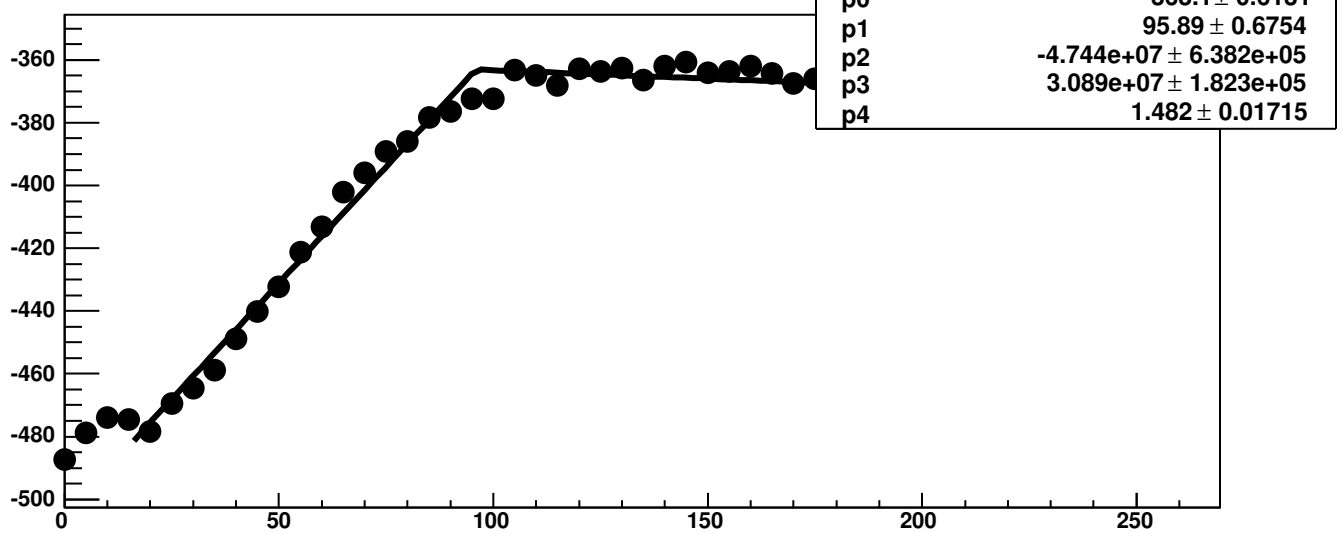
Chip 3, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



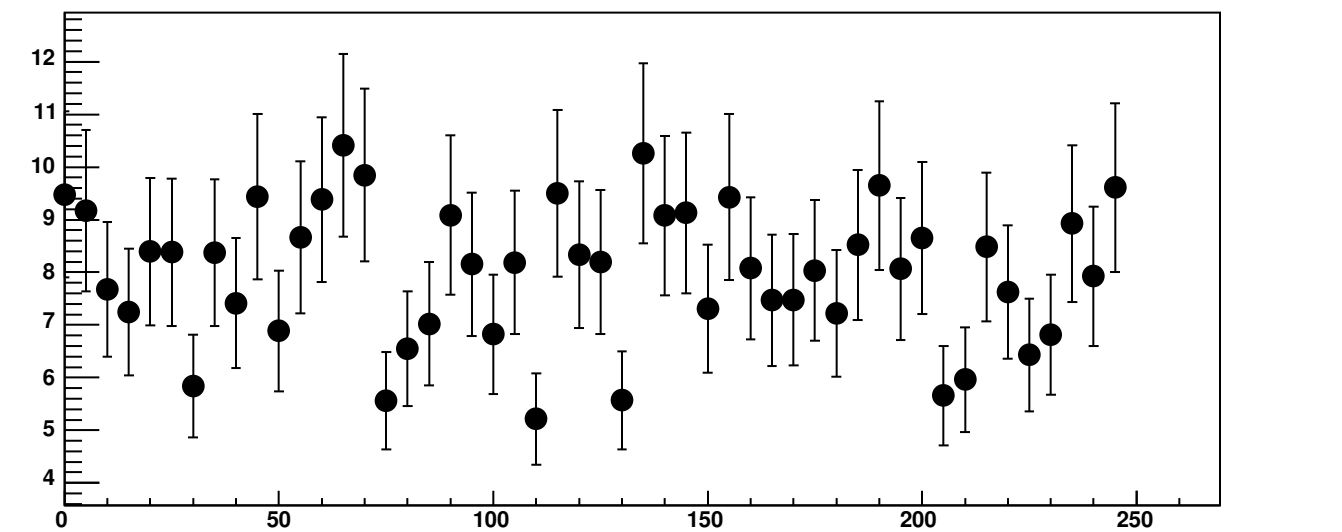
Chip 3, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold



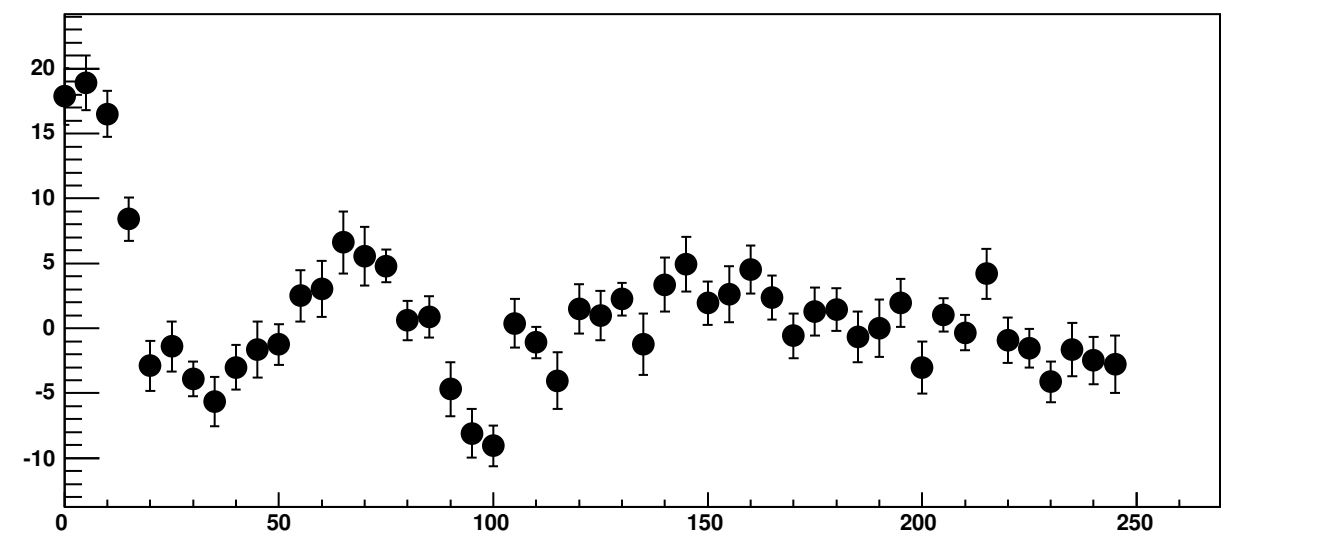
Chip 3, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold



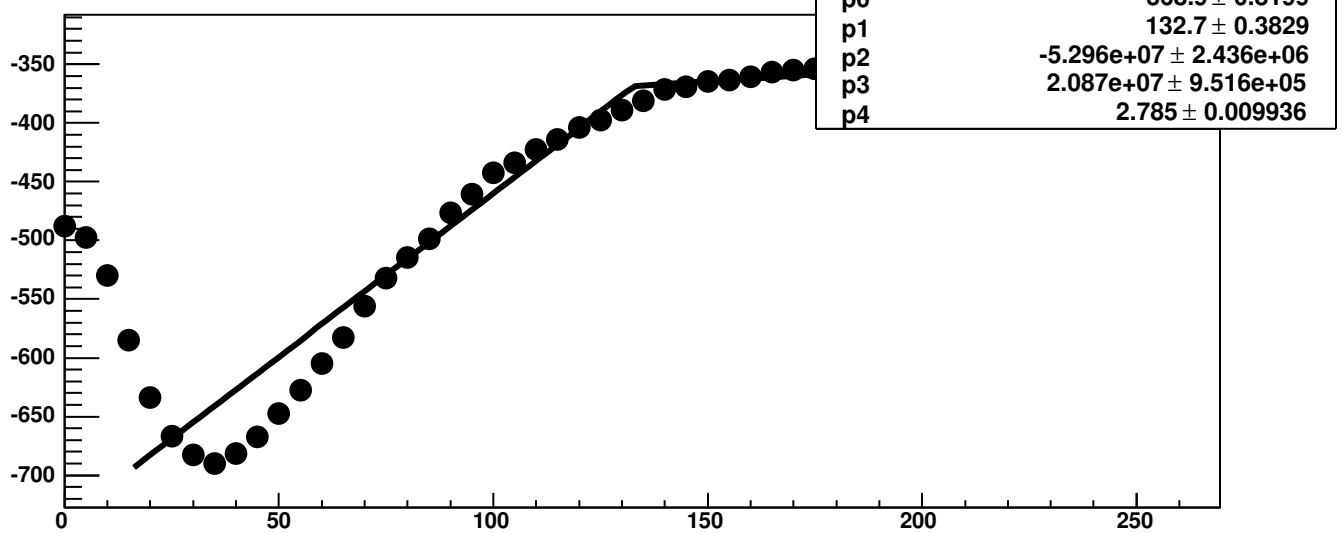
Chip 3, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold



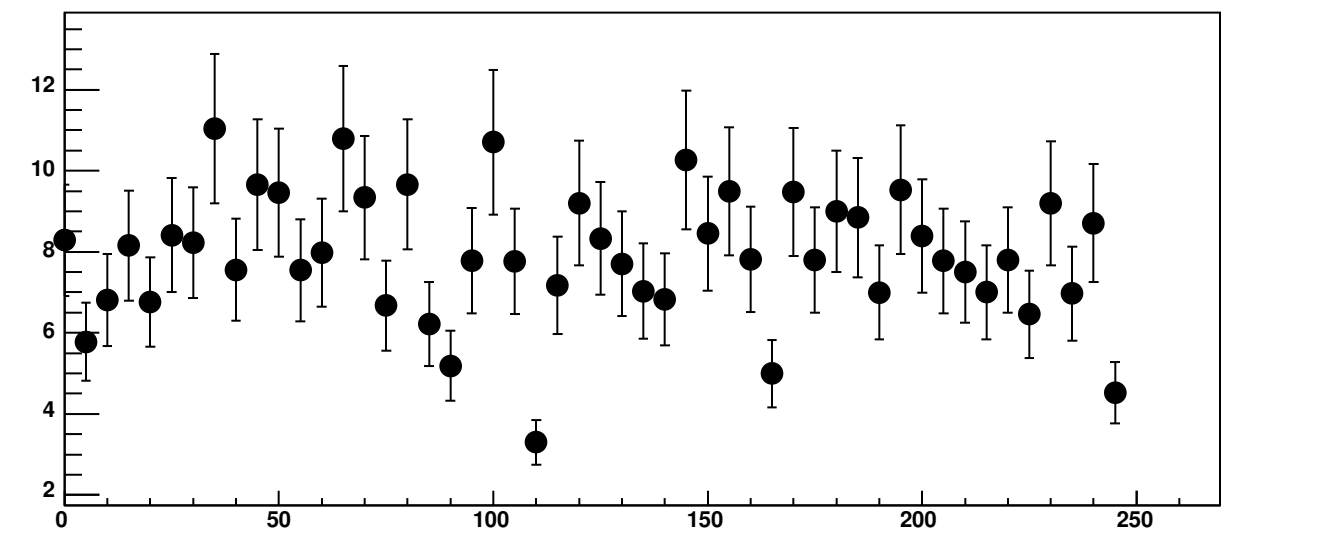
Chip 3, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold



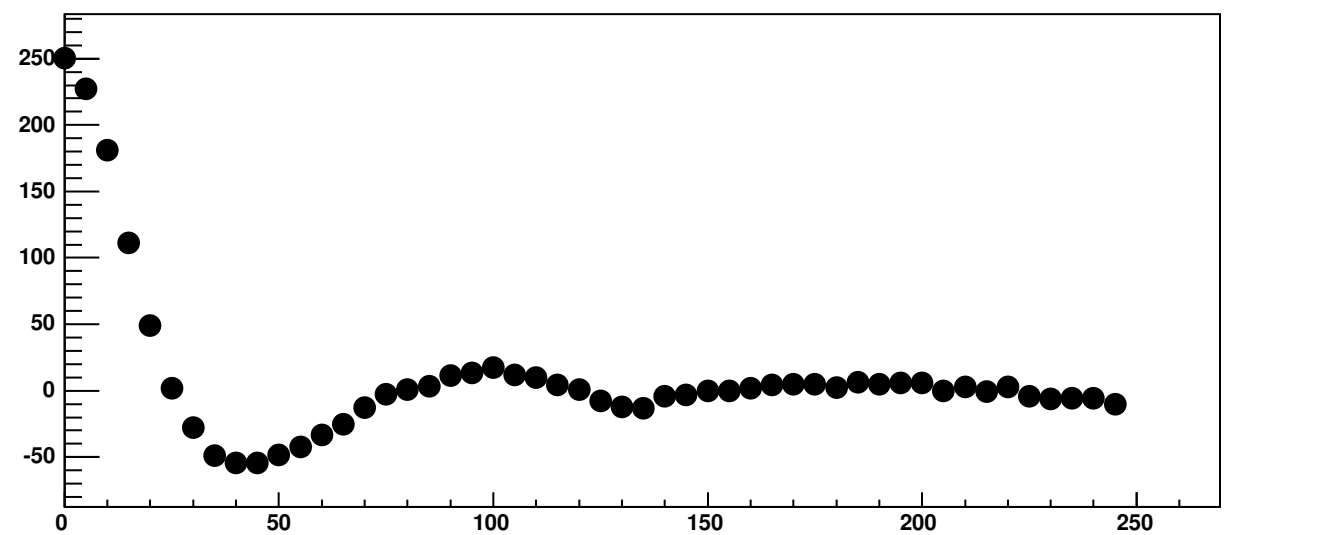
Chip 3, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold



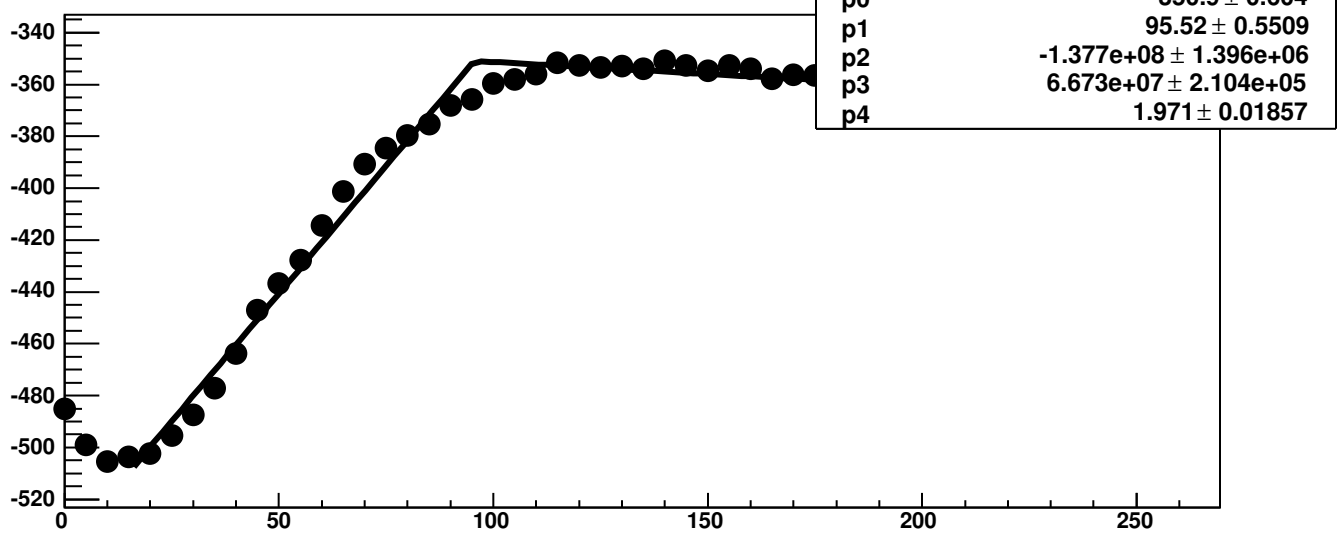
Chip 3, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



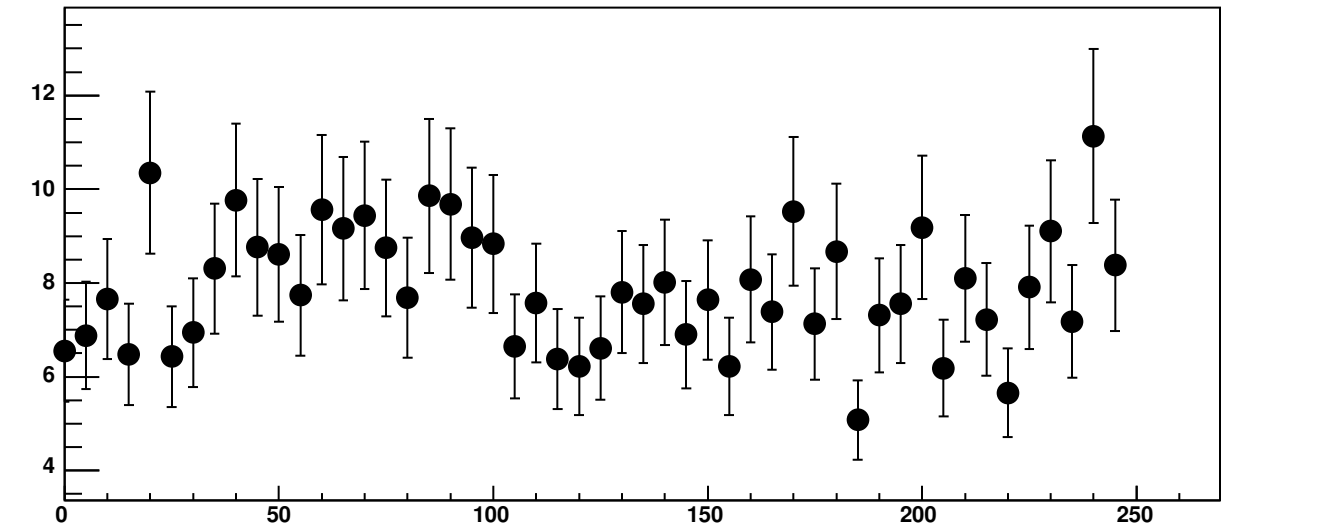
Chip 3, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold



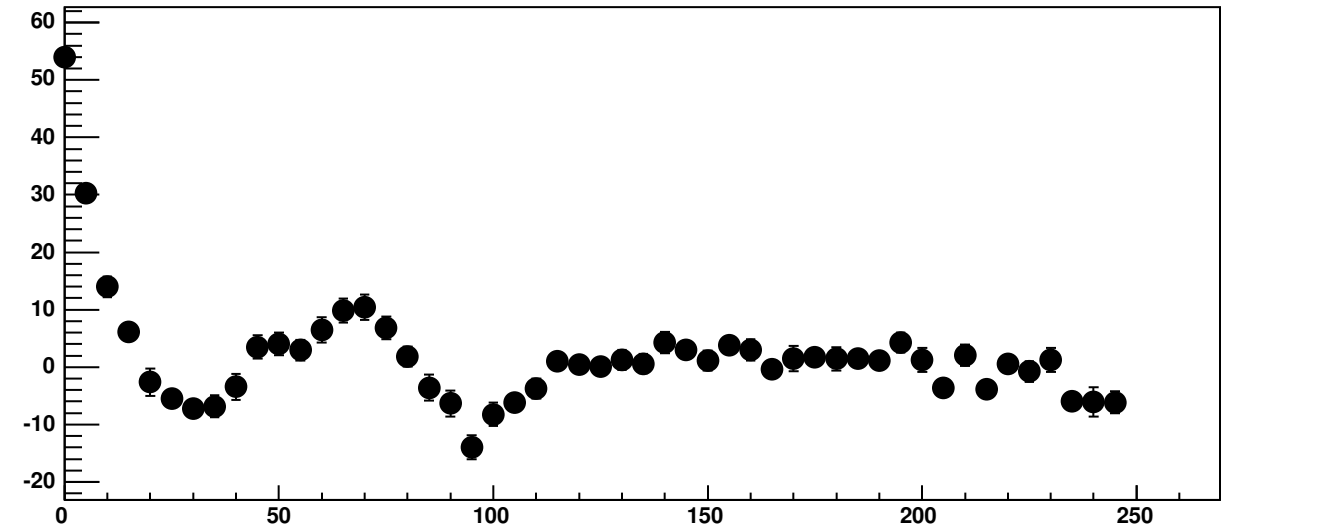
Chip 3, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold



Chip 3, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold

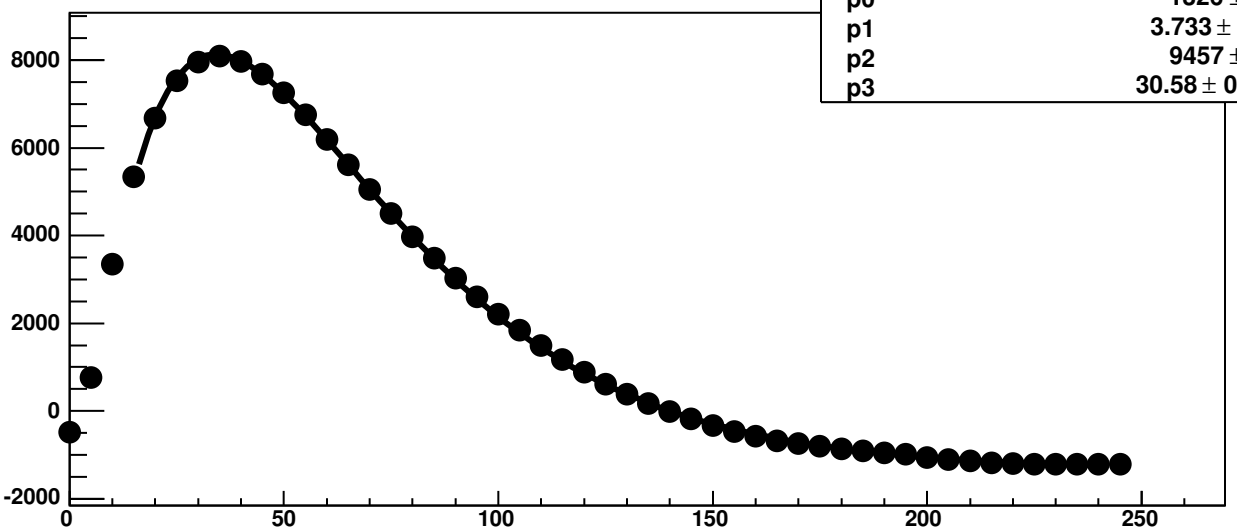


Chip 3, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

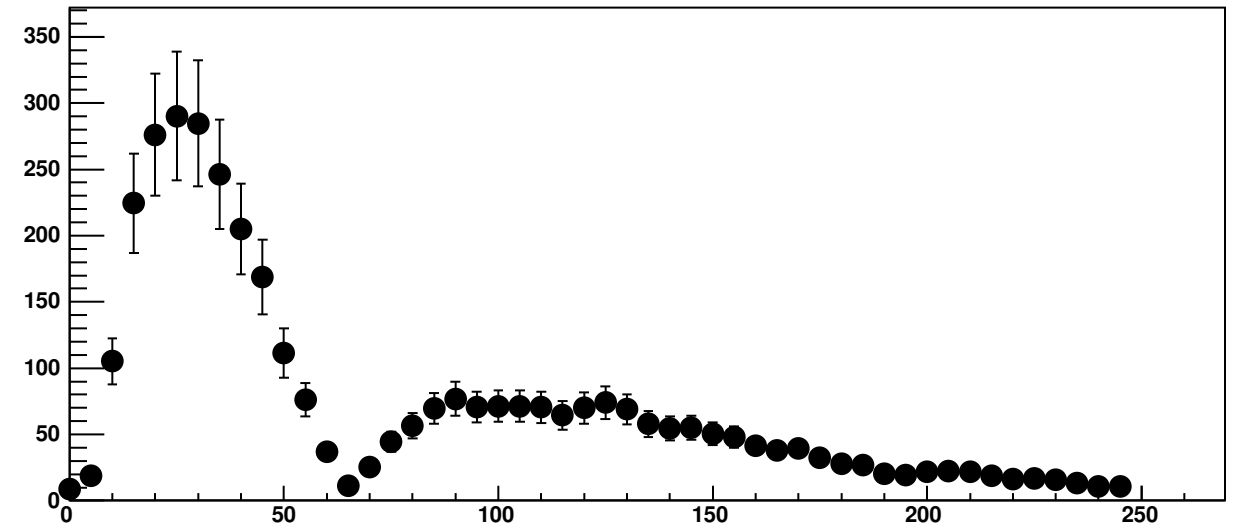


Chip 3, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold

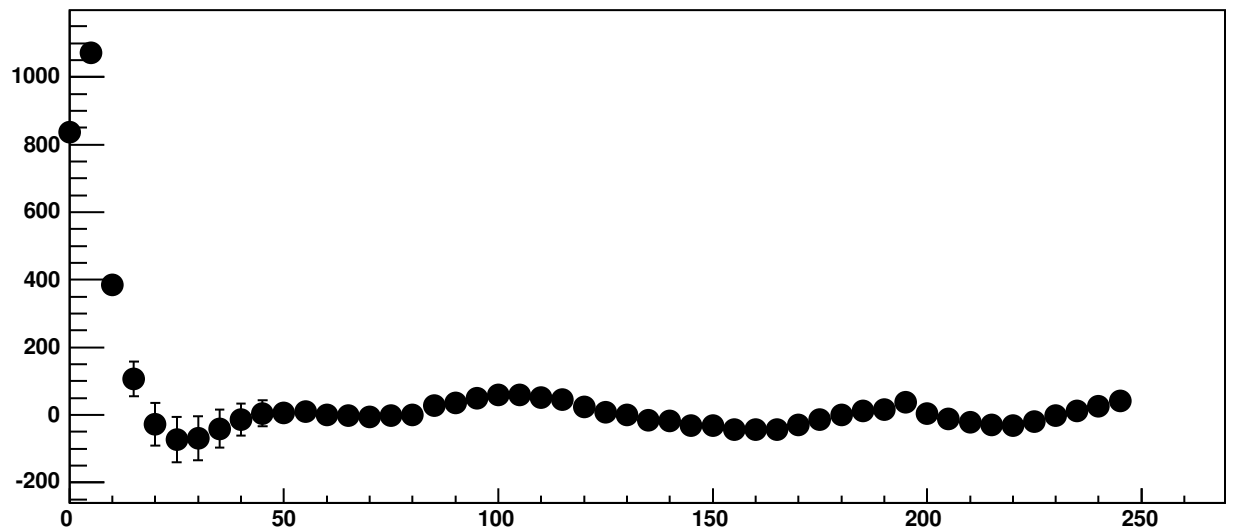
χ^2 / ndf	539.3 / 42
p0	-1326 ± 1.673
p1	3.733 ± 0.1013
p2	9457 ± 11.44
p3	30.58 ± 0.04015



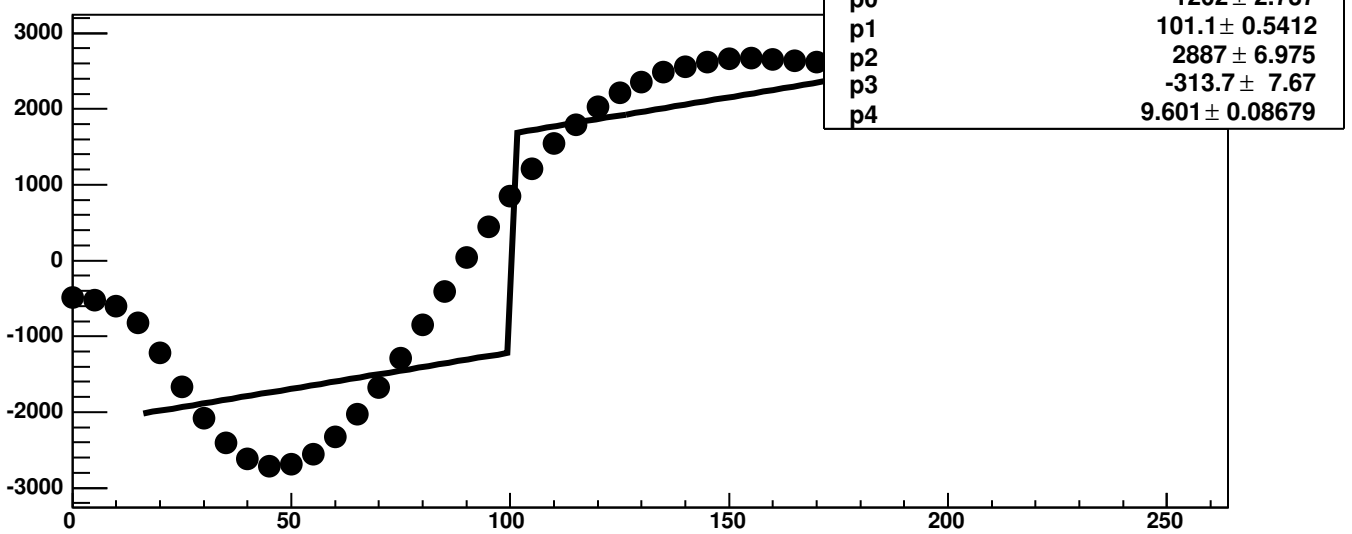
Chip 3, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold



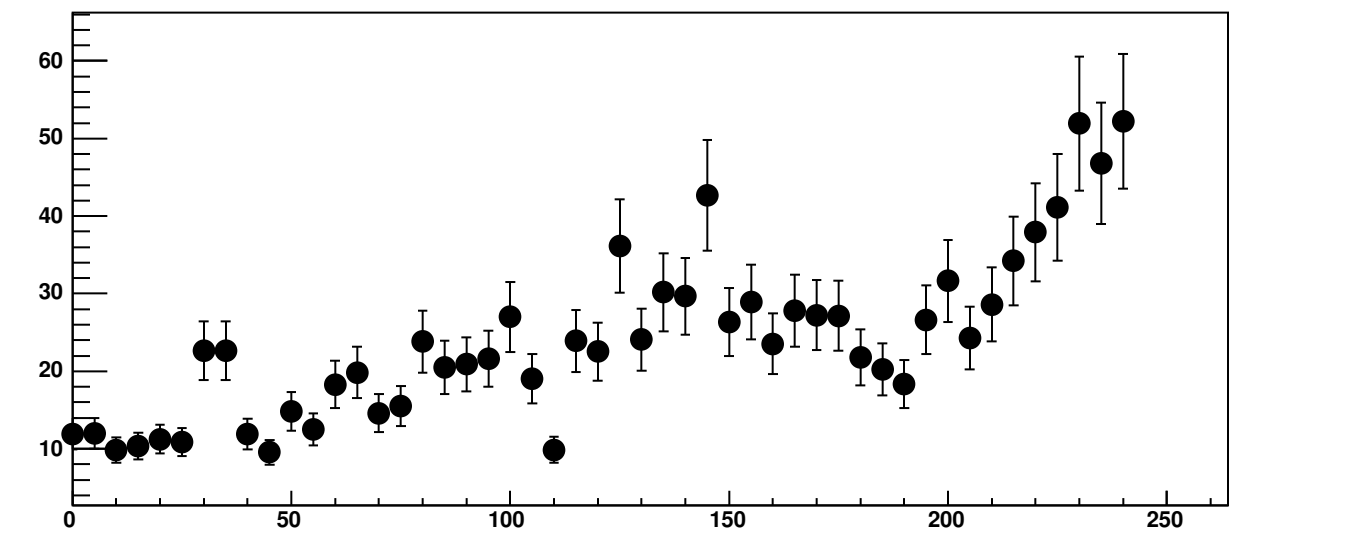
Chip 3, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold



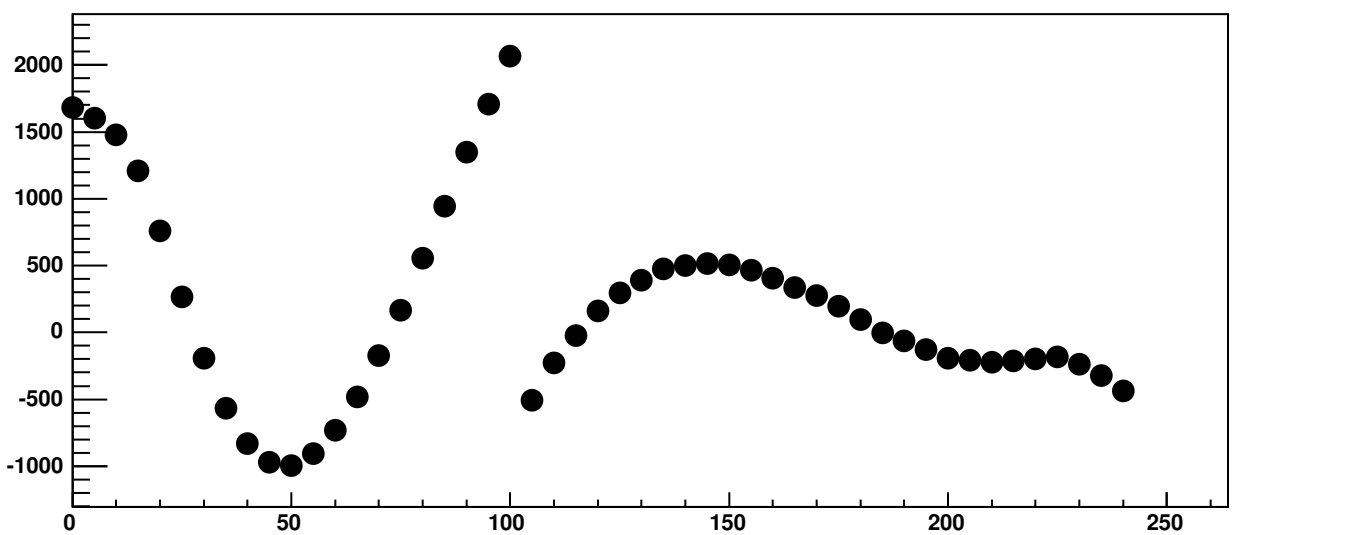
Chip 3, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

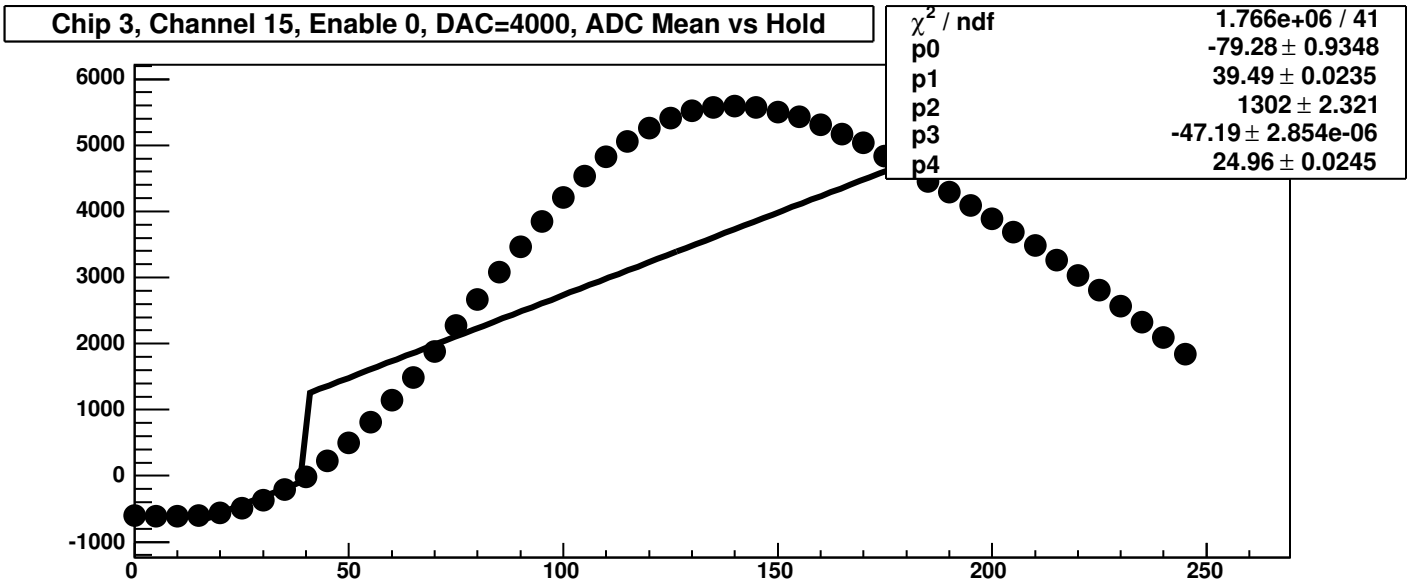


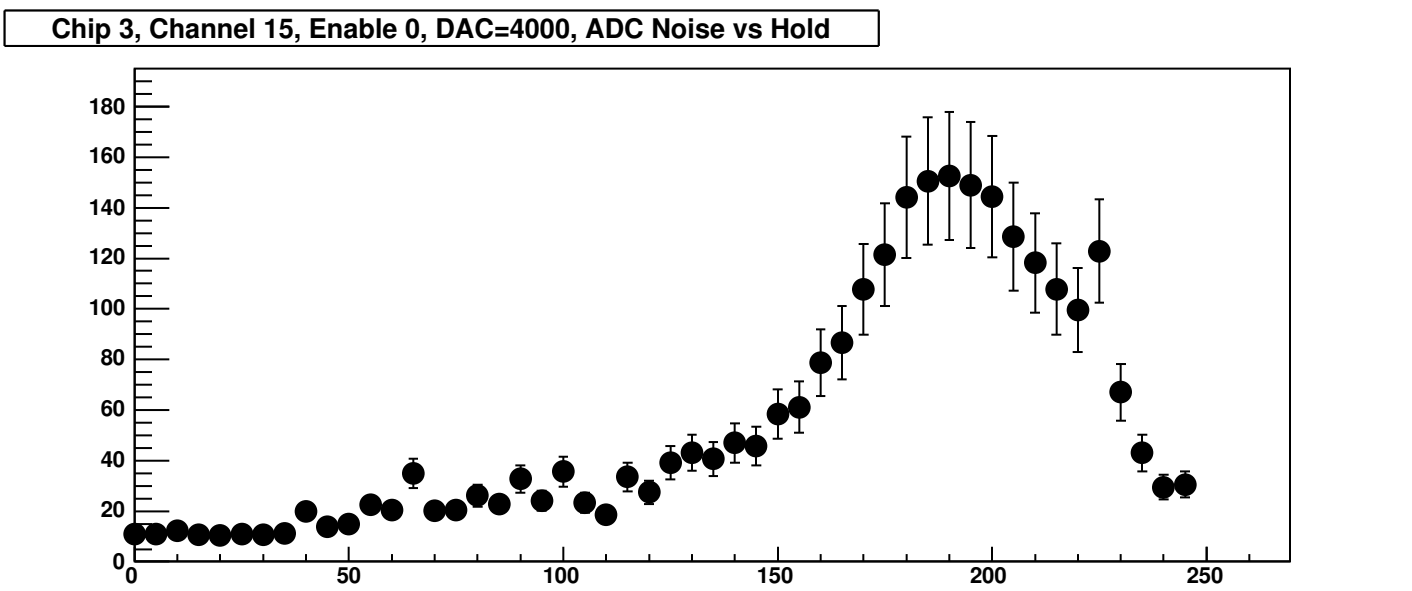
Chip 3, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold

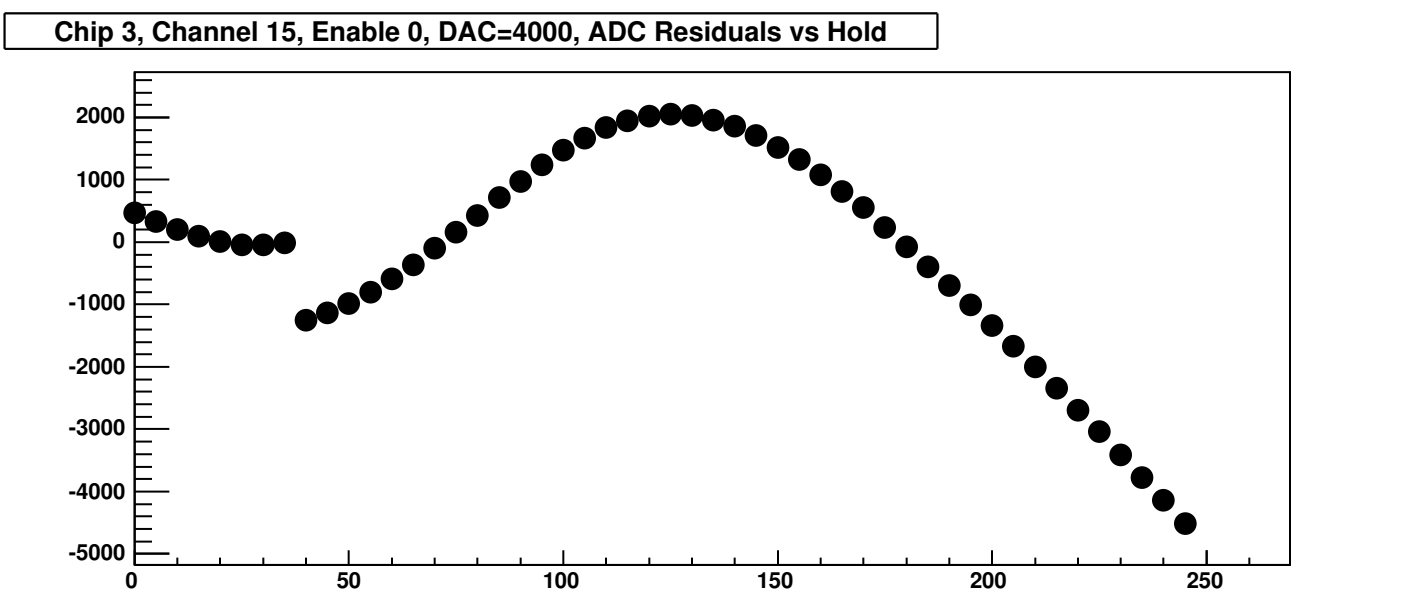


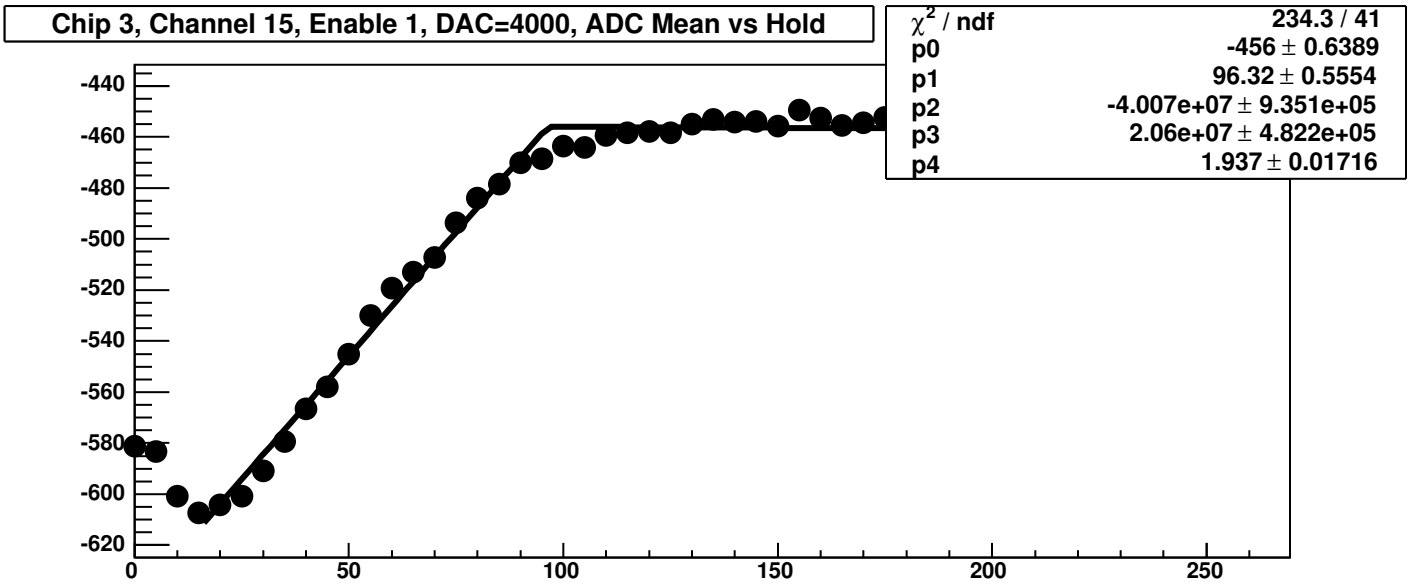
Chip 3, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold

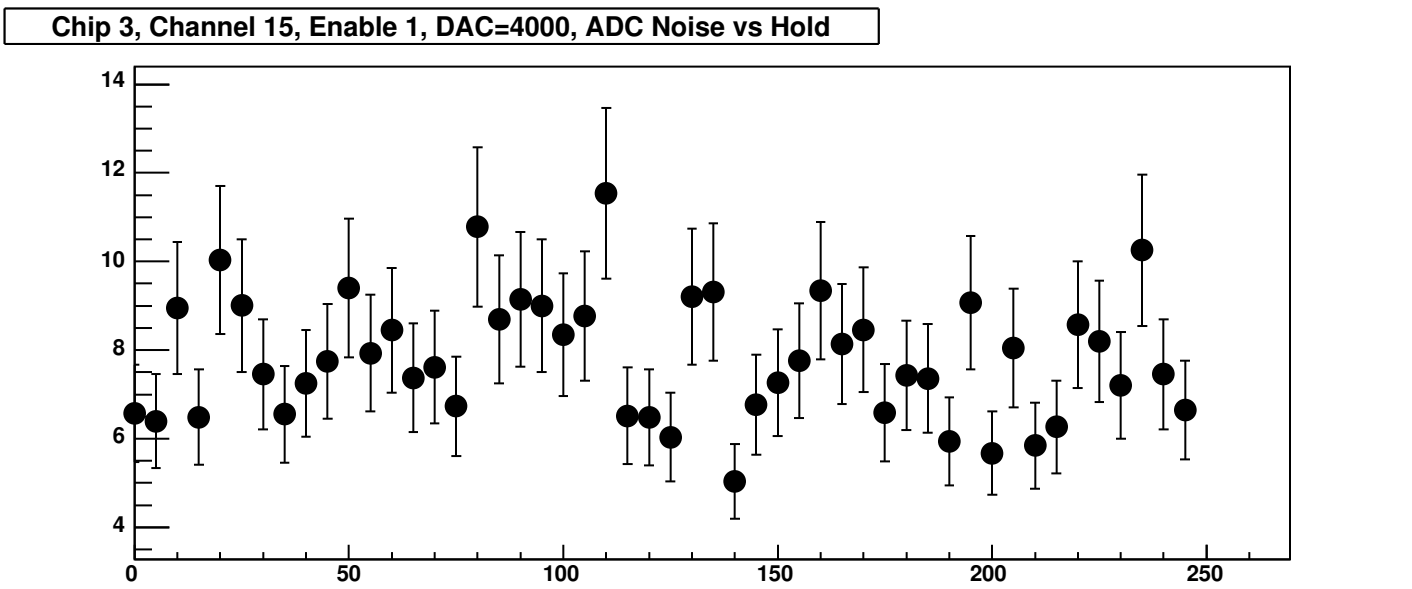


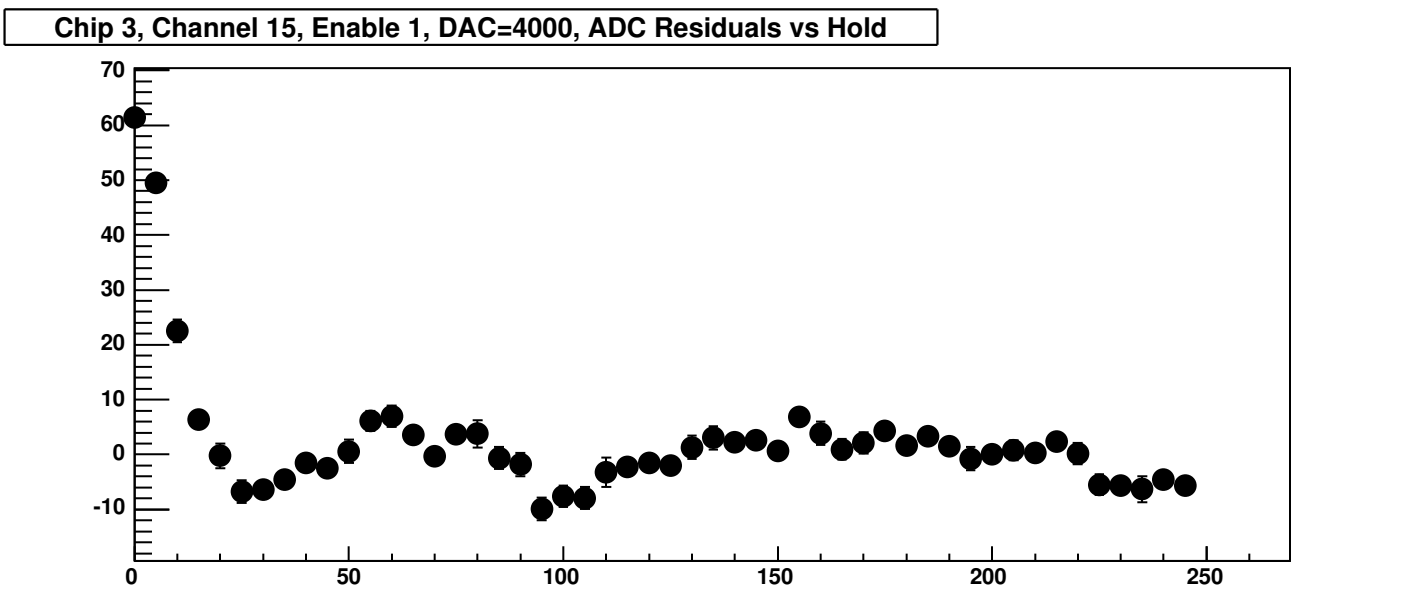

Chip 3, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.766e+06 / 41
p0 -79.28 ± 0.9348
p1 39.49 ± 0.0235
p2 1302 ± 2.321
p3 -47.19 ± 2.854e-06
p4 24.96 ± 0.0245
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

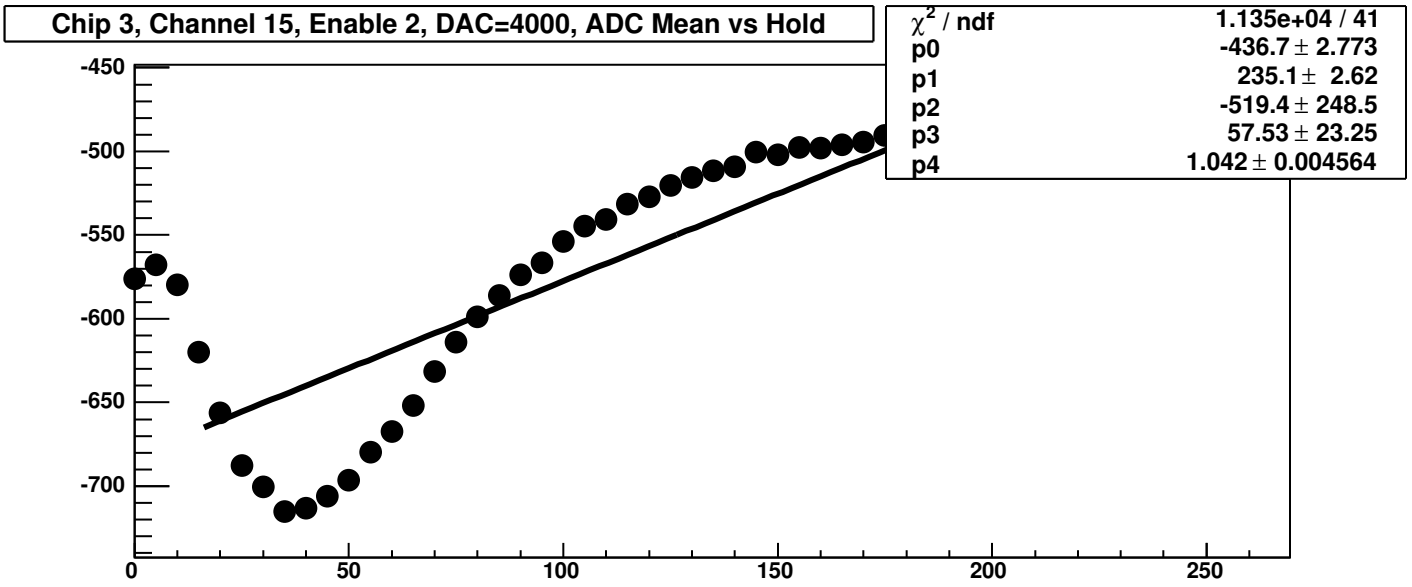

Chip 3, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

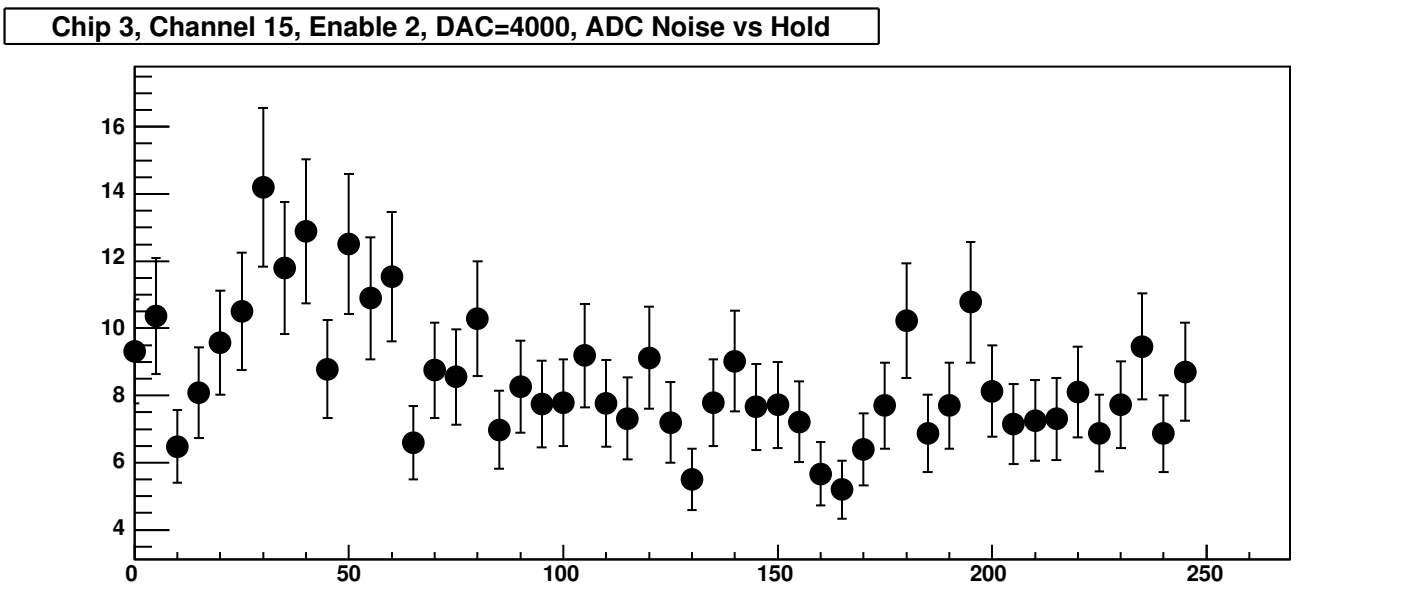

Chip 3, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

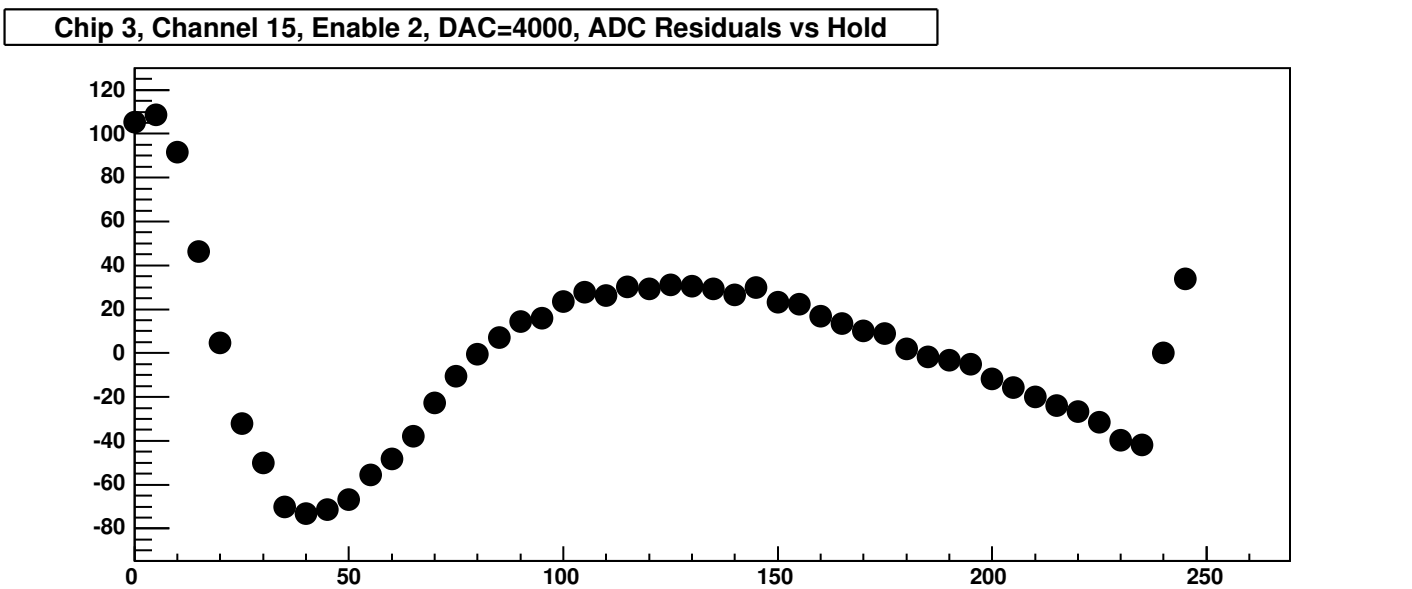

Chip 3, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 234.3 / 41
p0 -456 ± 0.6389
p1 96.32 ± 0.5554
p2 -4.007e+07 ± 9.351e+05
p3 2.06e+07 ± 4.822e+05
p4 1.937 ± 0.01716
-440
-460
-480
-500
-520
-540
-560
-580
-600
-620
0
50
100
150
200
250

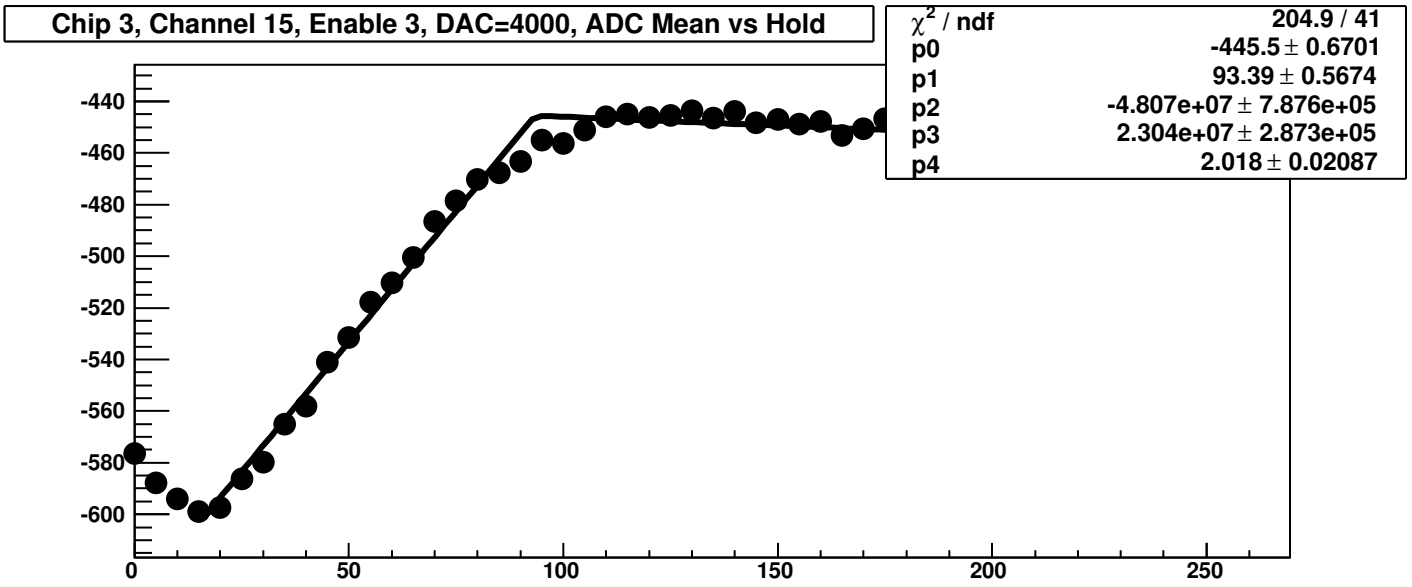

Chip 3, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

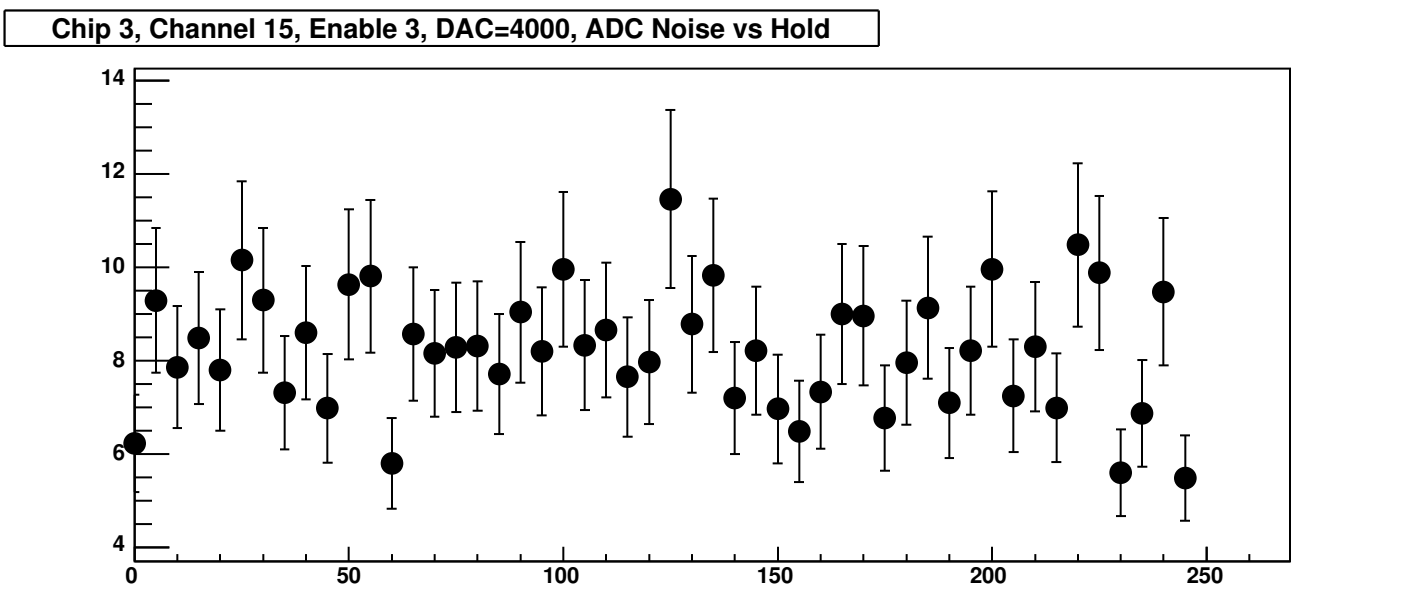

Chip 3, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

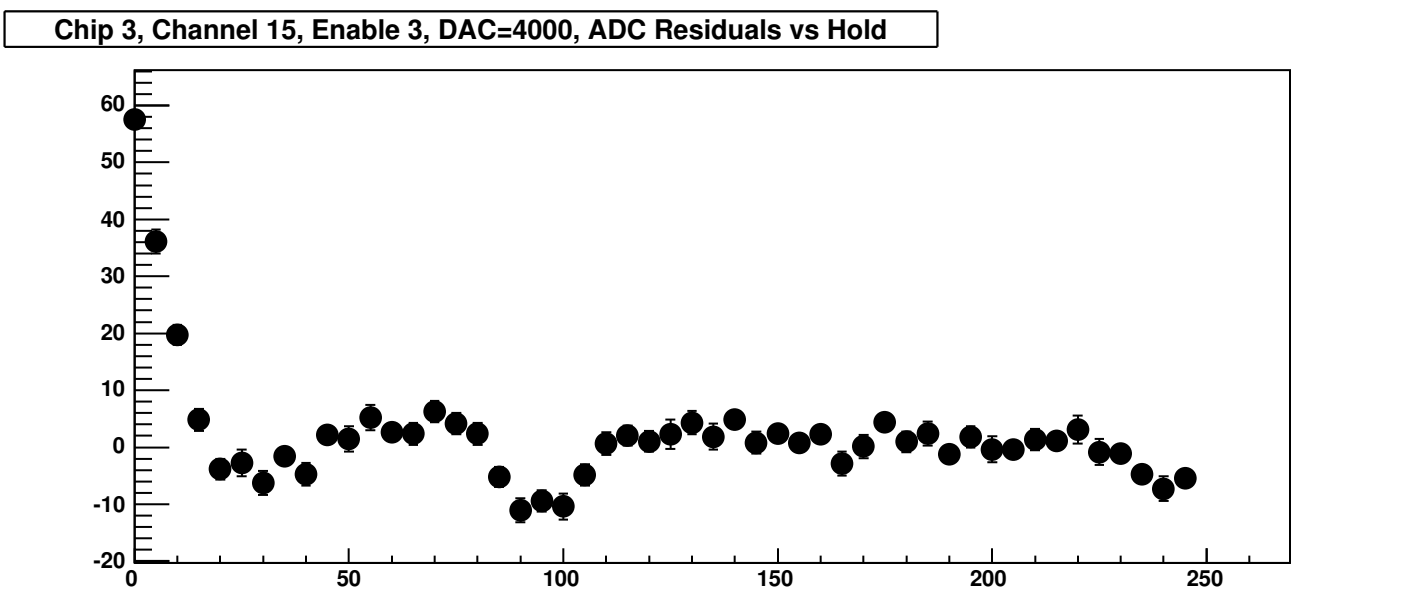

Chip 3, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.135e+04 / 41
p0 -436.7 ± 2.773
p1 235.1 ± 2.62
p2 -519.4 ± 248.5
p3 57.53 ± 23.25
p4 1.042 ± 0.004564
-450
-500
-550
-600
-650
-700
0
50
100
150
200
250


Chip 3, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold
16
14
12
10
8
6
4
0
50
100
150
200
250

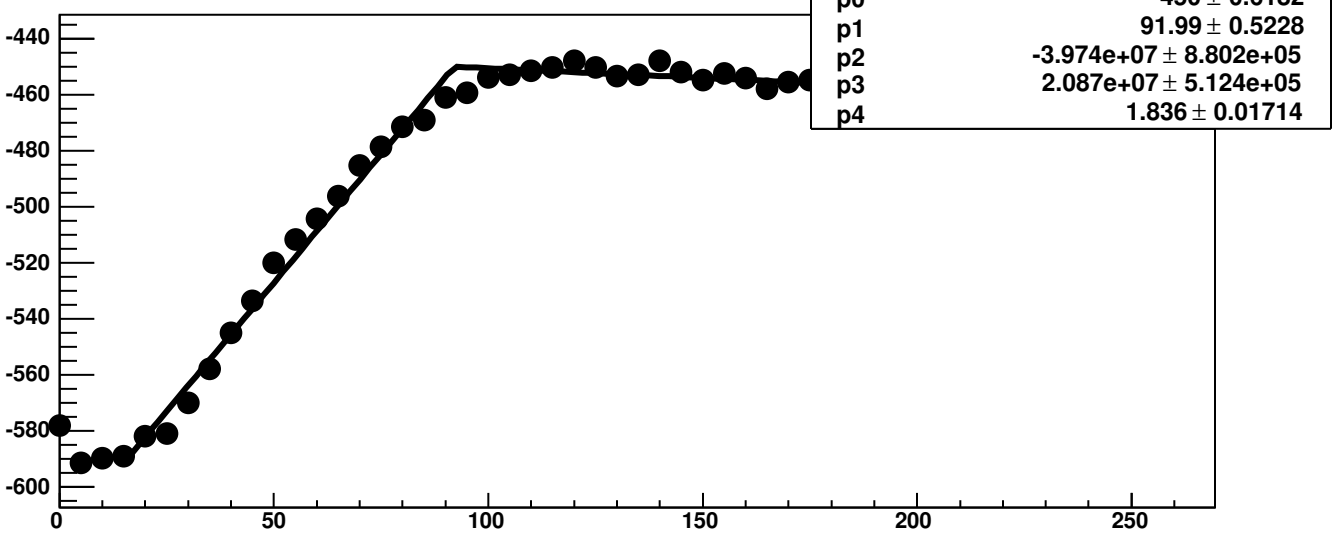

Chip 3, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold
120
100
80
60
40
20
0
-20
-40
-60
-80
0
50
100
150
200
250


Chip 3, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 204.9 / 41
p0 -445.5 ± 0.6701
p1 93.39 ± 0.5674
p2 -4.807e+07 ± 7.876e+05
p3 2.304e+07 ± 2.873e+05
p4 2.018 ± 0.02087
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
50
100
150
200
250

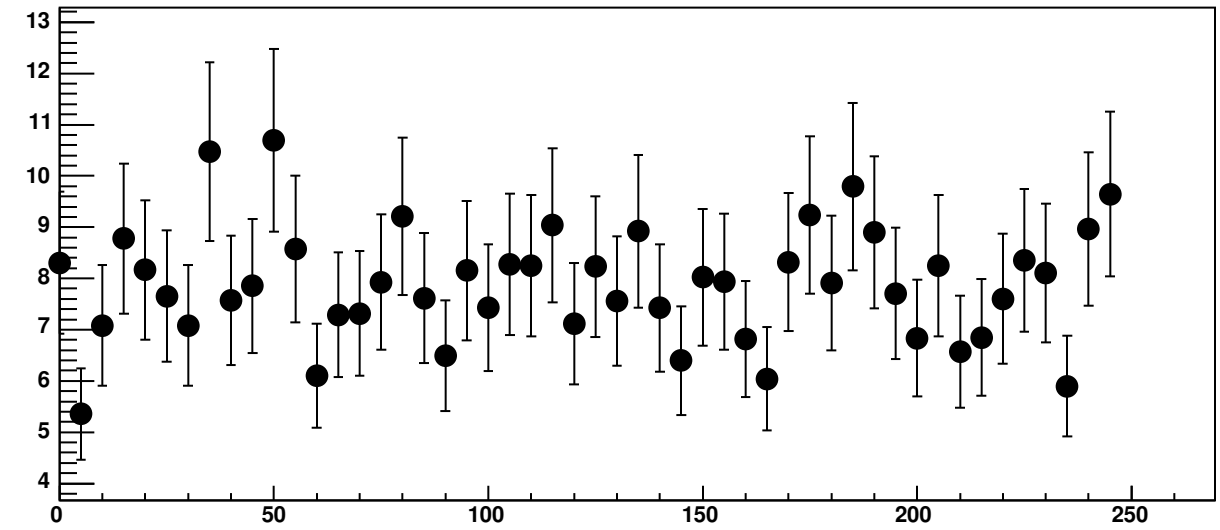

Chip 3, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 3, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold
60
50
40
30
20
10
0
-10
-20
0
50
100
150
200
250

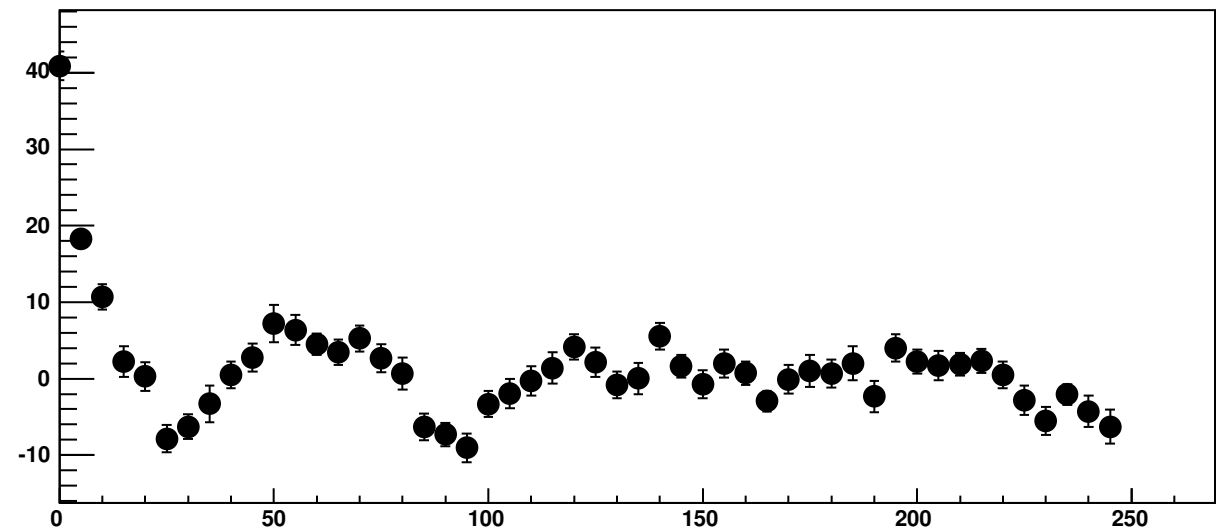
Chip 3, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold



Chip 3, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold

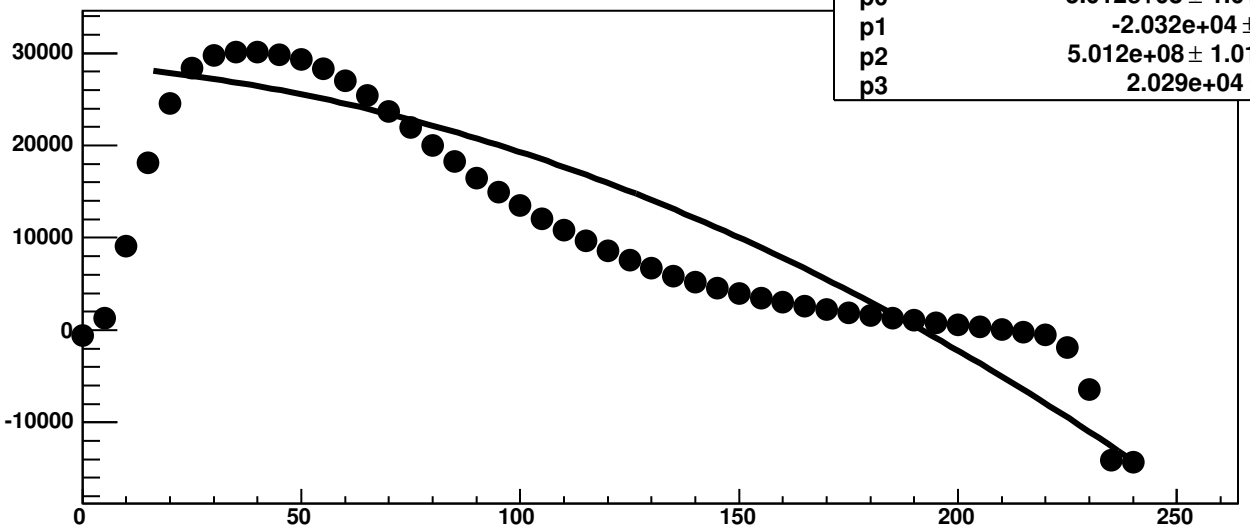


Chip 3, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

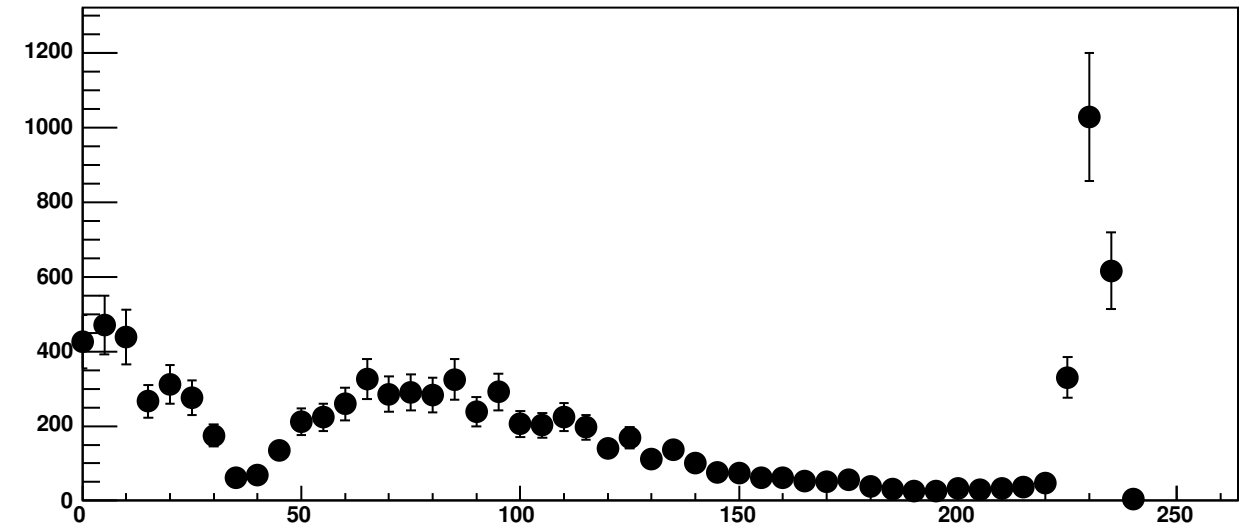


Chip 3, Channel 15, Enable 5!, DAC=4000, ADC Mean vs Hold

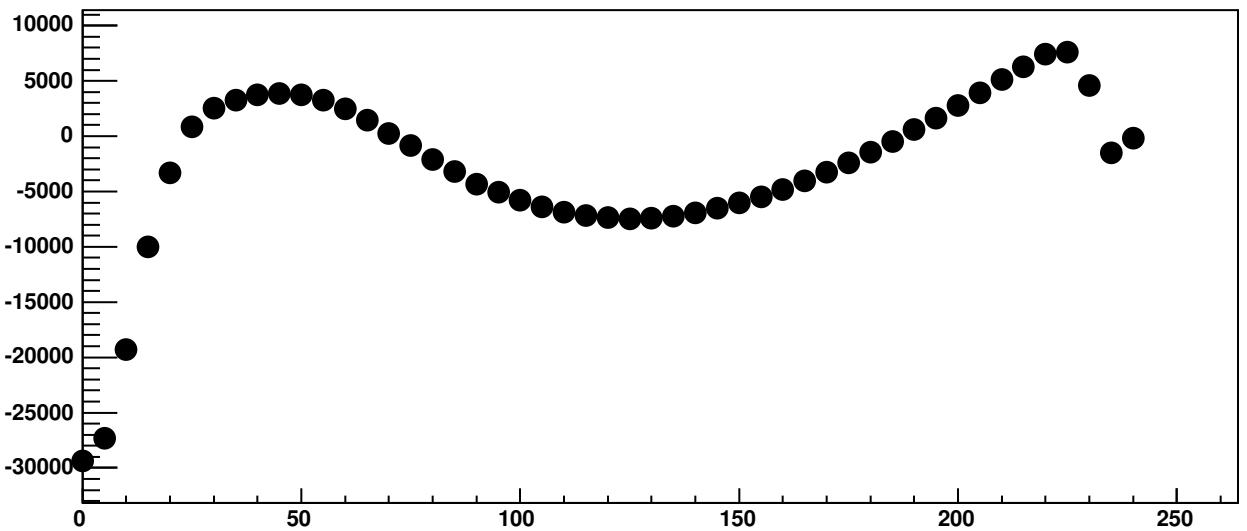
χ^2 / ndf	3.444e+06 / 42
p0	-5.012e+08 ± 1.018e+06
p1	-2.032e+04 ± 21.83
p2	5.012e+08 ± 1.018e+06
p3	2.029e+04 ± 21.8

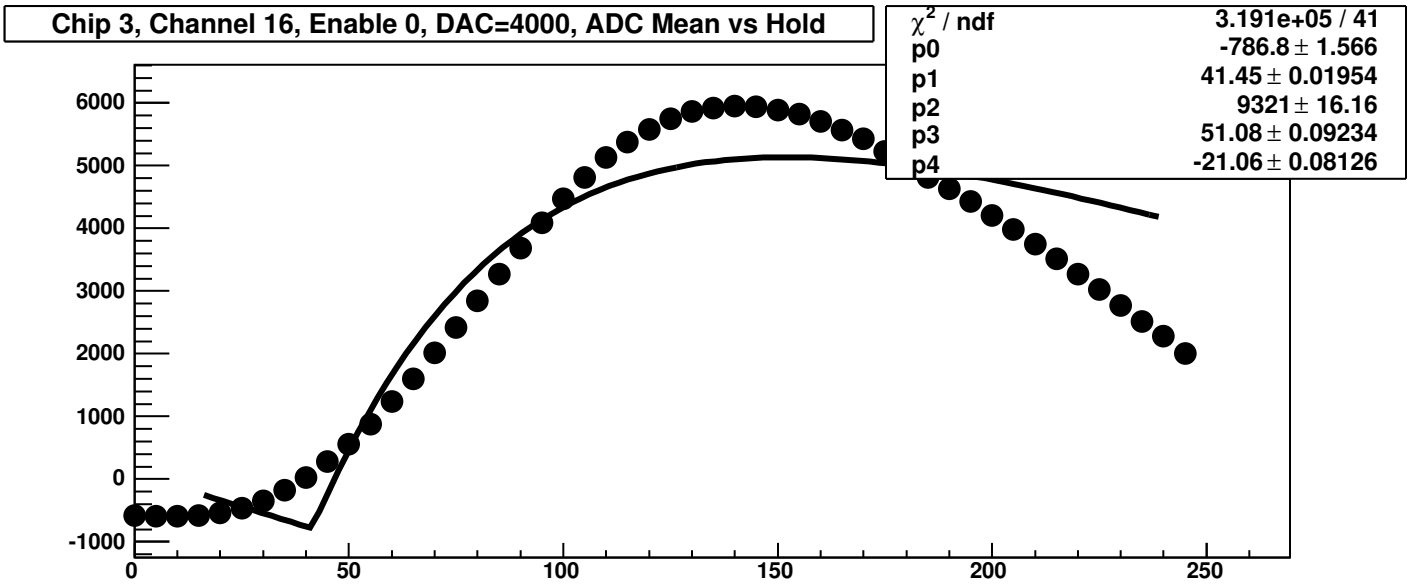


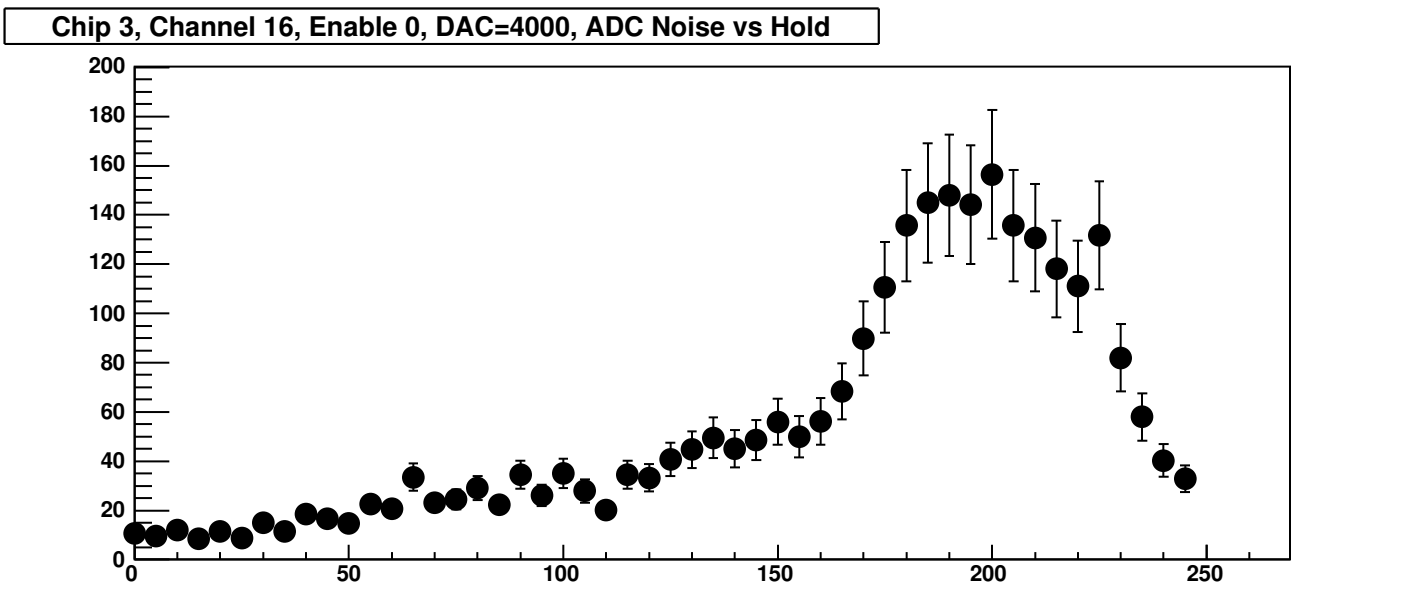
Chip 3, Channel 15, Enable 5!, DAC=4000, ADC Noise vs Hold

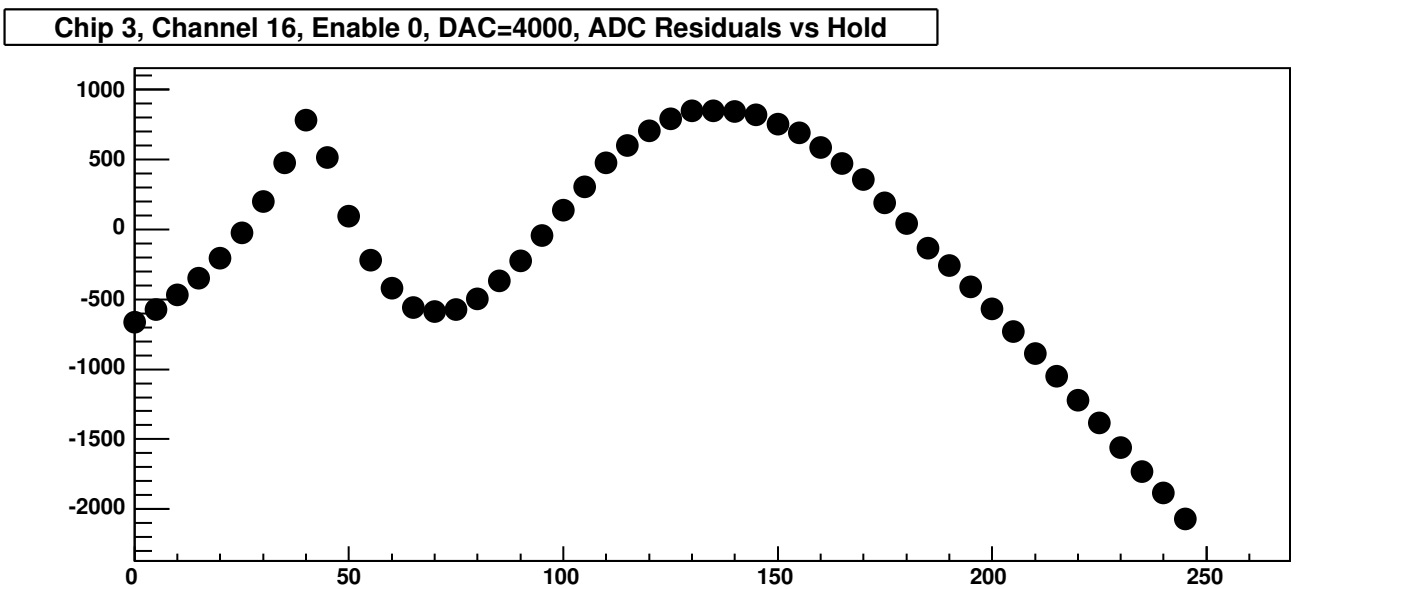


Chip 3, Channel 15, Enable 5!, DAC=4000, ADC Residuals vs Hold

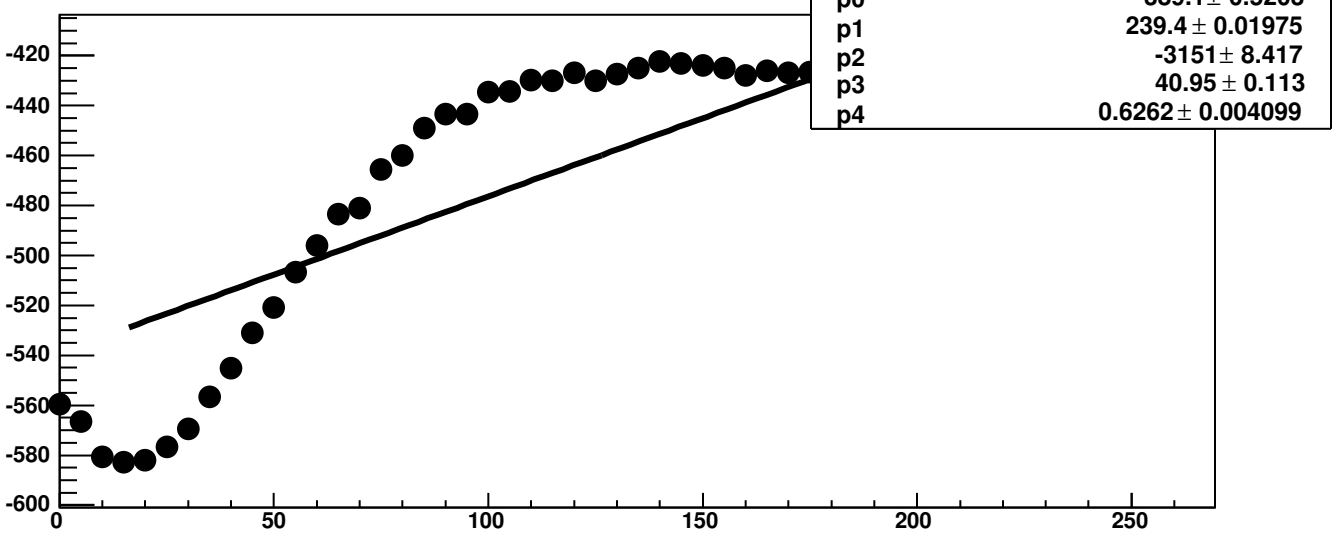



Chip 3, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.191e+05 / 41
p0 -786.8 ± 1.566
p1 41.45 ± 0.01954
p2 9321 ± 16.16
p3 51.08 ± 0.09234
p4 -21.06 ± 0.08126
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

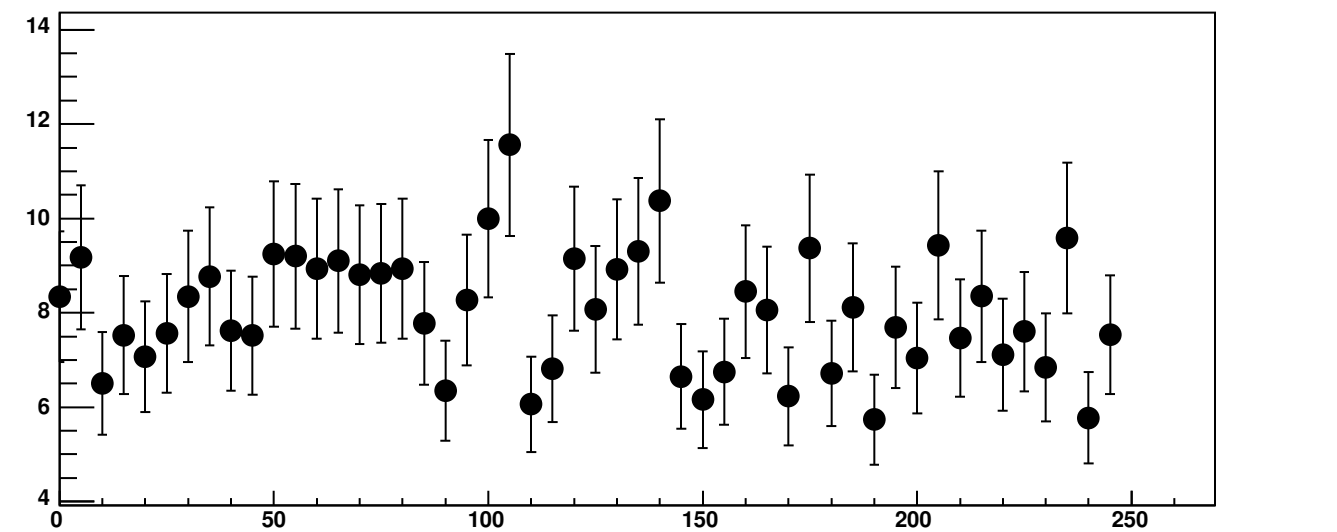

Chip 3, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 3, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

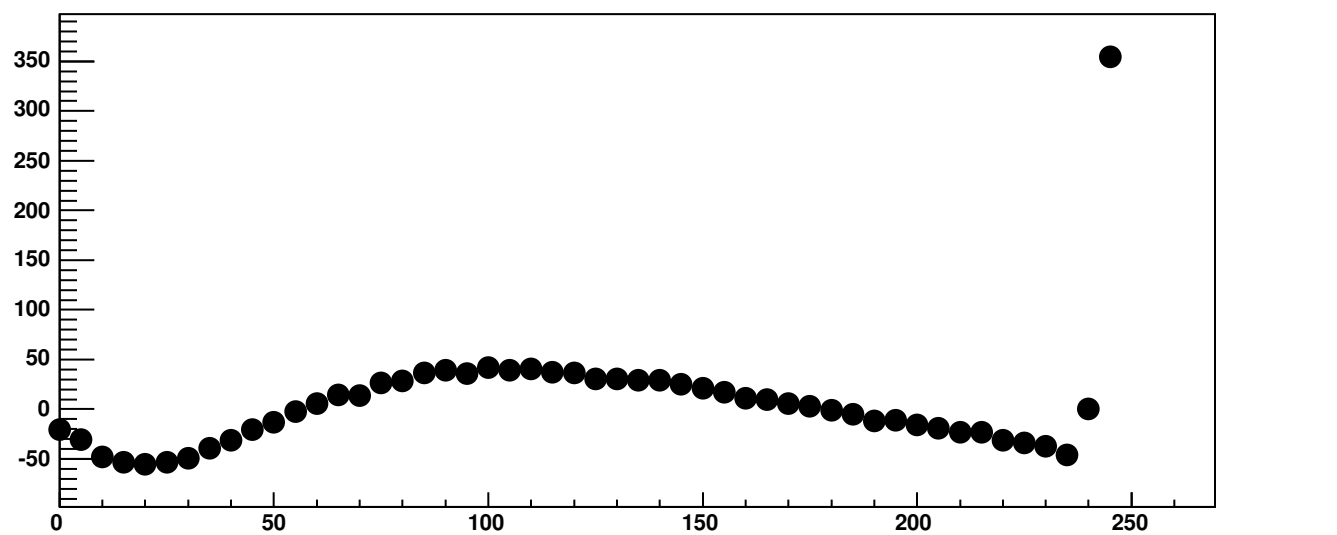
Chip 3, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 3, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold

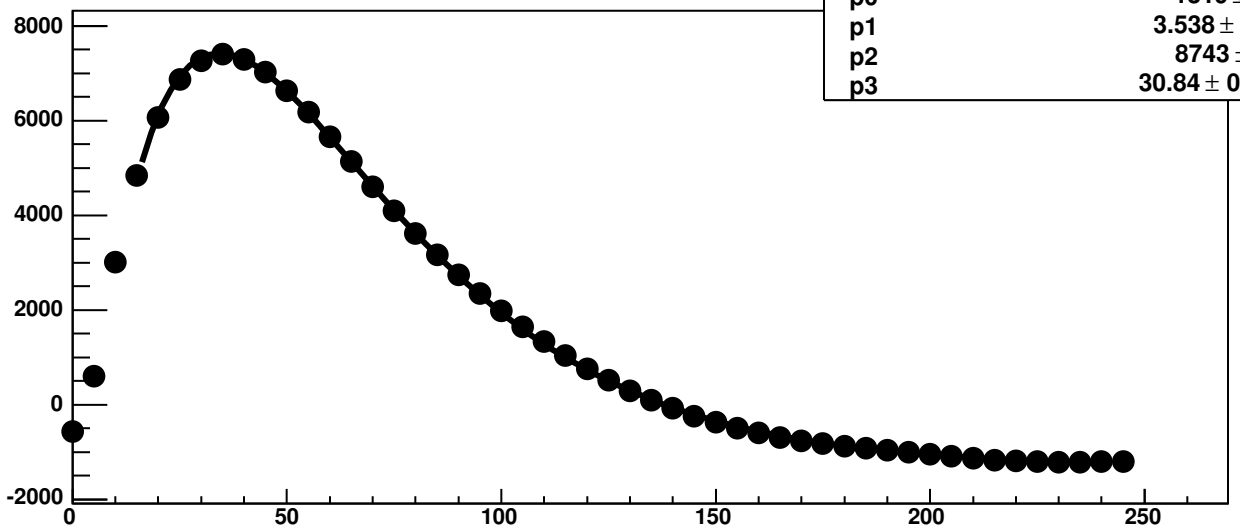


Chip 3, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold

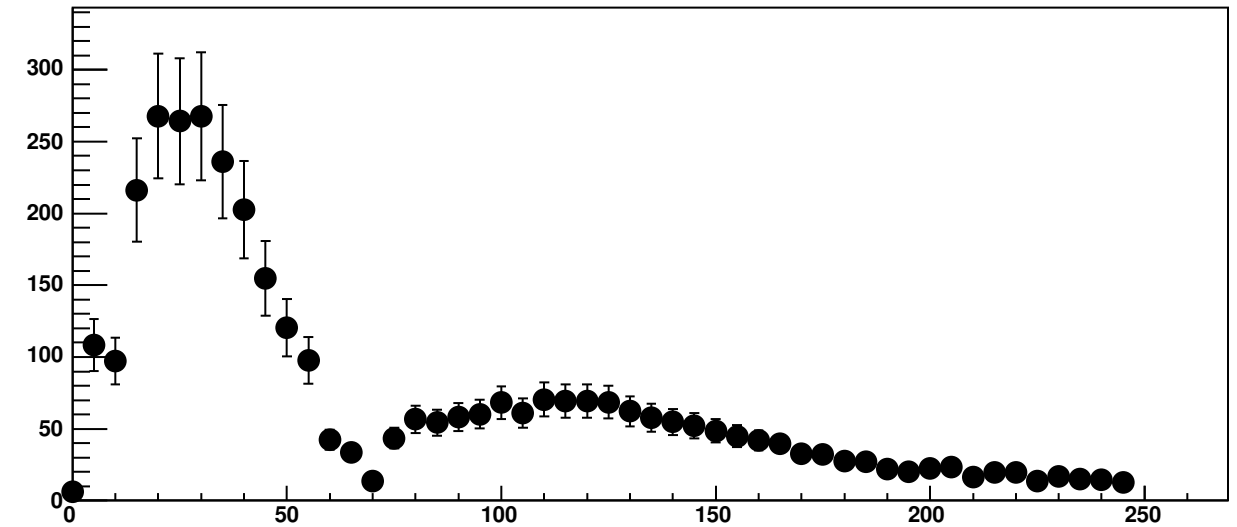


Chip 3, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold

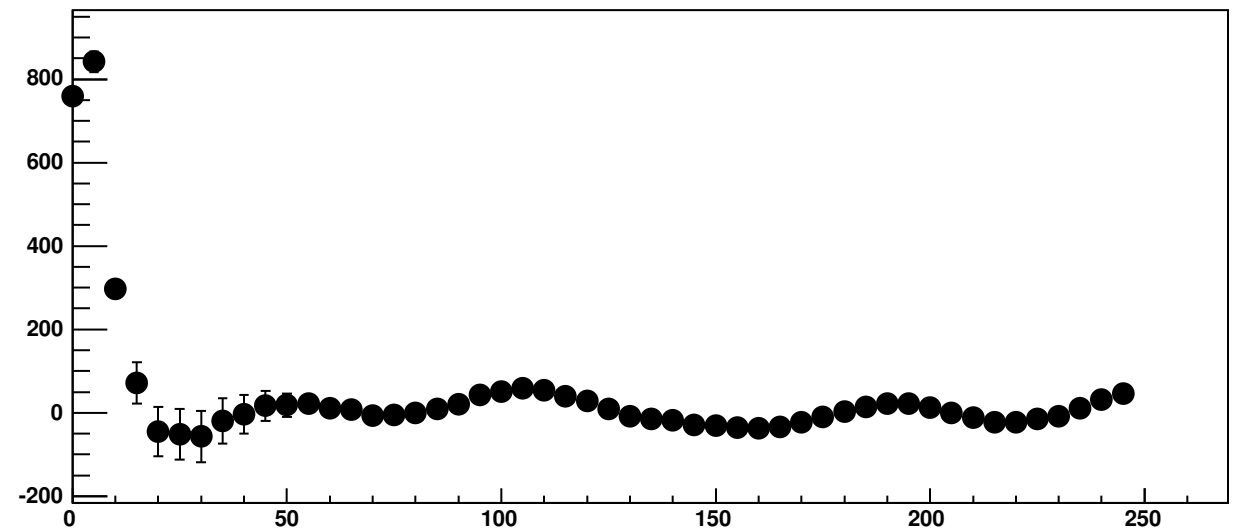
χ^2 / ndf	379.3 / 42
p0	-1319 ± 1.797
p1	3.538 ± 0.1083
p2	8743 ± 11.49
p3	30.84 ± 0.04487



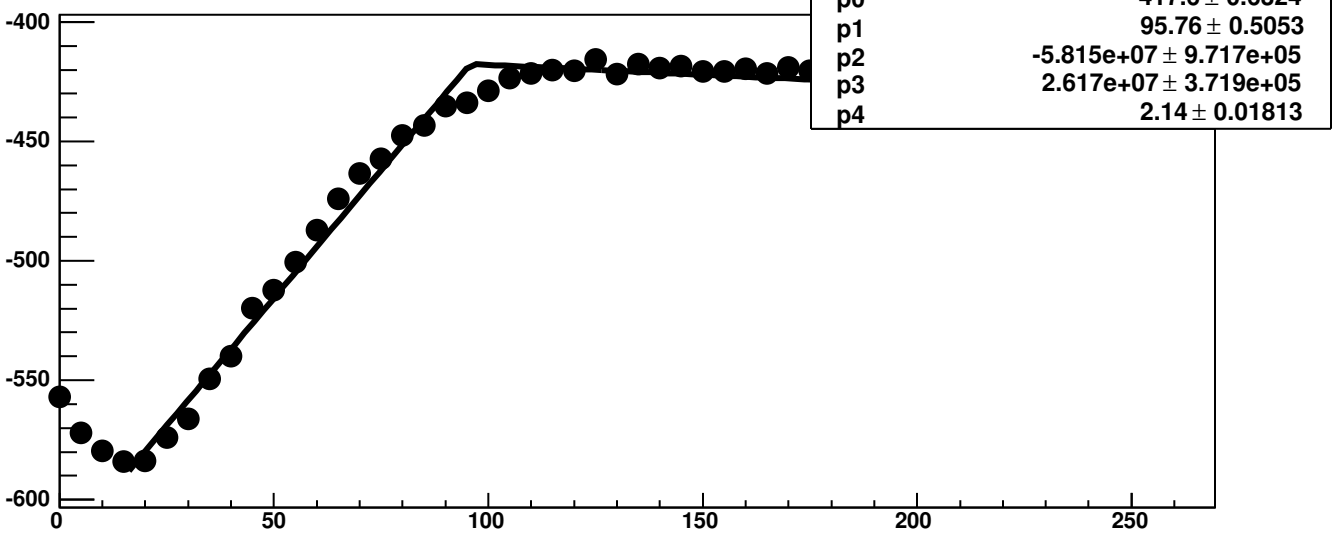
Chip 3, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold



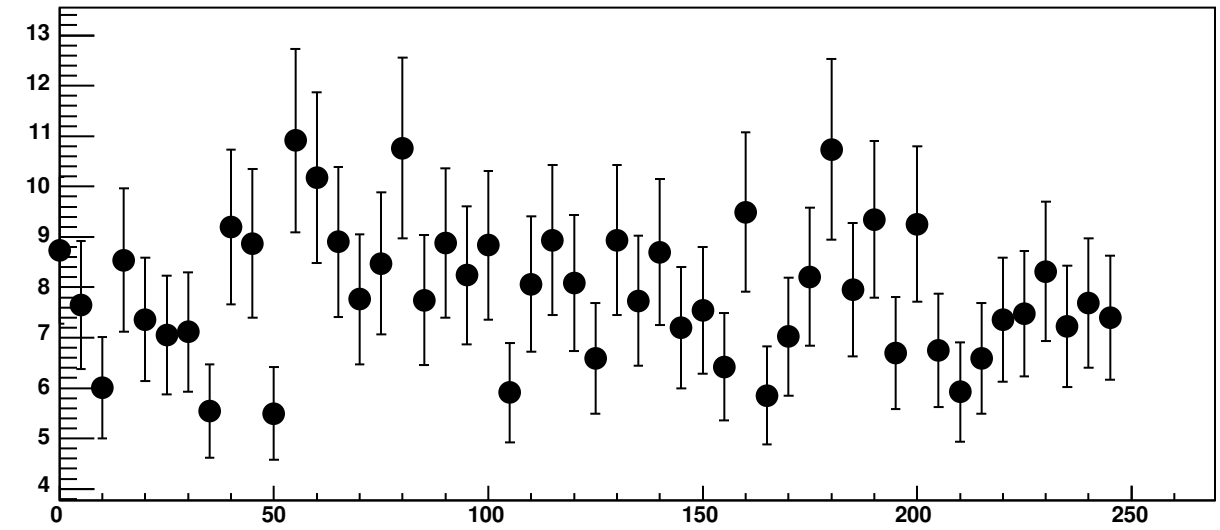
Chip 3, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold



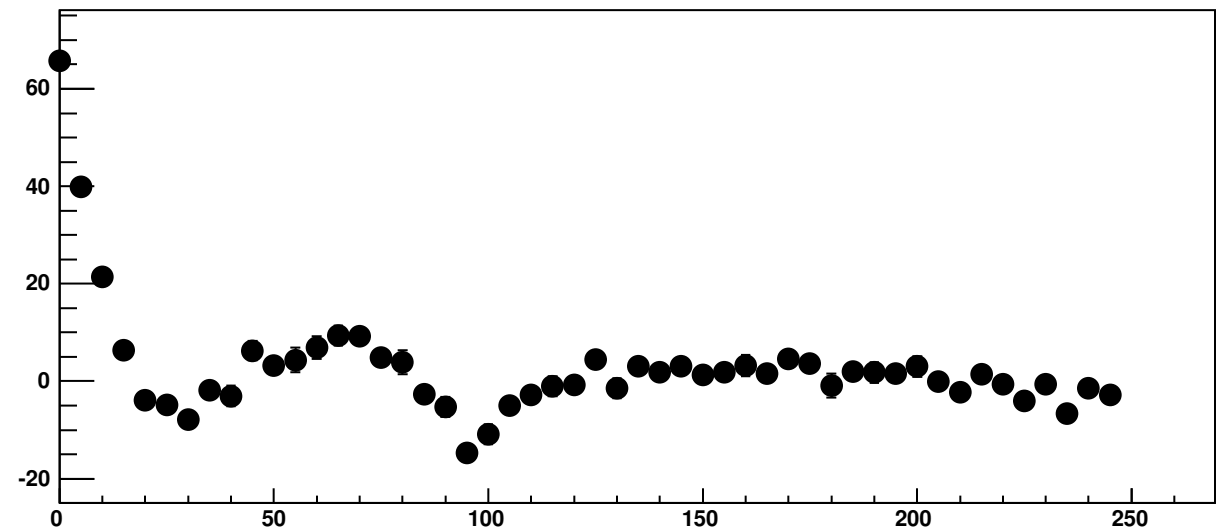
Chip 3, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

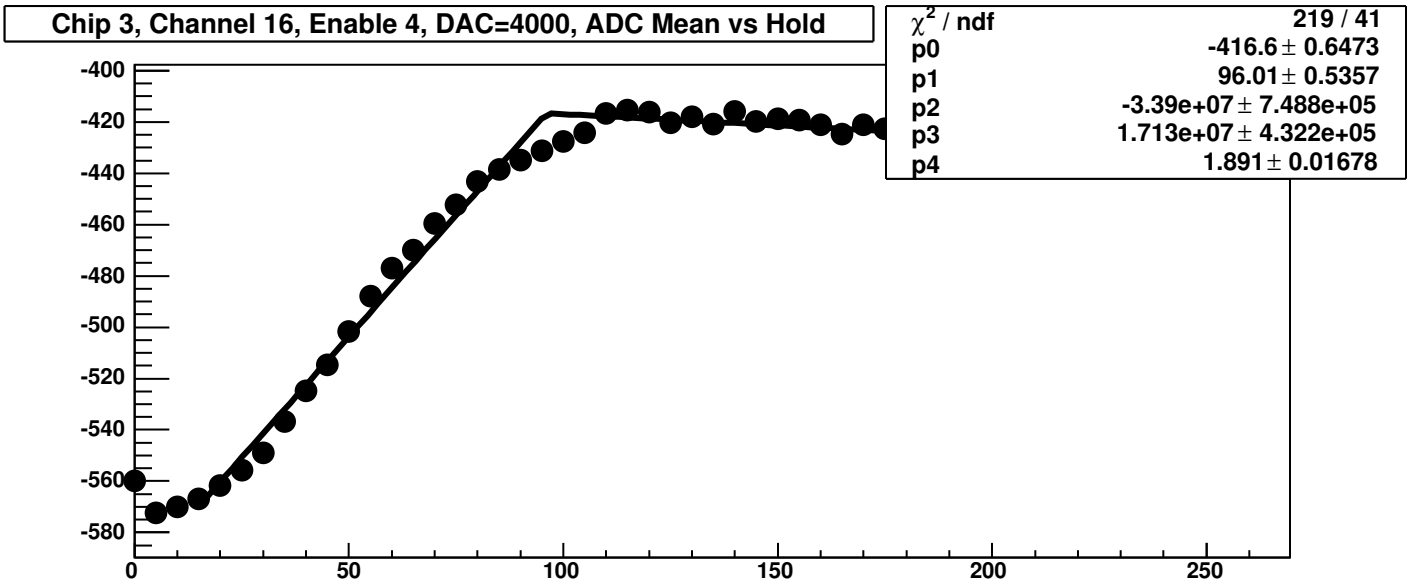


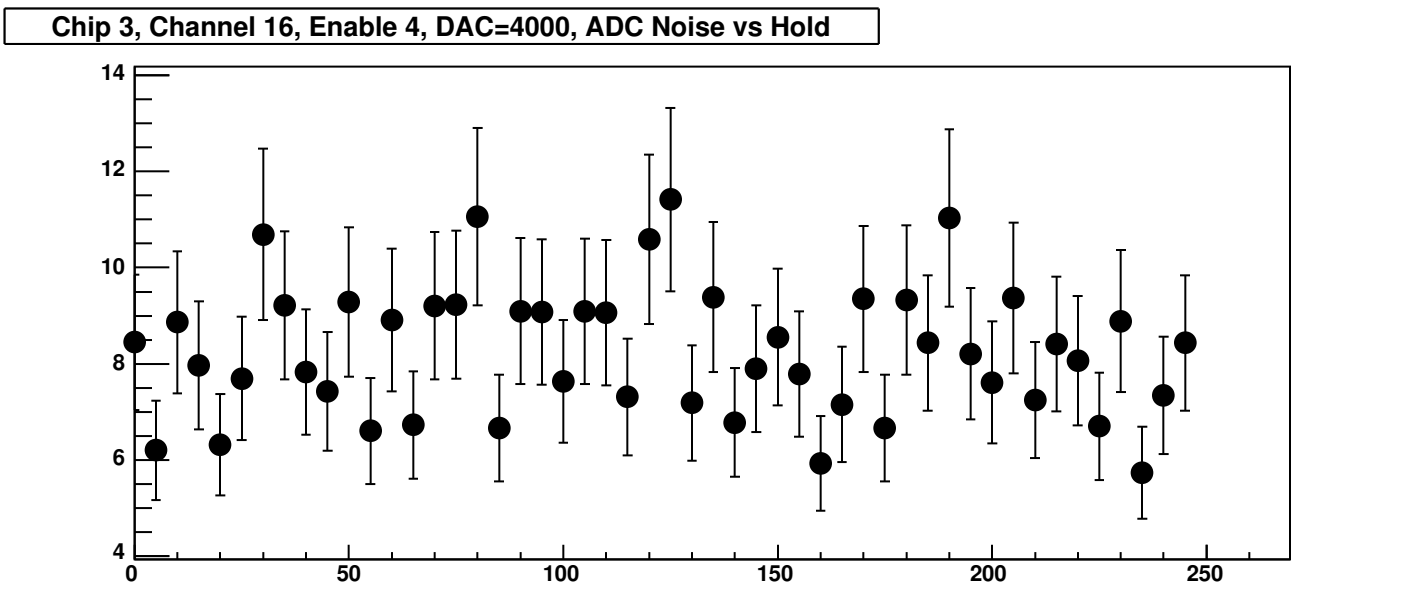
Chip 3, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold

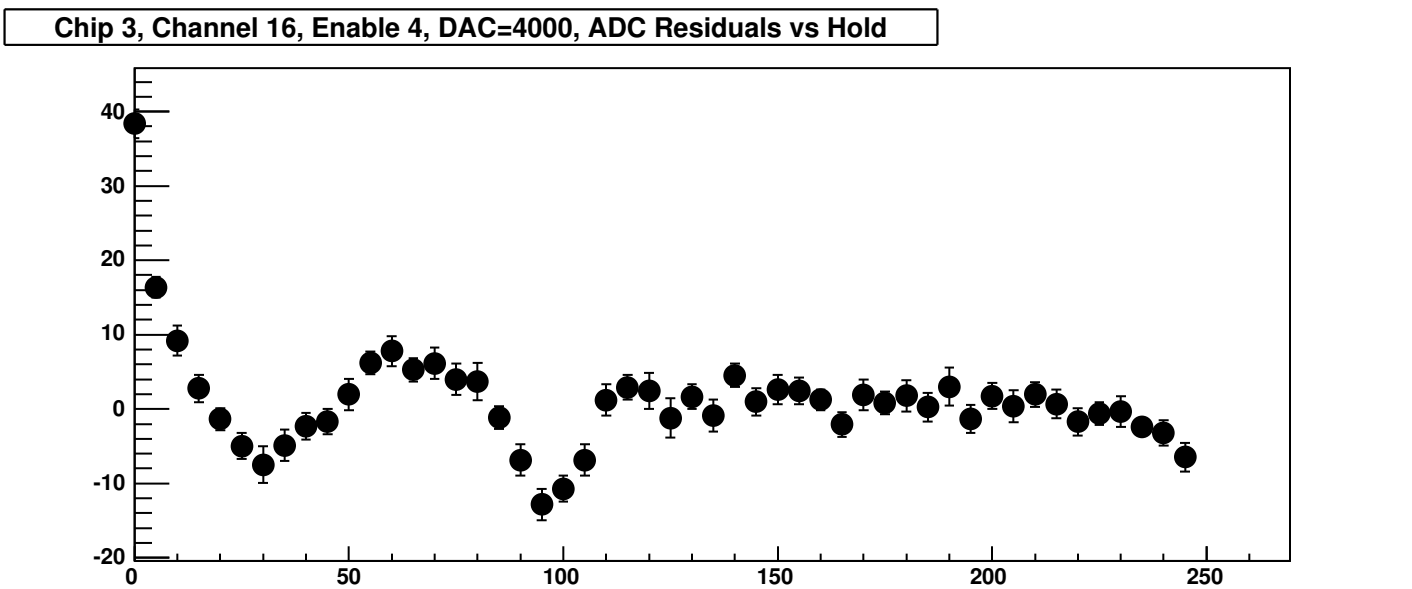


Chip 3, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

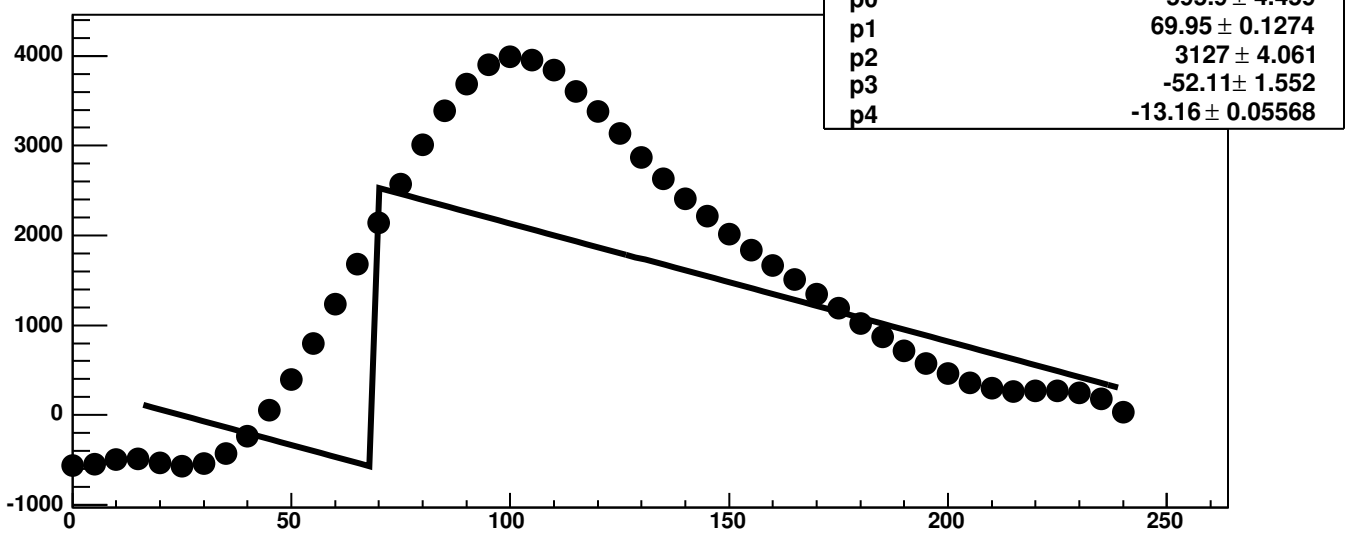



Chip 3, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 219 / 41
p0 -416.6 ± 0.6473
p1 96.01 ± 0.5357
p2 -3.39e+07 ± 7.488e+05
p3 1.713e+07 ± 4.322e+05
p4 1.891 ± 0.01678
-400
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
50
100
150
200
250

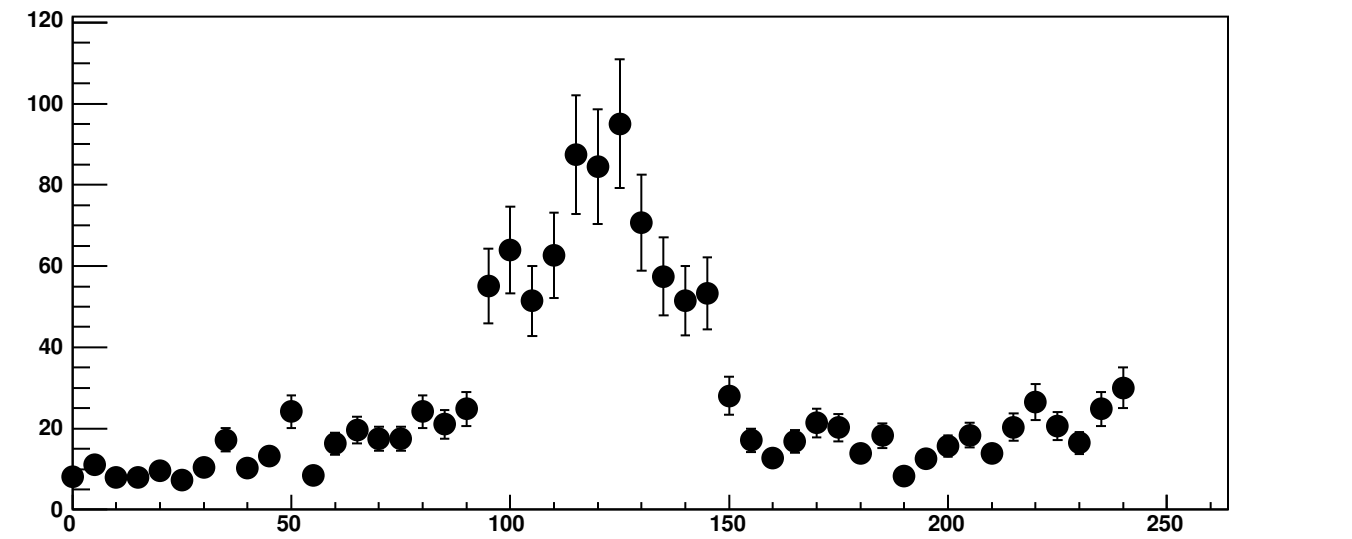

Chip 3, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 3, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
-20
0
50
100
150
200
250

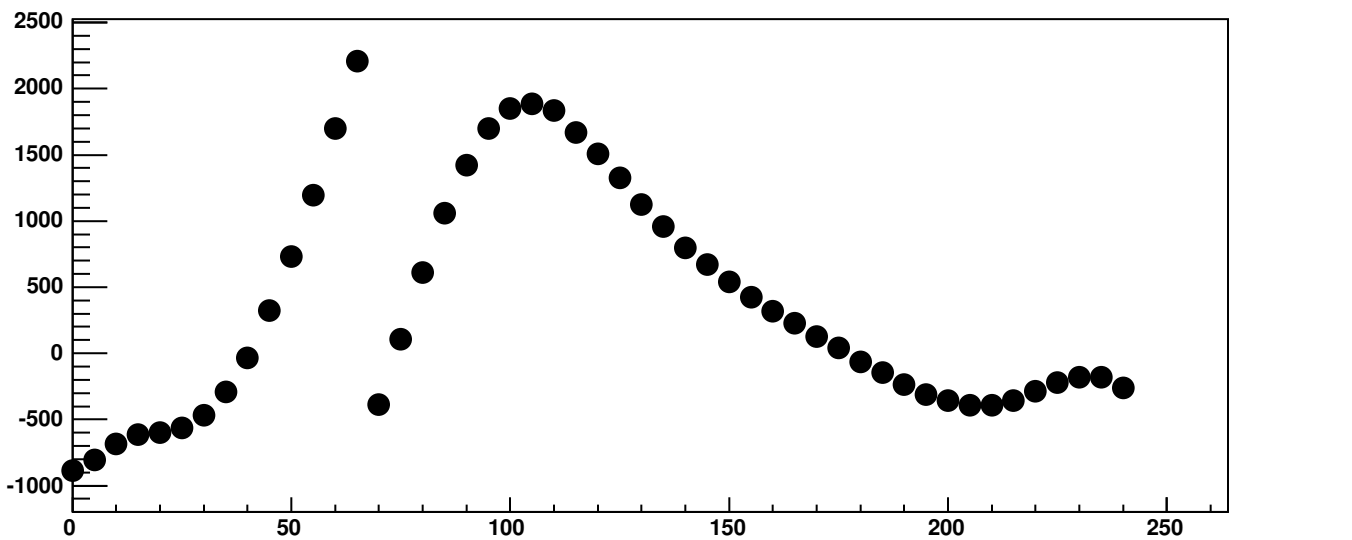
Chip 3, Channel 16, Enable 5, DAC=4000, ADC Mean vs Hold



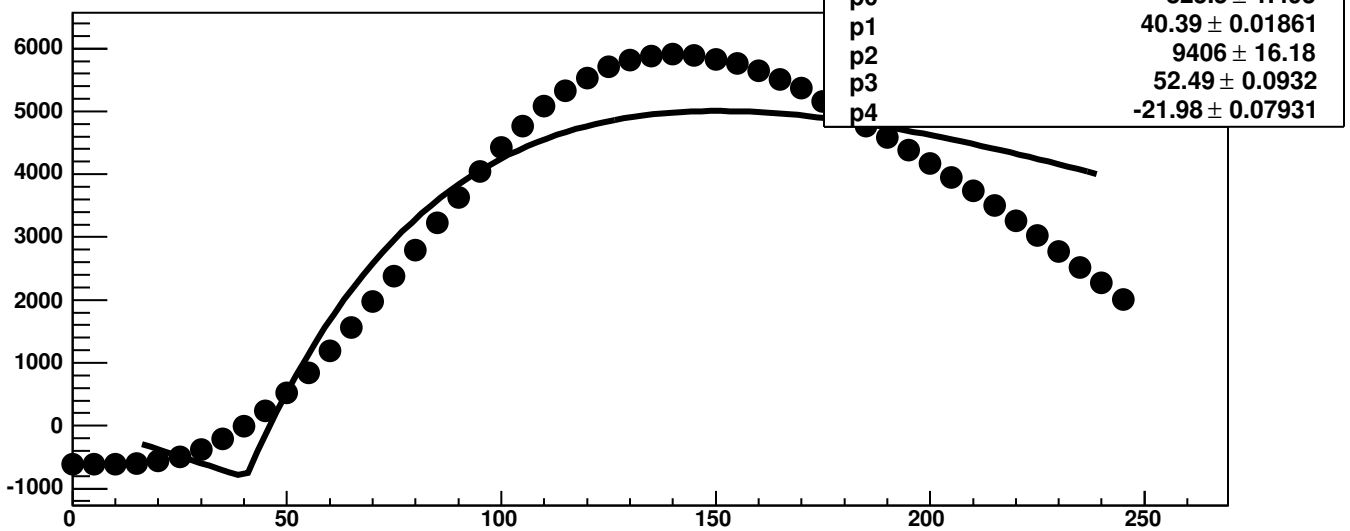
Chip 3, Channel 16, Enable 5, DAC=4000, ADC Noise vs Hold



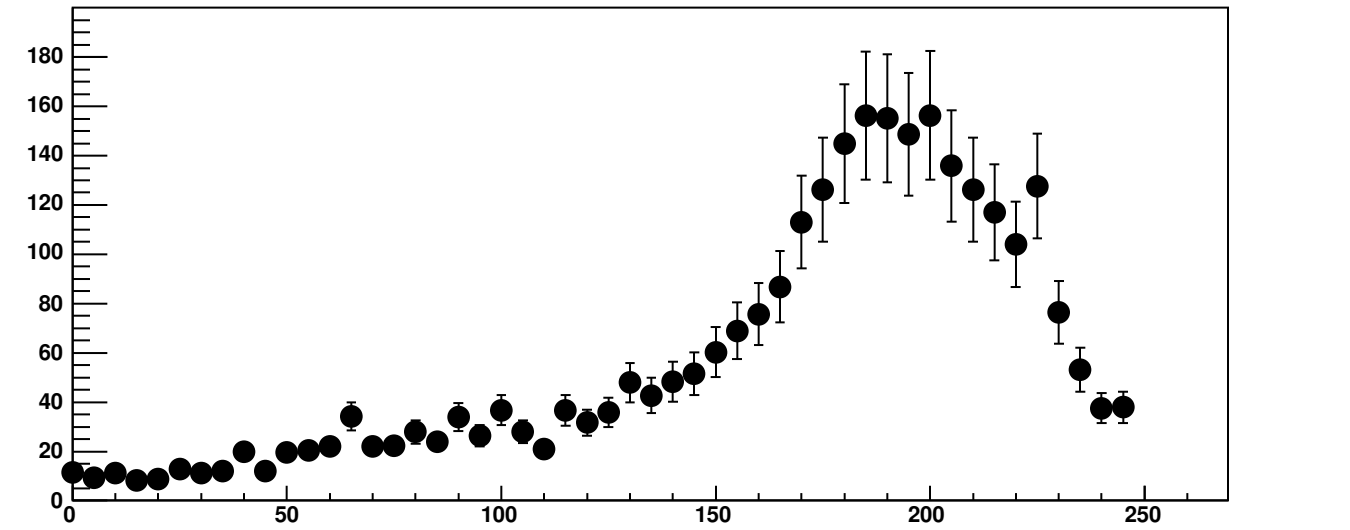
Chip 3, Channel 16, Enable 5, DAC=4000, ADC Residuals vs Hold



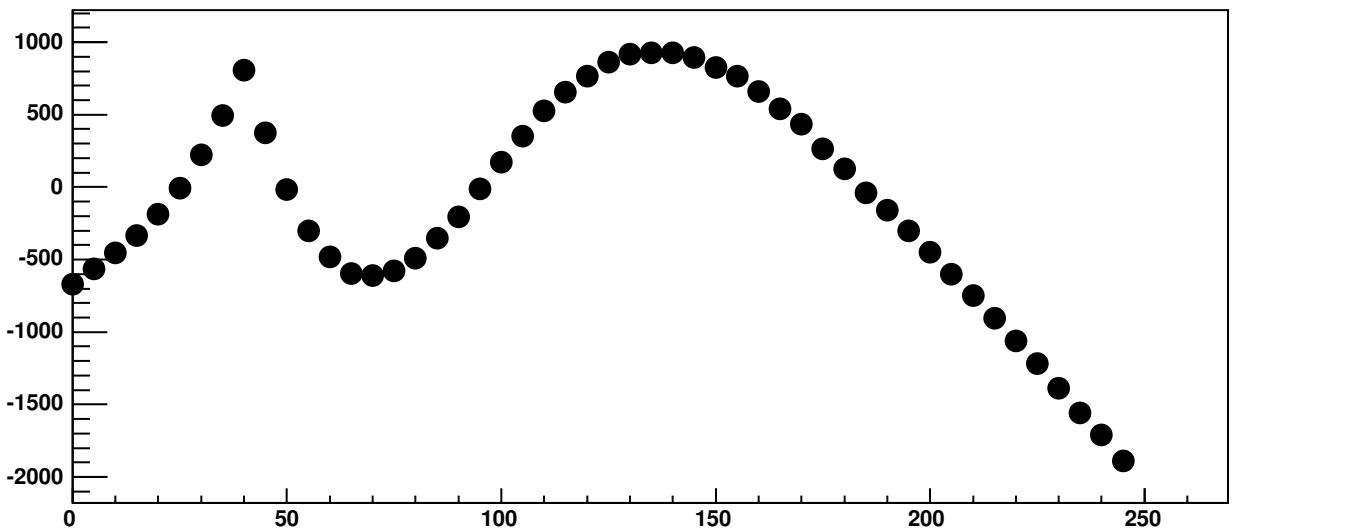
Chip 3, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold



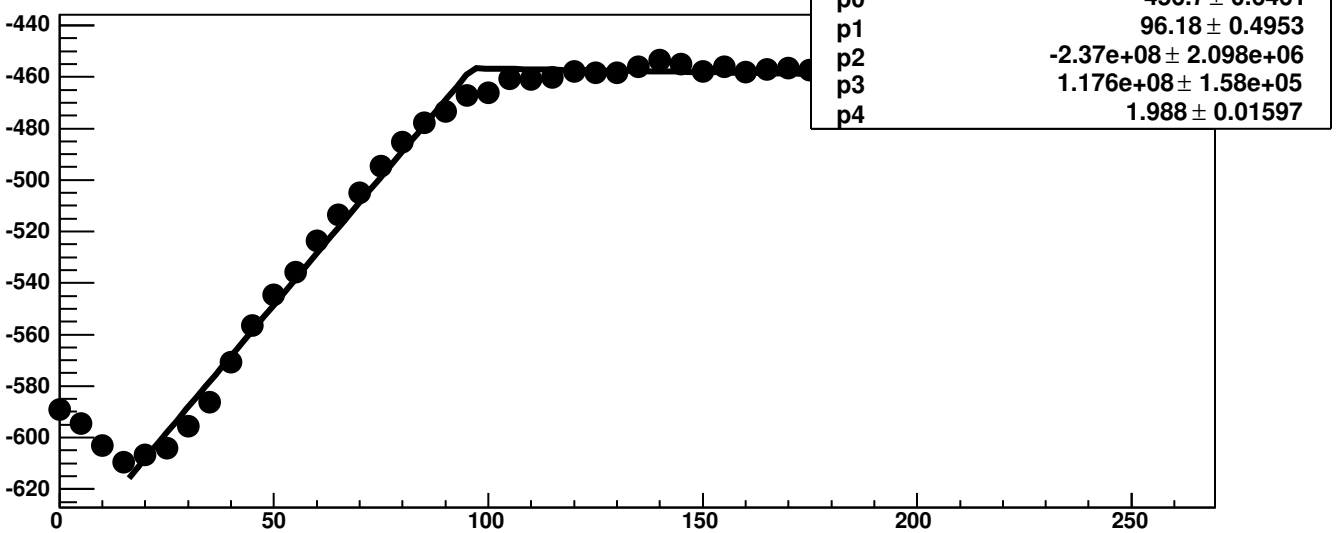
Chip 3, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



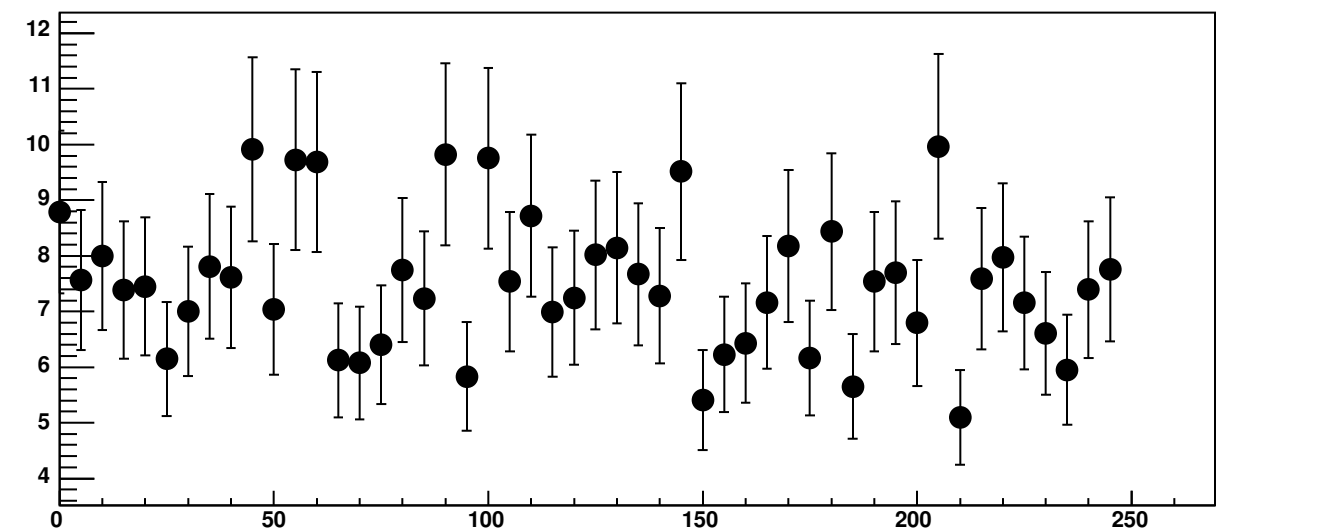
Chip 3, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold



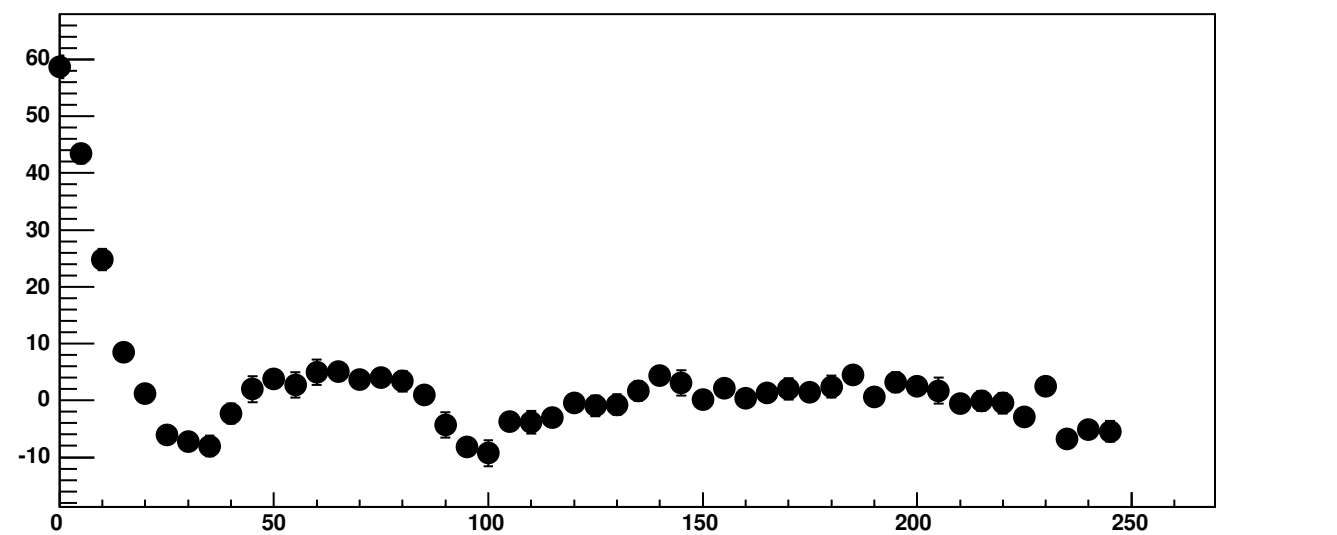
Chip 3, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold



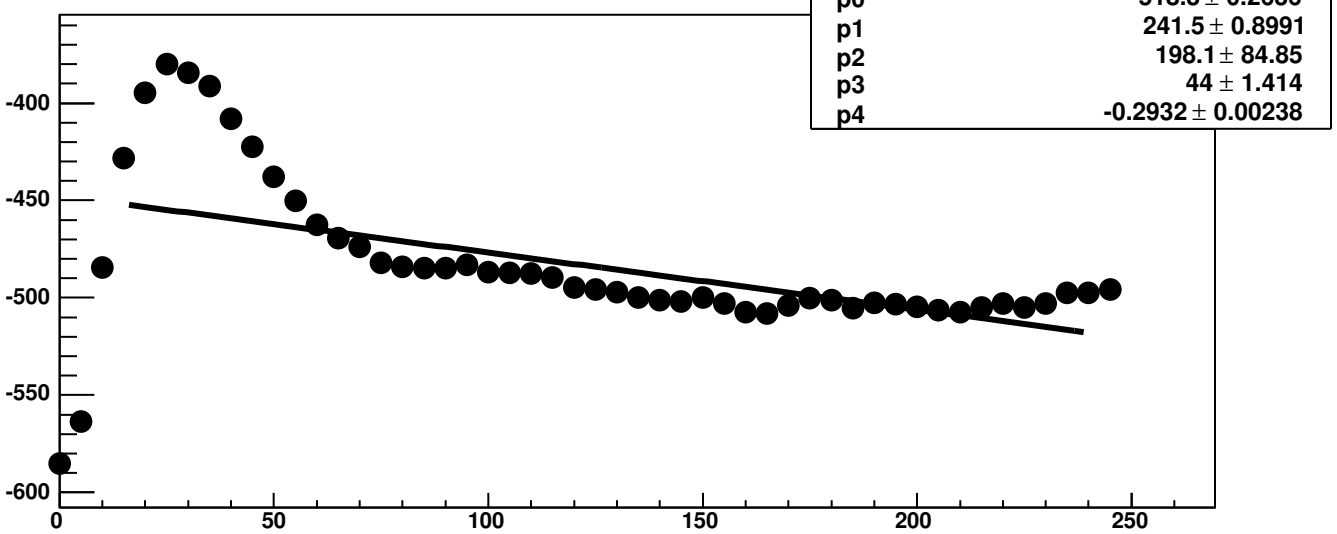
Chip 3, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



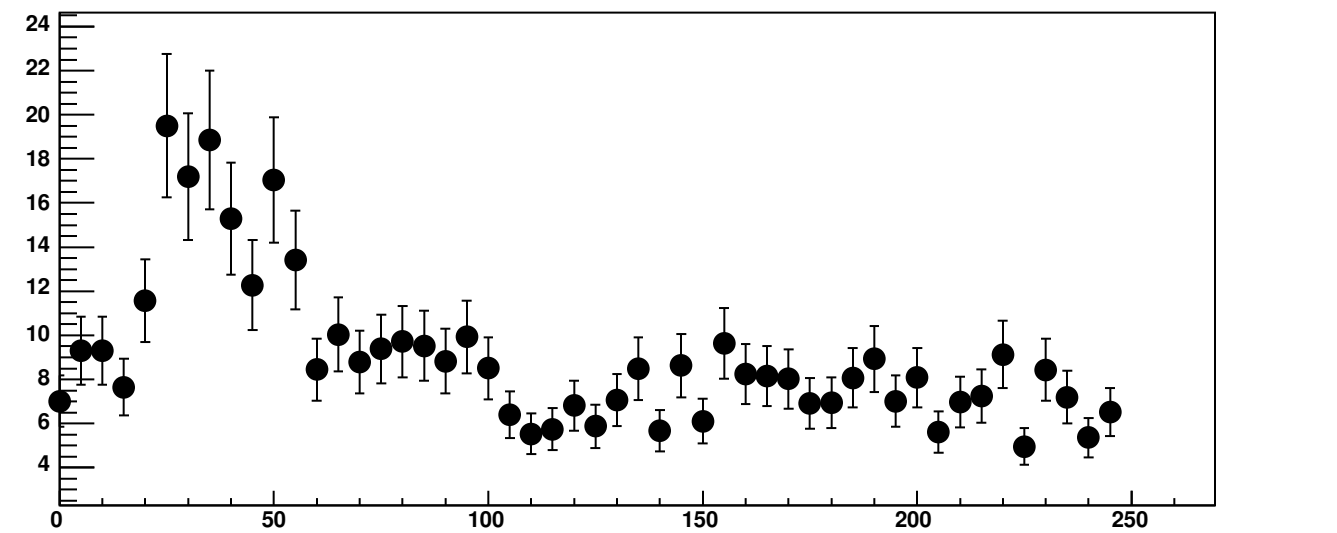
Chip 3, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold



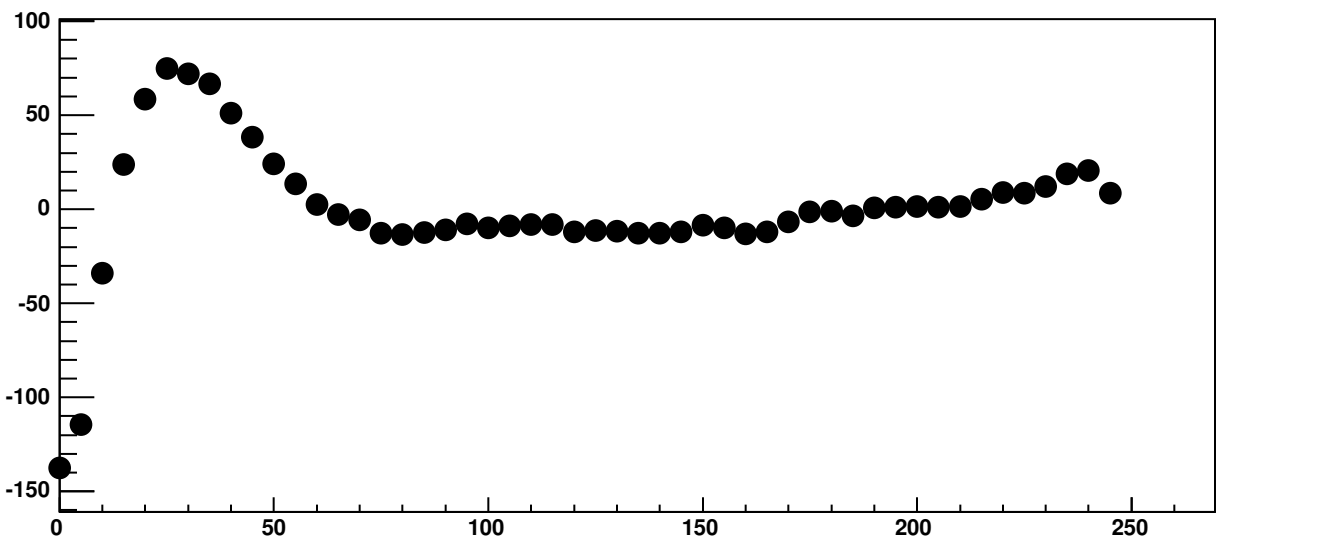
Chip 3, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold



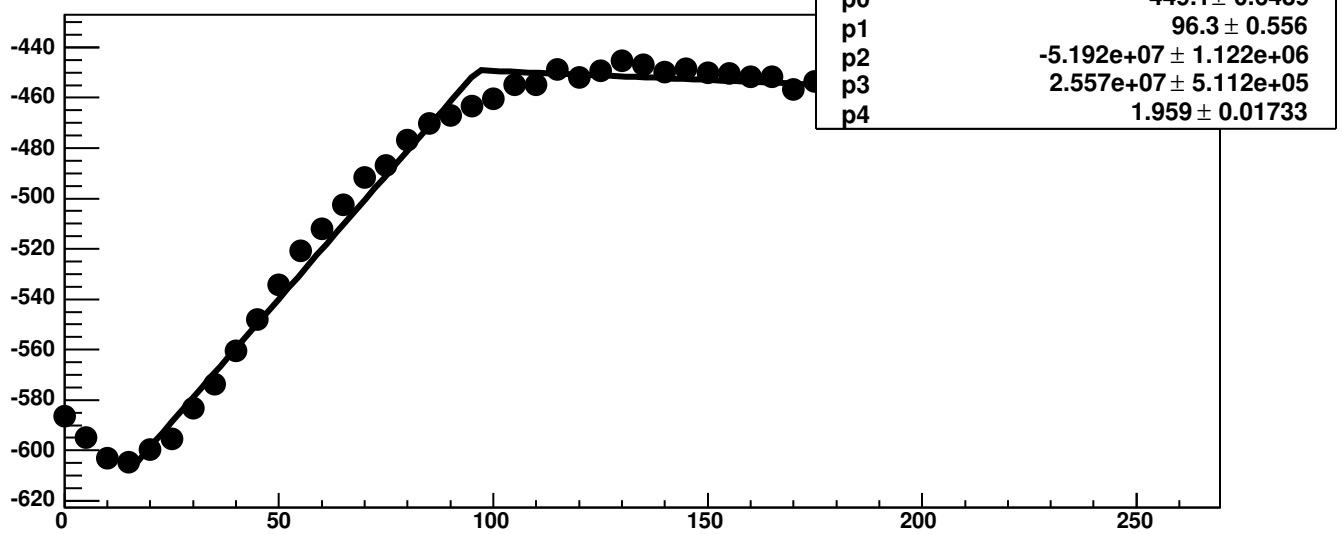
Chip 3, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold



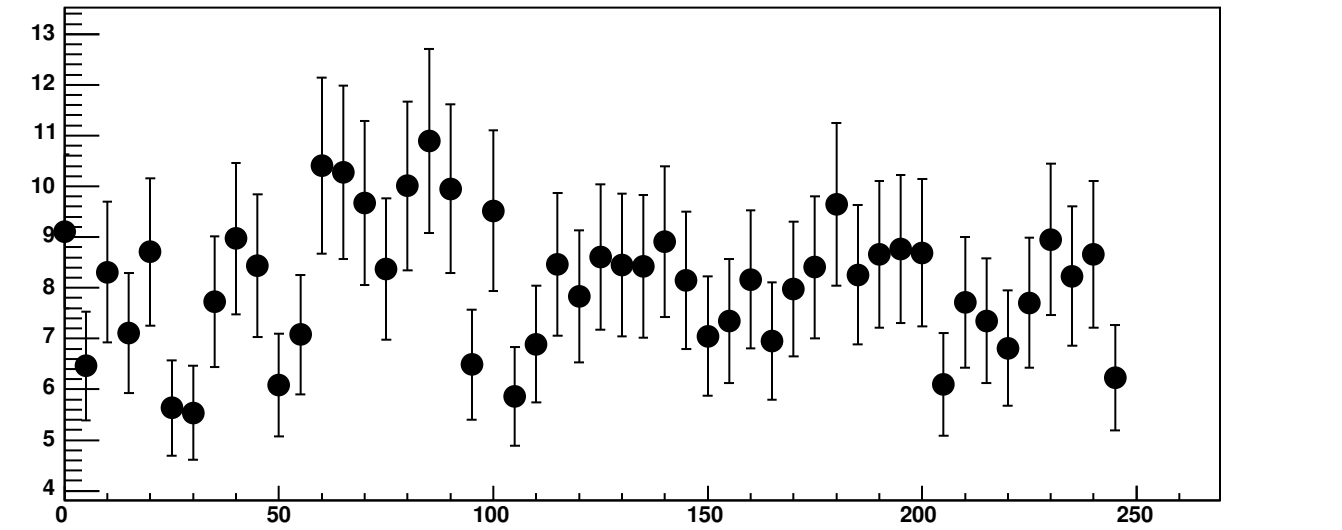
Chip 3, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold



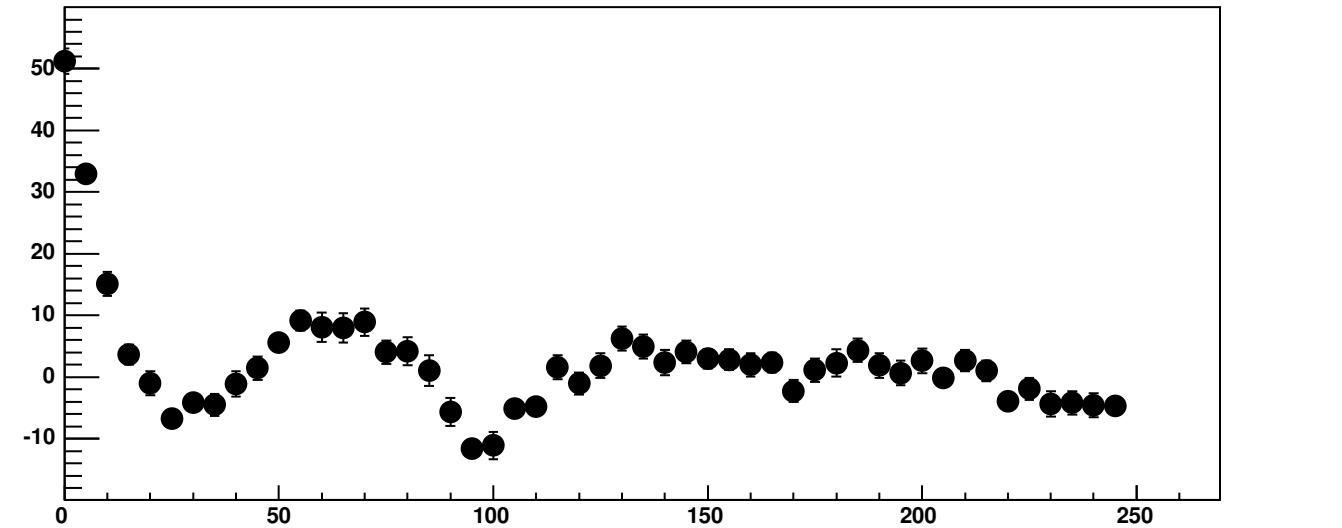
Chip 3, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



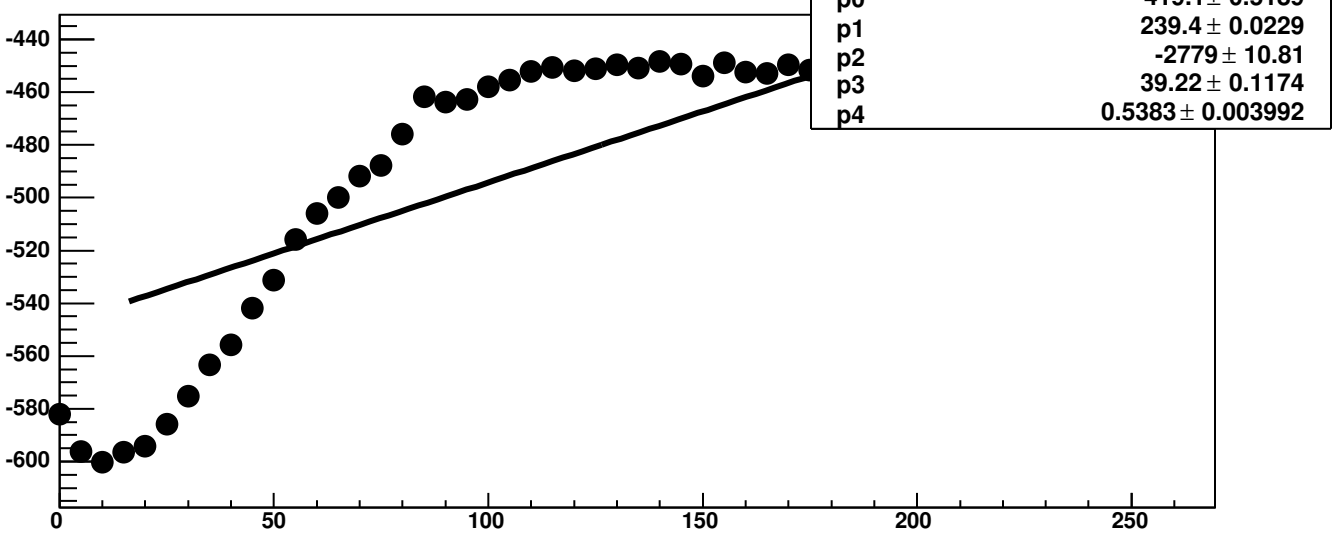
Chip 3, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



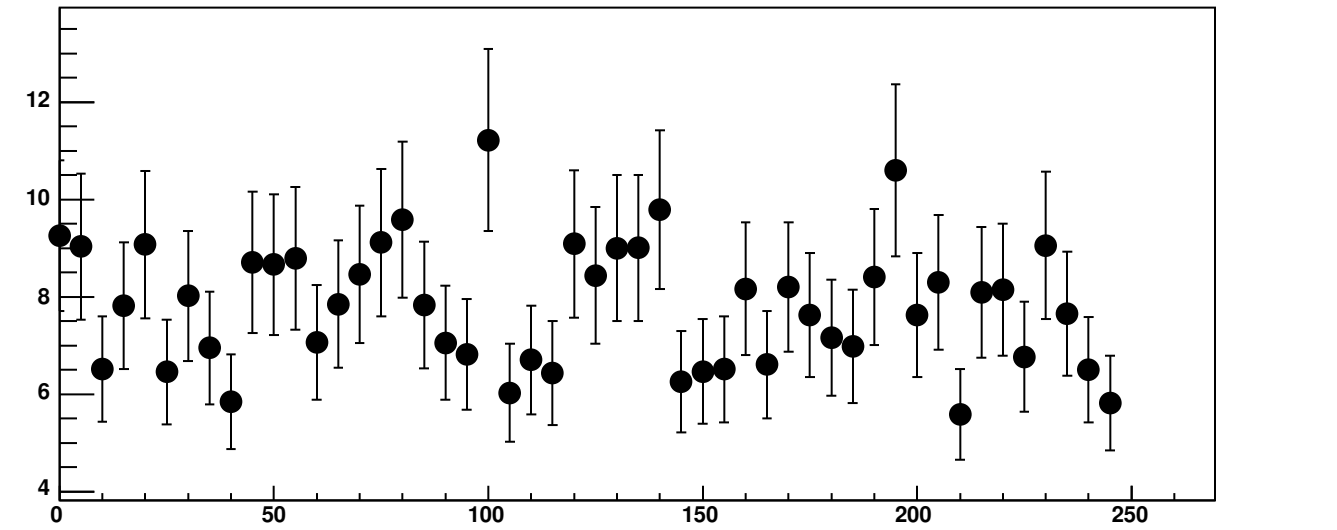
Chip 3, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold



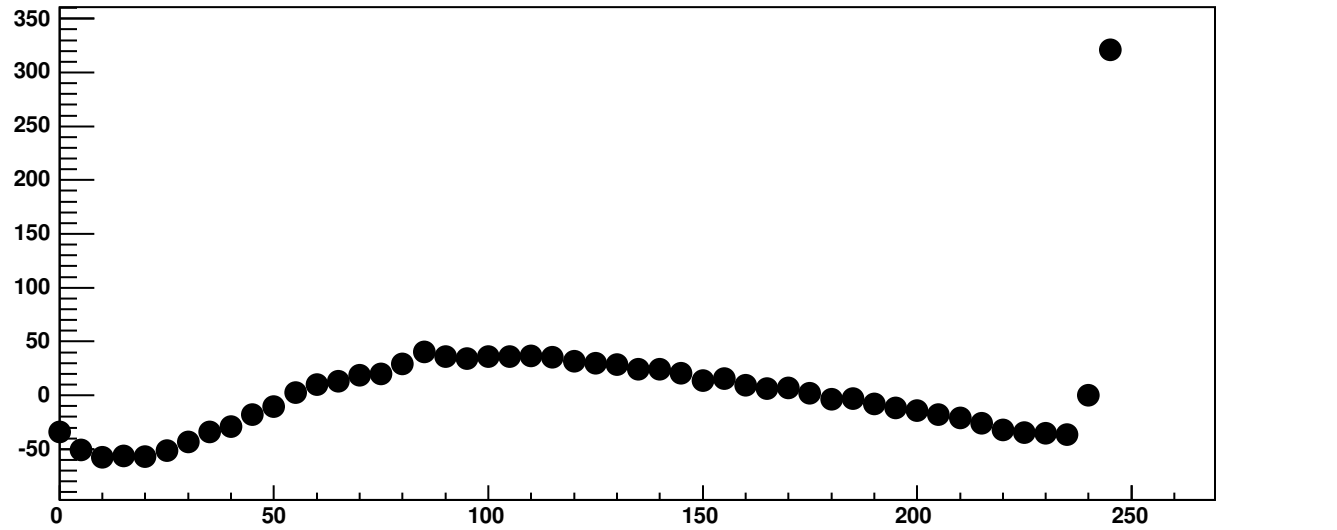
Chip 3, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



Chip 3, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold

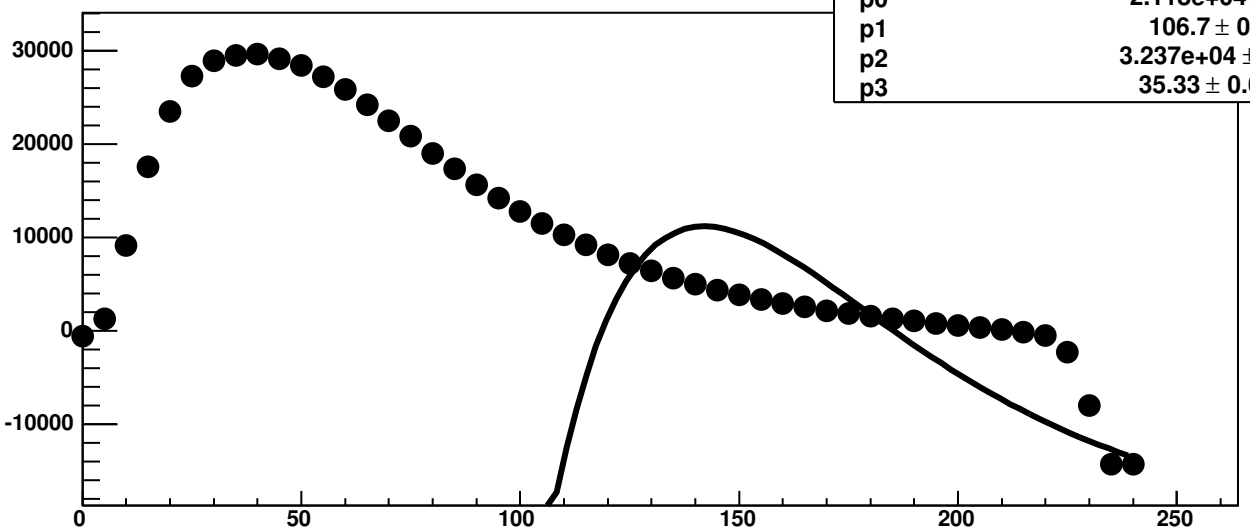


Chip 3, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

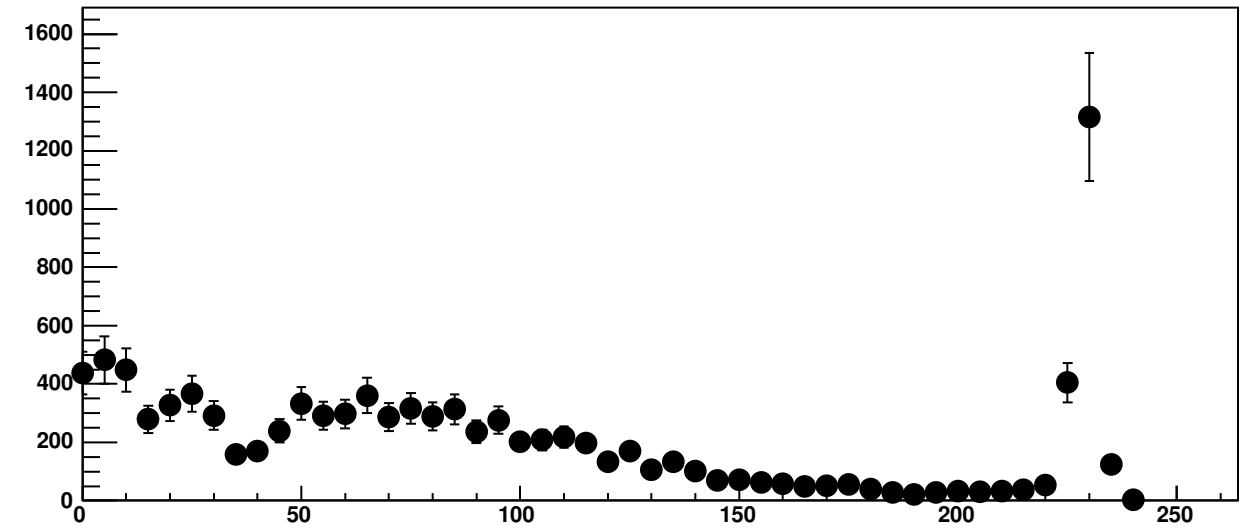


Chip 3, Channel 17, Enable 5!, DAC=4000, ADC Mean vs Hold

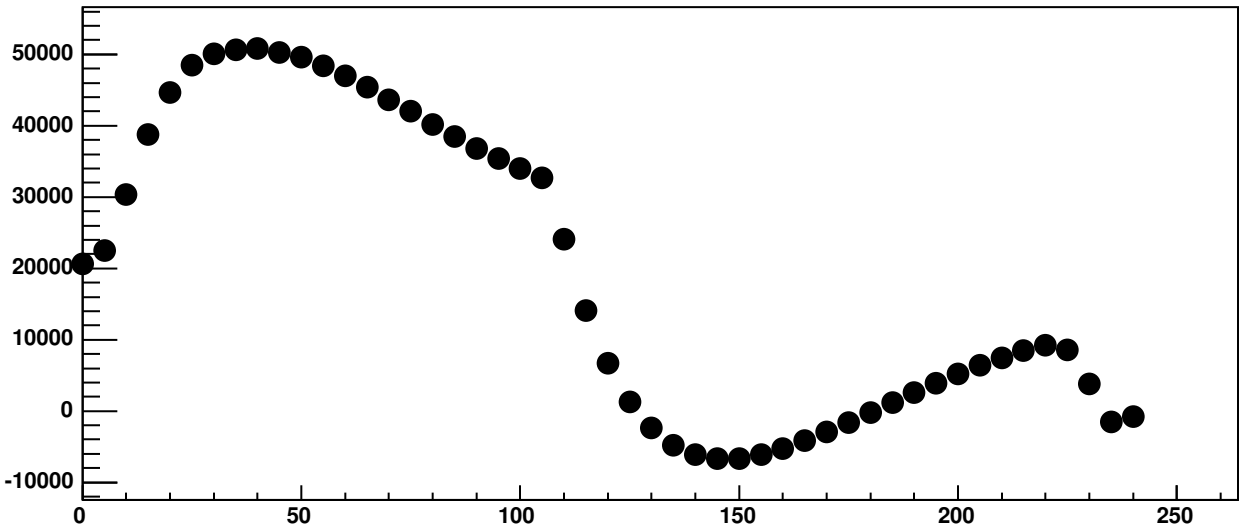
χ^2 / ndf	1.743e+07 / 42
p0	-2.118e+04 ± 4.42
p1	106.7 ± 0.01298
p2	3.237e+04 ± 6.125
p3	35.33 ± 0.007691



Chip 3, Channel 17, Enable 5!, DAC=4000, ADC Noise vs Hold

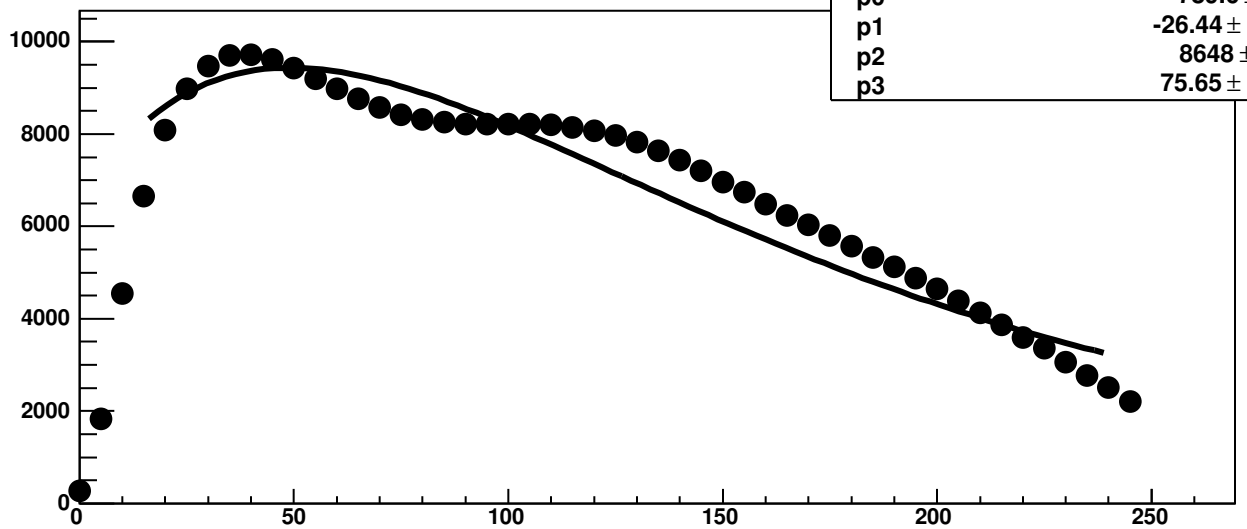


Chip 3, Channel 17, Enable 5!, DAC=4000, ADC Residuals vs Hold

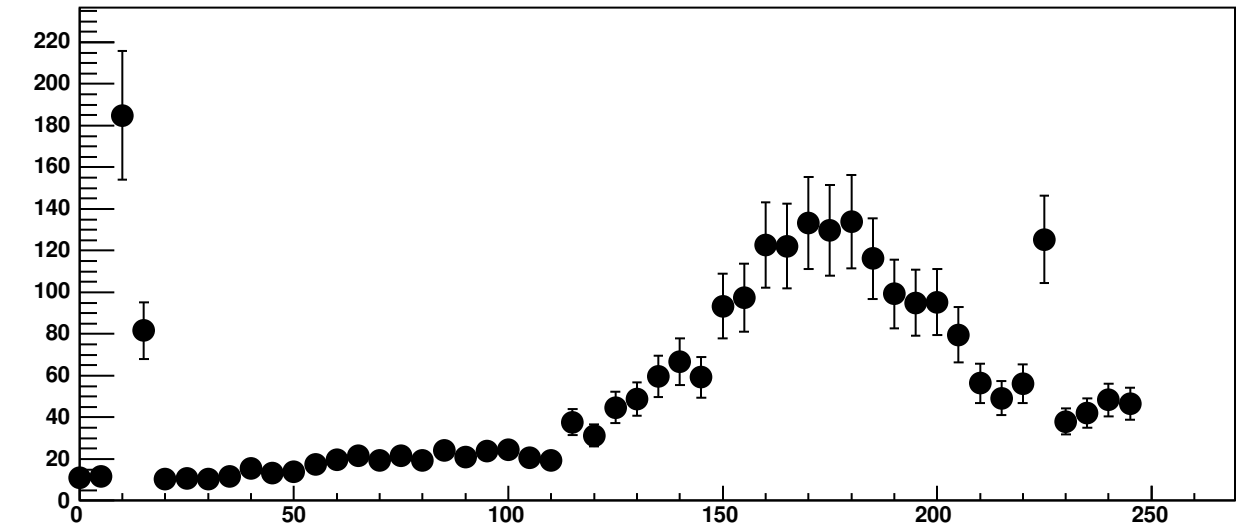


Chip 4, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold

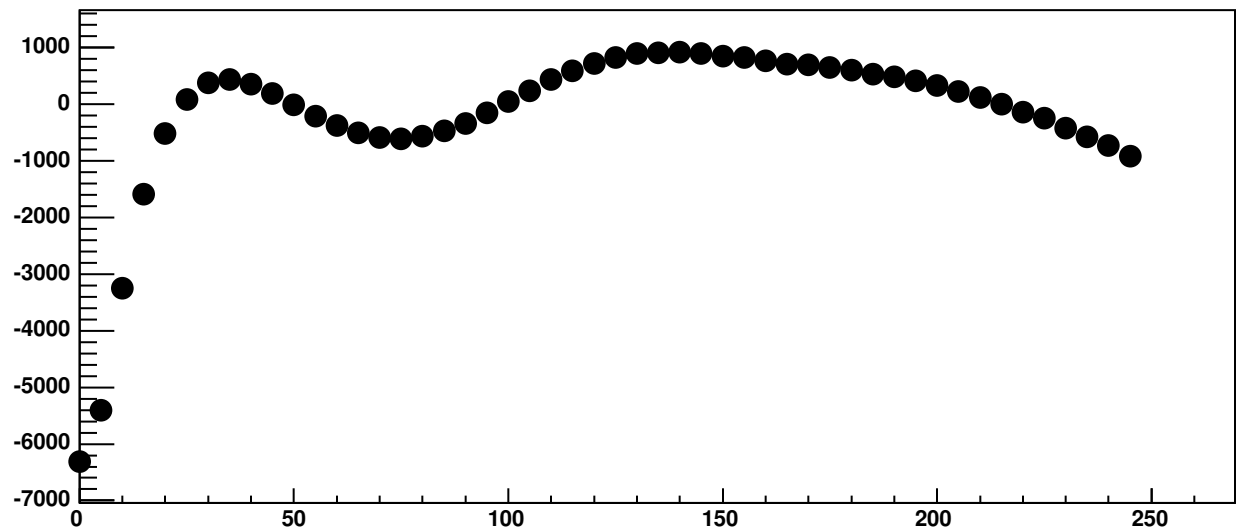
χ^2 / ndf	2.739e+05 / 42
p0	789.6 ± 19.11
p1	-26.44 ± 0.1629
p2	8648 ± 18.37
p3	75.65 ± 0.1677

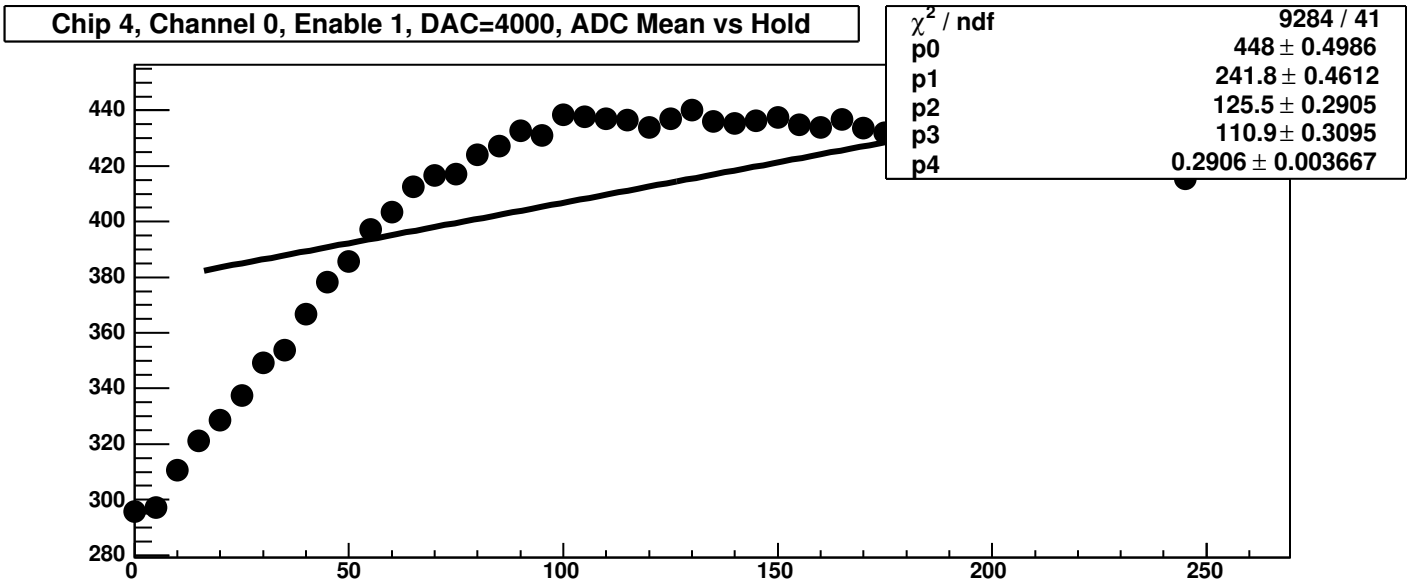


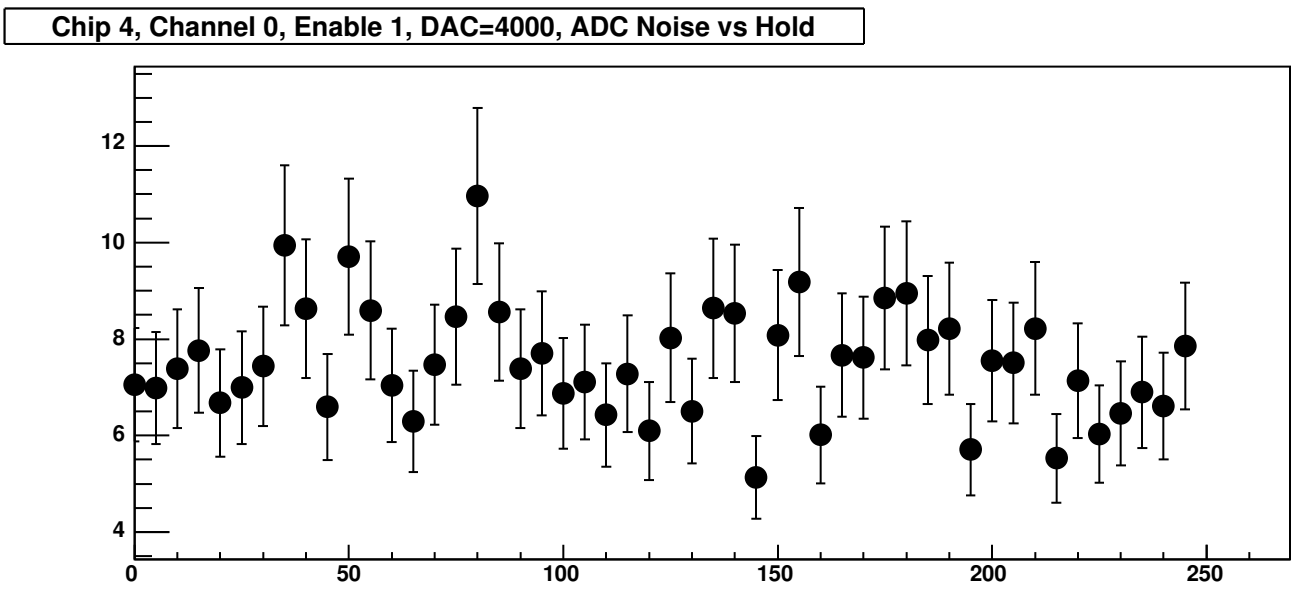
Chip 4, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold

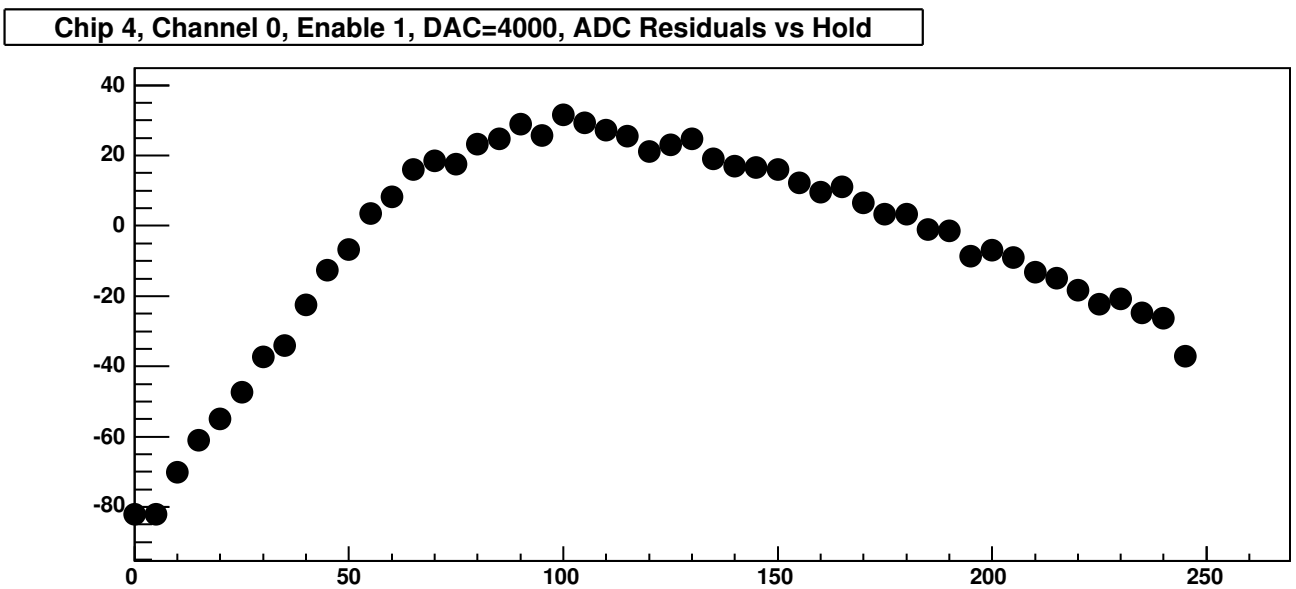


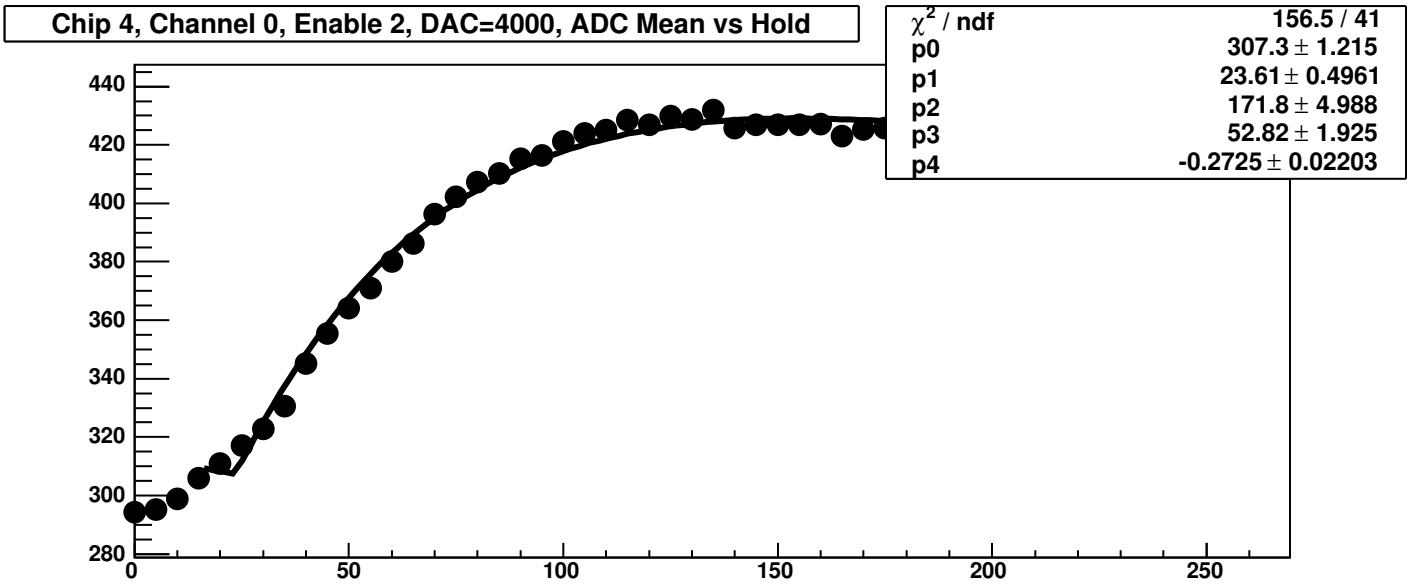
Chip 4, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold

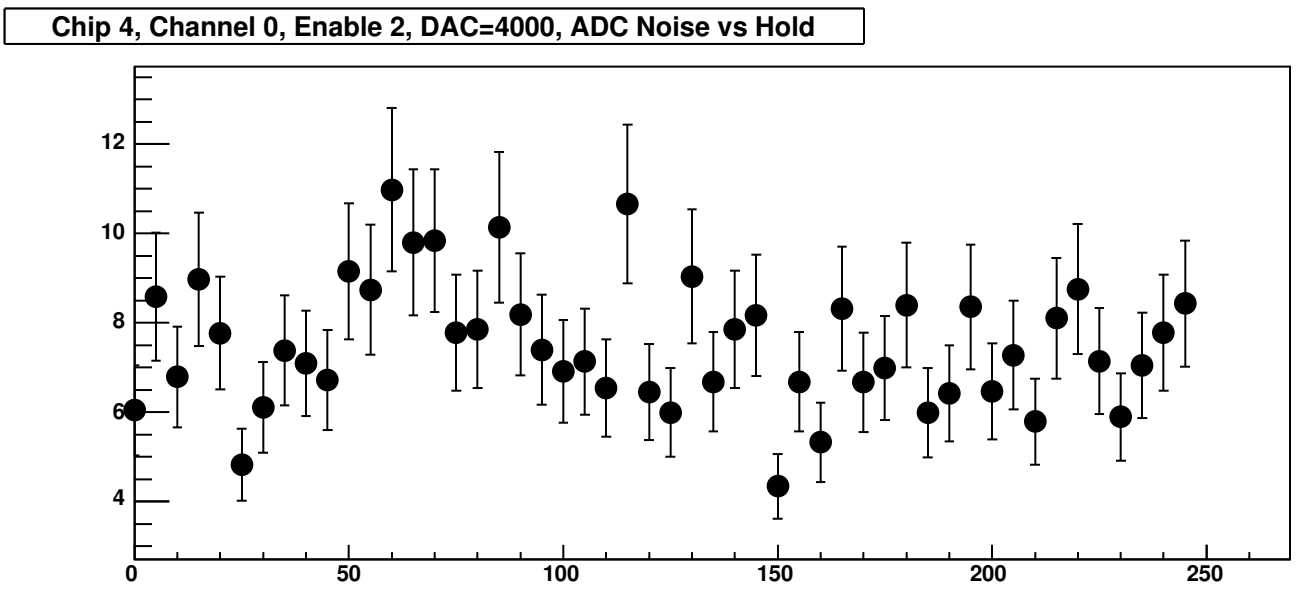


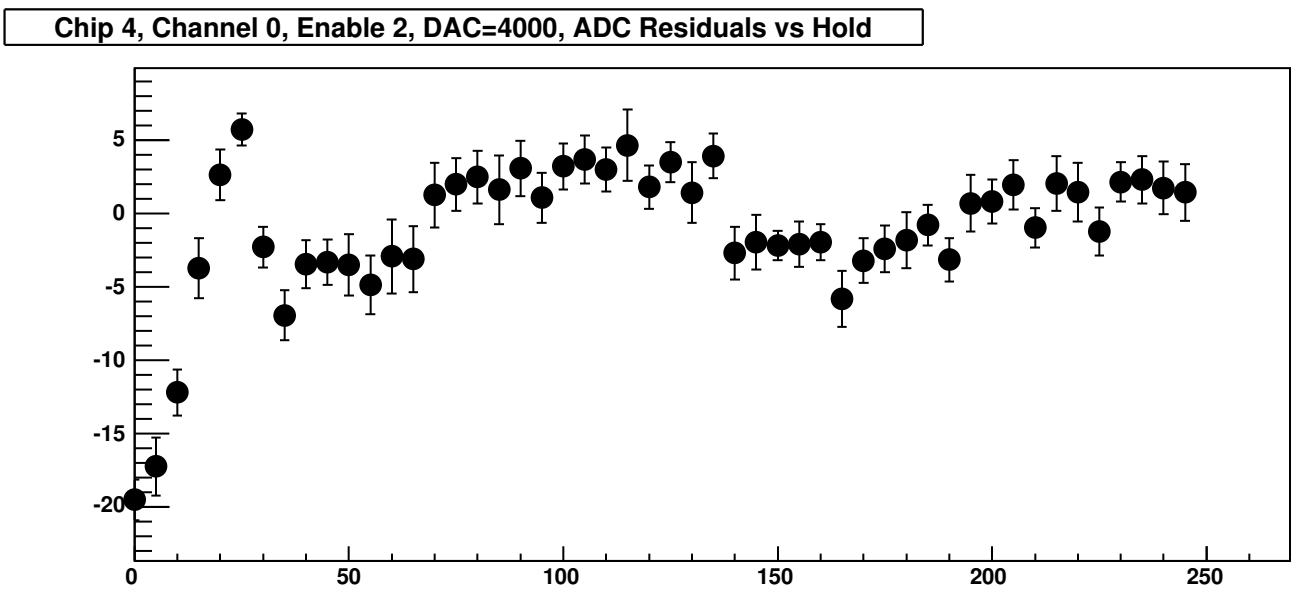

Chip 4, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 9284 / 41
p0 448 ± 0.4986
p1 241.8 ± 0.4612
p2 125.5 ± 0.2905
p3 110.9 ± 0.3095
p4 0.2906 ± 0.003667
440
420
400
380
360
340
320
300
280
0
50
100
150
200
250


Chip 4, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

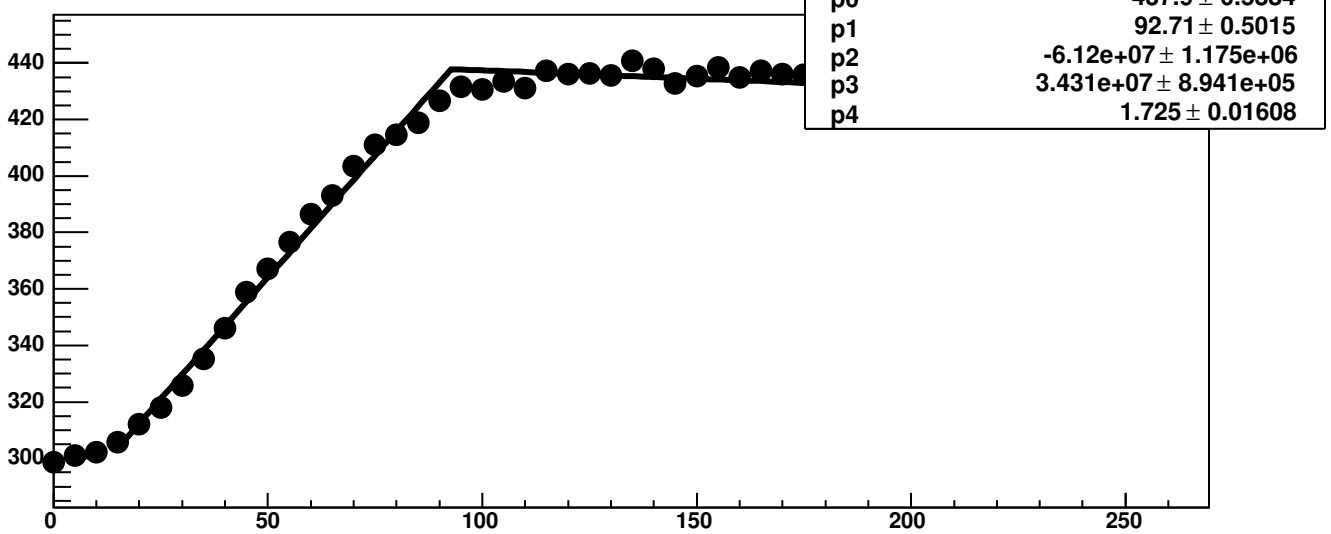

Chip 4, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
0
50
100
150
200
250


Chip 4, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 156.5 / 41
p0 307.3 ± 1.215
p1 23.61 ± 0.4961
p2 171.8 ± 4.988
p3 52.82 ± 1.925
p4 -0.2725 ± 0.02203
440
420
400
380
360
340
320
300
280
0
50
100
150
200
250

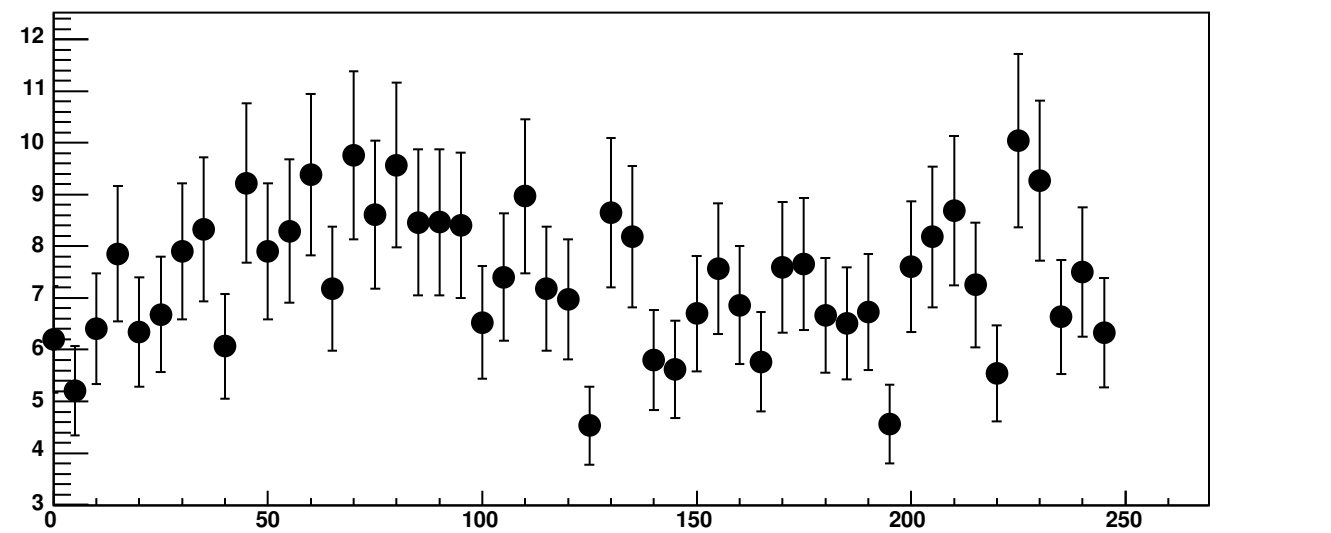

Chip 4, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 4, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold
5
0
-5
-10
-15
-20
0
50
100
150
200
250

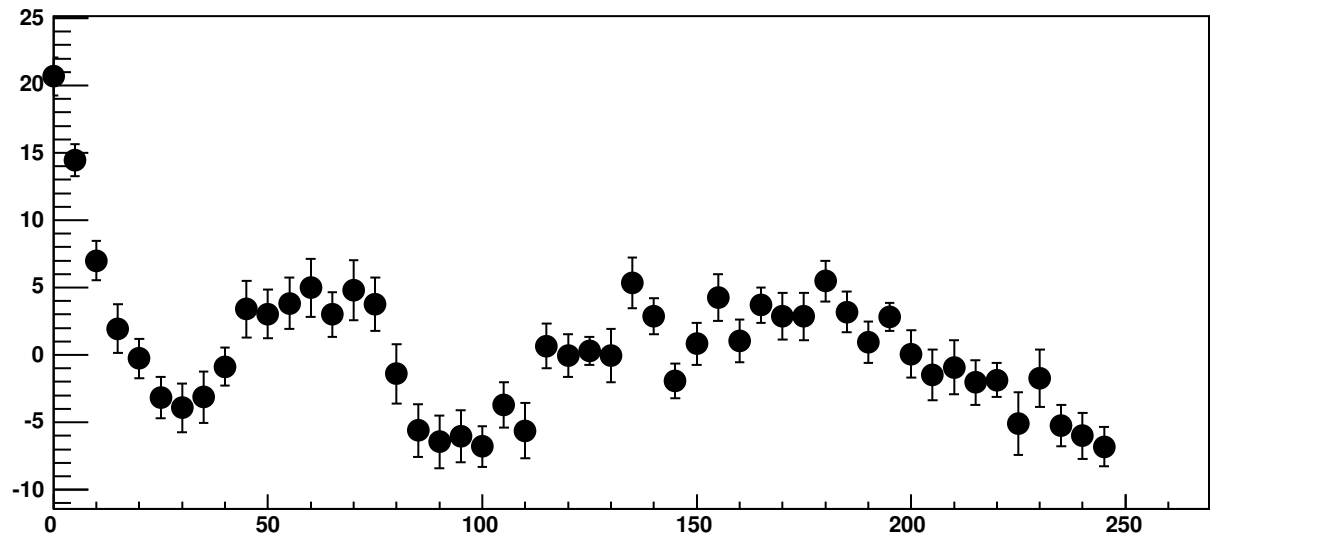
Chip 4, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold



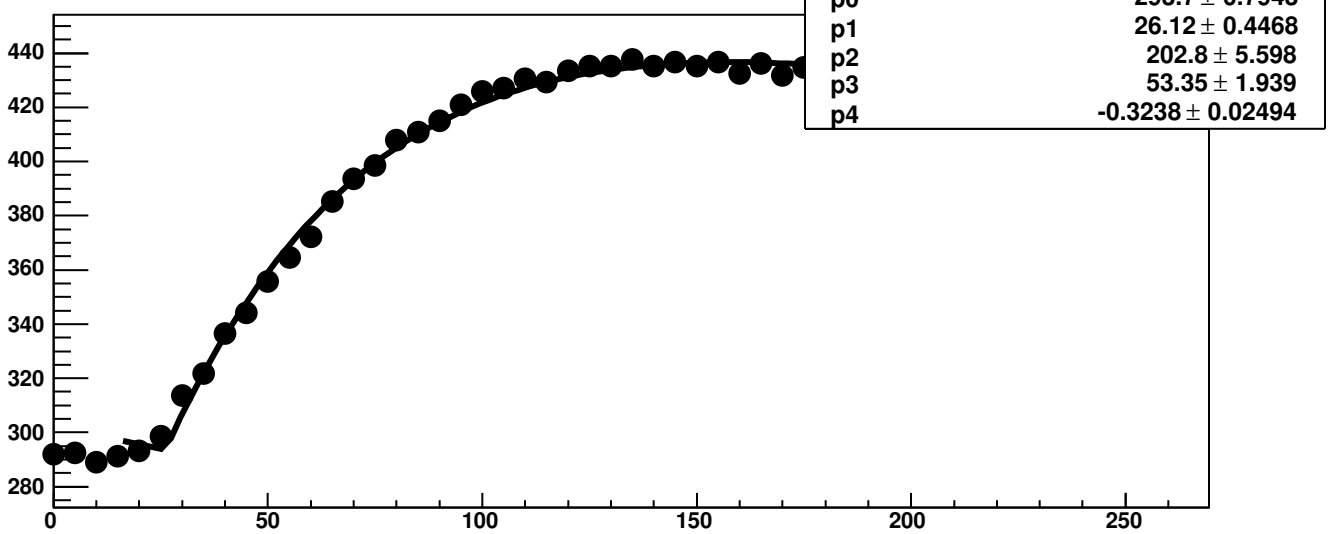
Chip 4, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold



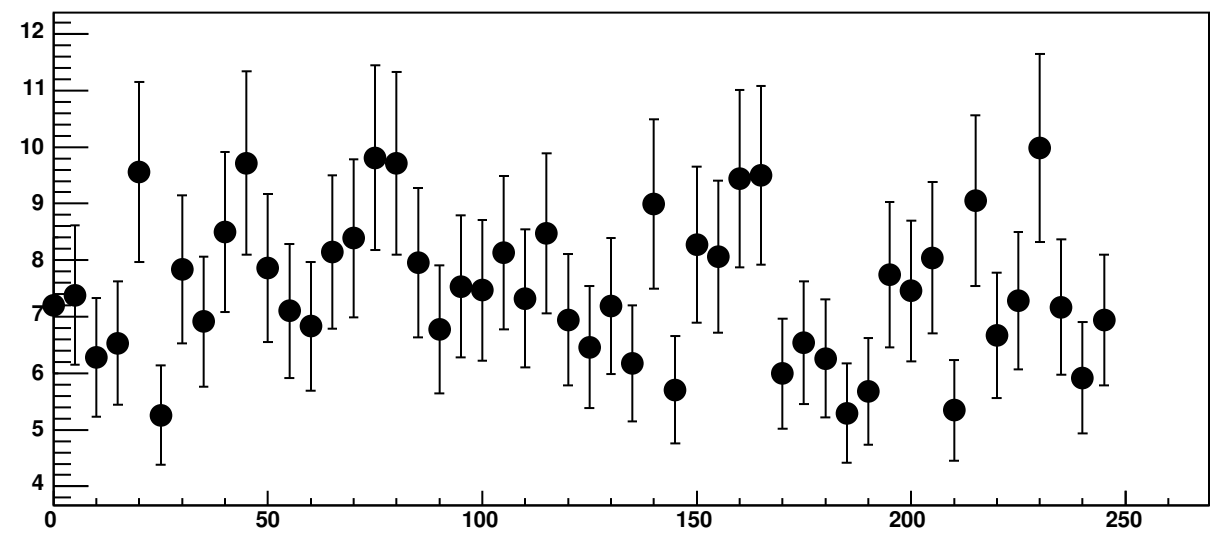
Chip 4, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold



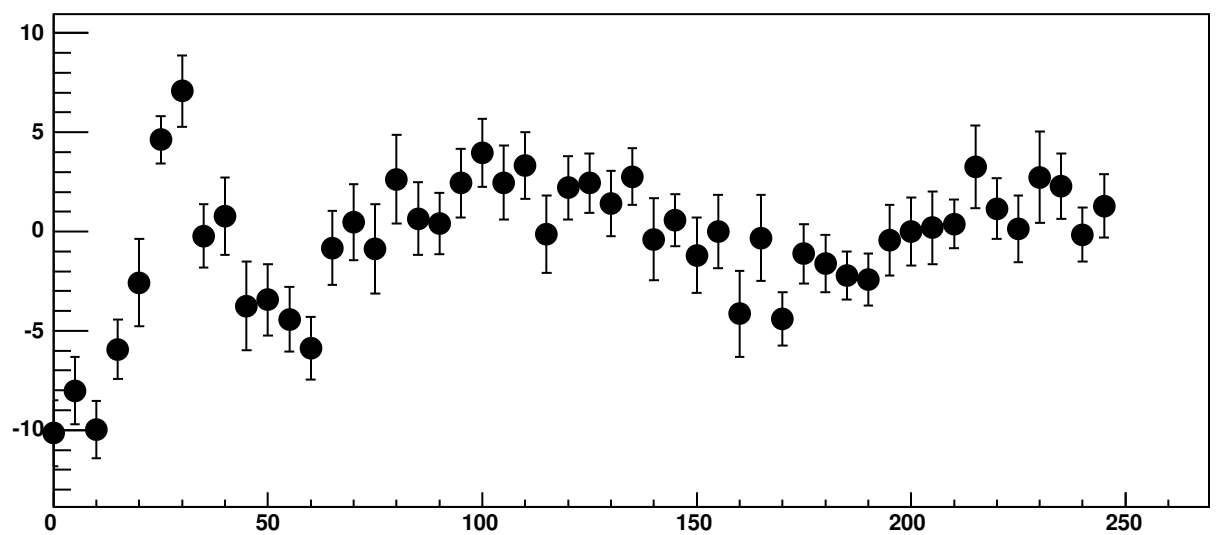
Chip 4, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

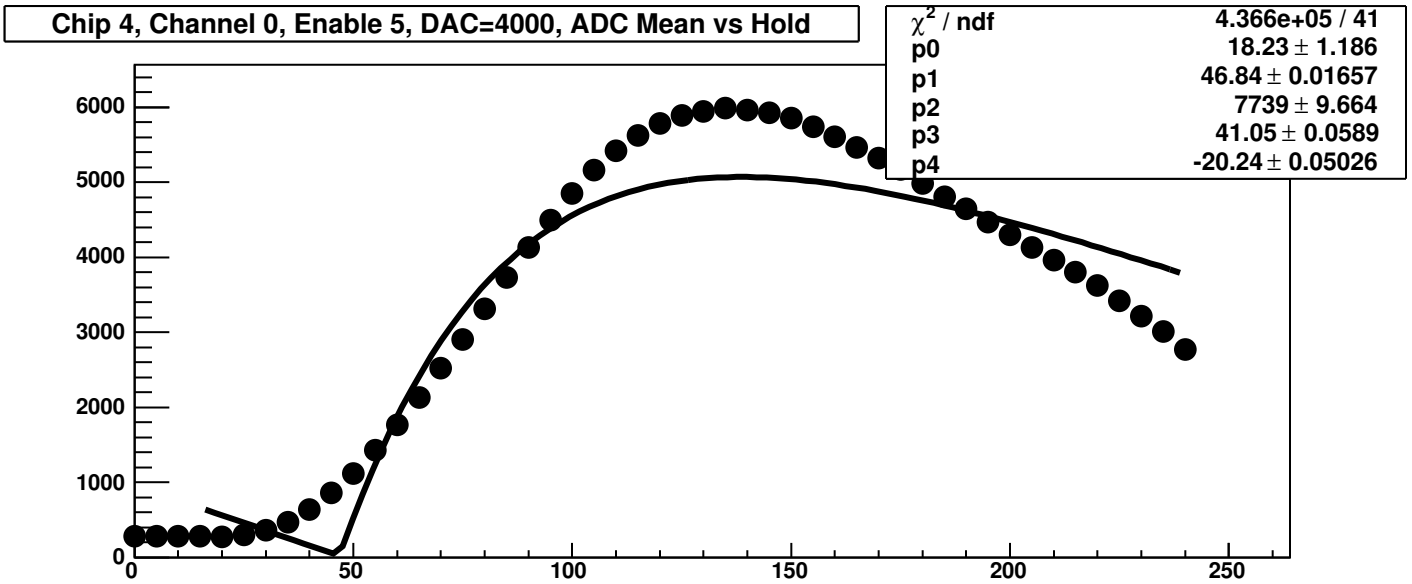


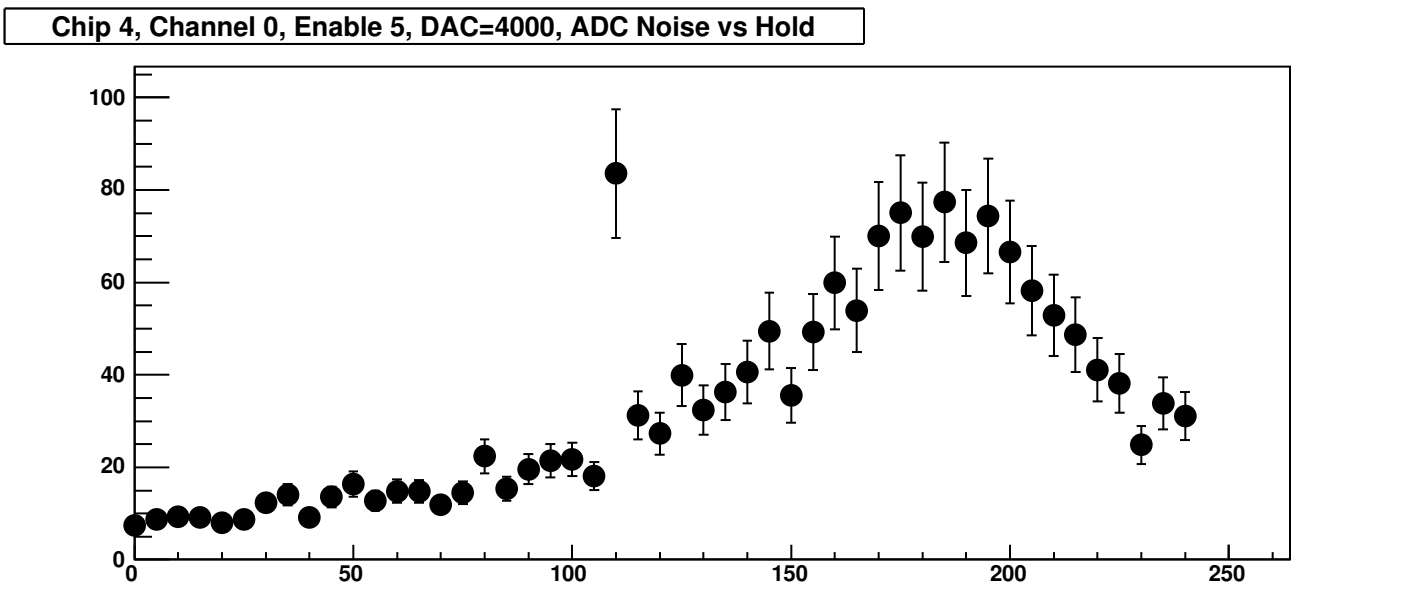
Chip 4, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold

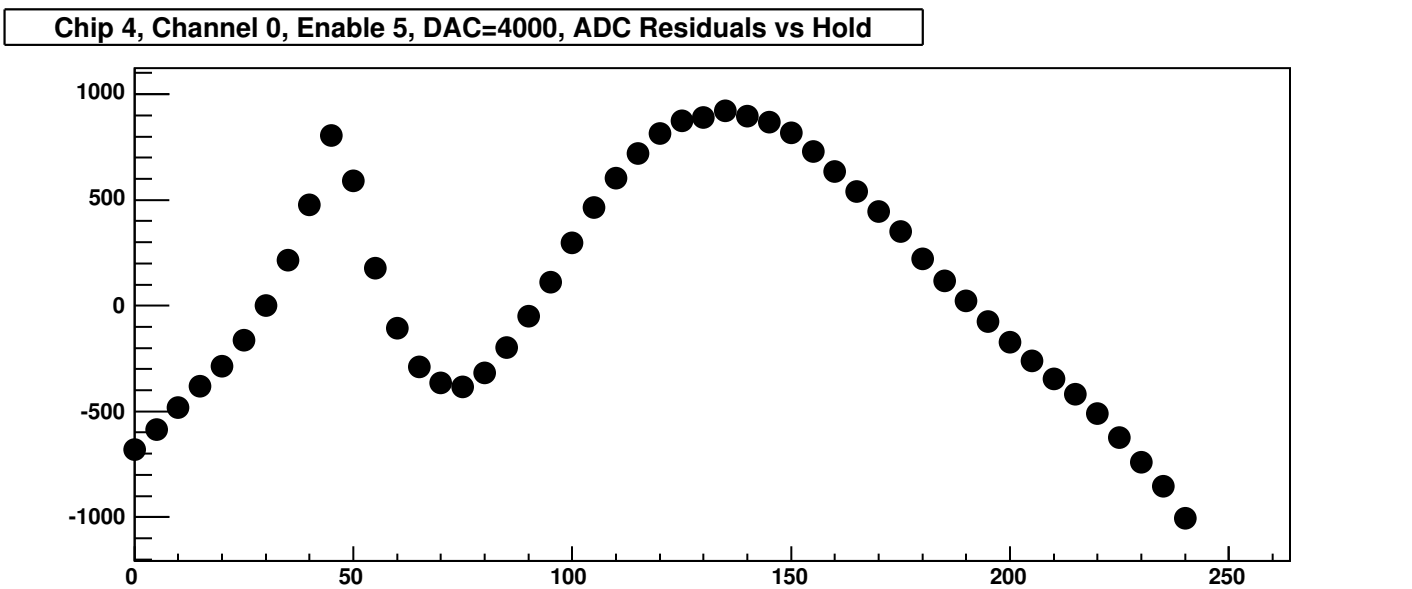


Chip 4, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

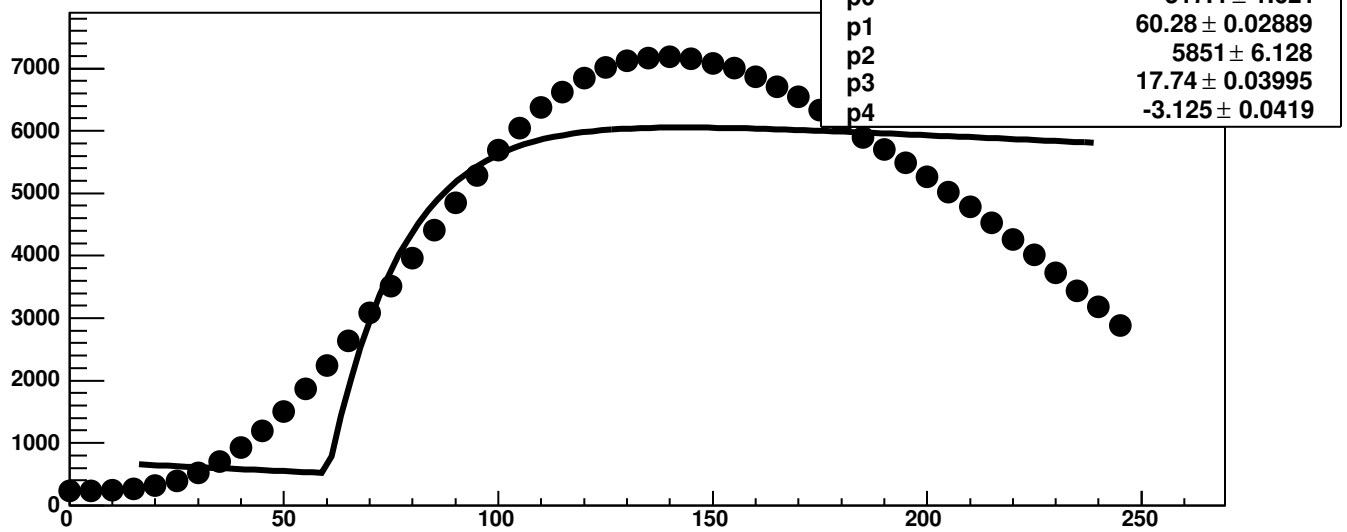



Chip 4, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.366e+05 / 41
p0 18.23 ± 1.186
p1 46.84 ± 0.01657
p2 7739 ± 9.664
p3 41.05 ± 0.0589
p4 -20.24 ± 0.05026
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

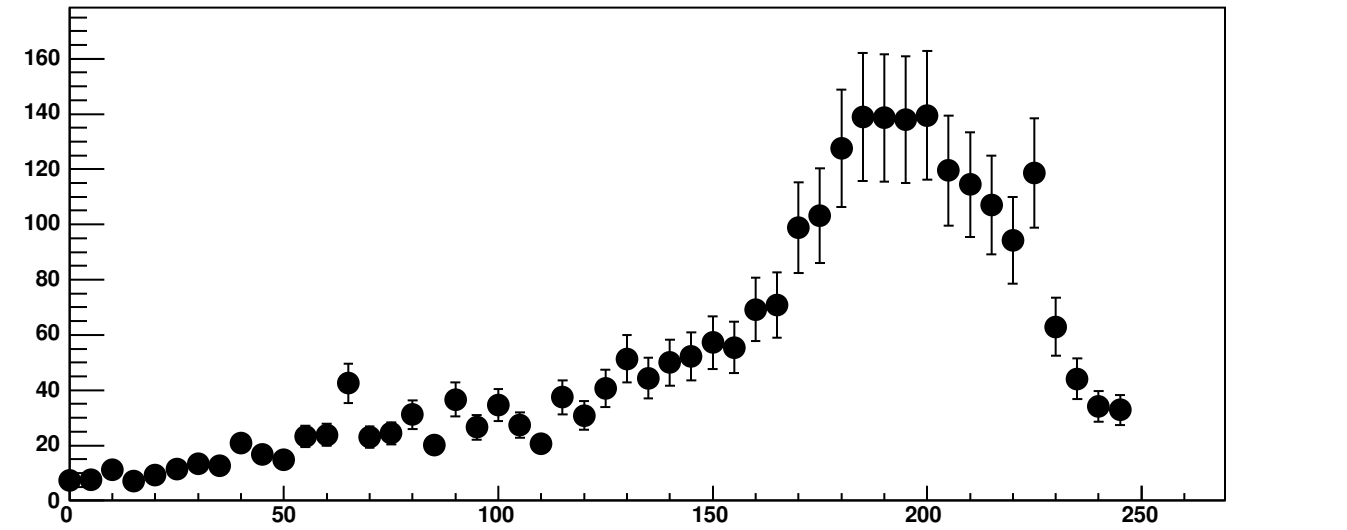

Chip 4, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250


Chip 4, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
0
50
100
150
200
250

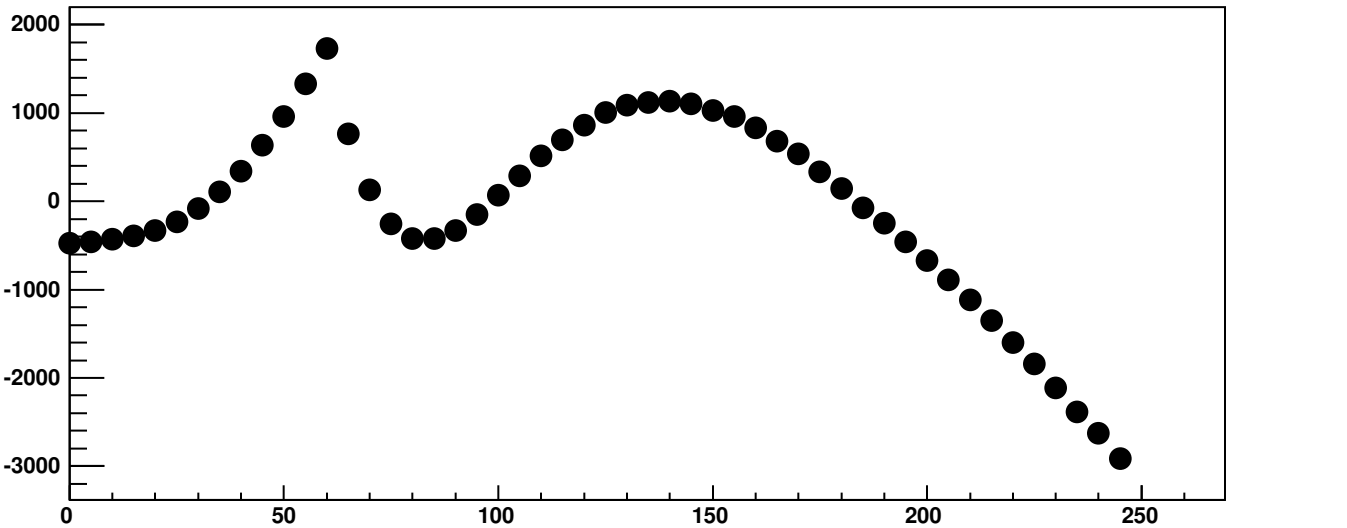
Chip 4, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold



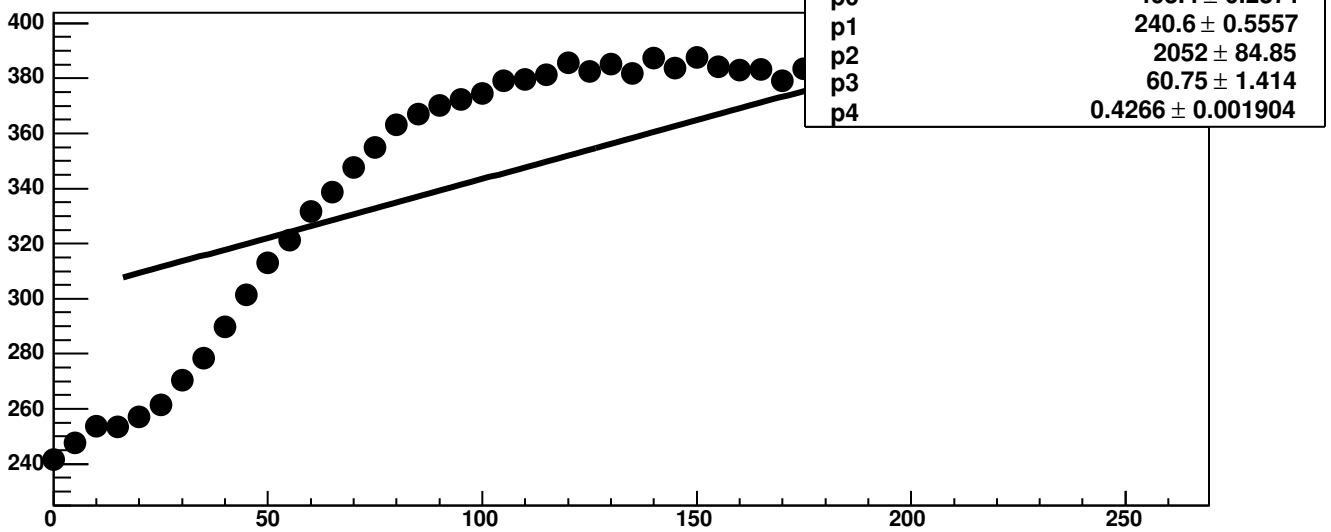
Chip 4, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold



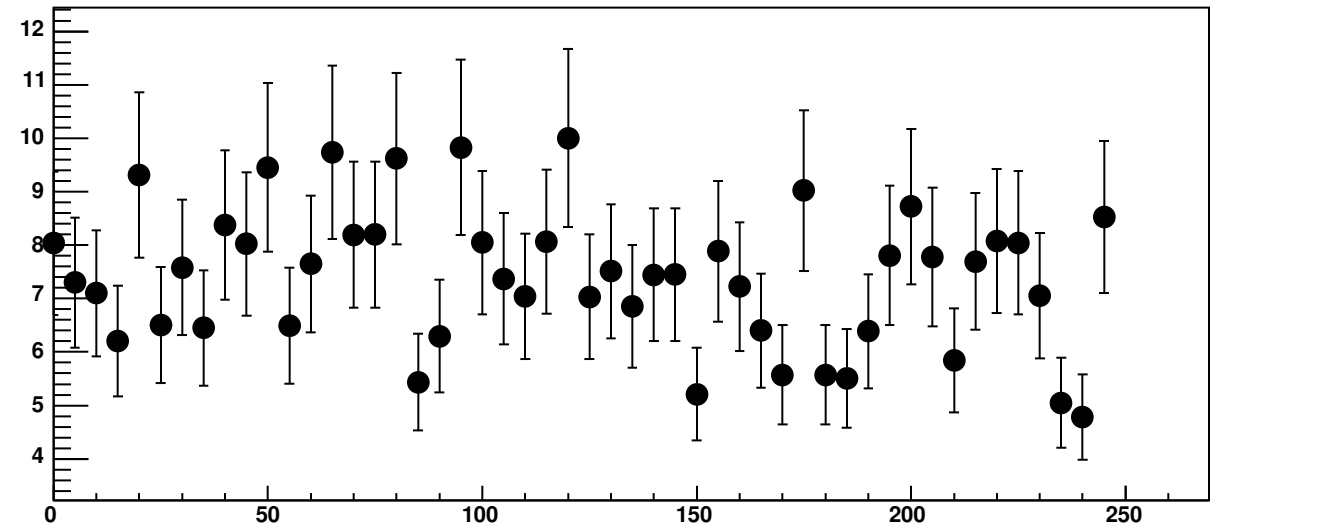
Chip 4, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold



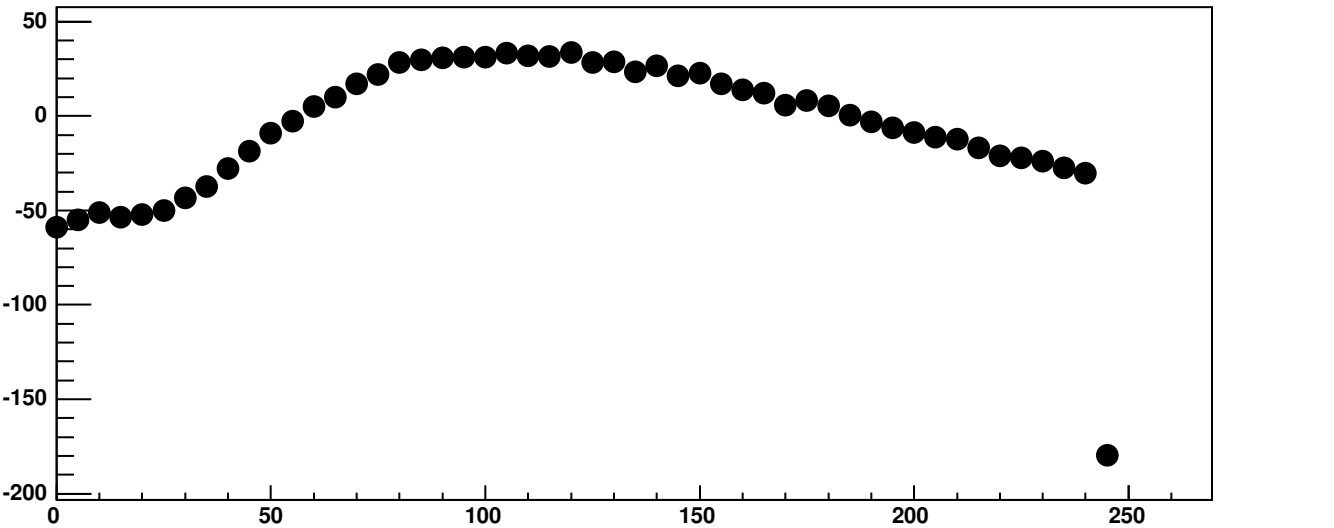
Chip 4, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold



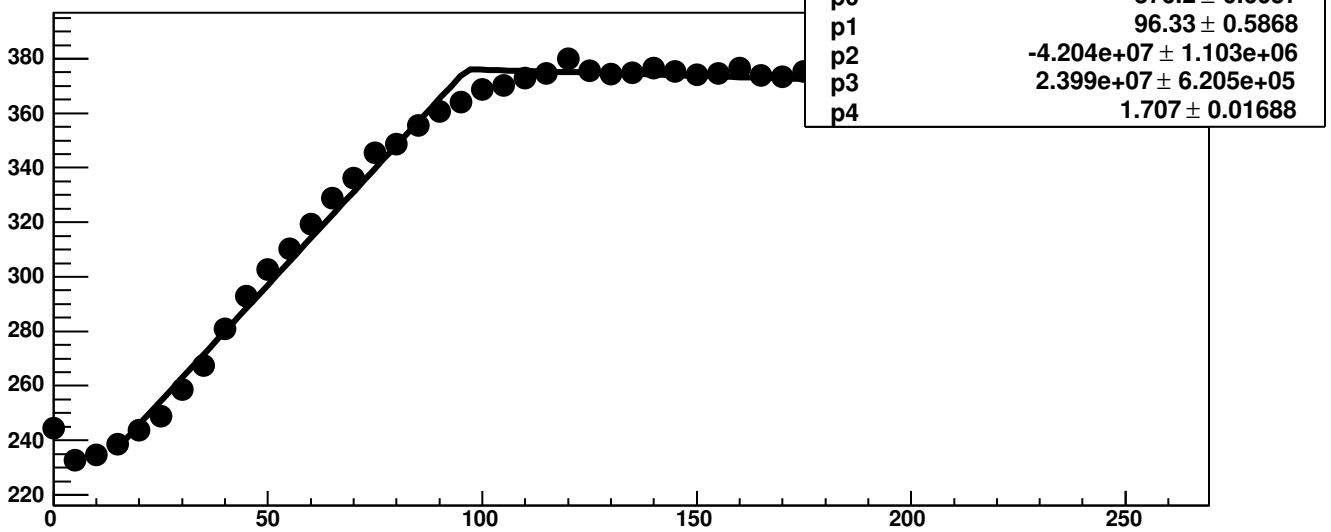
Chip 4, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



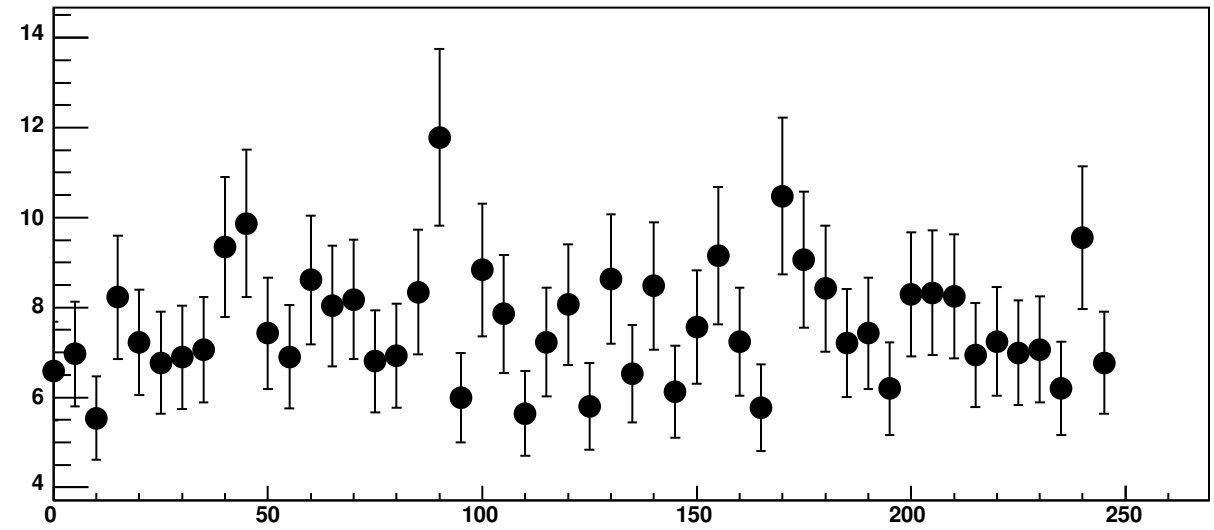
Chip 4, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold



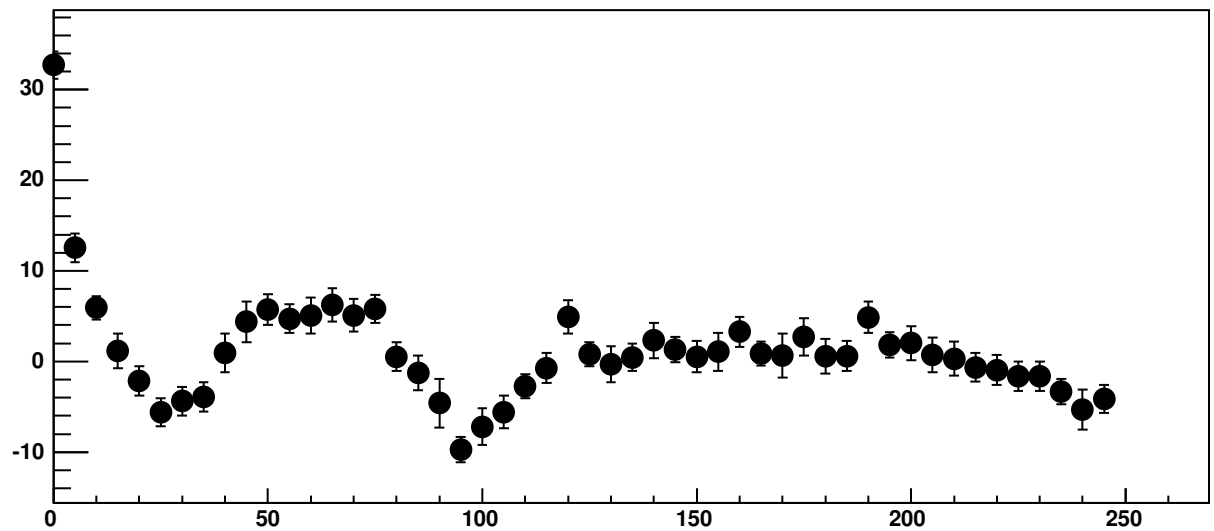
Chip 4, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold



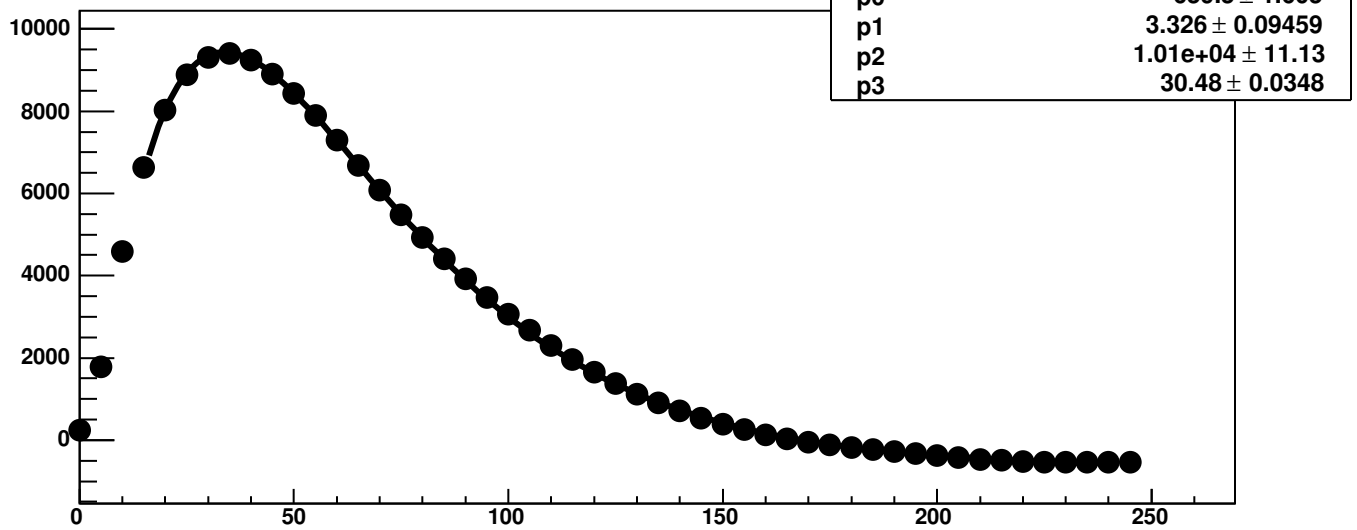
Chip 4, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



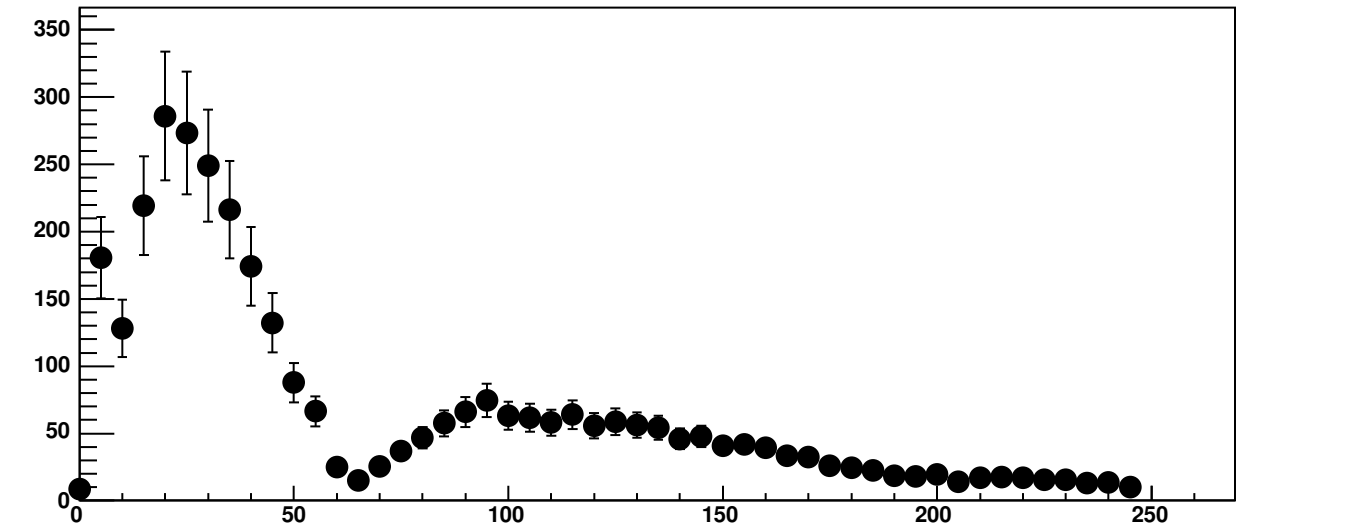
Chip 4, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold



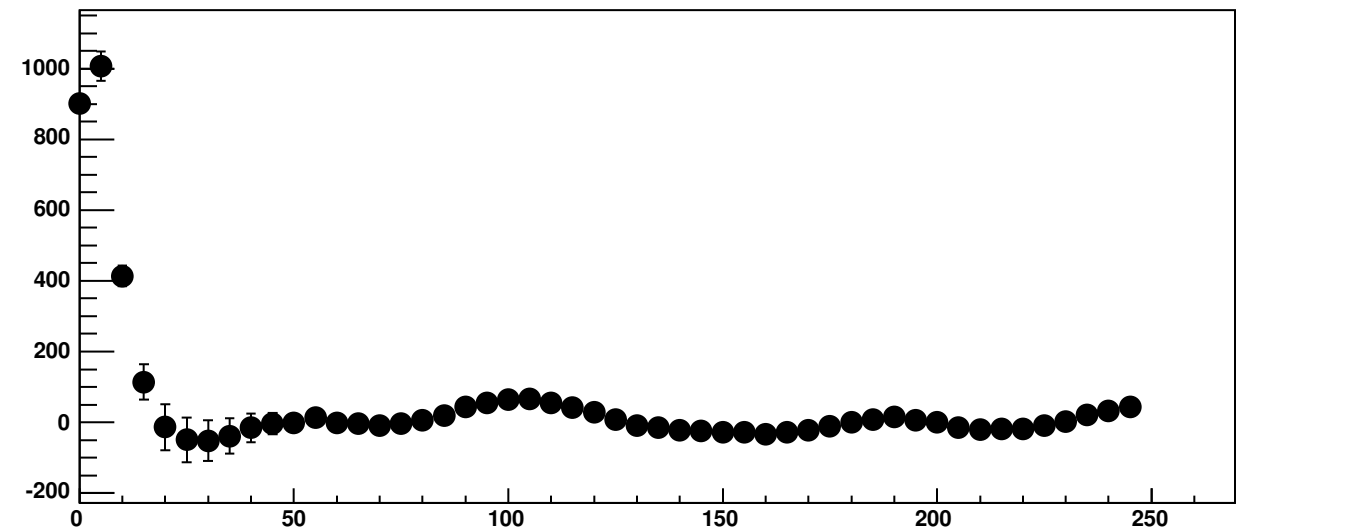
Chip 4, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold



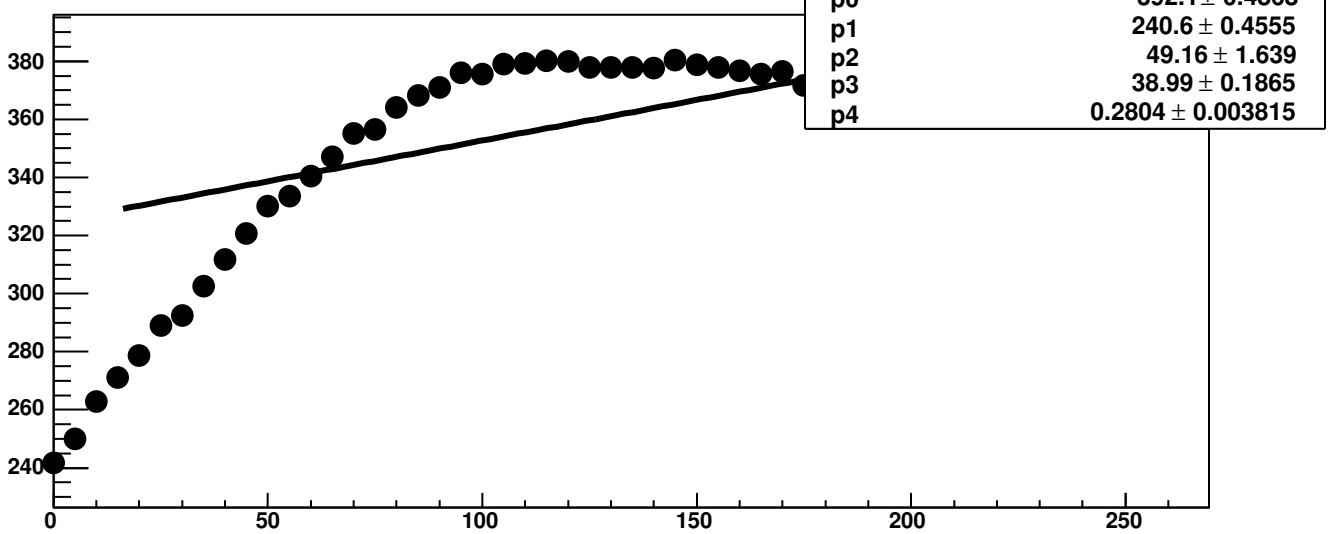
Chip 4, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold



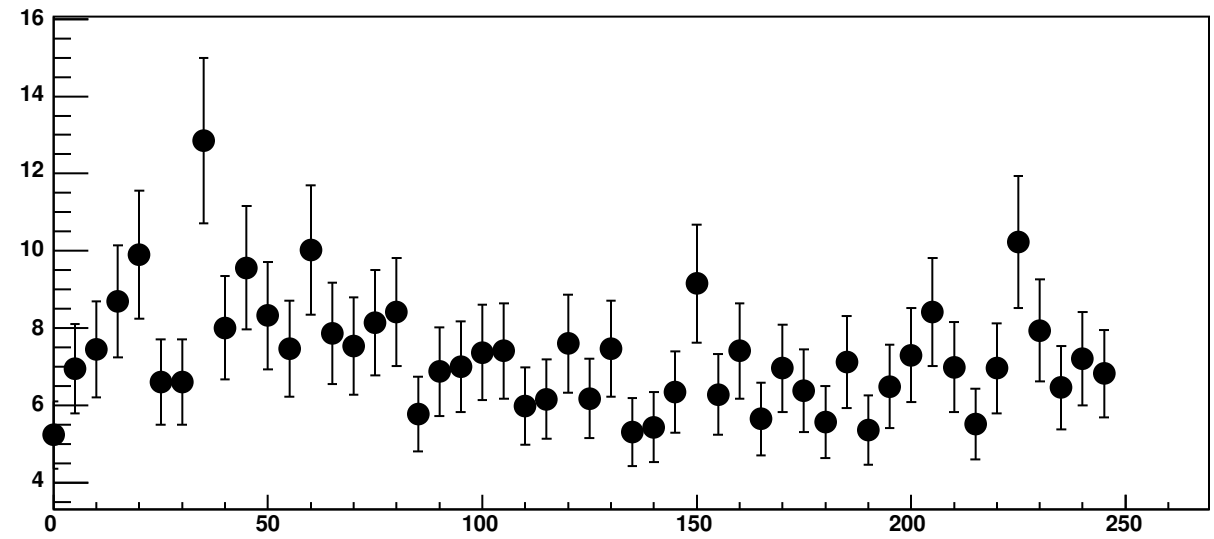
Chip 4, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold



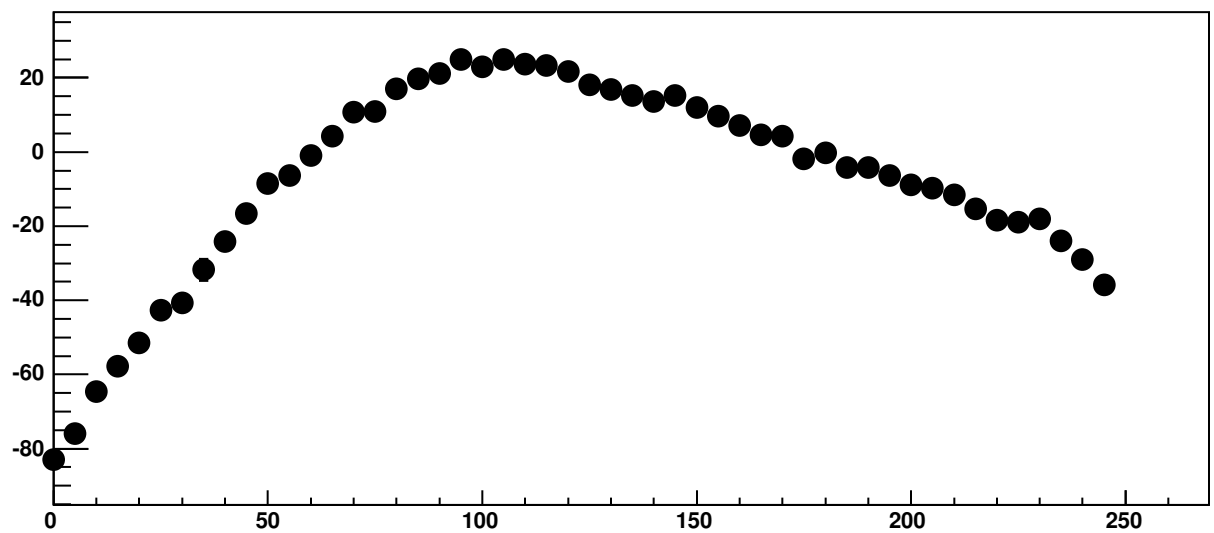
Chip 4, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold



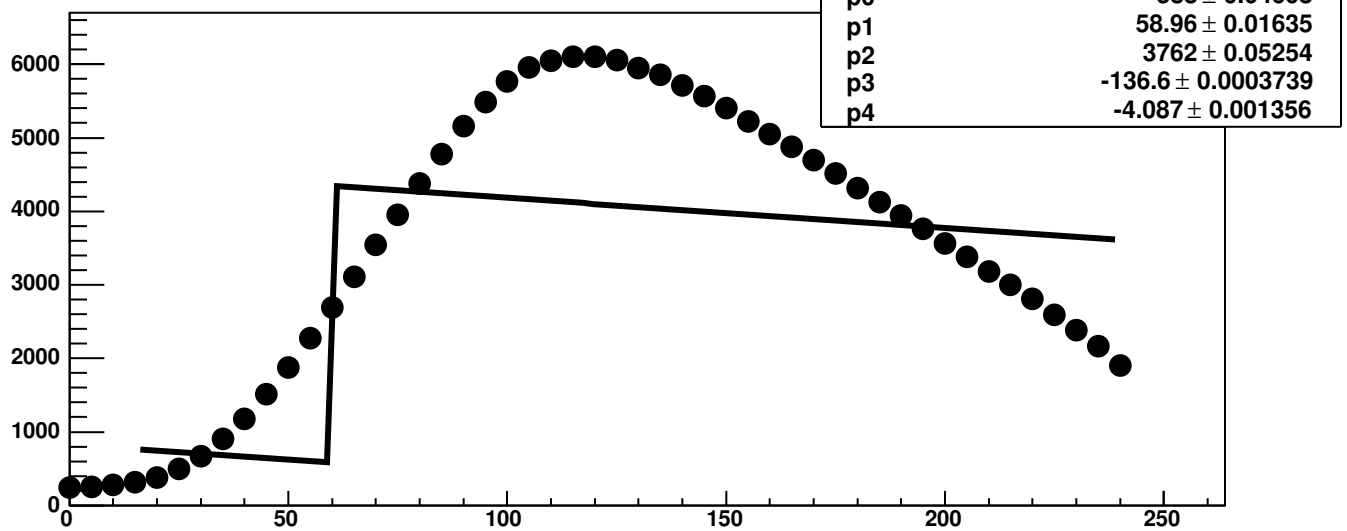
Chip 4, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



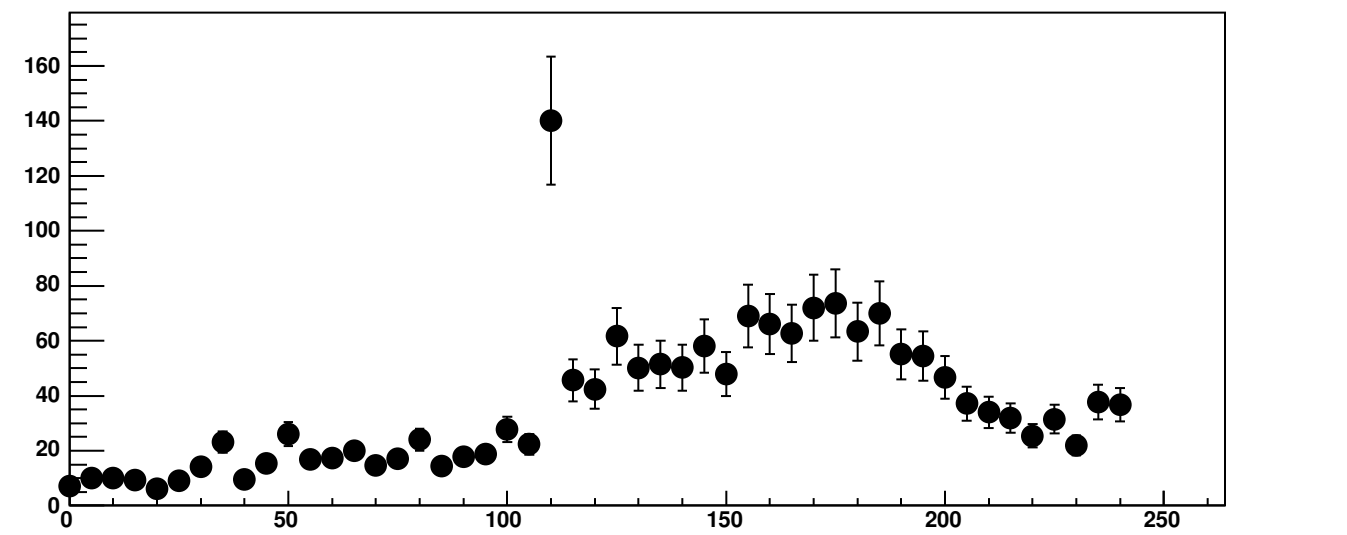
Chip 4, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold



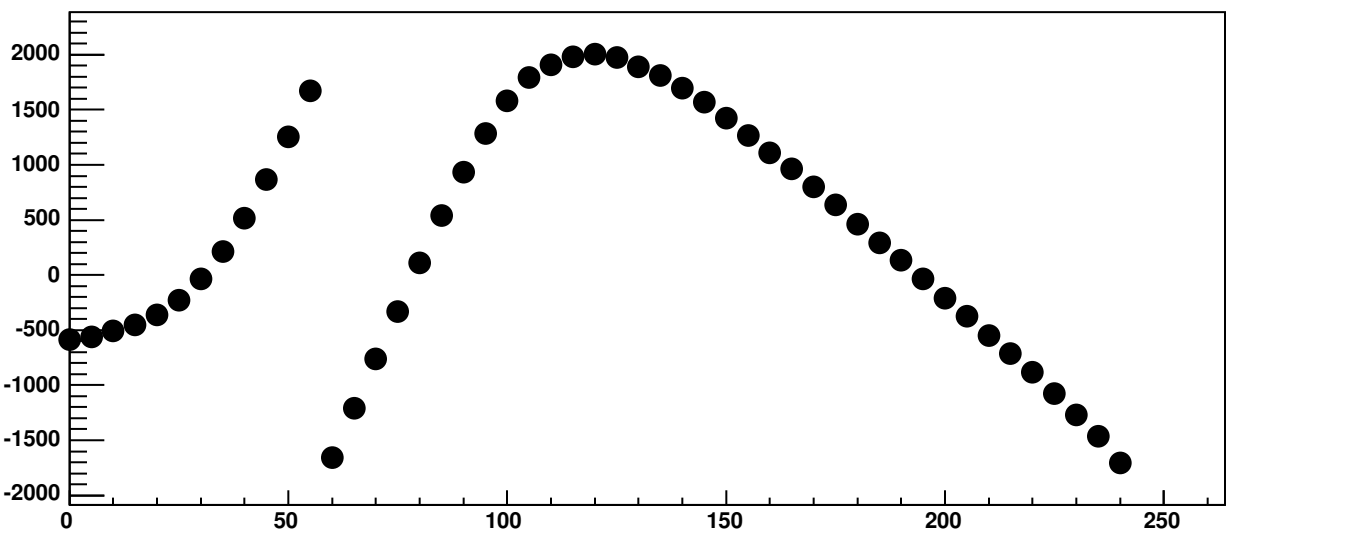
Chip 4, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

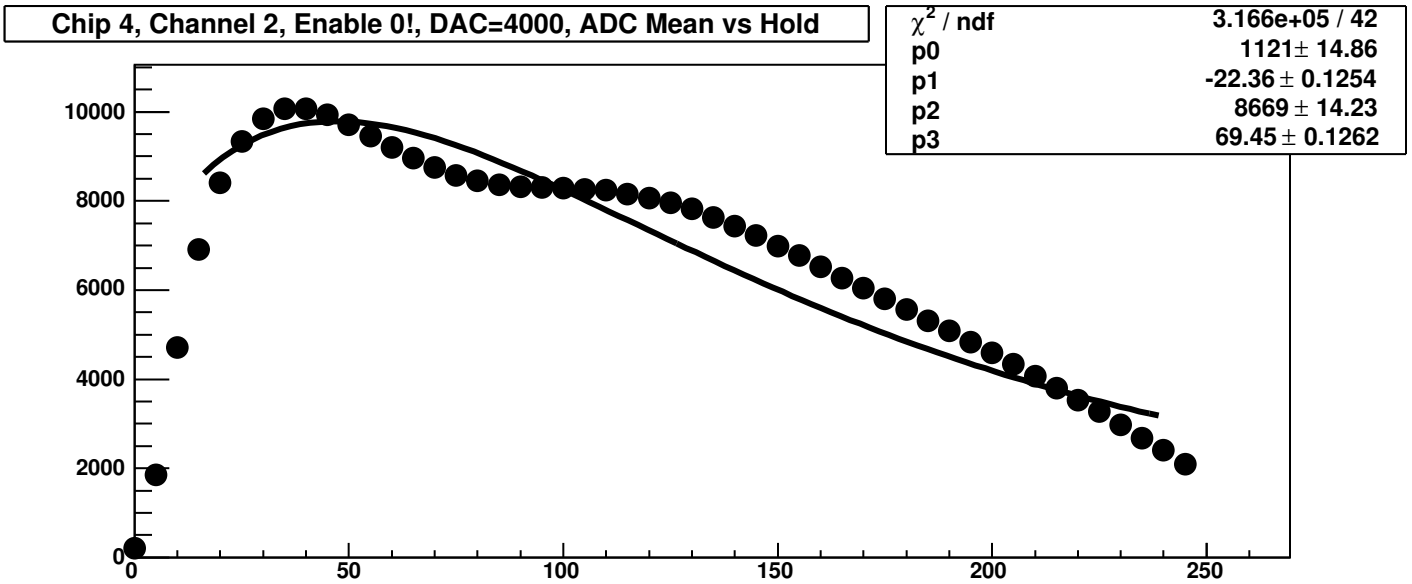


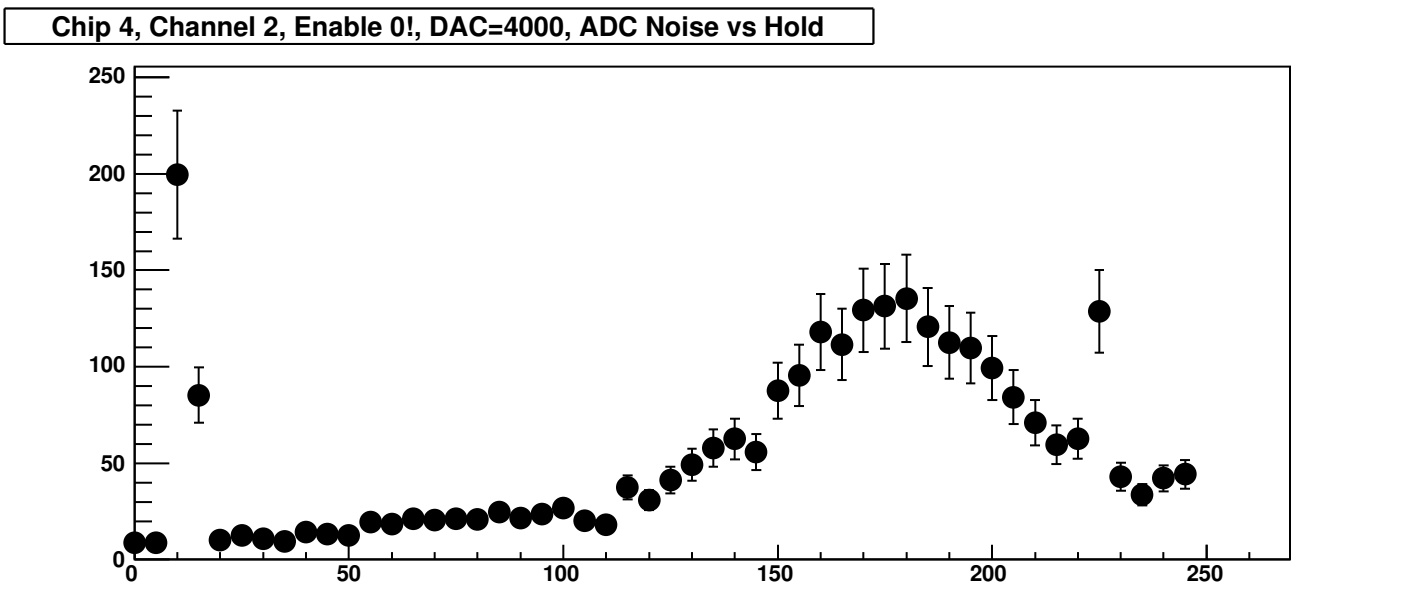
Chip 4, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold

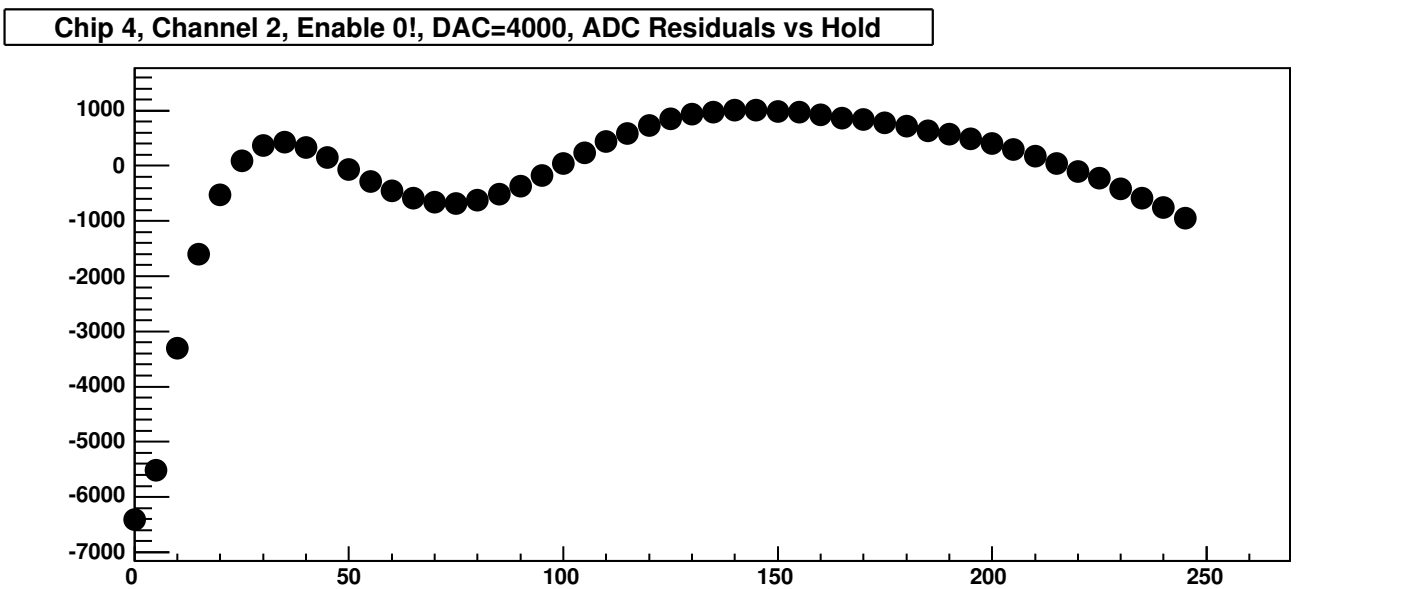


Chip 4, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold

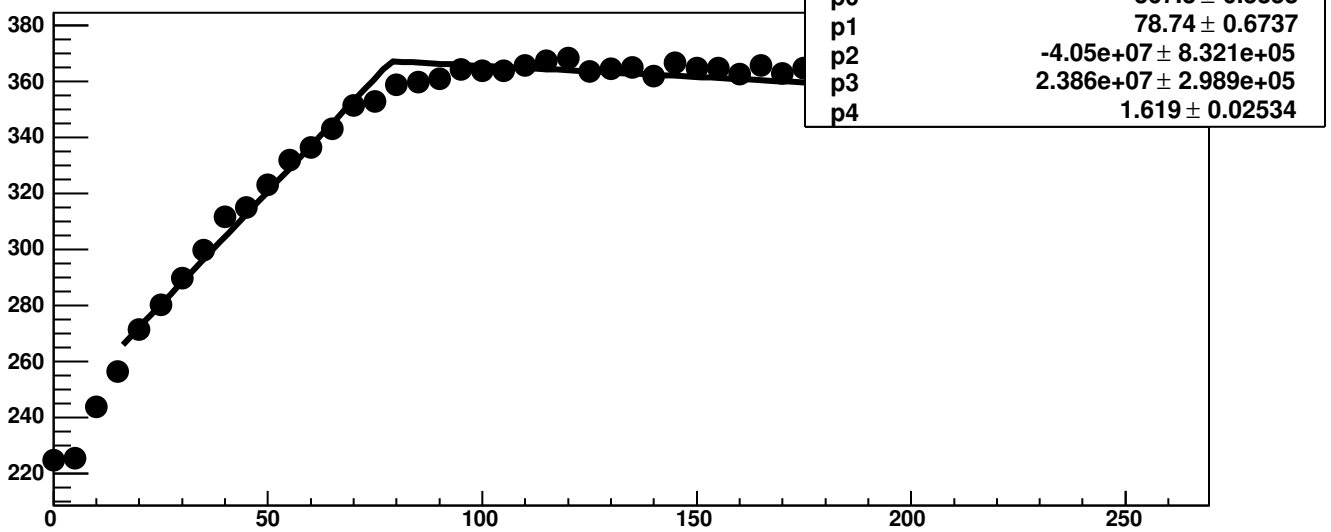



Chip 4, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.166e+05 / 42
p0 1121 ± 14.86
p1 -22.36 ± 0.1254
p2 8669 ± 14.23
p3 69.45 ± 0.1262
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

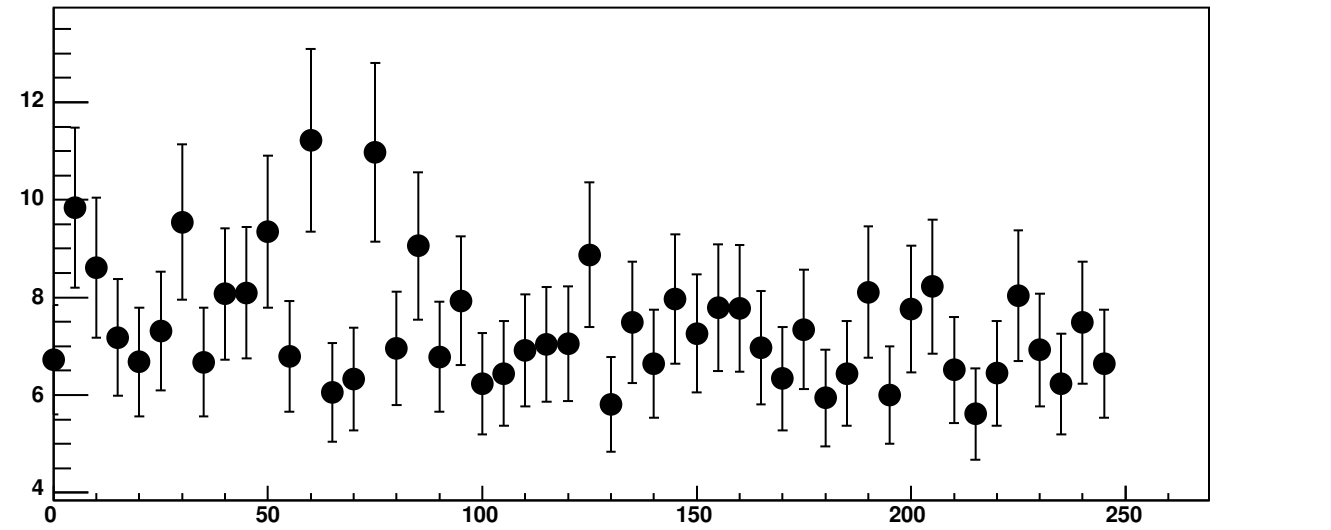

Chip 4, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250


Chip 4, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
-7000
0
50
100
150
200
250

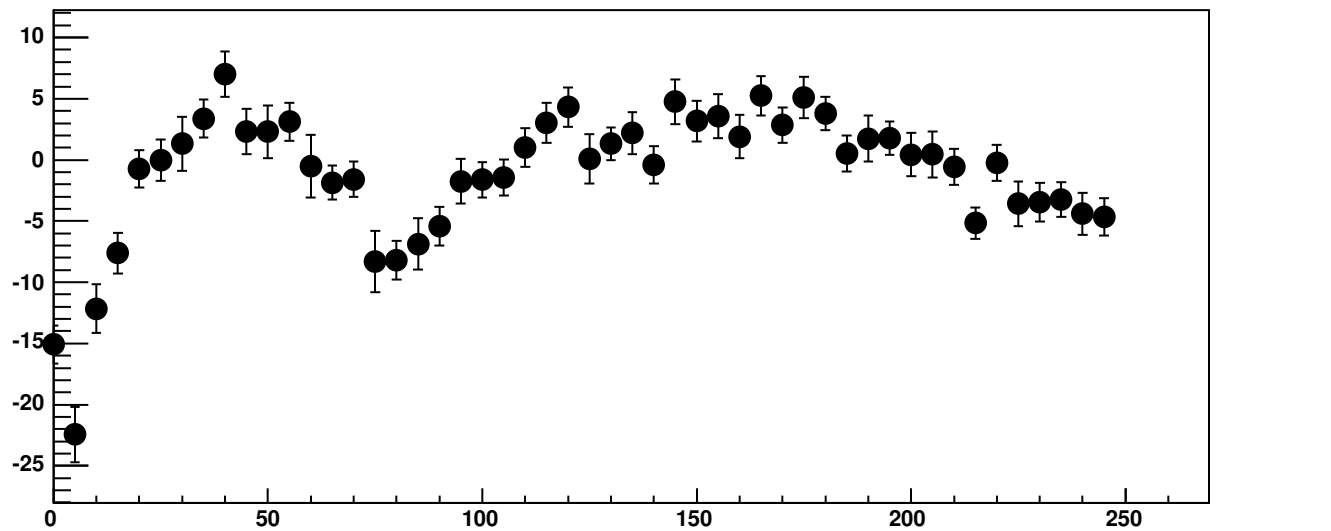
Chip 4, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold



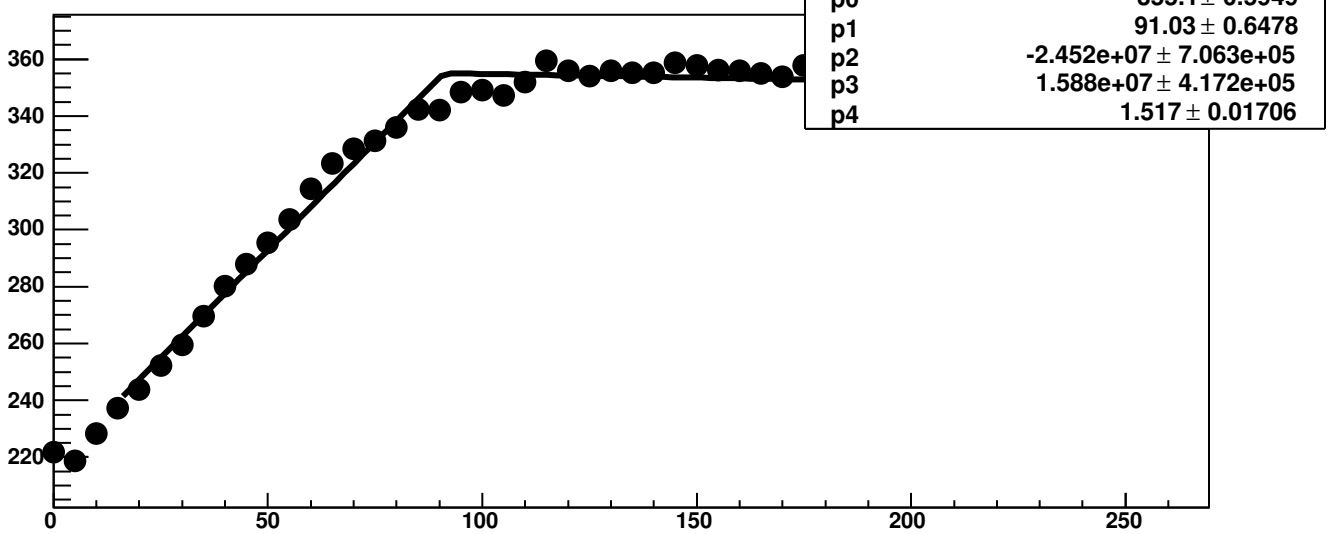
Chip 4, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



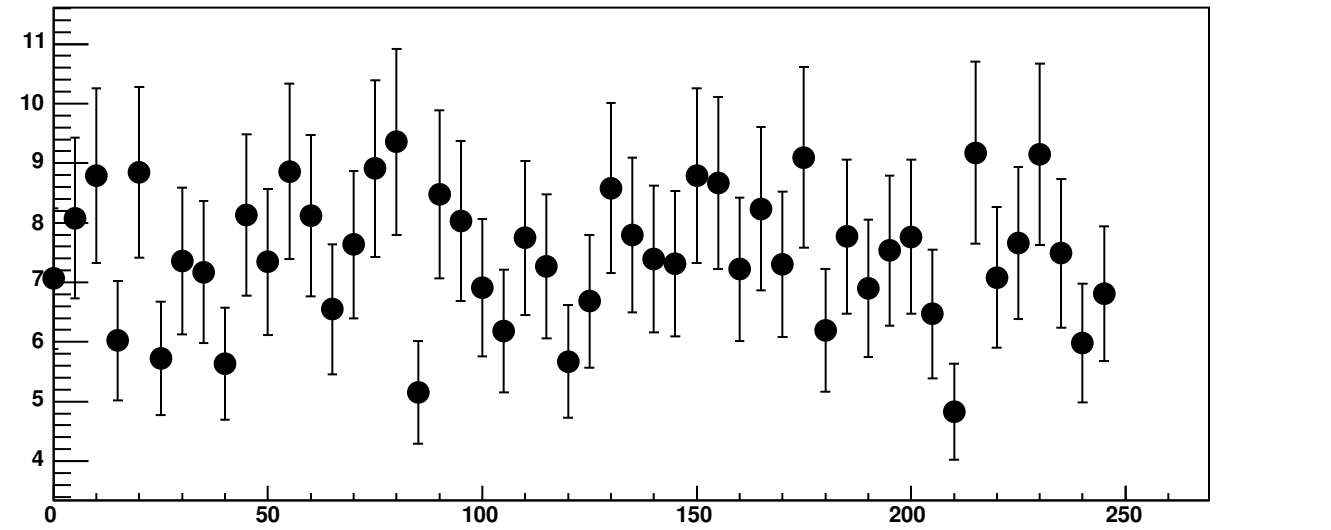
Chip 4, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold



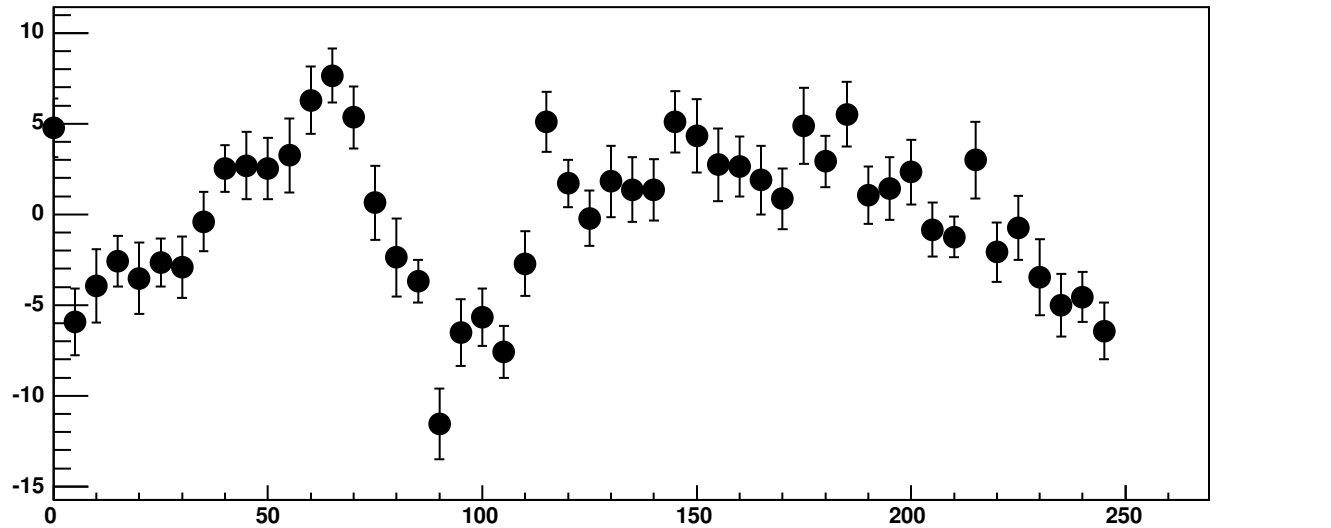
Chip 4, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold



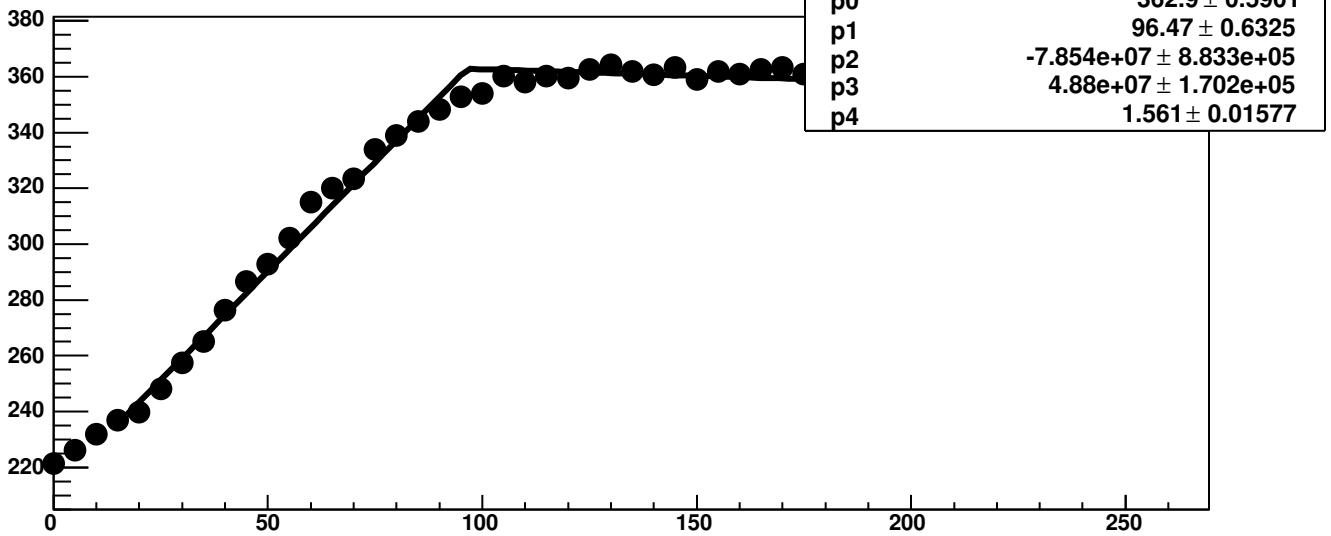
Chip 4, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



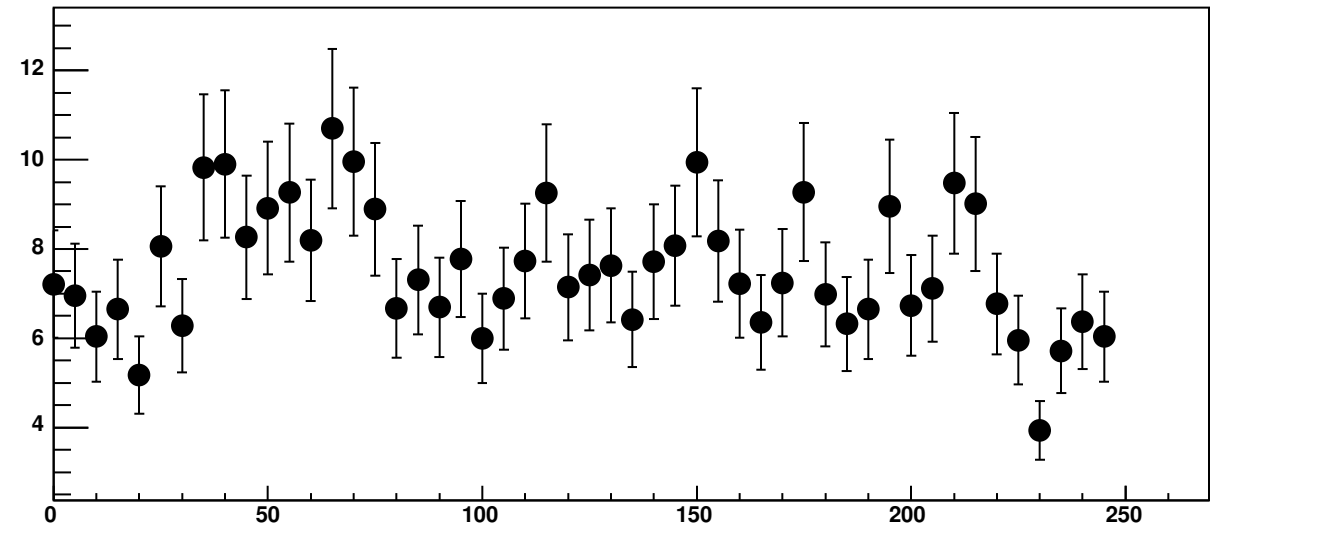
Chip 4, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold



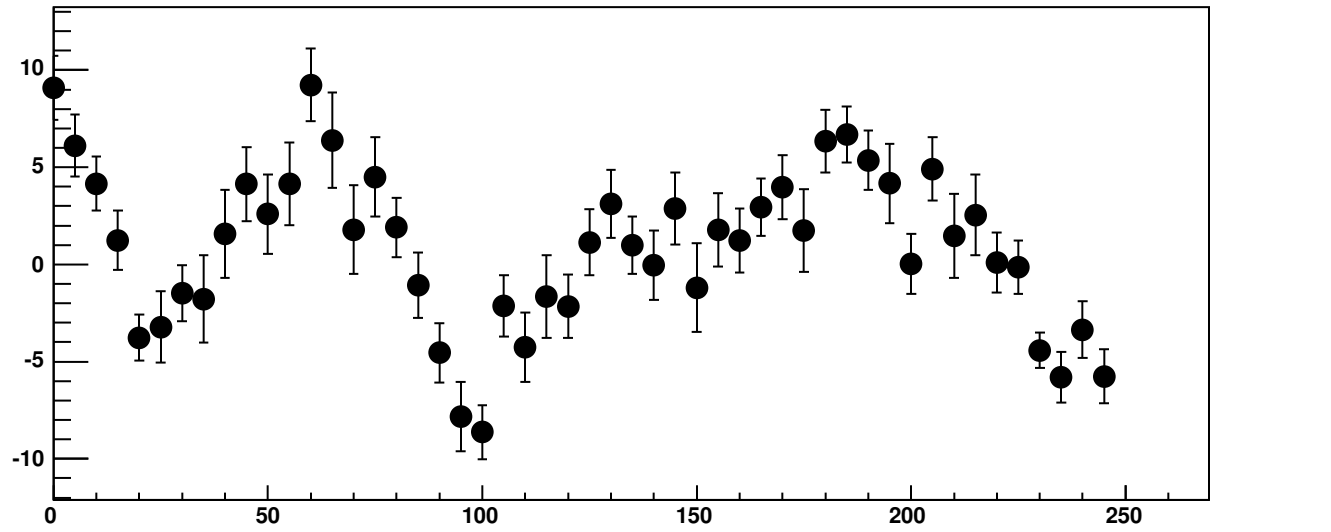
Chip 4, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold

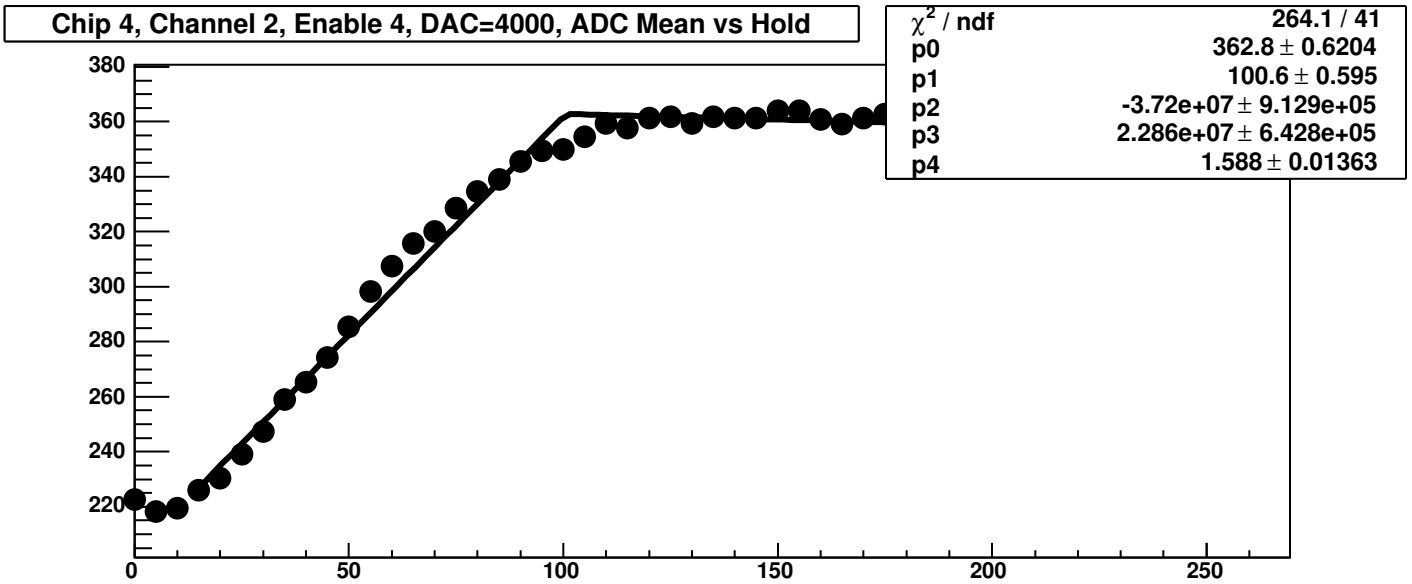


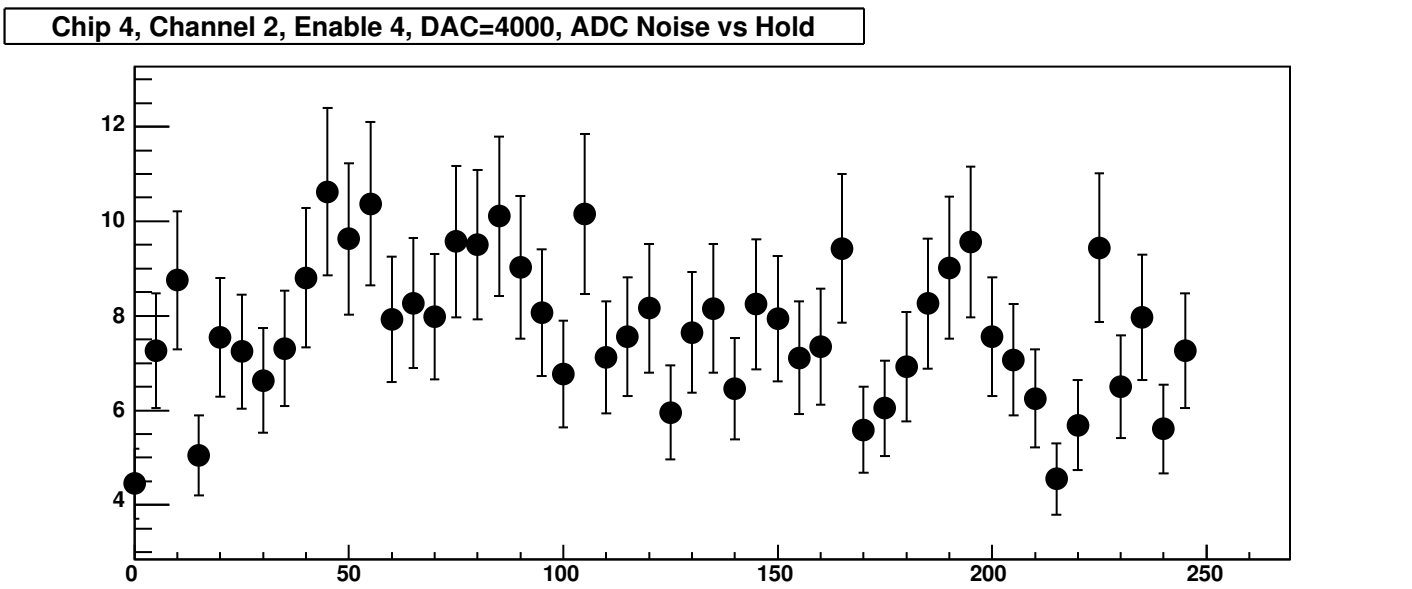
Chip 4, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold

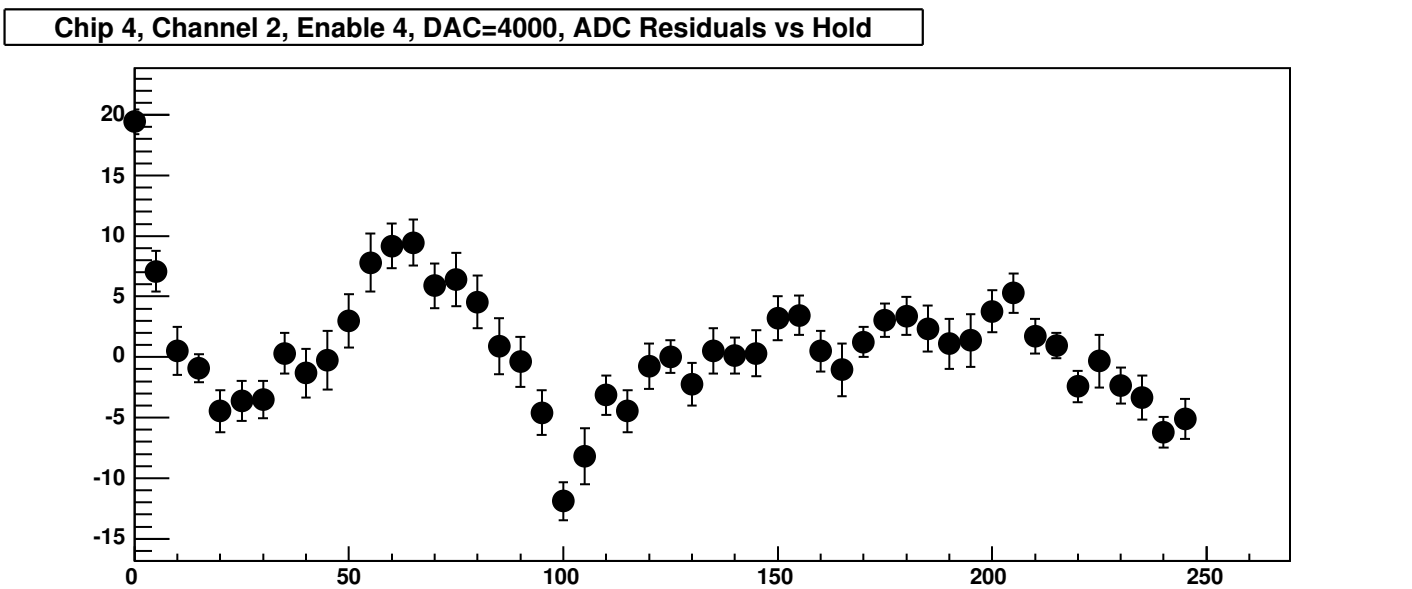


Chip 4, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold

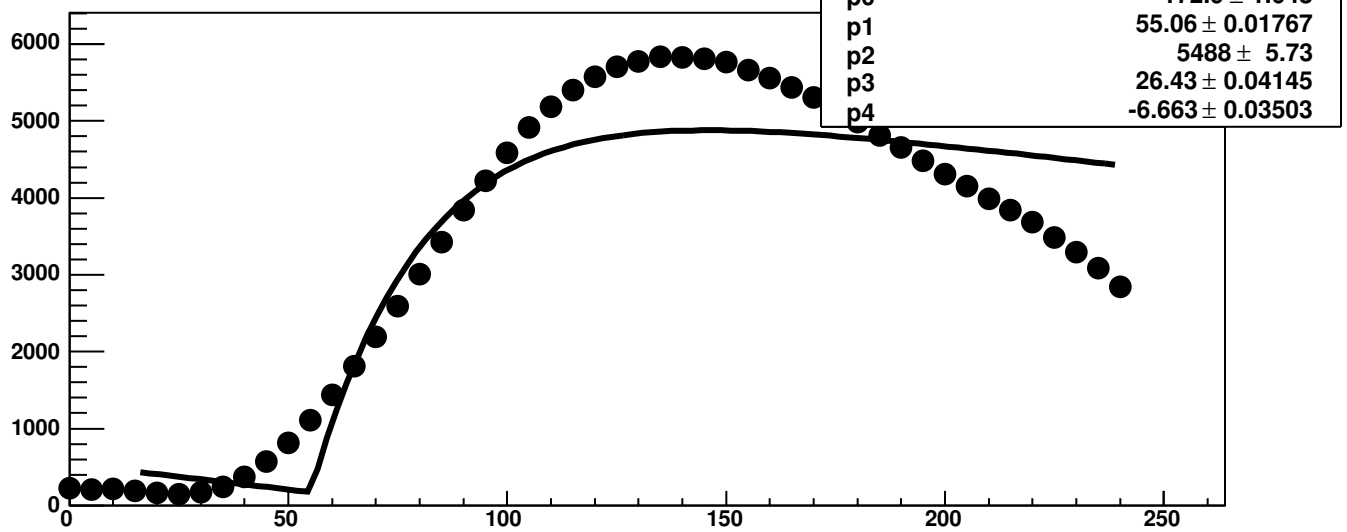



Chip 4, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 264.1 / 41
p0 362.8 ± 0.6204
p1 100.6 ± 0.595
p2 -3.72e+07 ± 9.129e+05
p3 2.286e+07 ± 6.428e+05
p4 1.588 ± 0.01363
380
360
340
320
300
280
260
240
220
0
50
100
150
200
250

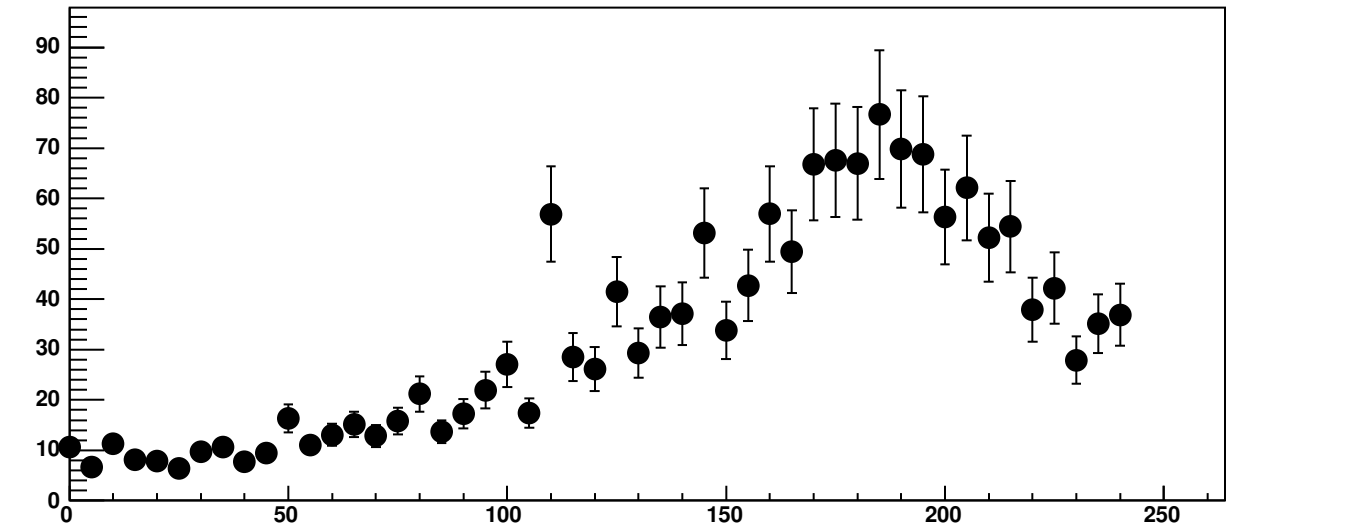

Chip 4, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 4, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

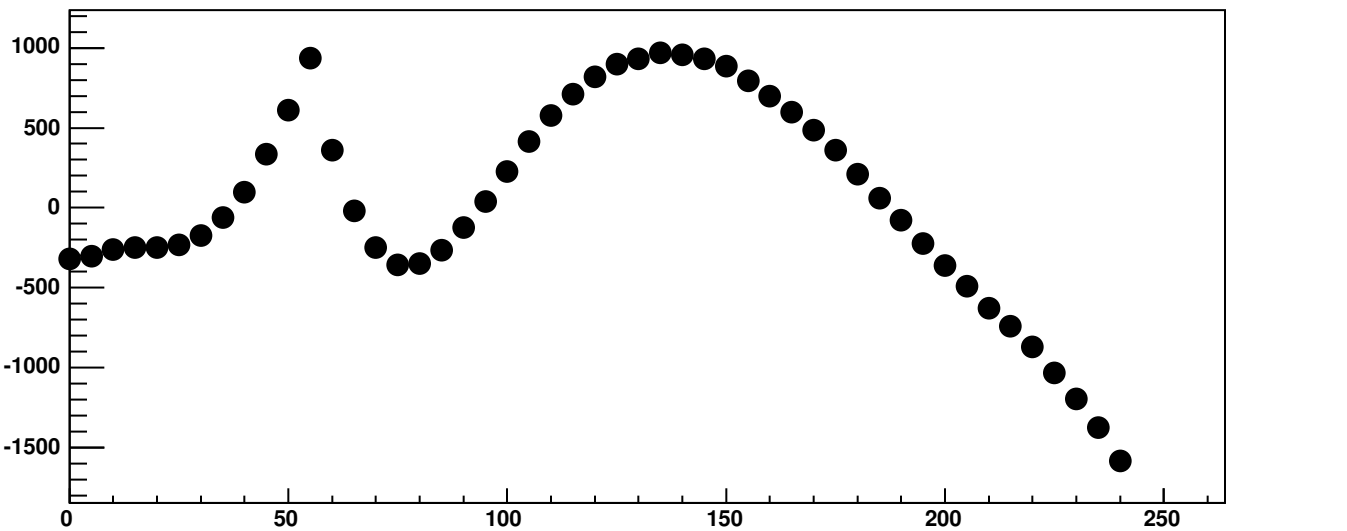
Chip 4, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

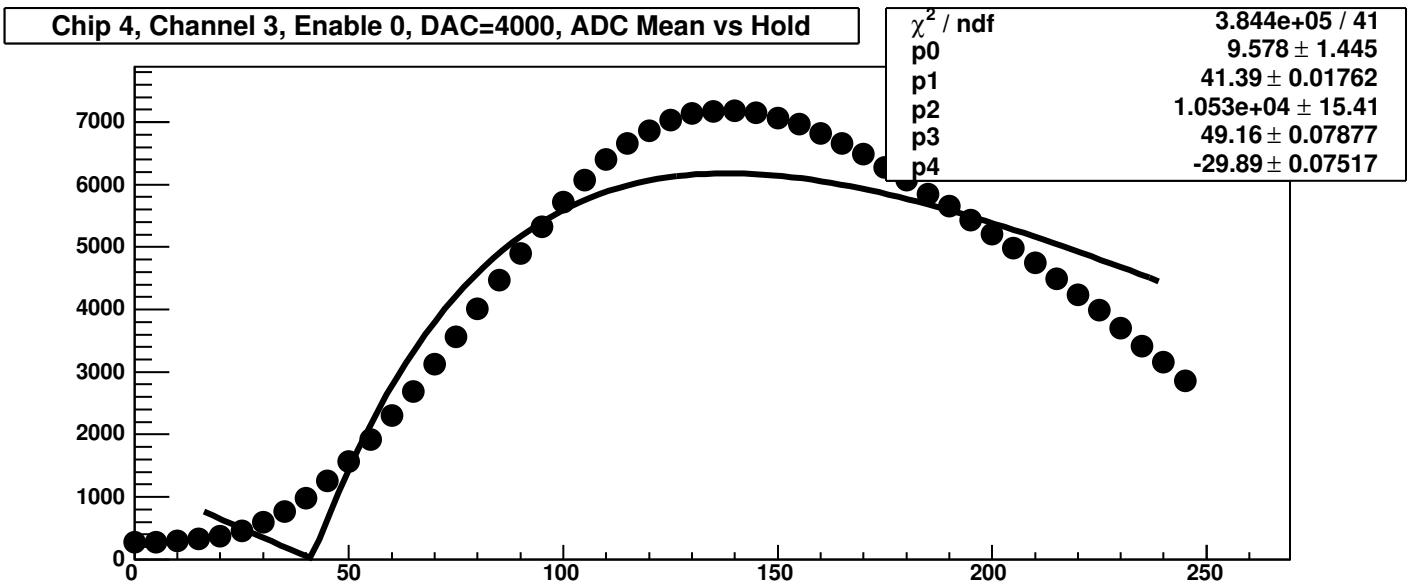


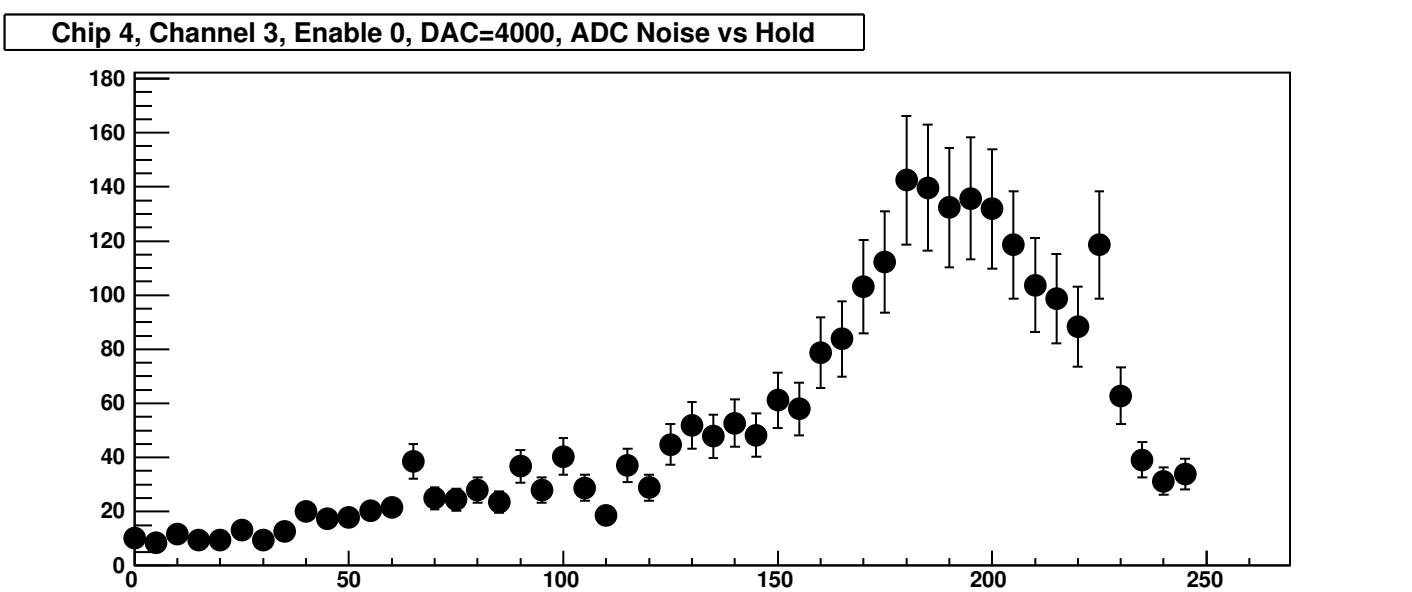
Chip 4, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold

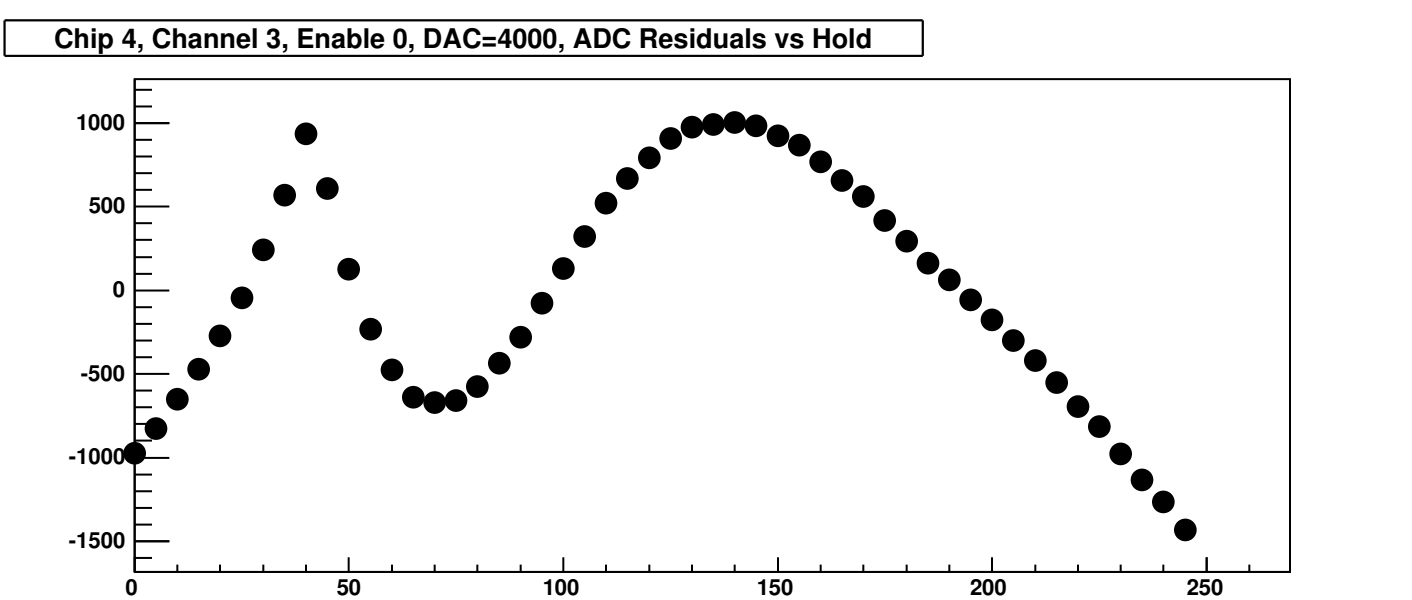


Chip 4, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold

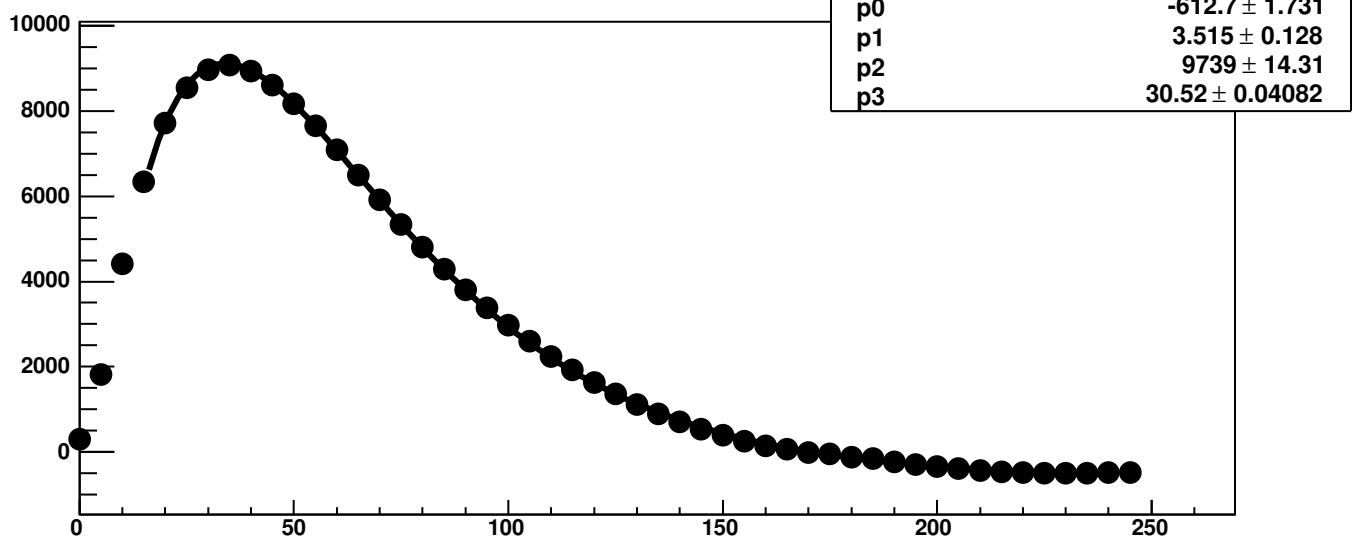



Chip 4, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.844e+05 / 41
p0 9.578 ± 1.445
p1 41.39 ± 0.01762
p2 1.053e+04 ± 15.41
p3 49.16 ± 0.07877
p4 -29.89 ± 0.07517
0 1000 2000 3000 4000 5000 6000 7000
0 50 100 150 200 250

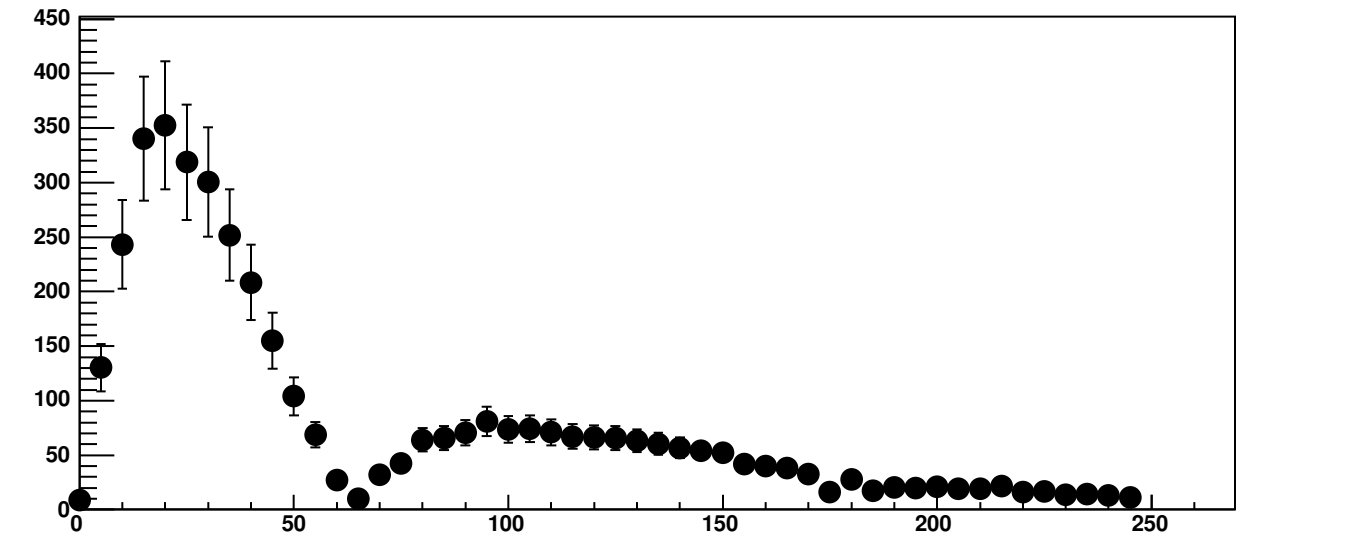

Chip 4, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold
0 20 40 60 80 100 120 140 160 180
0 50 100 150 200 250


Chip 4, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold
-1500 -1000 -500 0 500 1000
0 50 100 150 200 250

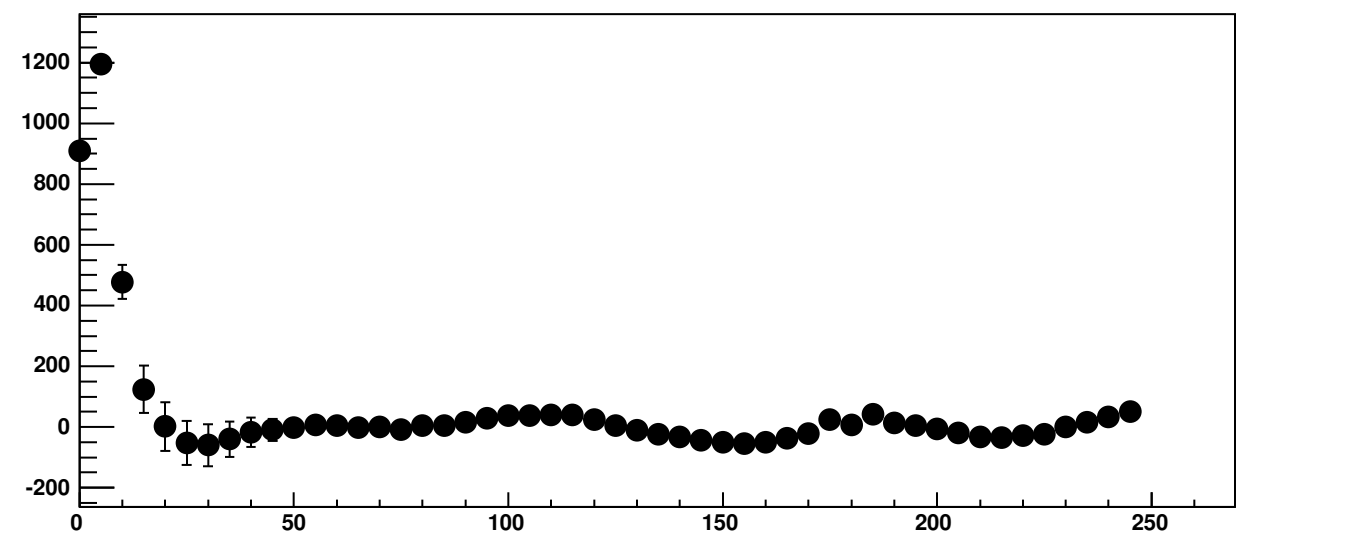
Chip 4, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold

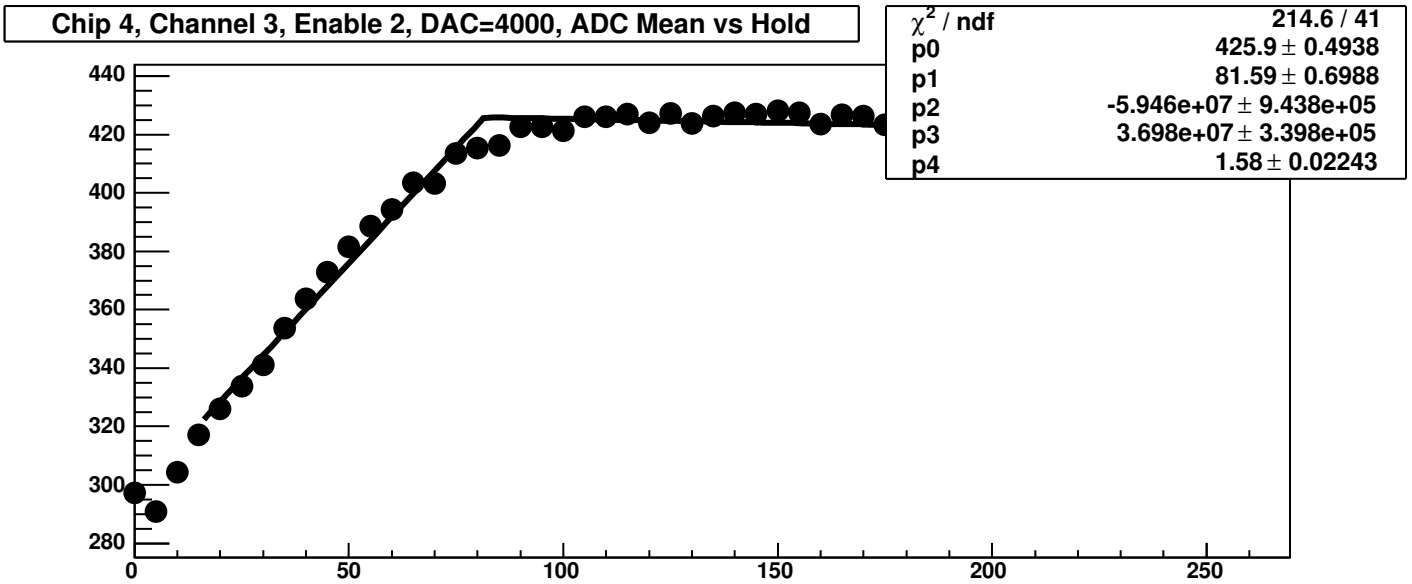


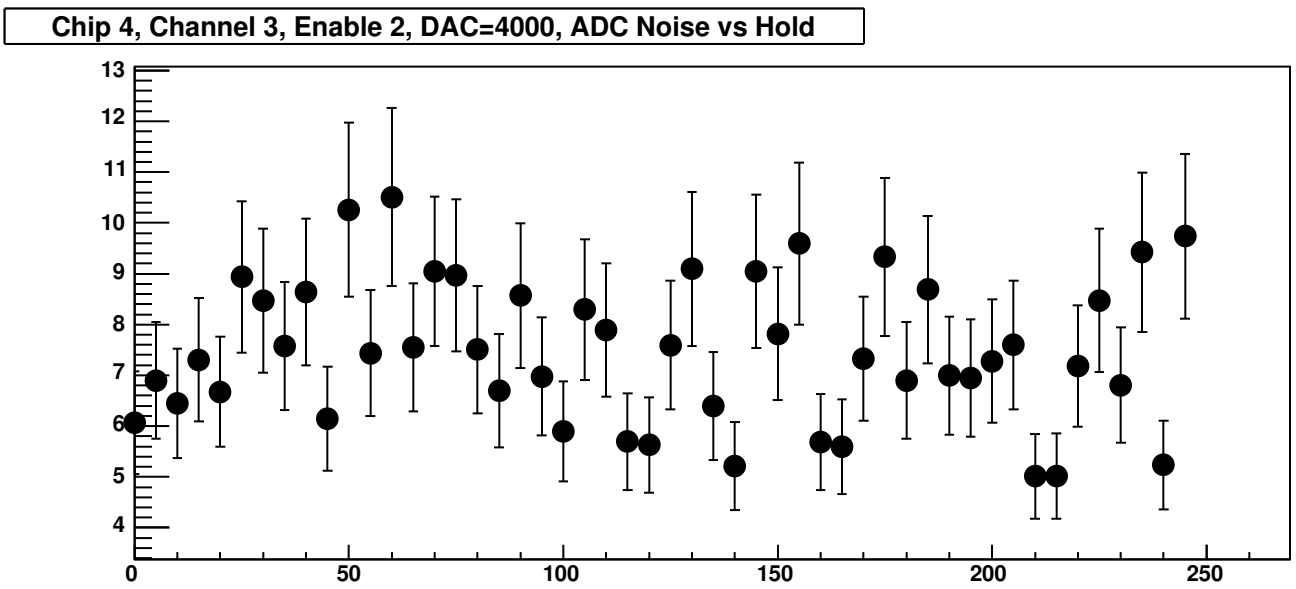
Chip 4, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold

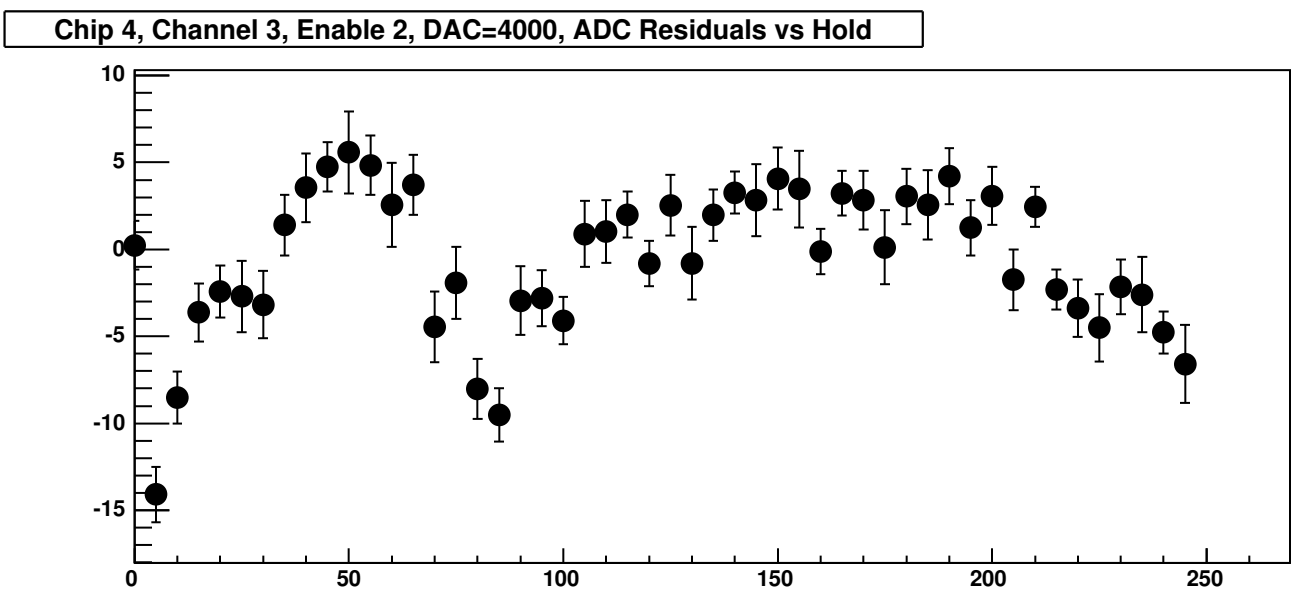


Chip 4, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold

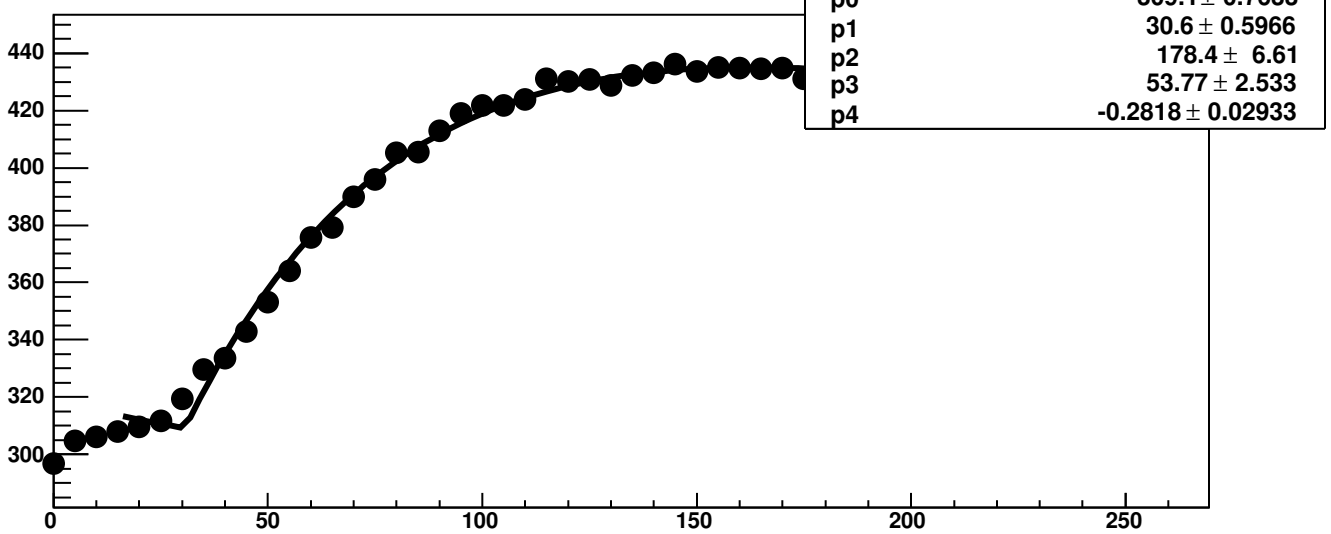



Chip 4, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 214.6 / 41
p0 425.9 ± 0.4938
p1 81.59 ± 0.6988
p2 -5.946e+07 ± 9.438e+05
p3 3.698e+07 ± 3.398e+05
p4 1.58 ± 0.02243
440
420
400
380
360
340
320
300
280
0
50
100
150
200
250

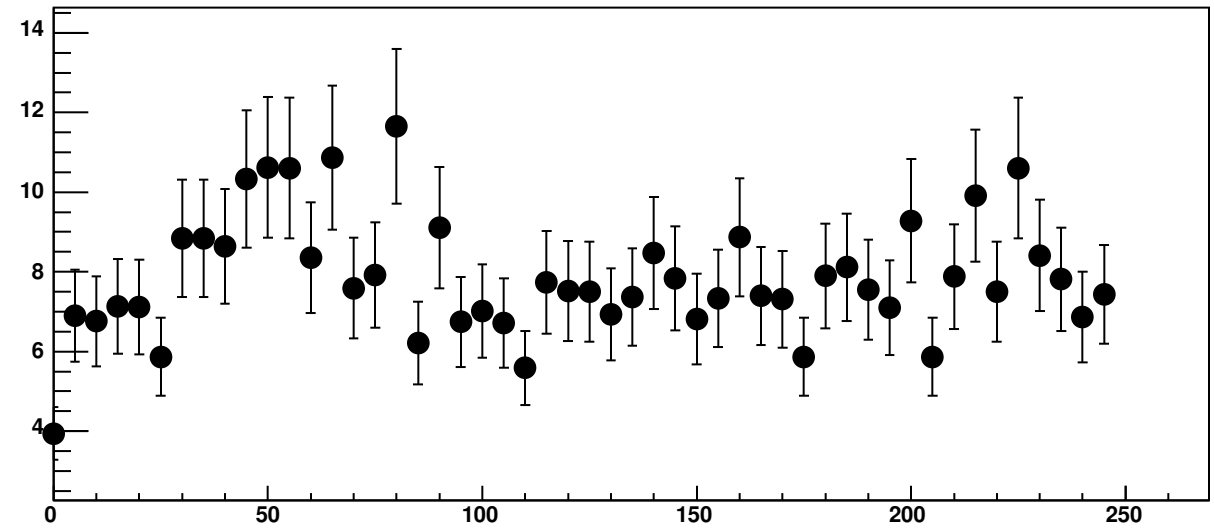

Chip 4, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 4, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
0
50
100
150
200
250

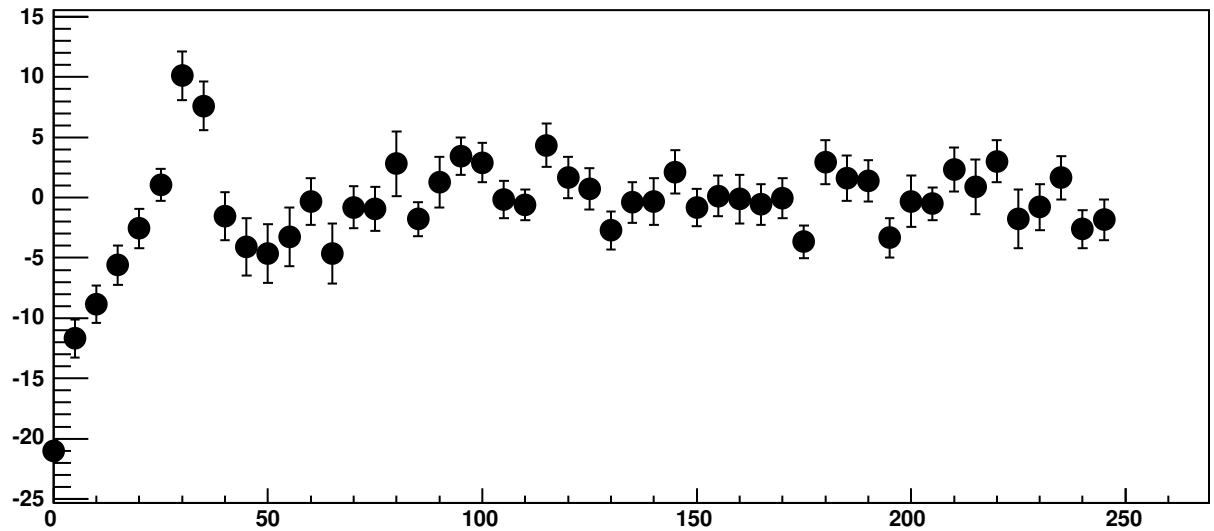
Chip 4, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

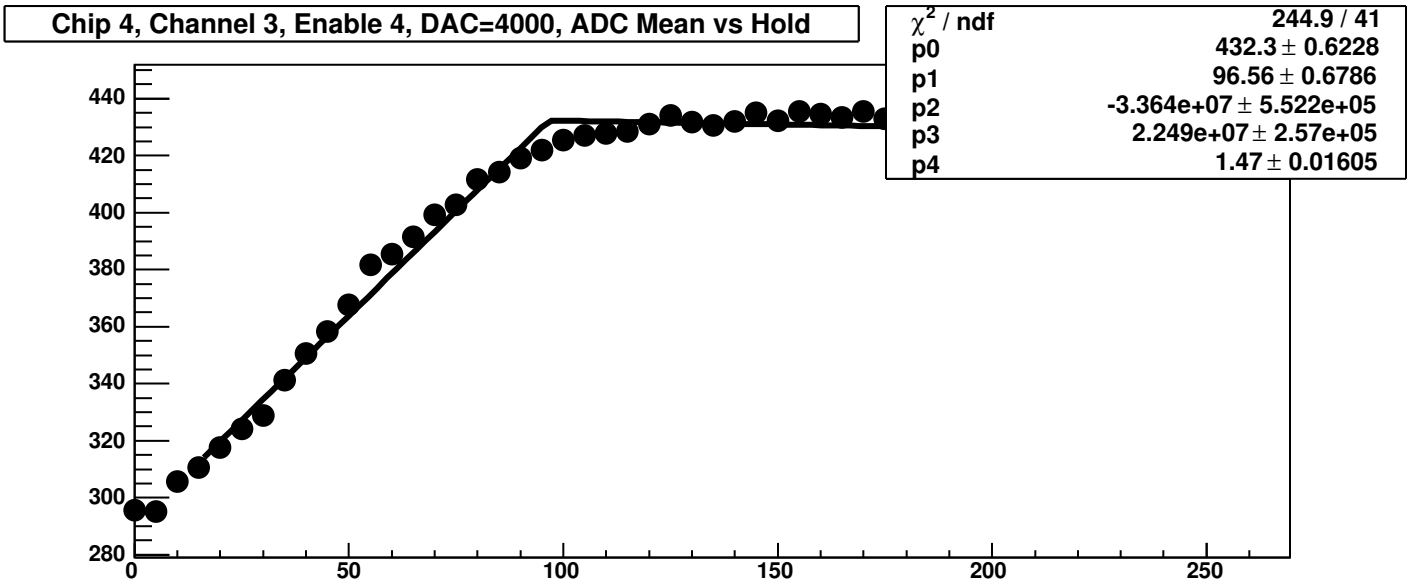


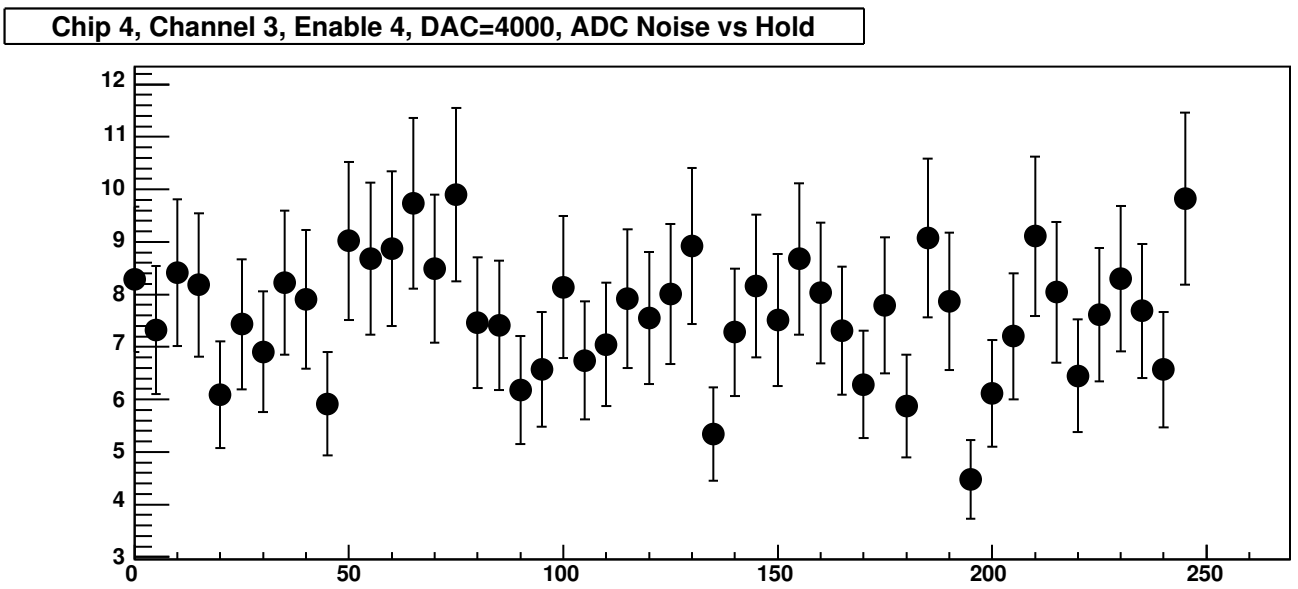
Chip 4, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold

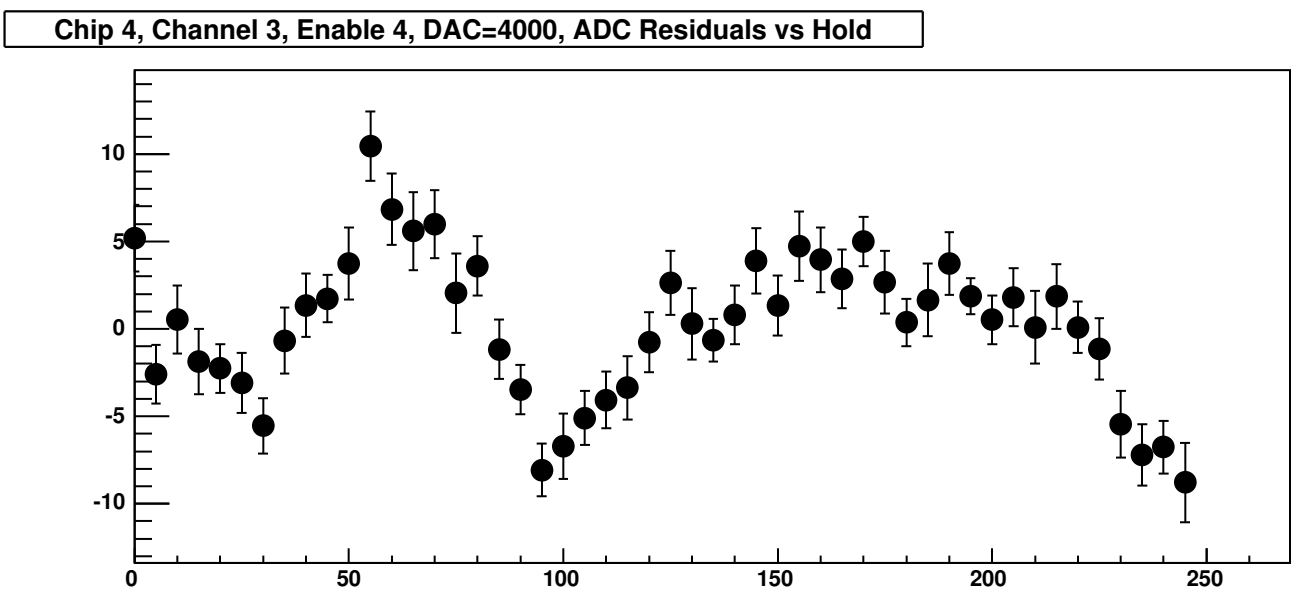


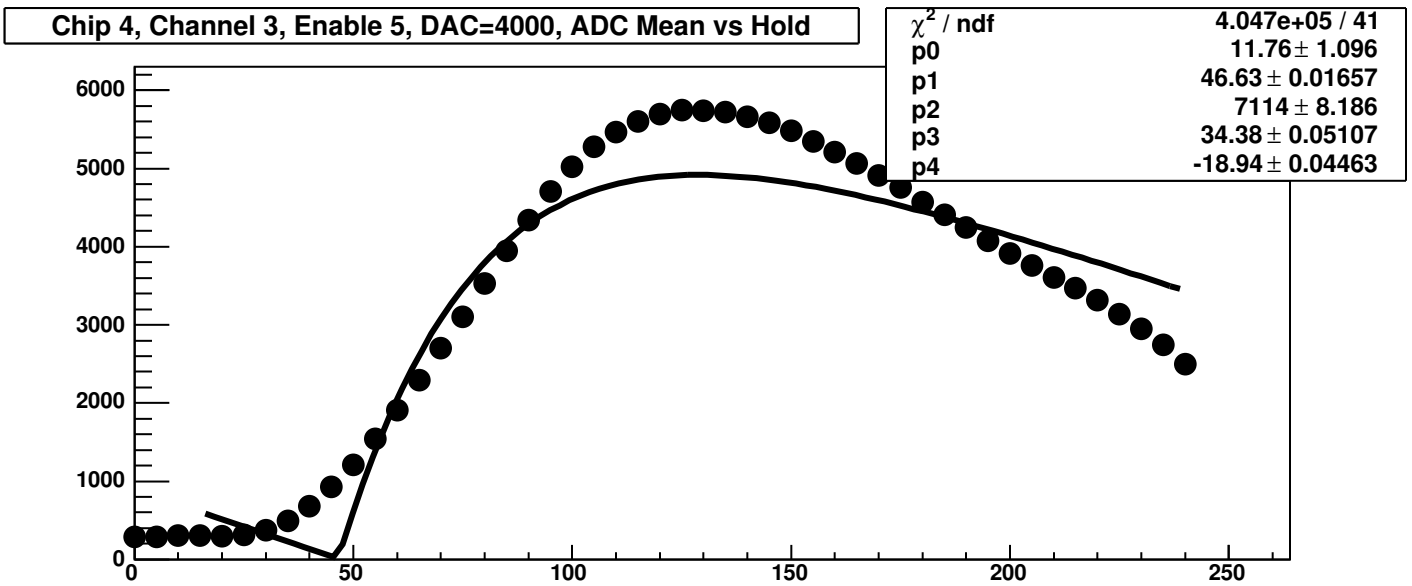
Chip 4, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

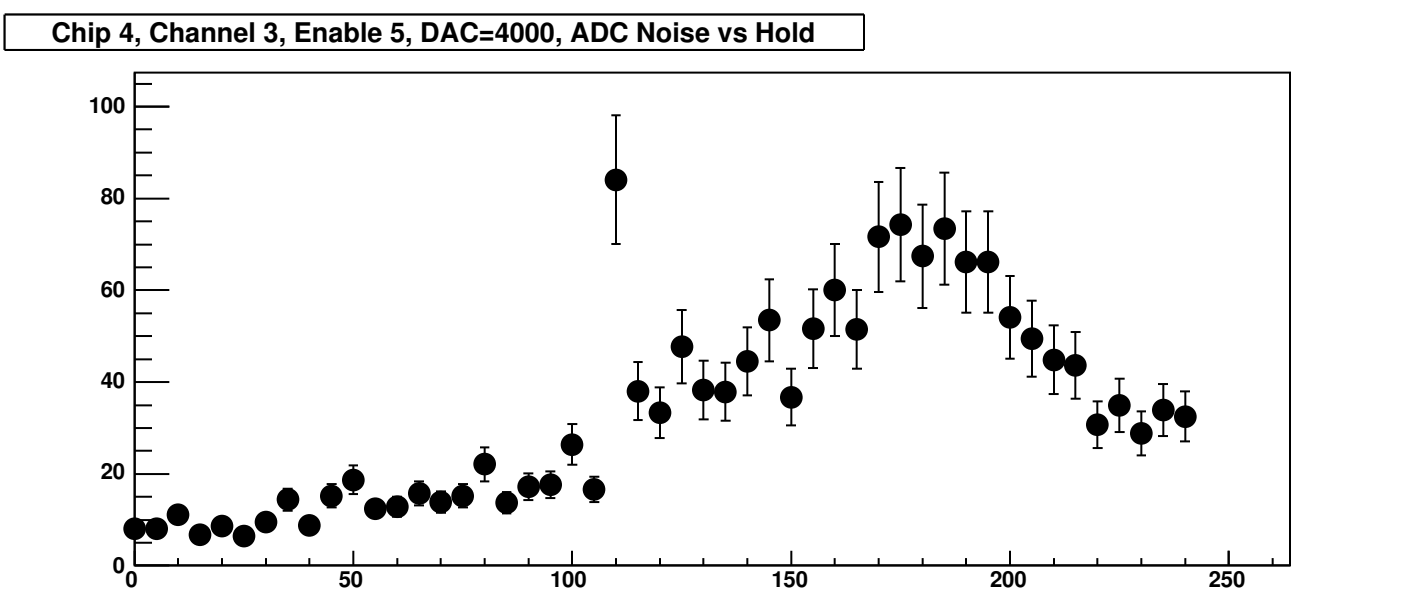


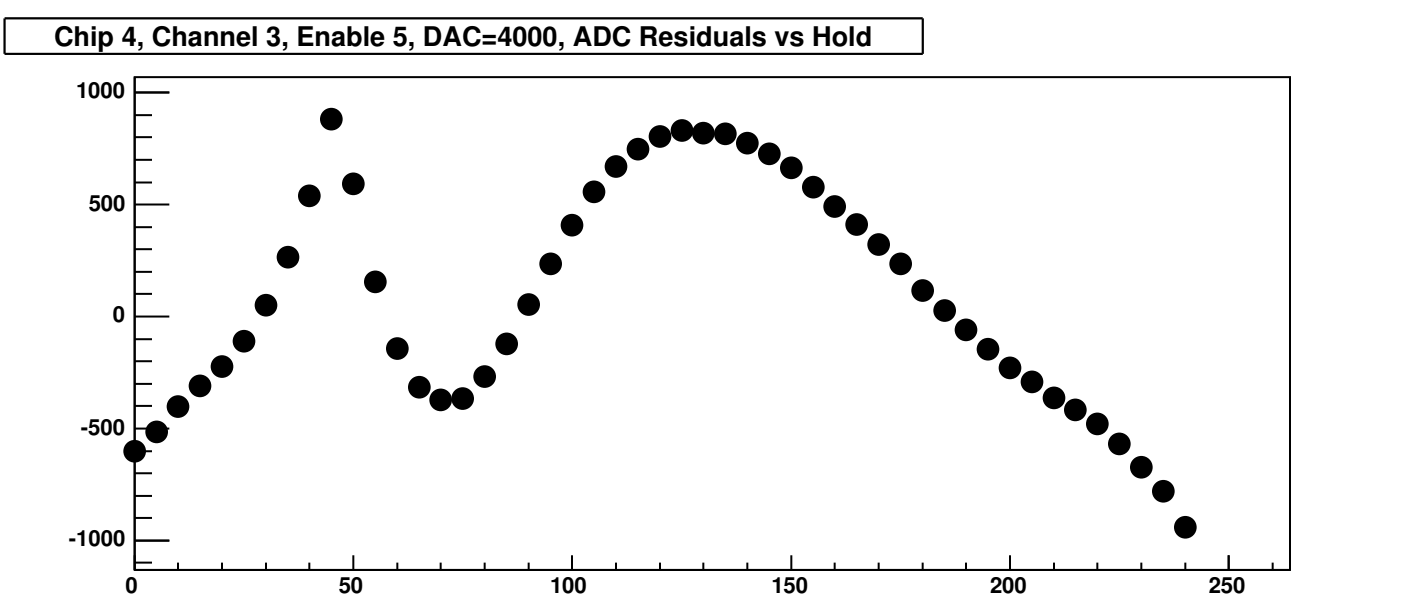

Chip 4, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 244.9 / 41
p0 432.3 ± 0.6228
p1 96.56 ± 0.6786
p2 -3.364e+07 ± 5.522e+05
p3 2.249e+07 ± 2.57e+05
p4 1.47 ± 0.01605
440
420
400
380
360
340
320
300
280
0
50
100
150
200
250


Chip 4, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

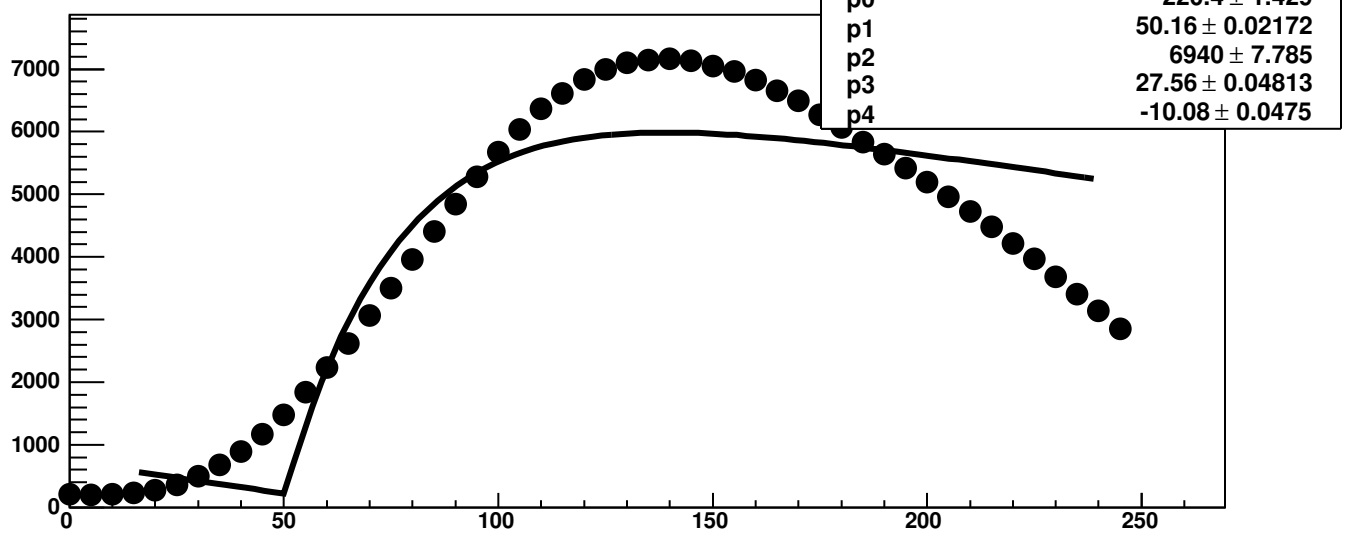

Chip 4, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250


Chip 4, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.047e+05 / 41
p0 11.76 ± 1.096
p1 46.63 ± 0.01657
p2 7114 ± 8.186
p3 34.38 ± 0.05107
p4 -18.94 ± 0.04463
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

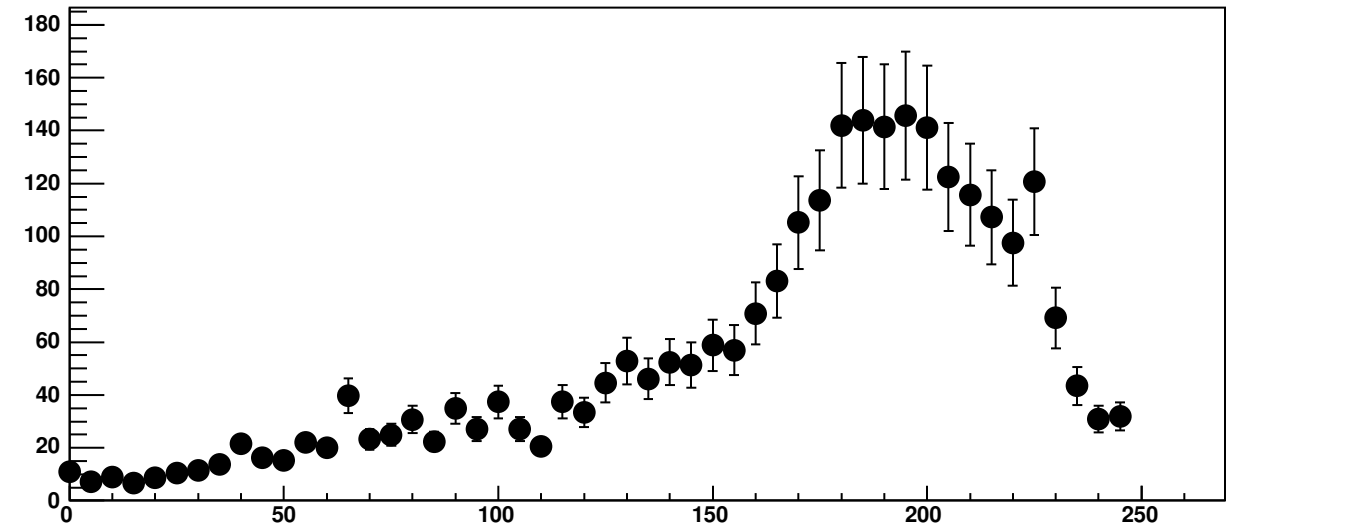

Chip 4, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250


Chip 4, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
0
50
100
150
200
250

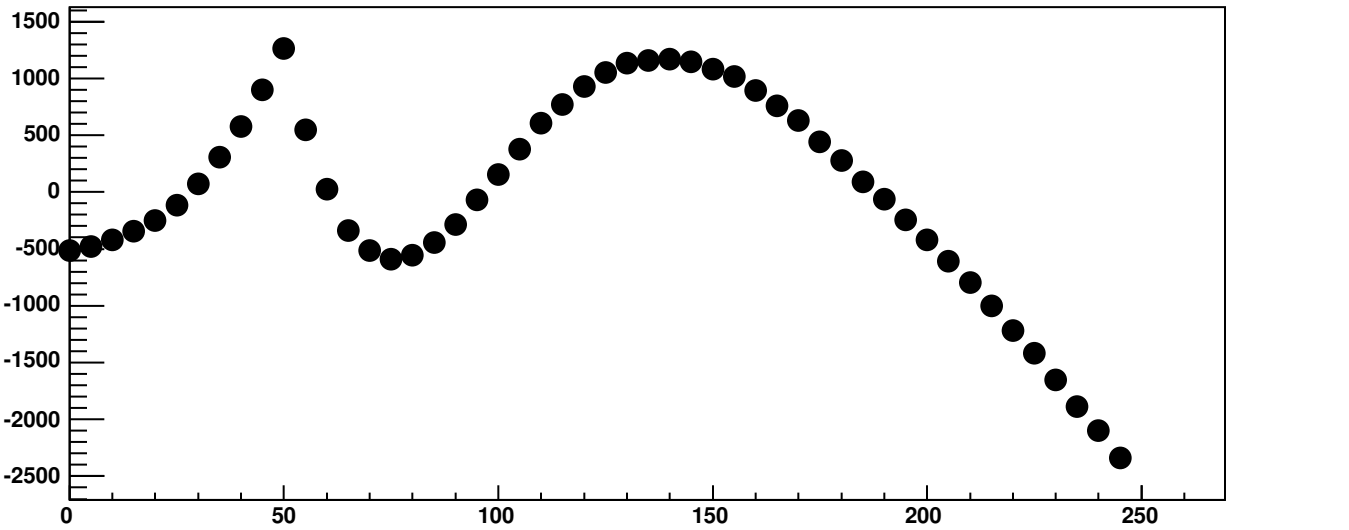
Chip 4, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

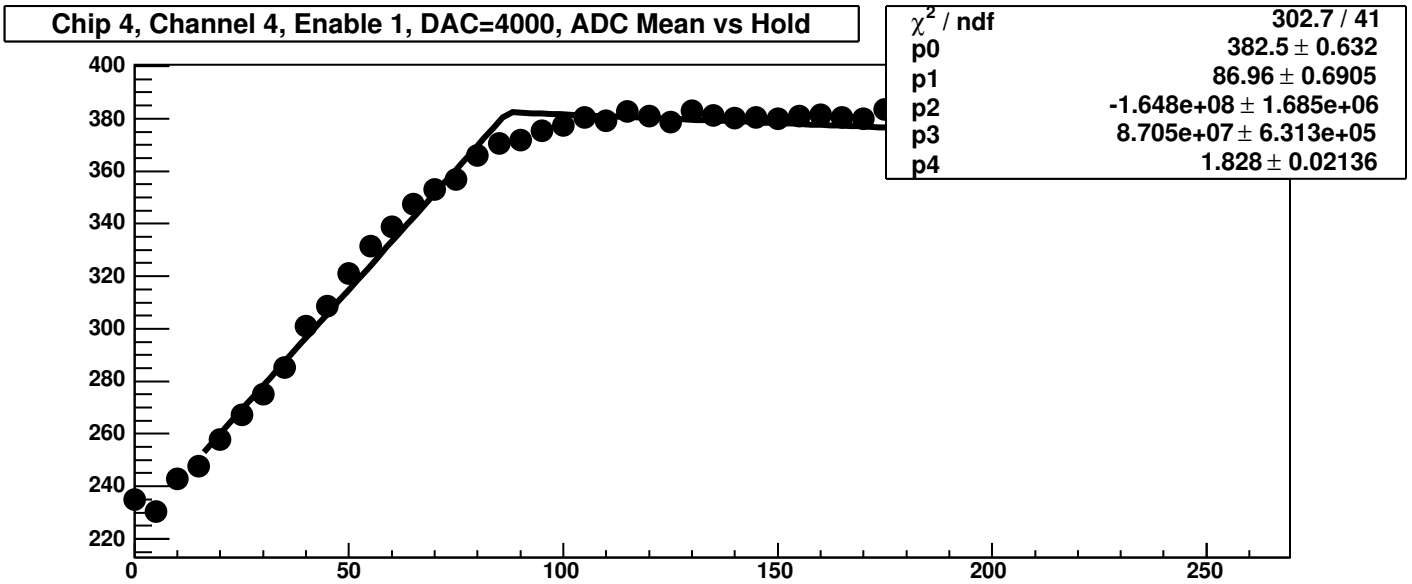


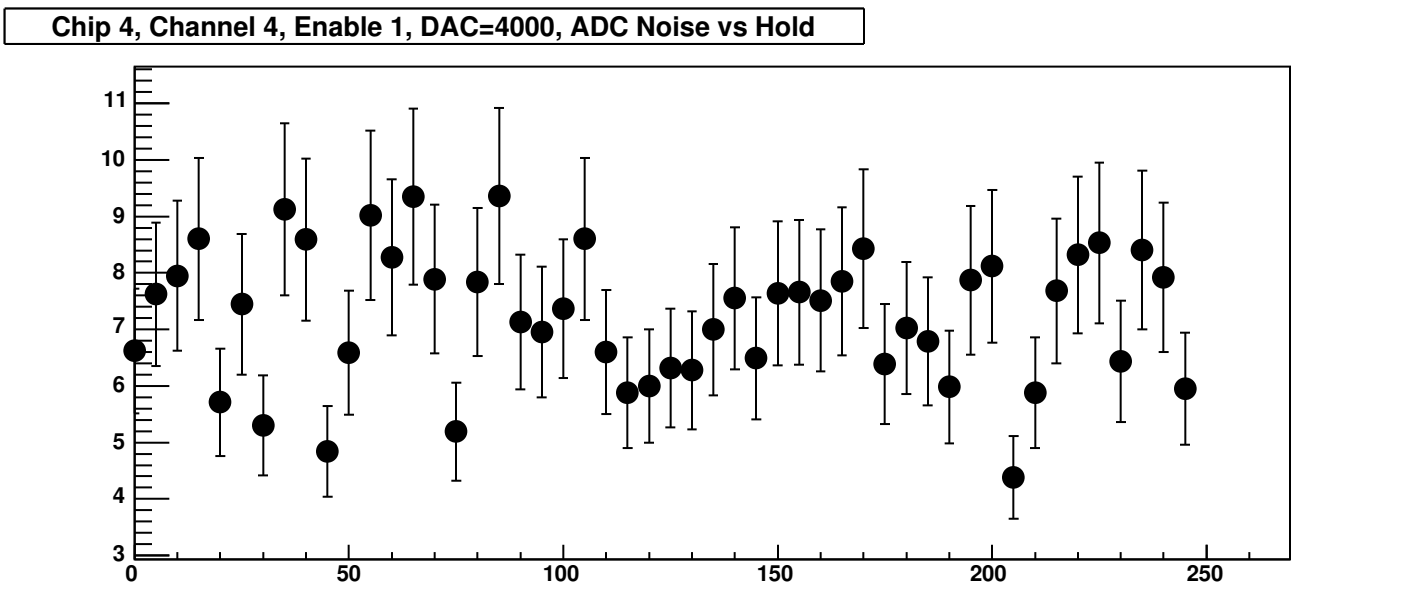
Chip 4, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold

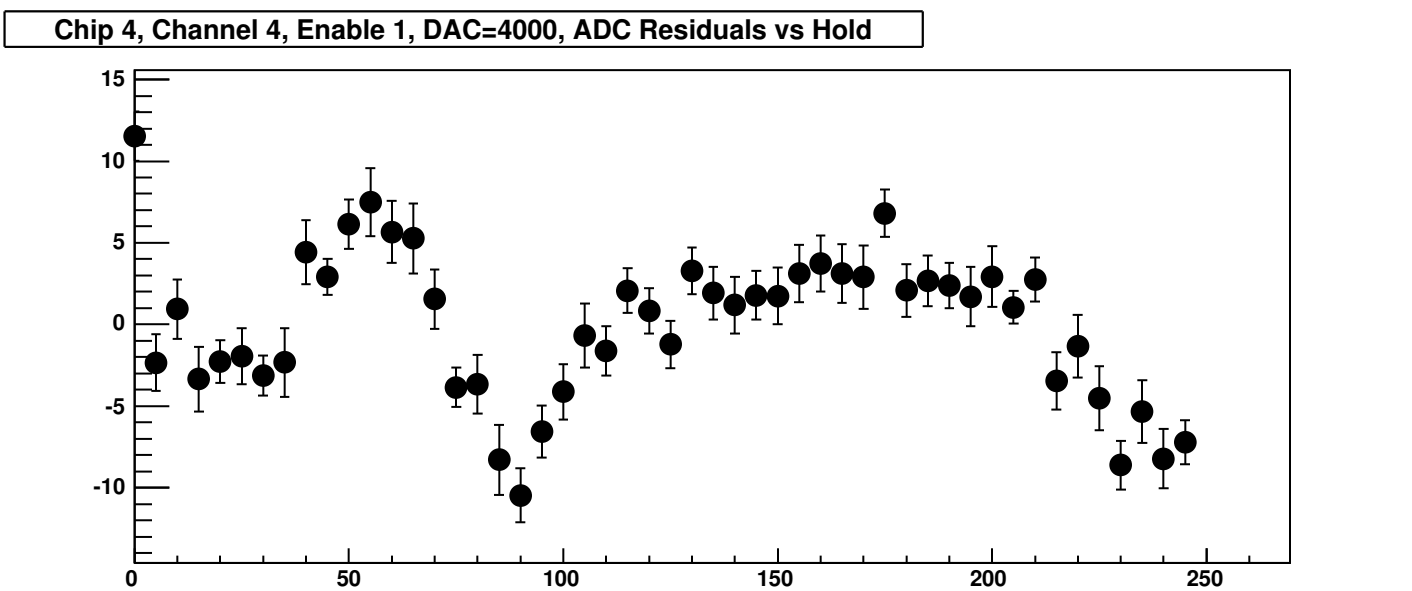


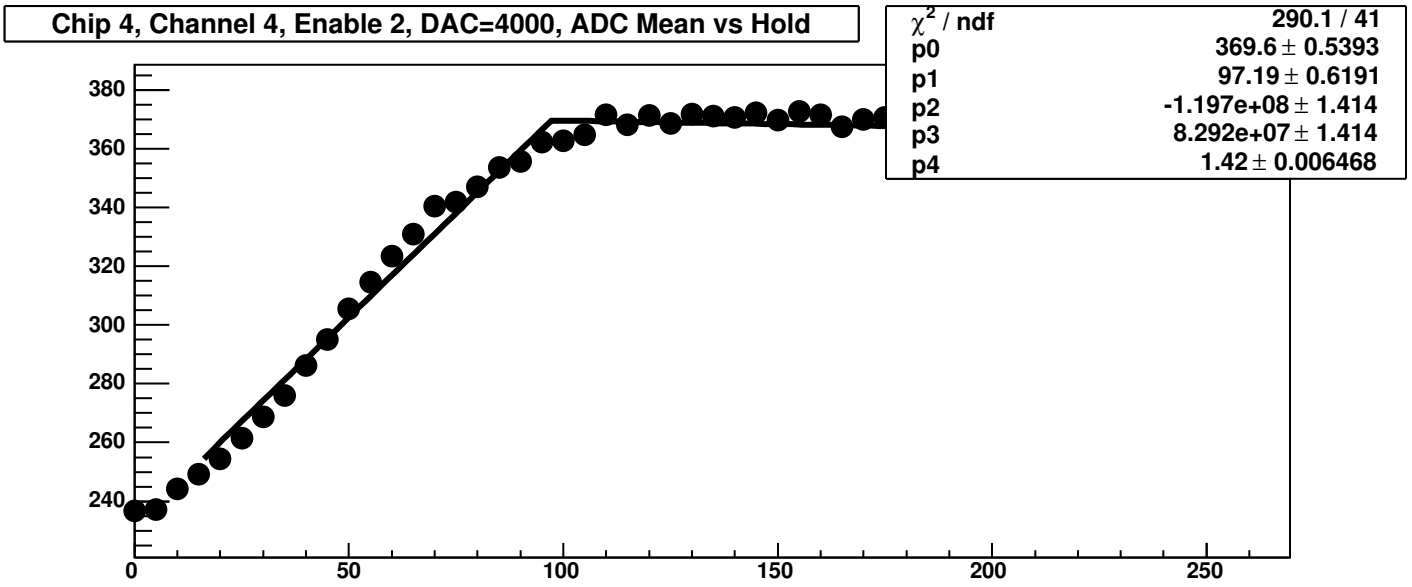
Chip 4, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

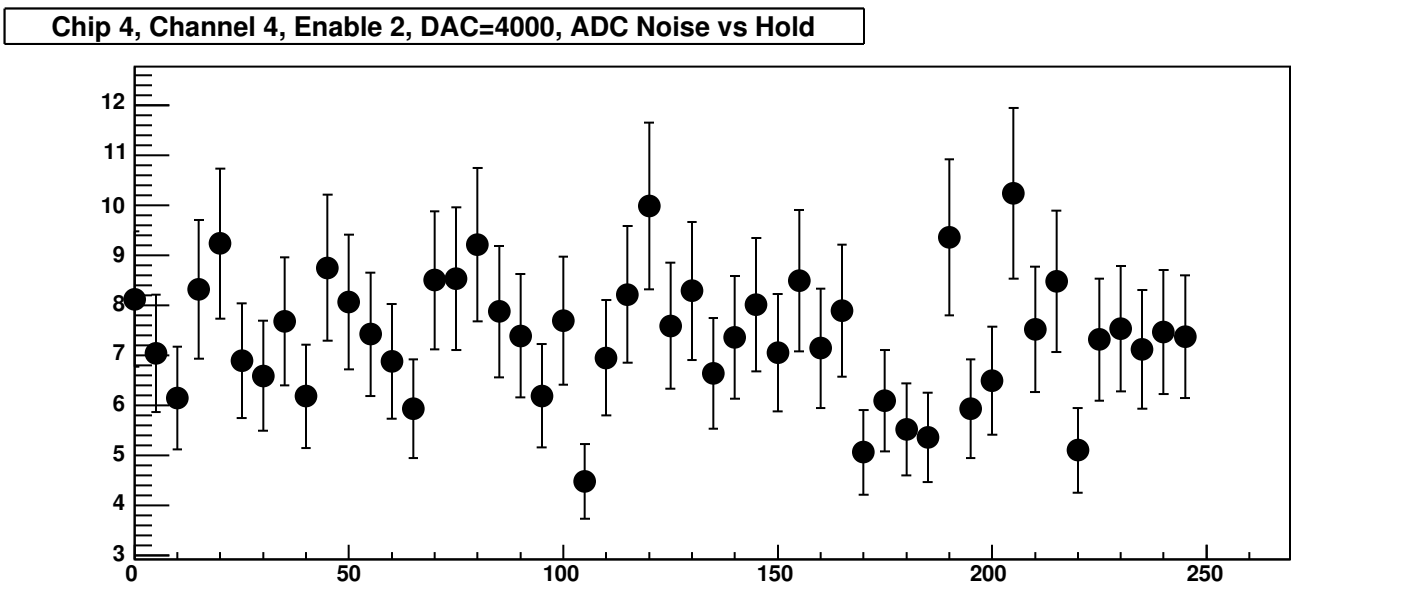


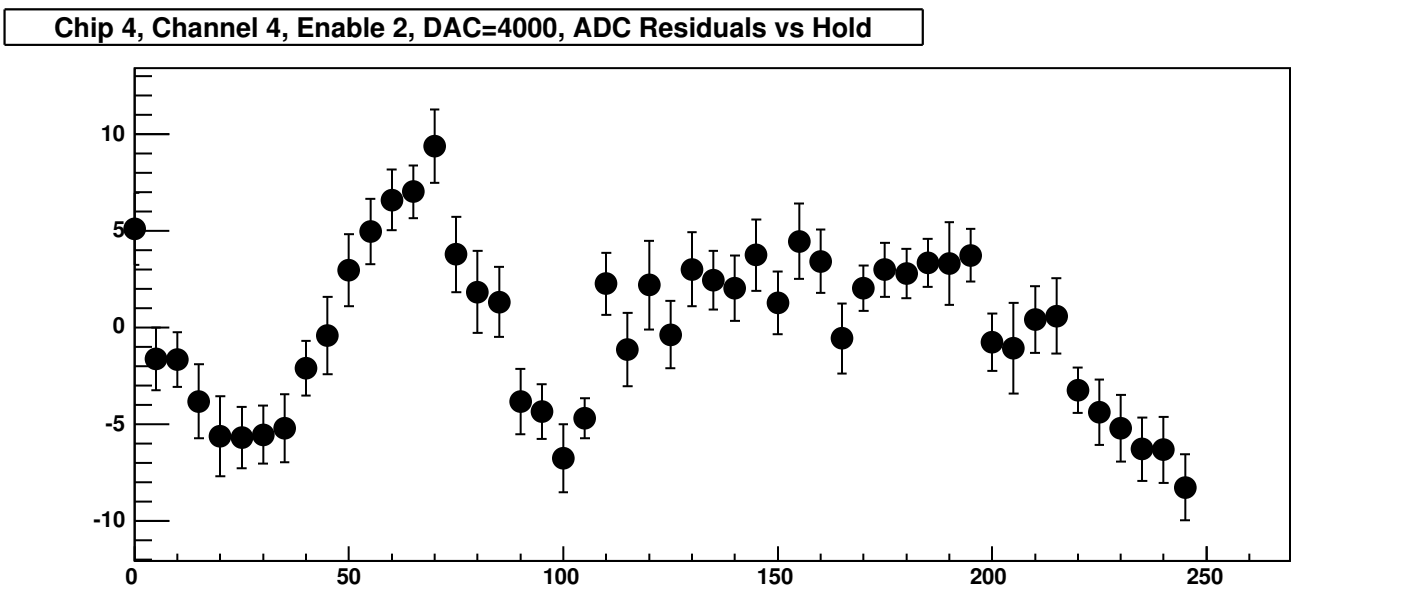

Chip 4, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 302.7 / 41
p0 382.5 ± 0.632
p1 86.96 ± 0.6905
p2 -1.648e+08 ± 1.685e+06
p3 8.705e+07 ± 6.313e+05
p4 1.828 ± 0.02136
400
380
360
340
320
300
280
260
240
220
0
50
100
150
200
250


Chip 4, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

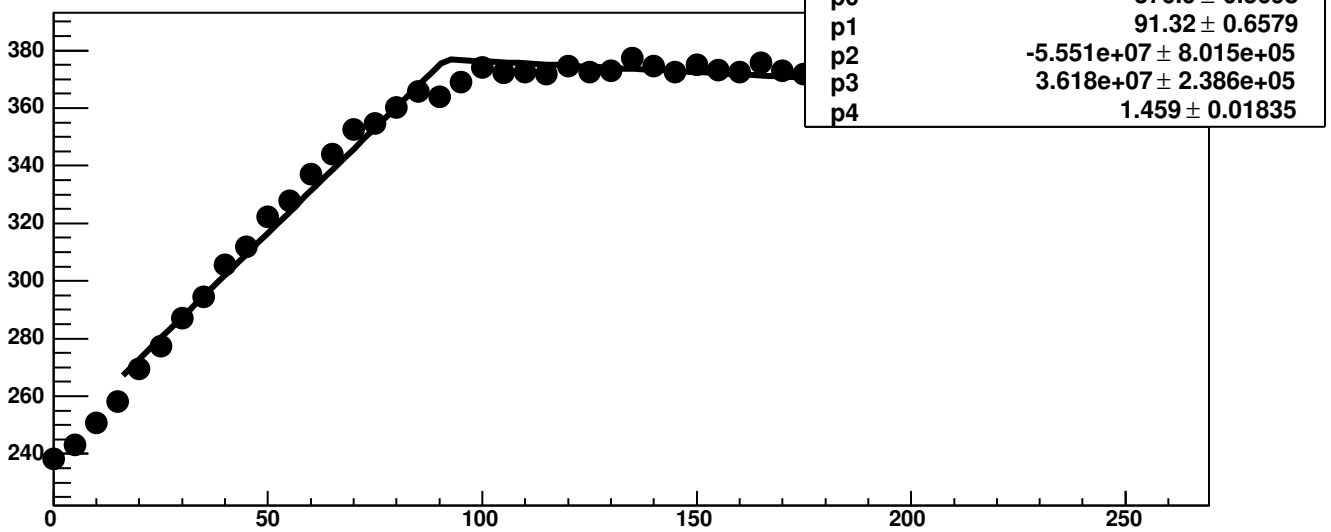

Chip 4, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
0
50
100
150
200
250


Chip 4, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 290.1 / 41
p0 369.6 ± 0.5393
p1 97.19 ± 0.6191
p2 -1.197e+08 ± 1.414
p3 8.292e+07 ± 1.414
p4 1.42 ± 0.006468
380
360
340
320
300
280
260
240
0
50
100
150
200
250

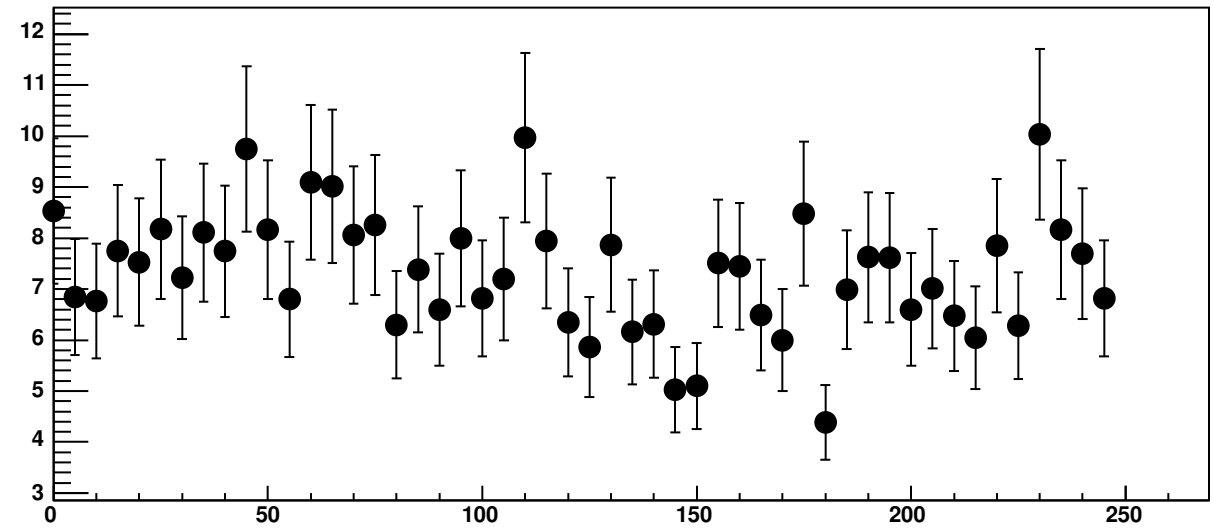

Chip 4, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 4, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250

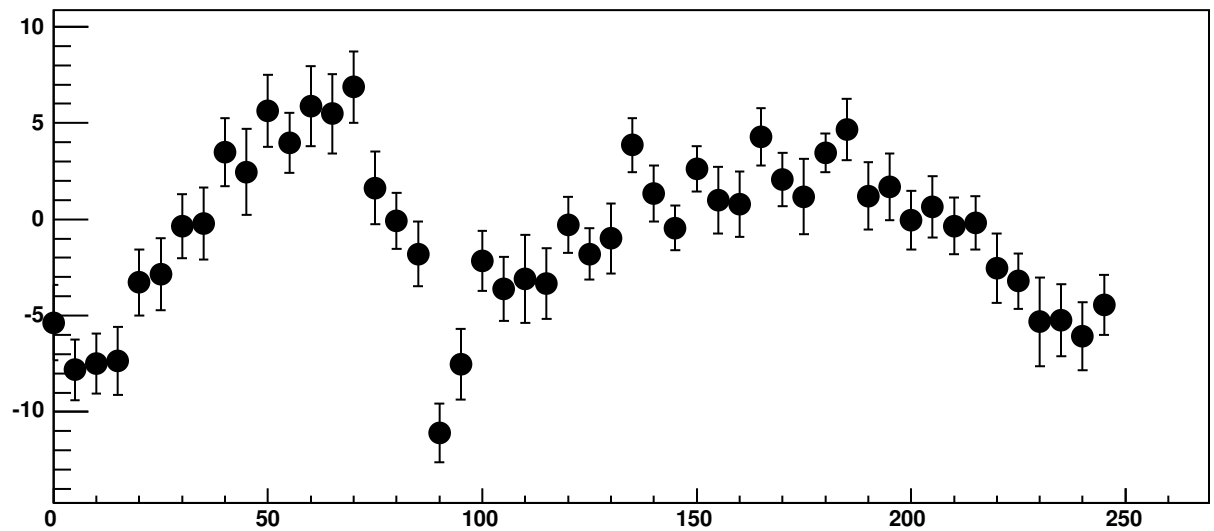
Chip 4, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold



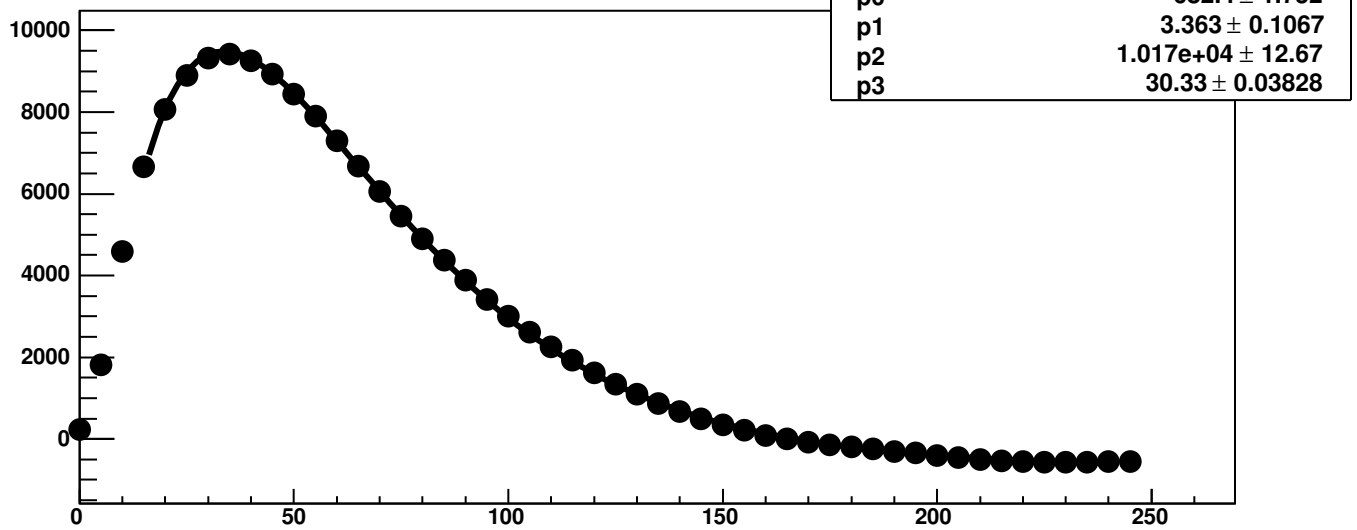
Chip 4, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



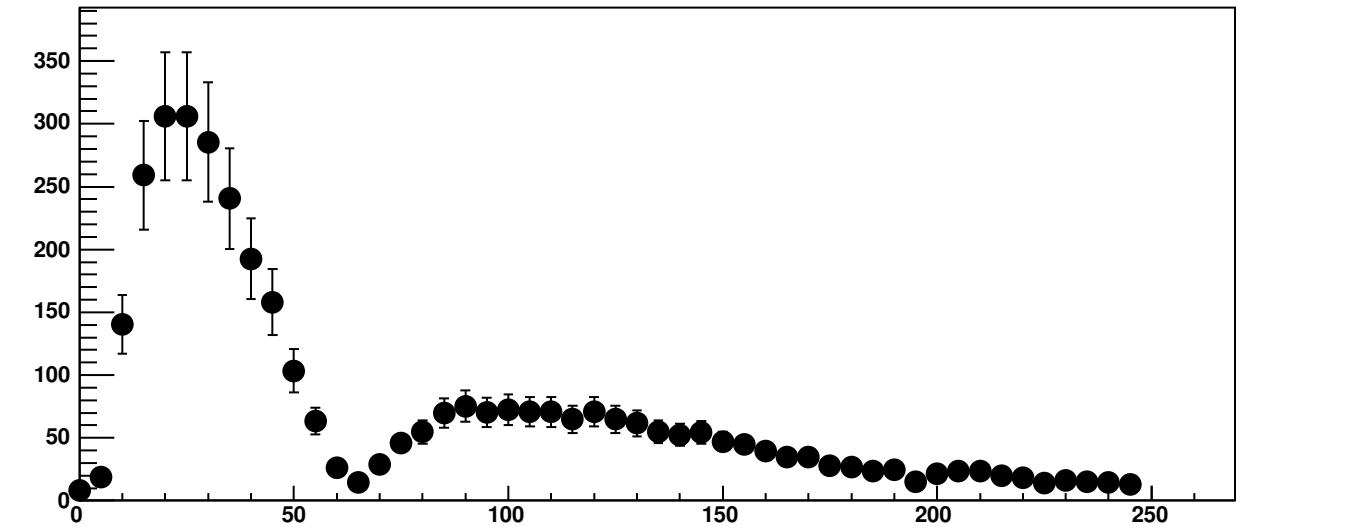
Chip 4, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold



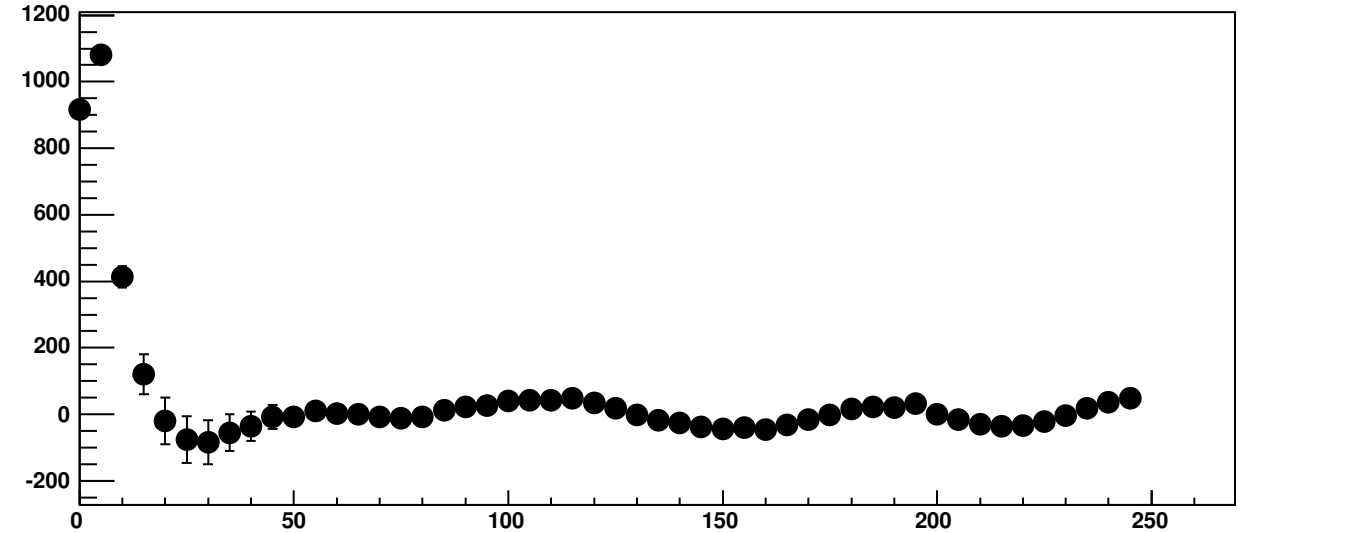
Chip 4, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold



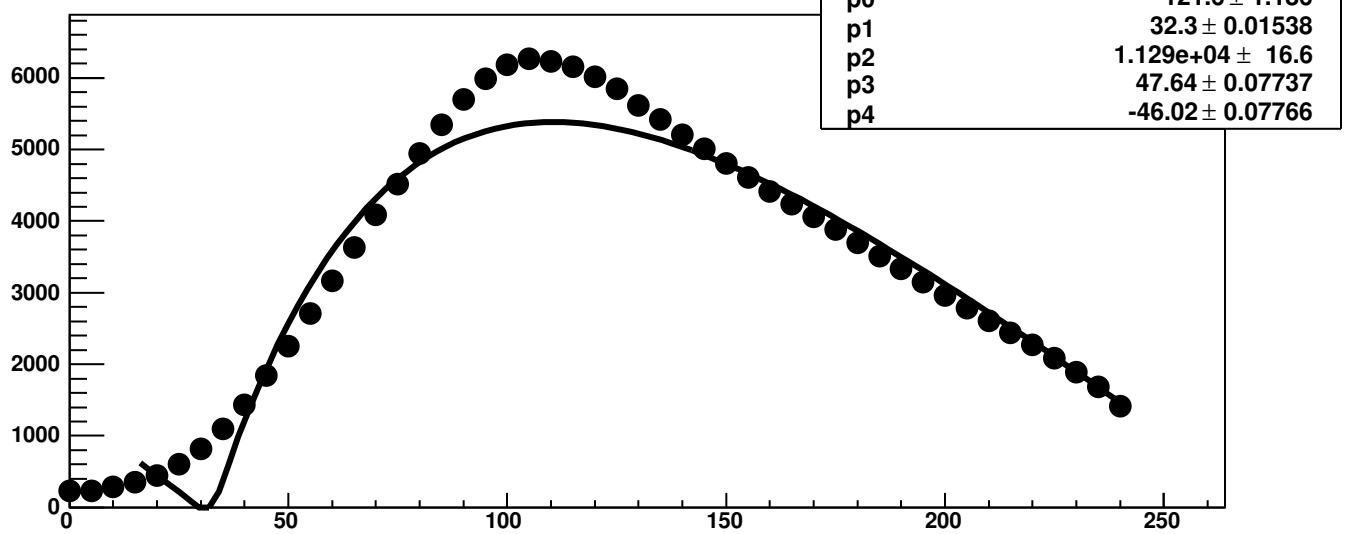
Chip 4, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold



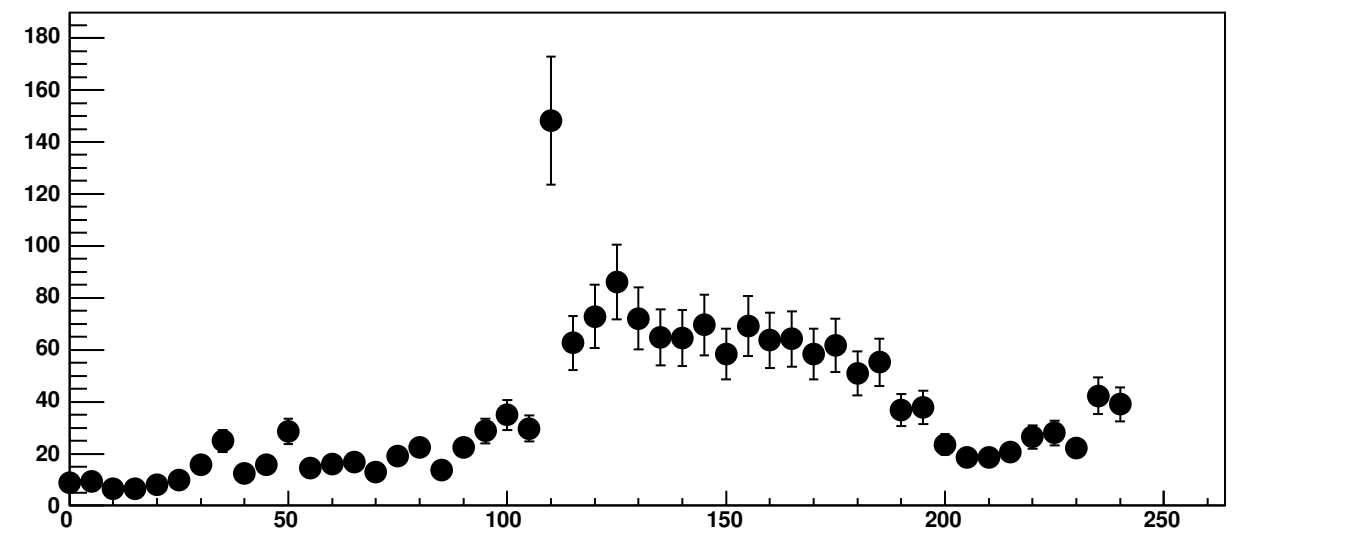
Chip 4, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold



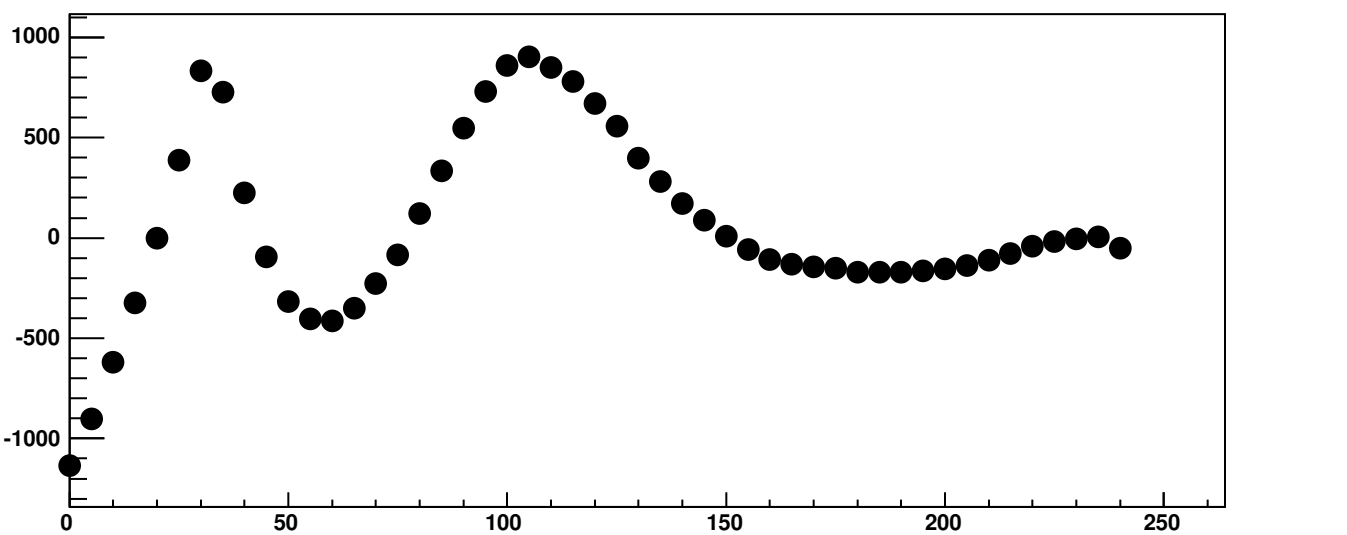
Chip 4, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold

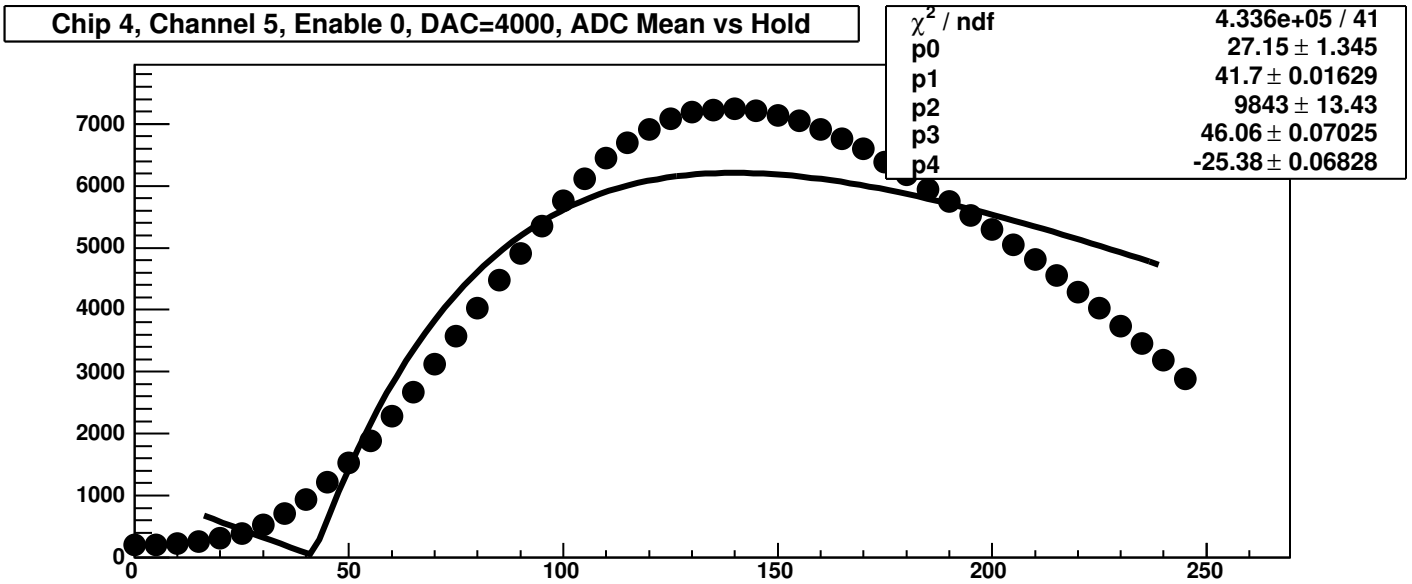


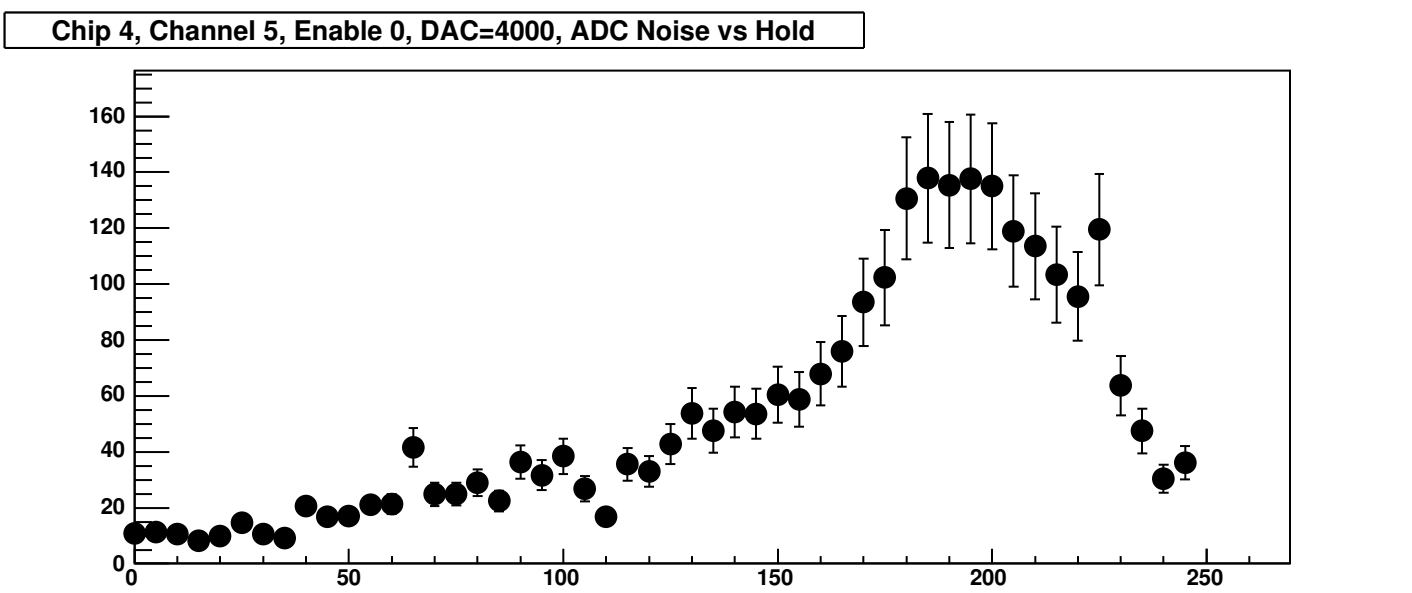
Chip 4, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold

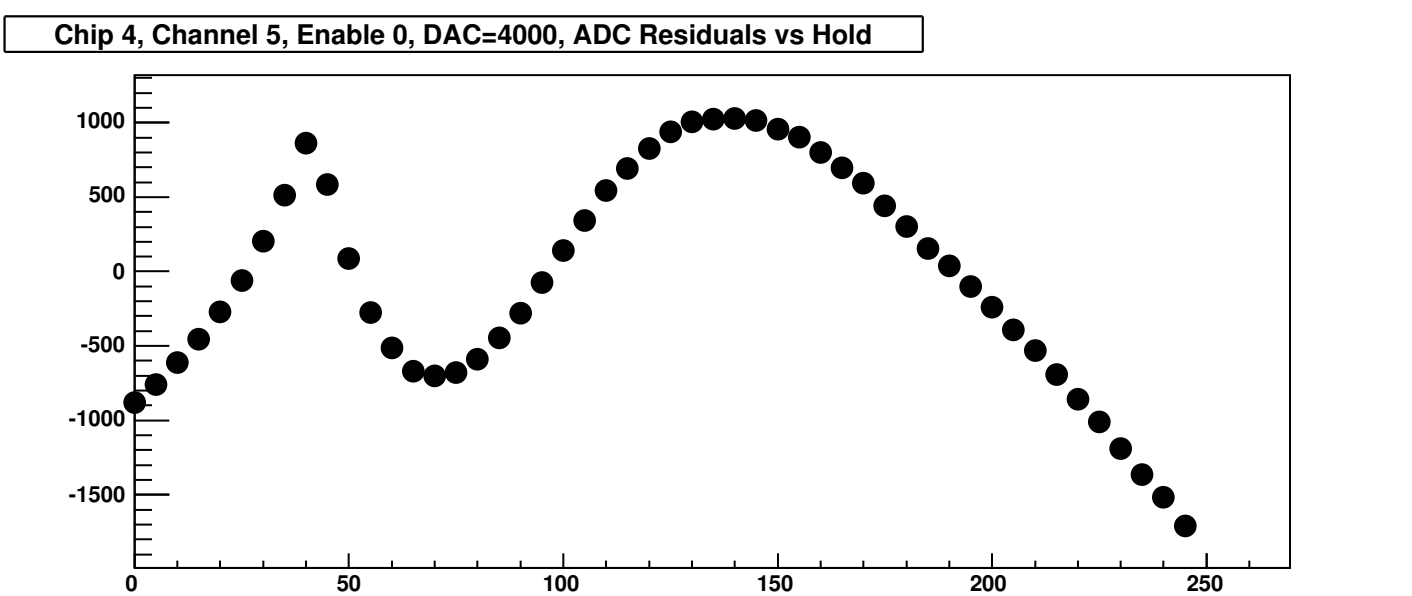


Chip 4, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold

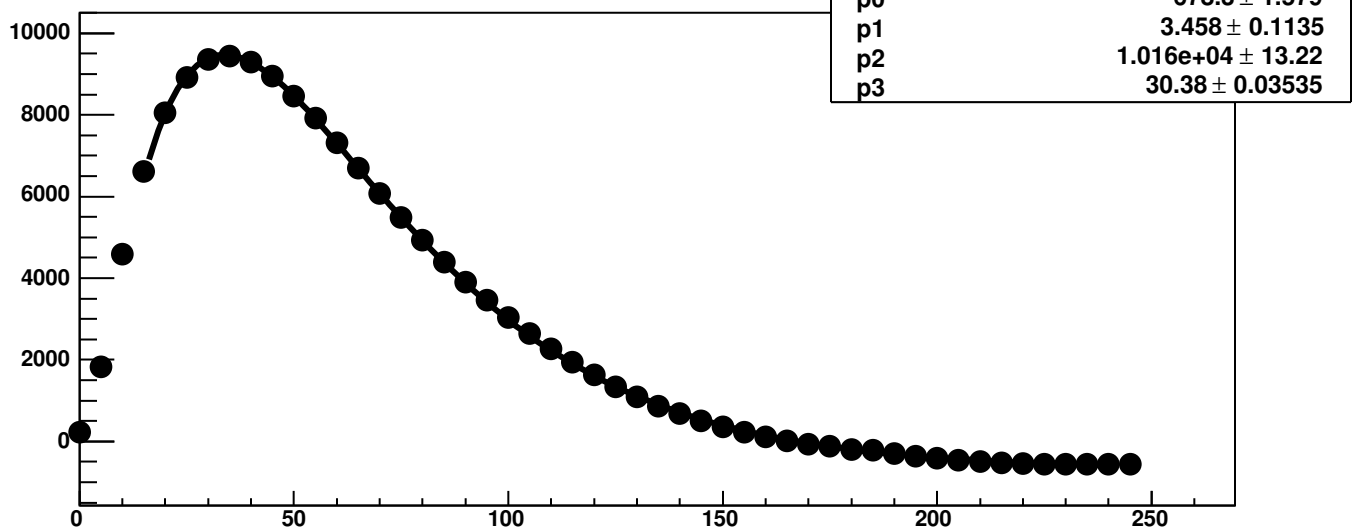



Chip 4, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.336e+05 / 41
p0 27.15 ± 1.345
p1 41.7 ± 0.01629
p2 9843 ± 13.43
p3 46.06 ± 0.07025
p4 -25.38 ± 0.06828
0 1000 2000 3000 4000 5000 6000 7000
0 50 100 150 200 250

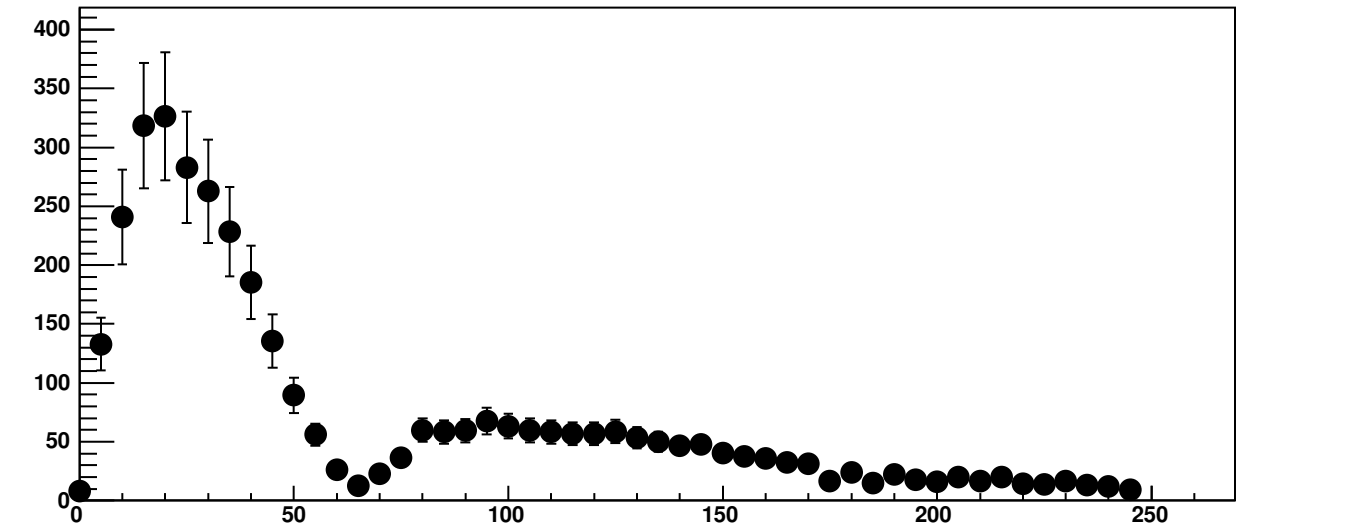

Chip 4, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold
0 20 40 60 80 100 120 140 160
0 50 100 150 200 250


Chip 4, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold
-1500 -1000 -500 0 500 1000
0 50 100 150 200 250

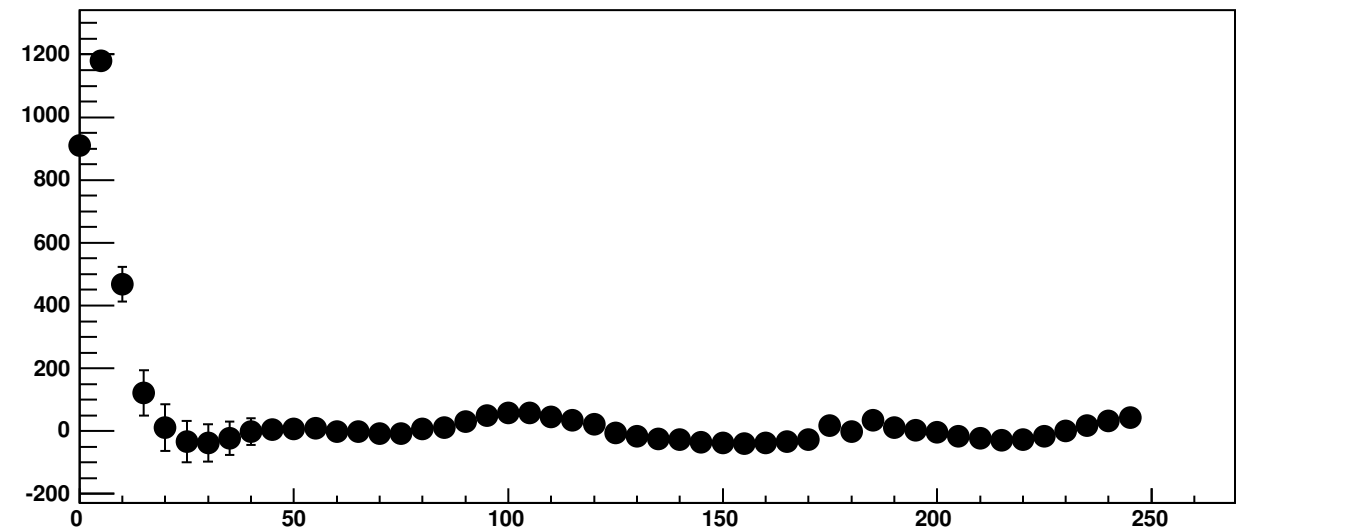
Chip 4, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold



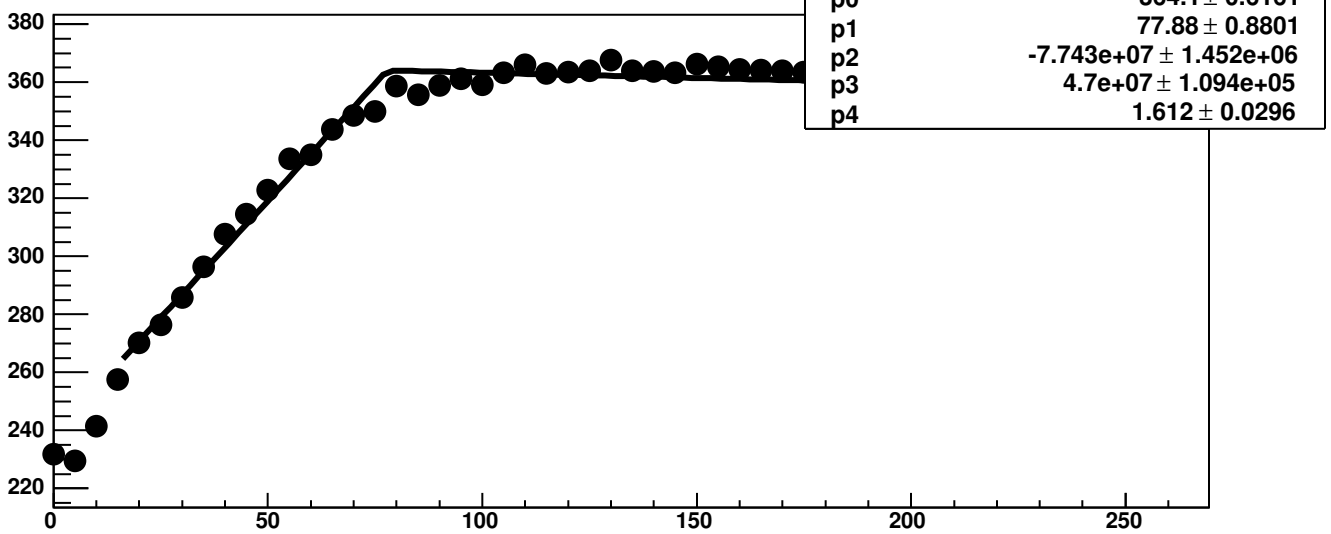
Chip 4, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold



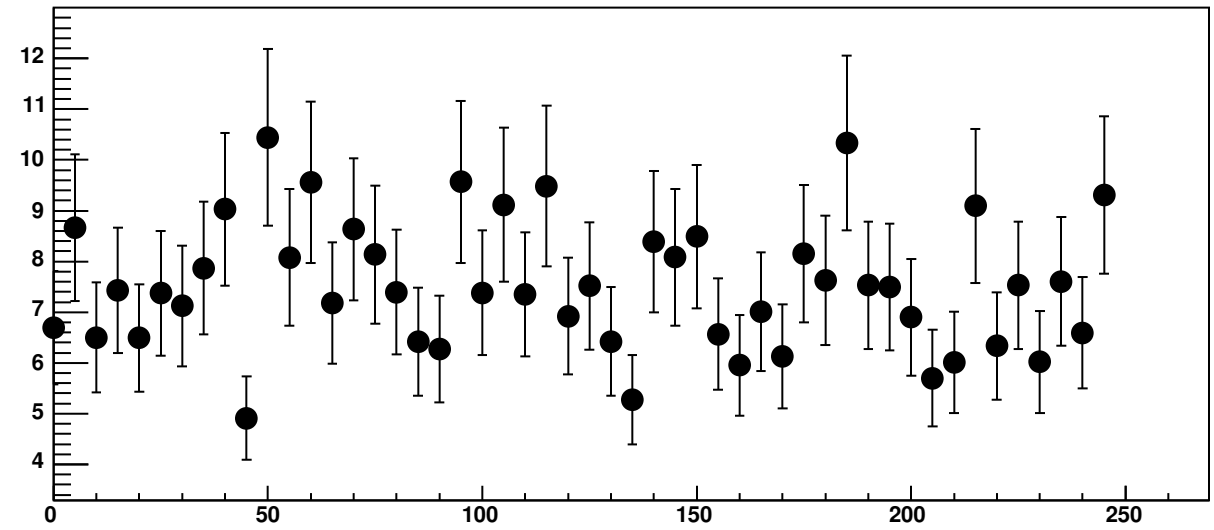
Chip 4, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold



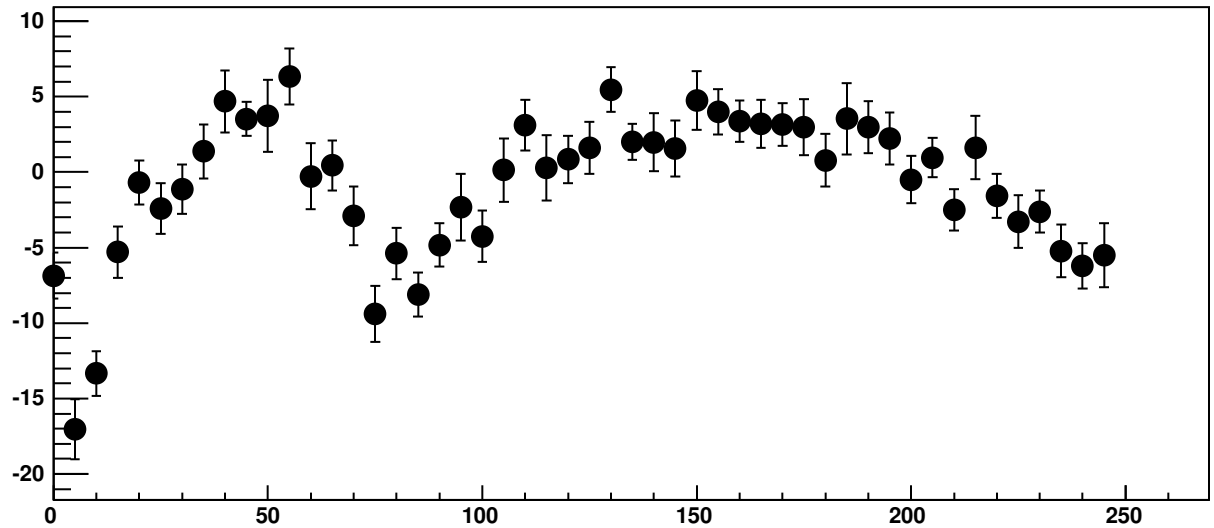
Chip 4, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold



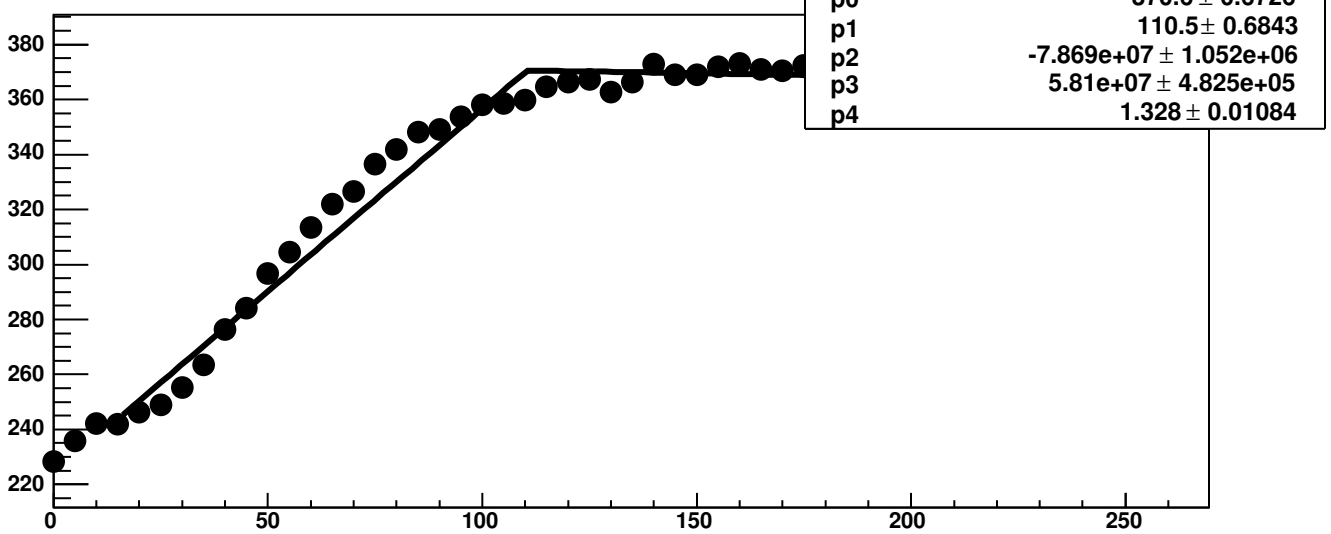
Chip 4, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



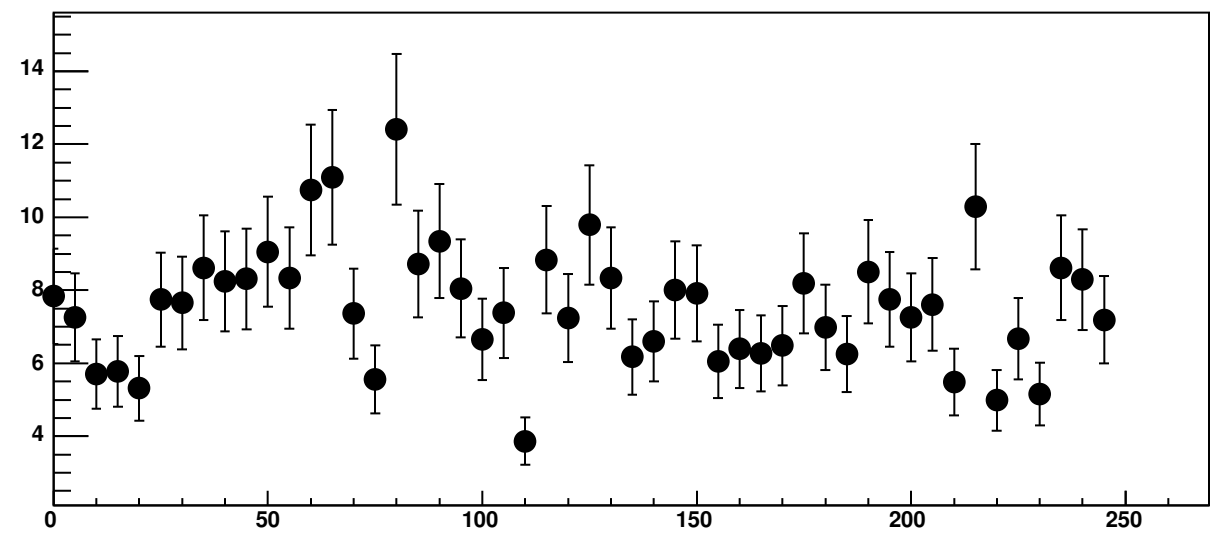
Chip 4, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold



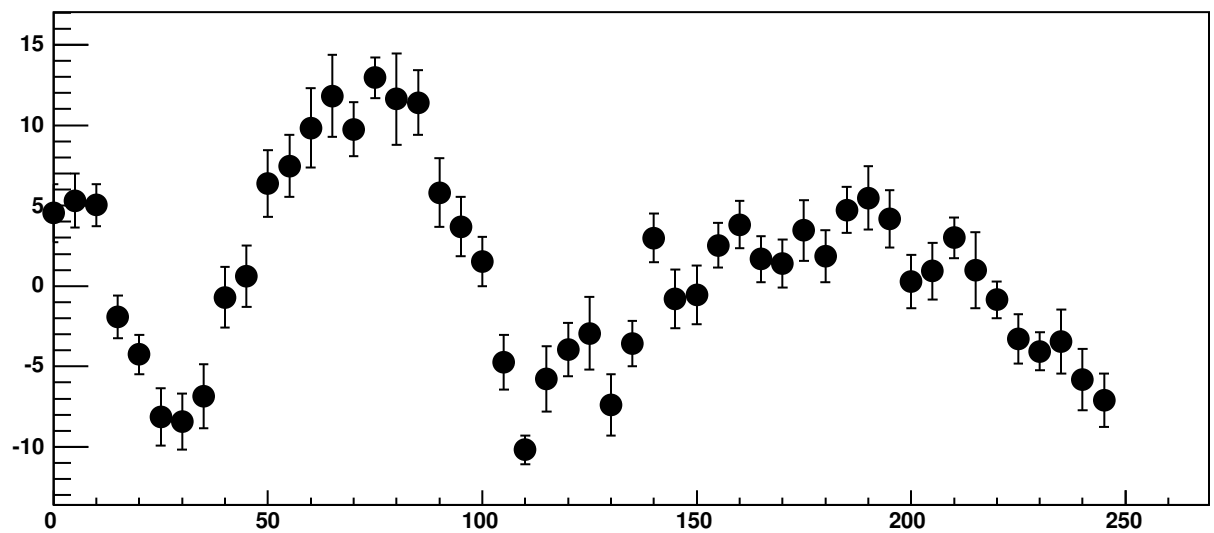
Chip 4, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

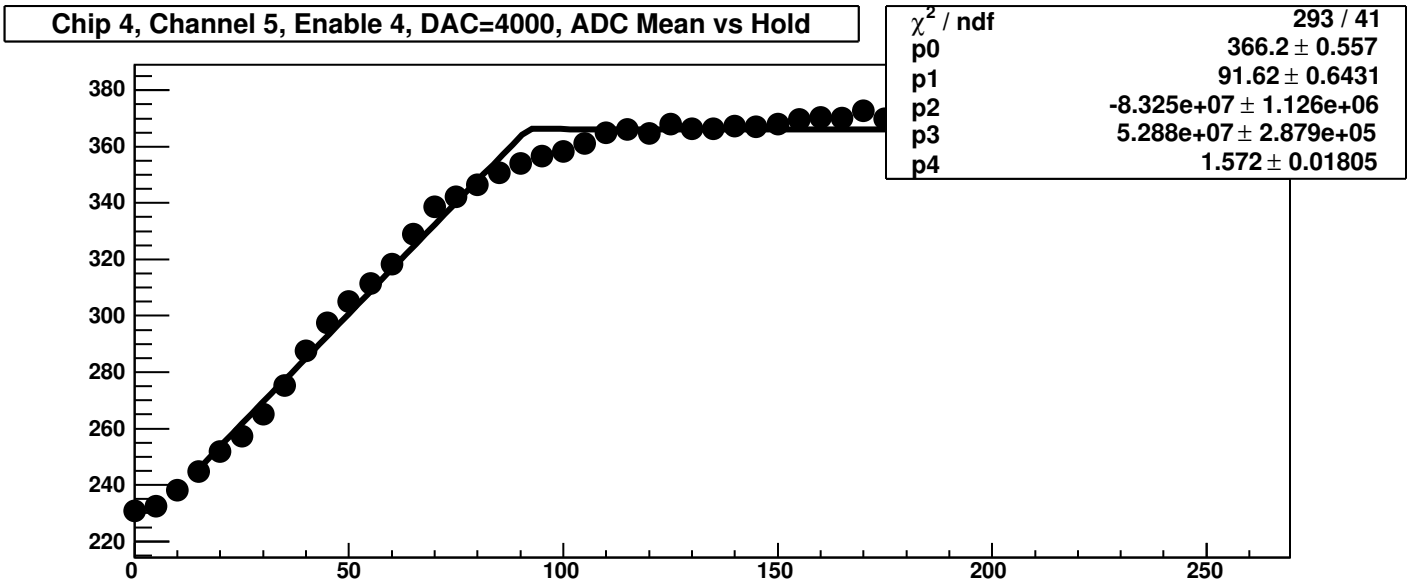


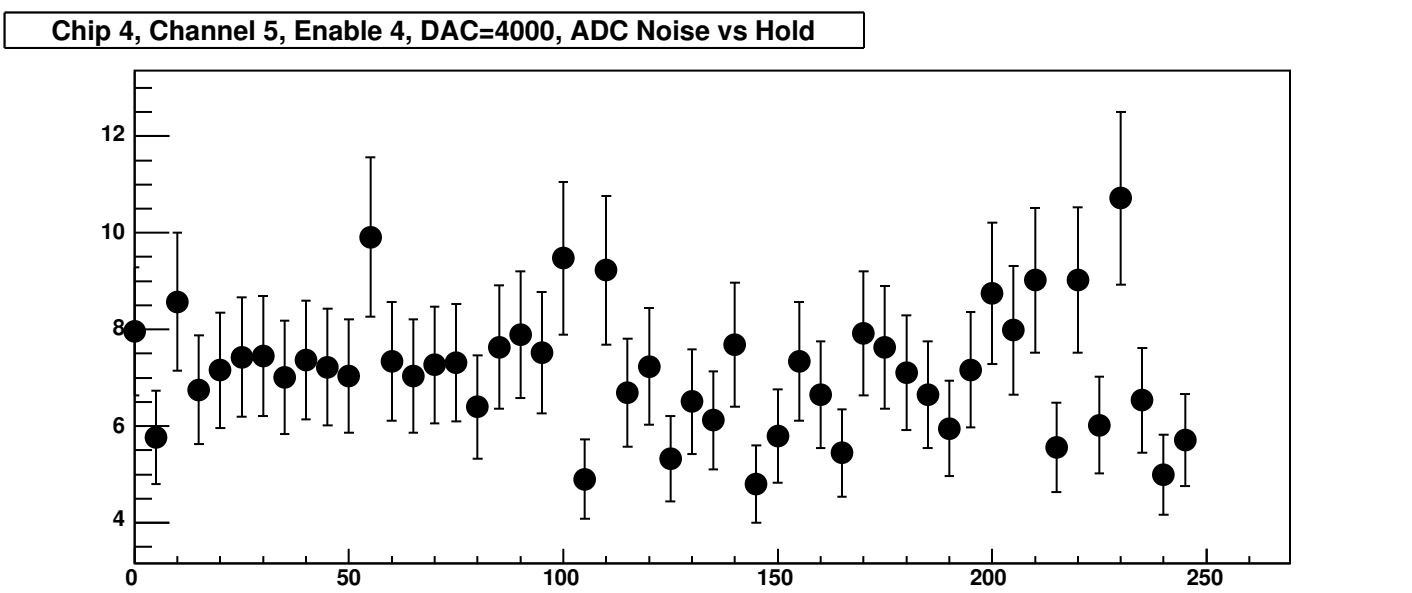
Chip 4, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold

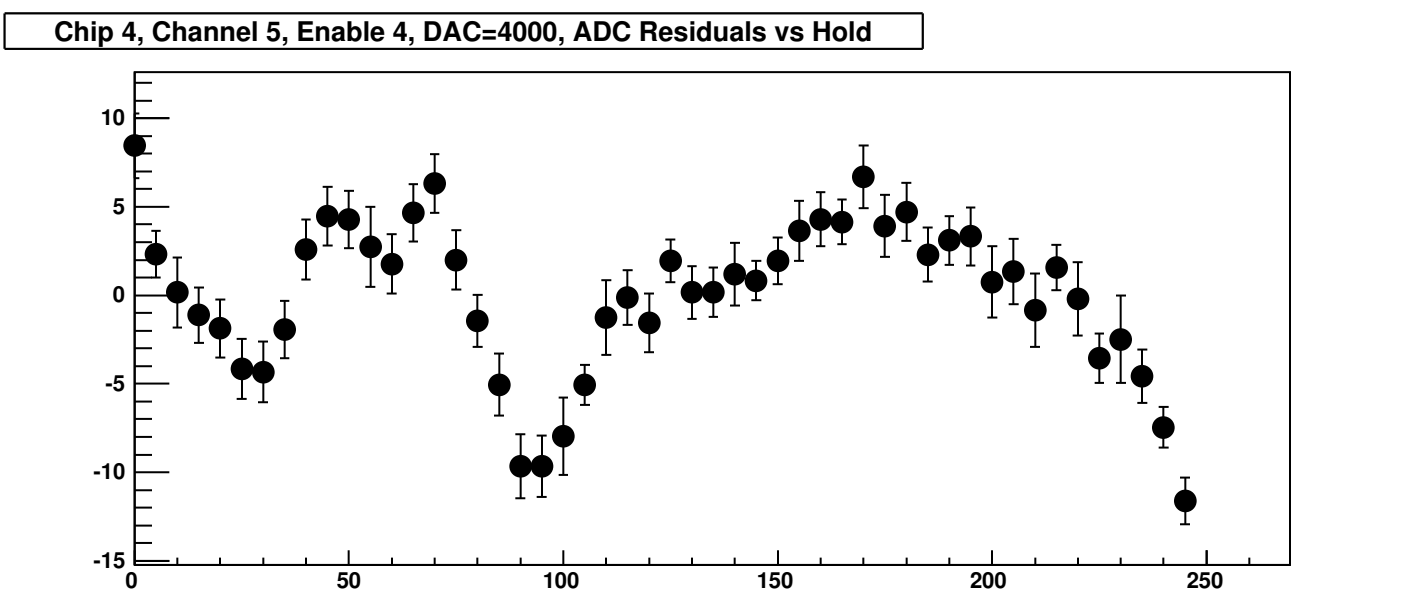


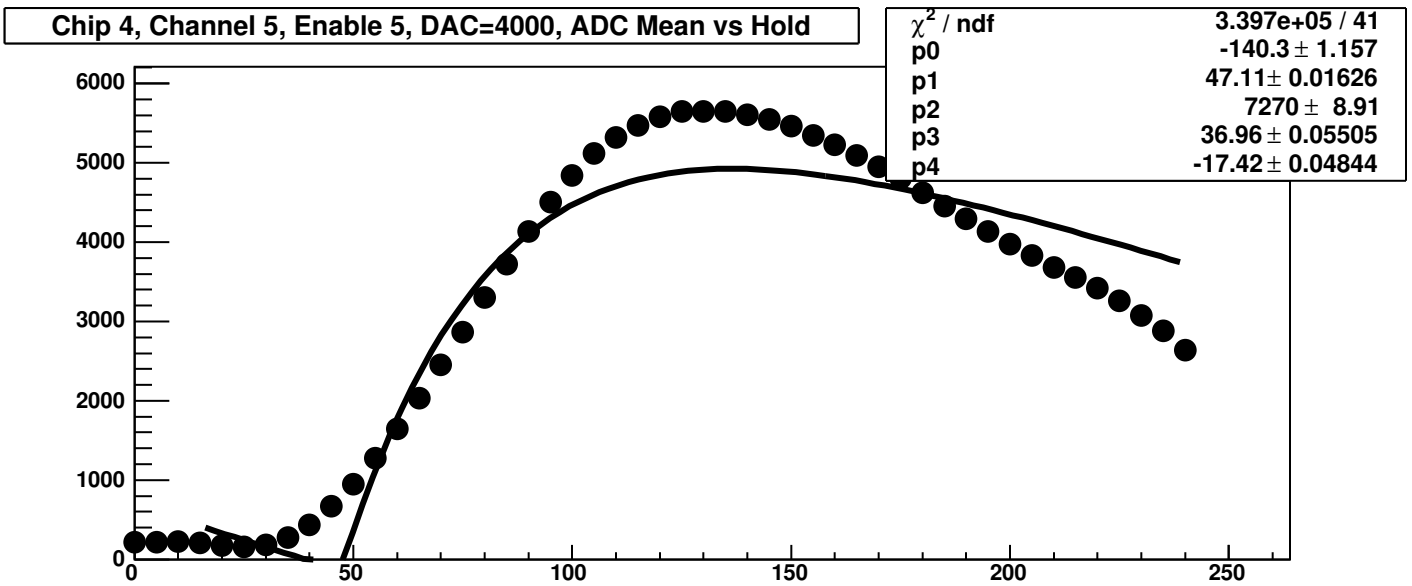
Chip 4, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

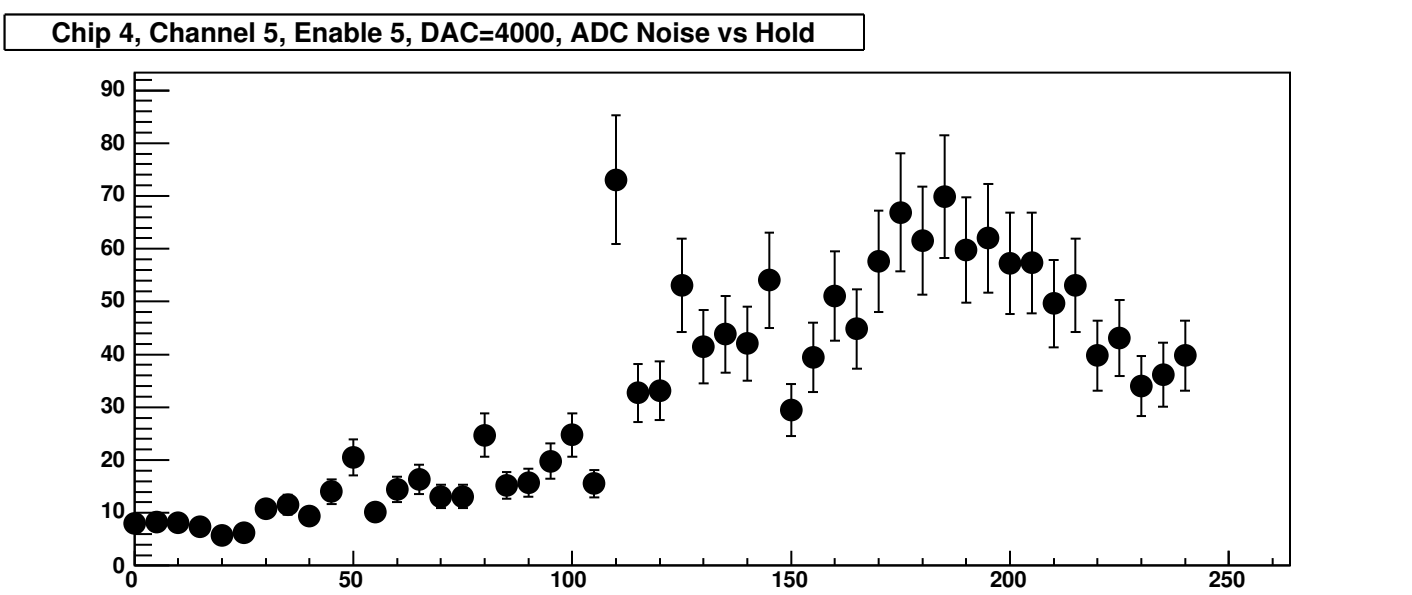


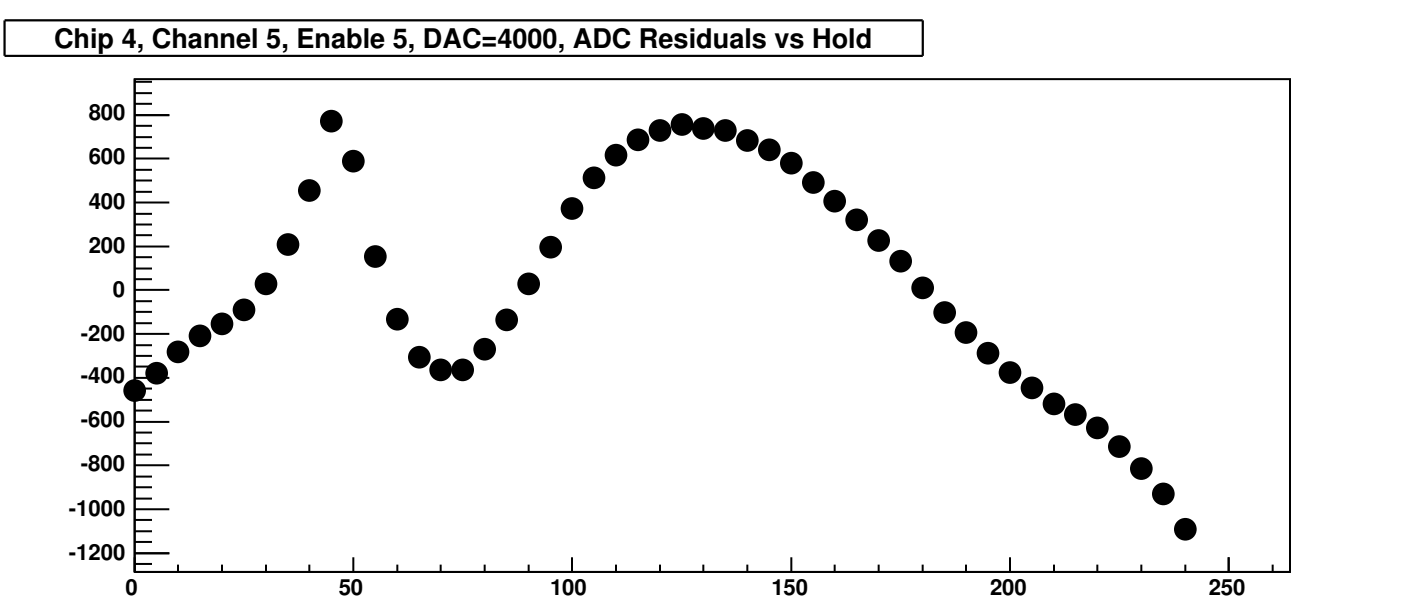

Chip 4, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 293 / 41
p0 366.2 ± 0.557
p1 91.62 ± 0.6431
p2 -8.325e+07 ± 1.126e+06
p3 5.288e+07 ± 2.879e+05
p4 1.572 ± 0.01805
380
360
340
320
300
280
260
240
220
0
50
100
150
200
250

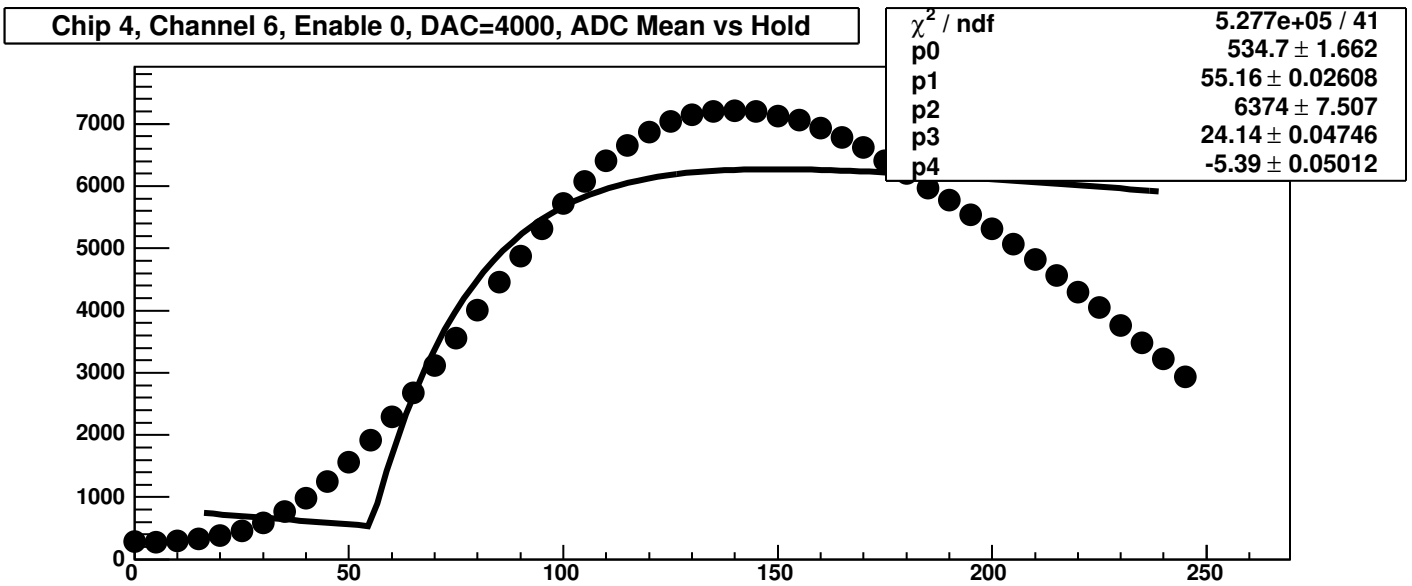

Chip 4, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

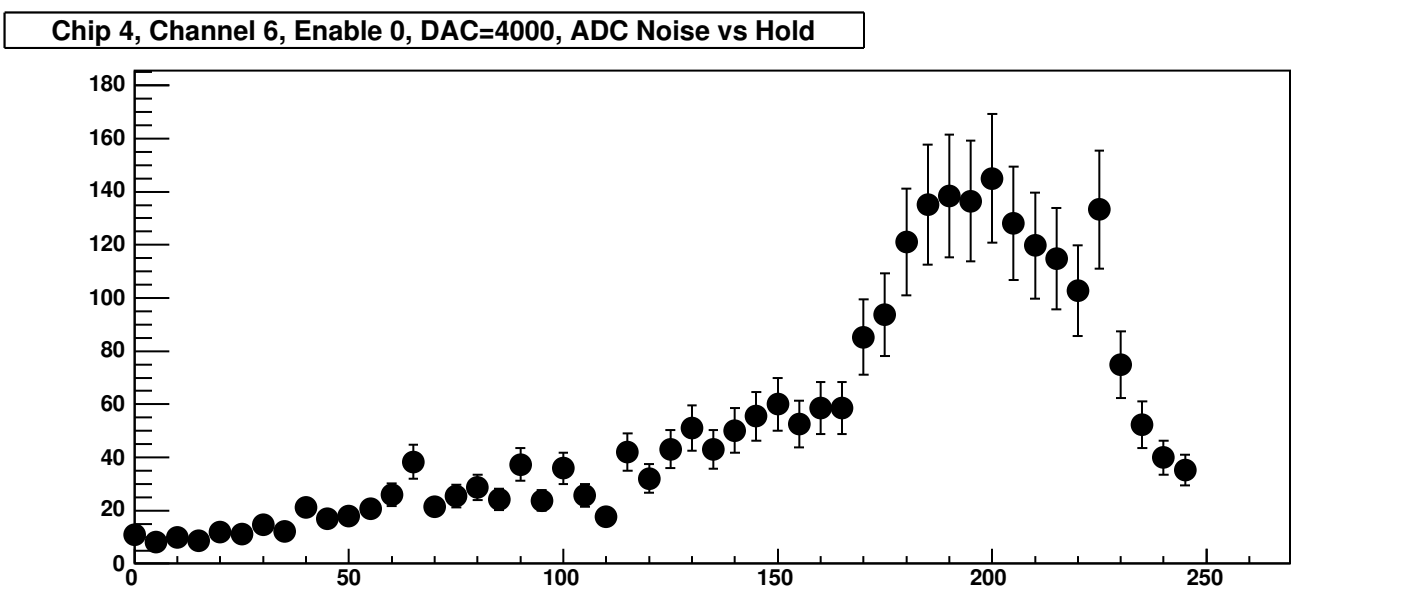

Chip 4, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
0
50
100
150
200
250

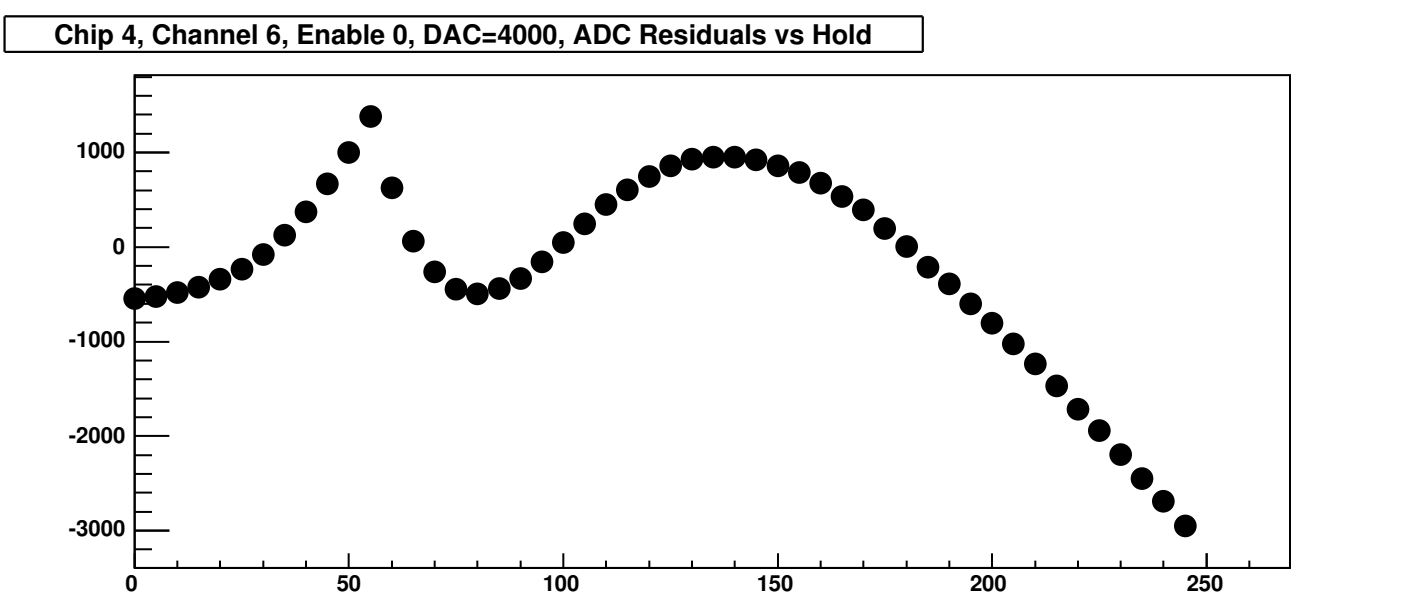

Chip 4, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.397e+05 / 41
p0 -140.3 ± 1.157
p1 47.11 ± 0.01626
p2 7270 ± 8.91
p3 36.96 ± 0.05505
p4 -17.42 ± 0.04844
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 4, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold
90
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250

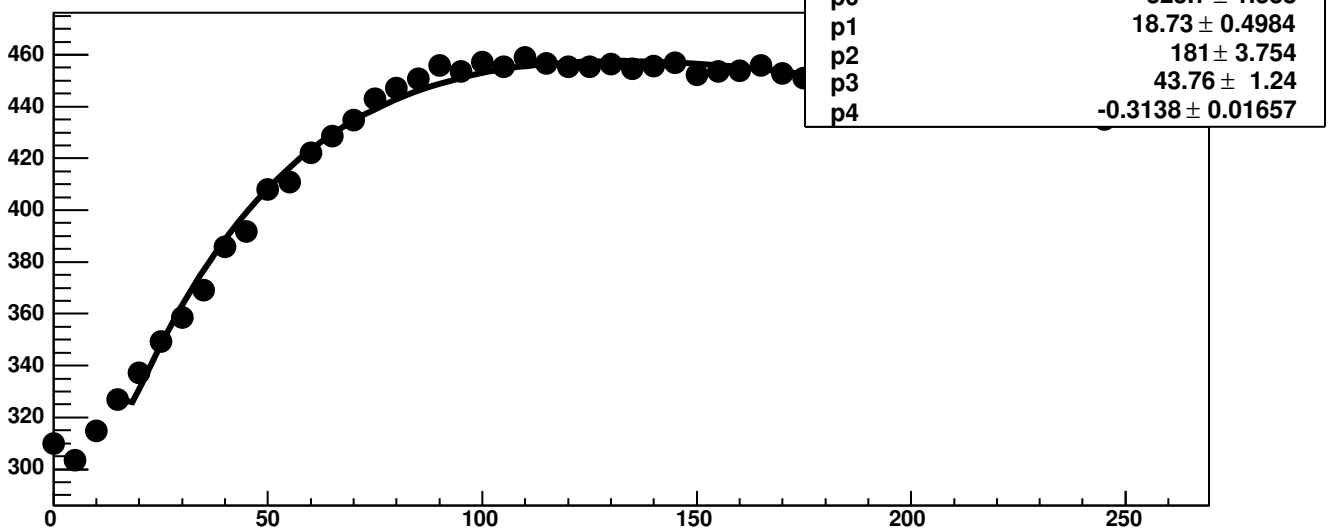

Chip 4, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold
800
600
400
200
0
-200
-400
-600
-800
-1000
-1200
0
50
100
150
200
250


Chip 4, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.277e+05 / 41
p0 534.7 ± 1.662
p1 55.16 ± 0.02608
p2 6374 ± 7.507
p3 24.14 ± 0.04746
p4 -5.39 ± 0.05012
0 1000 2000 3000 4000 5000 6000 7000
0 50 100 150 200 250

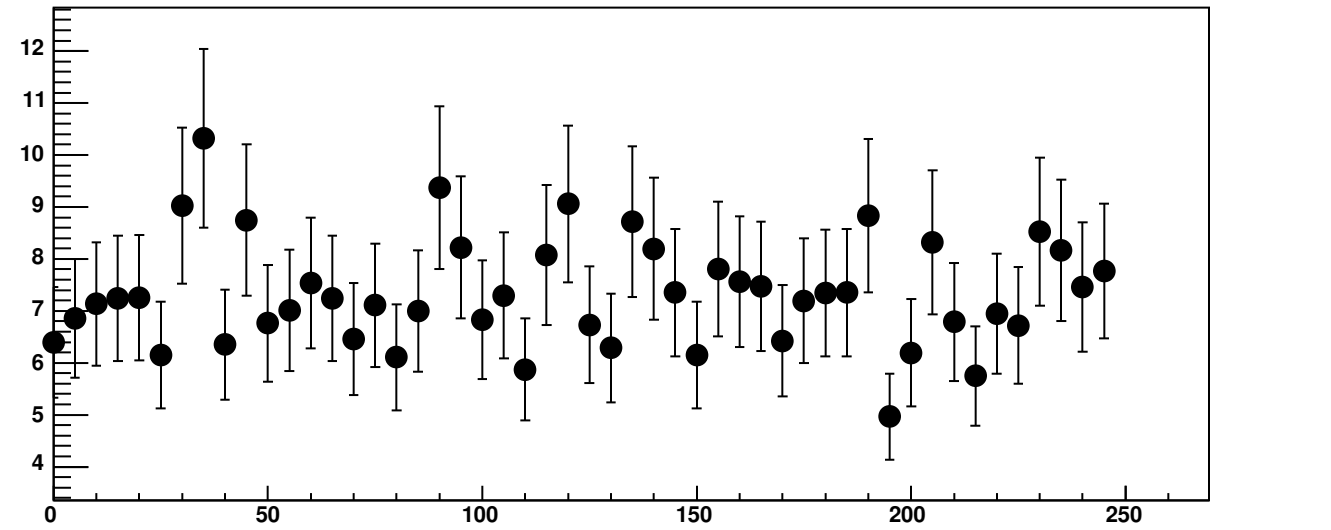

Chip 4, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold
0 20 40 60 80 100 120 140 160 180
0 50 100 150 200 250


Chip 4, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold
-3000 -2000 -1000 0 1000
0 50 100 150 200 250

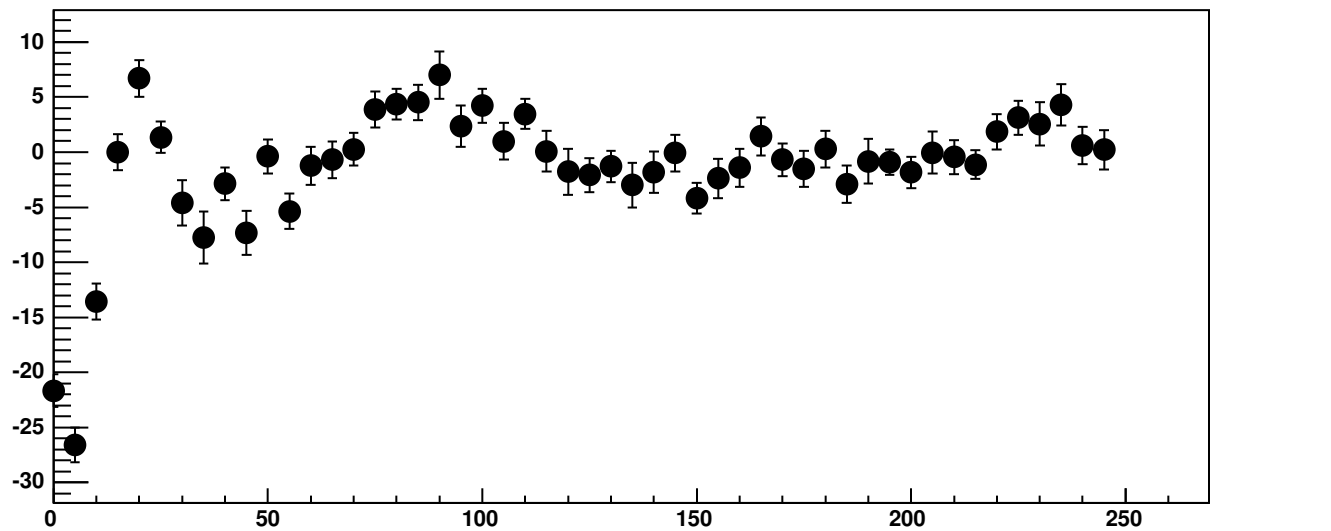
Chip 4, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold



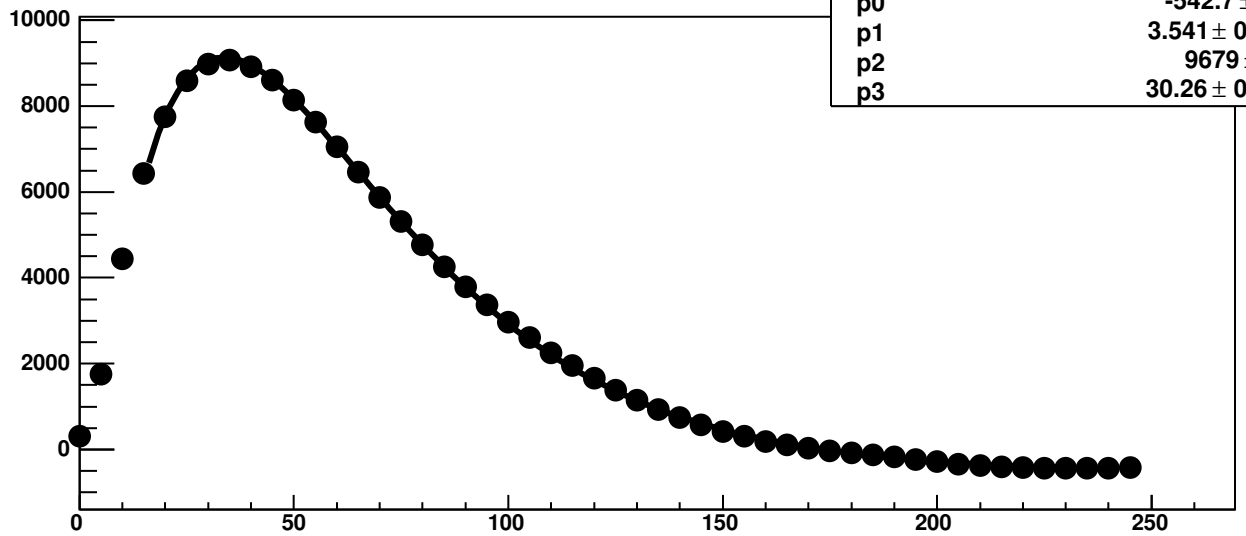
Chip 4, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



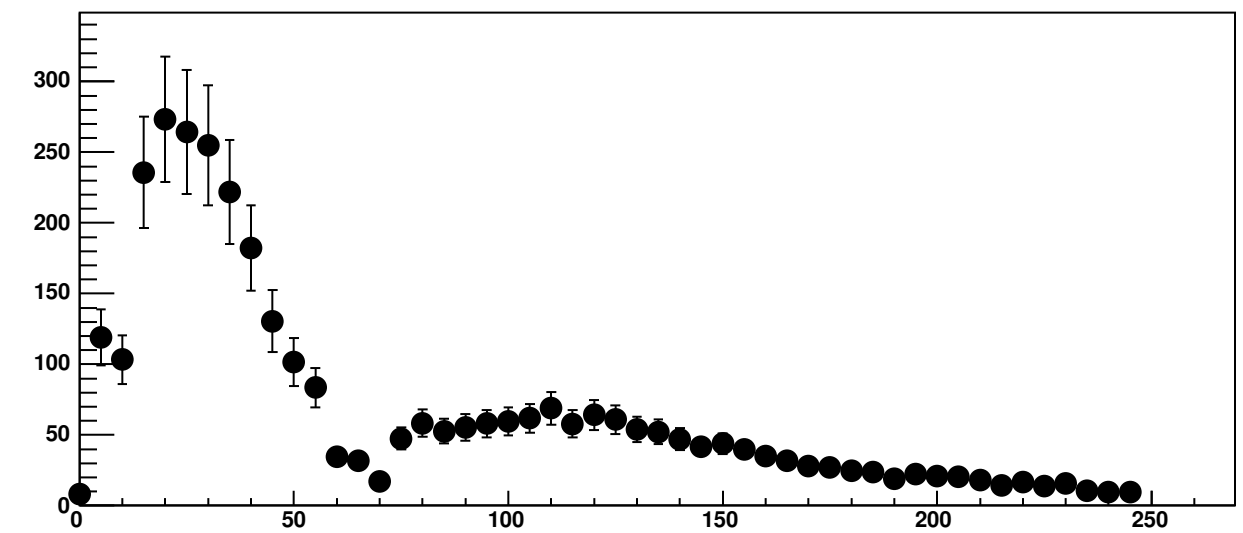
Chip 4, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold



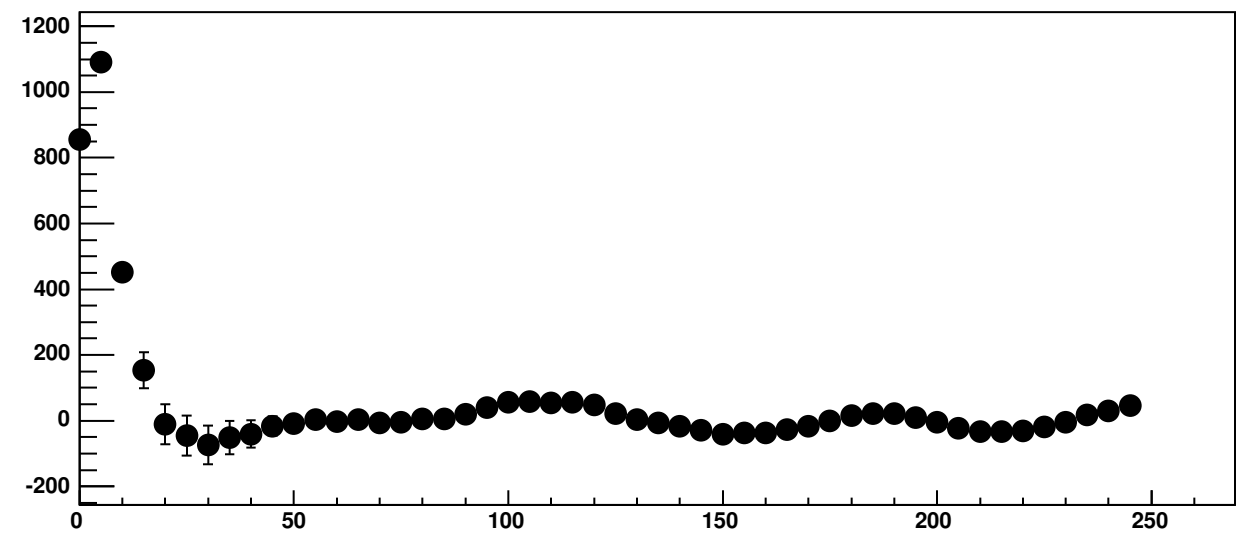
Chip 4, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold



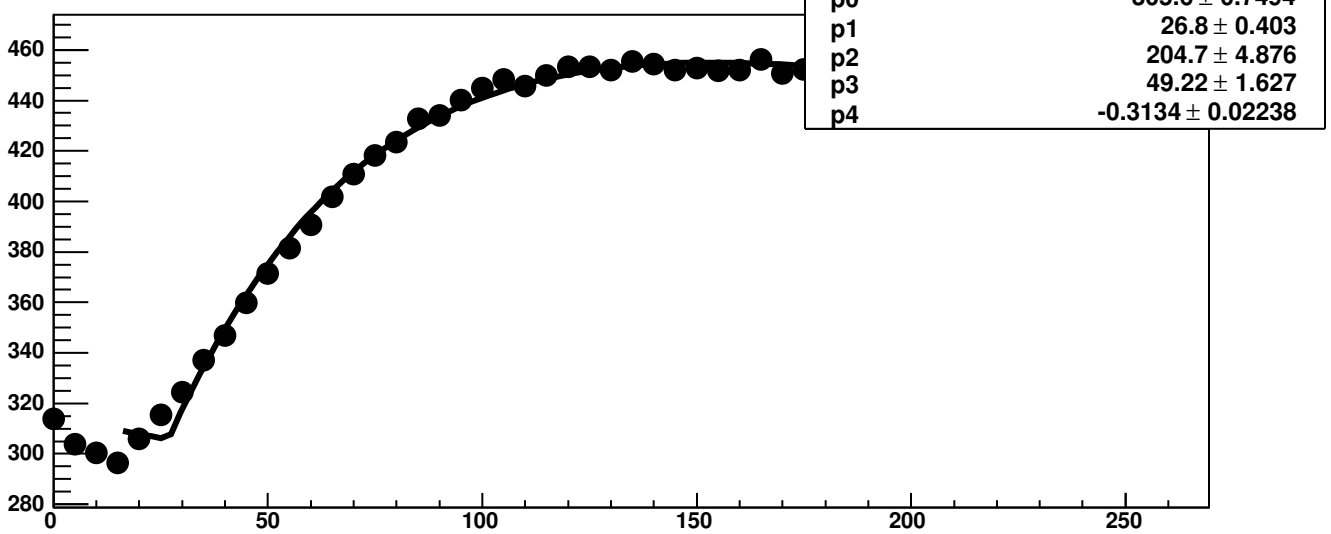
Chip 4, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold



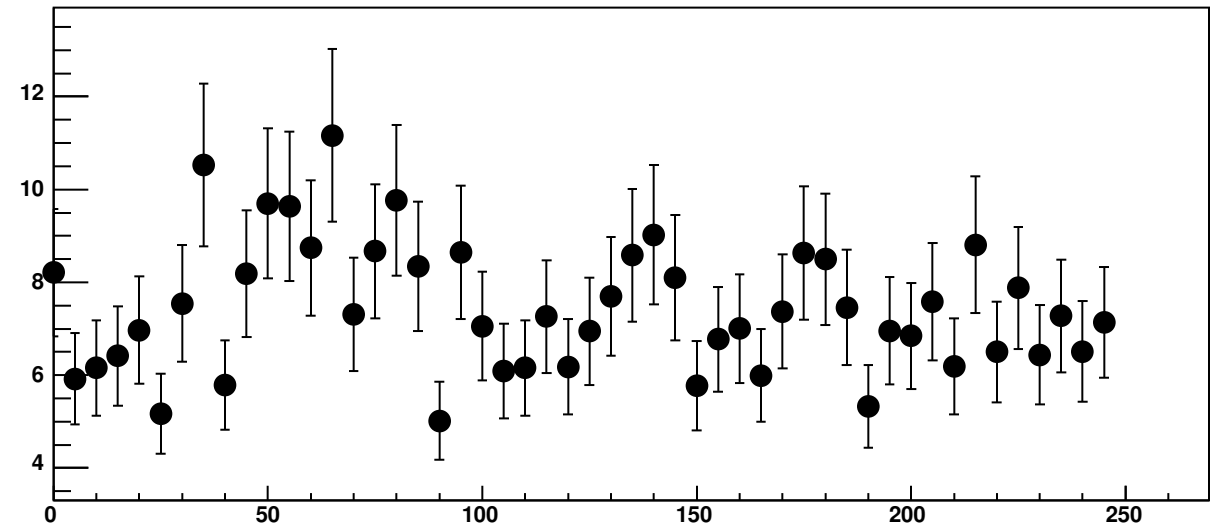
Chip 4, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold



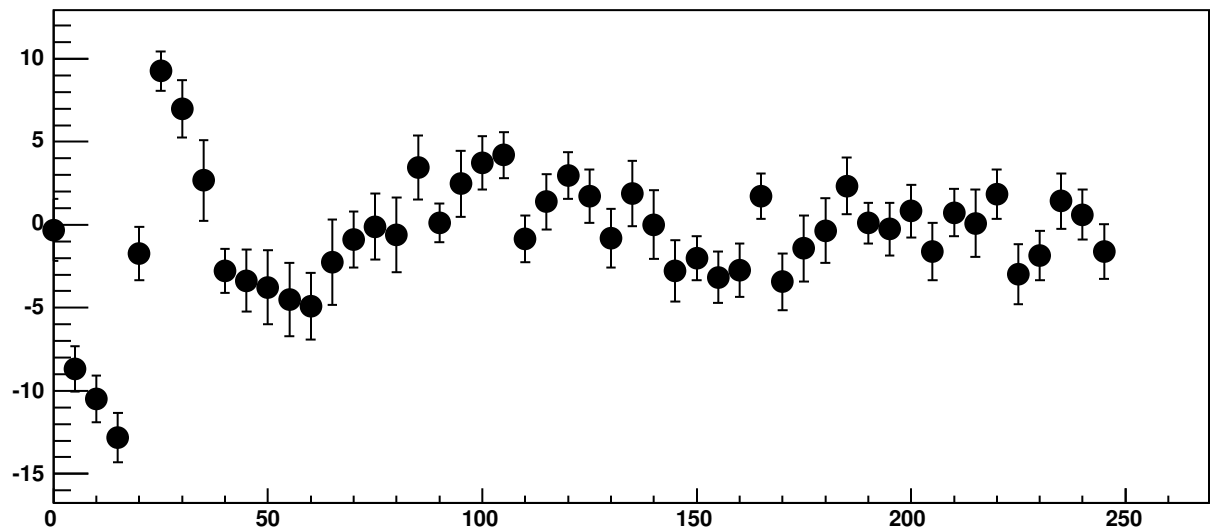
Chip 4, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

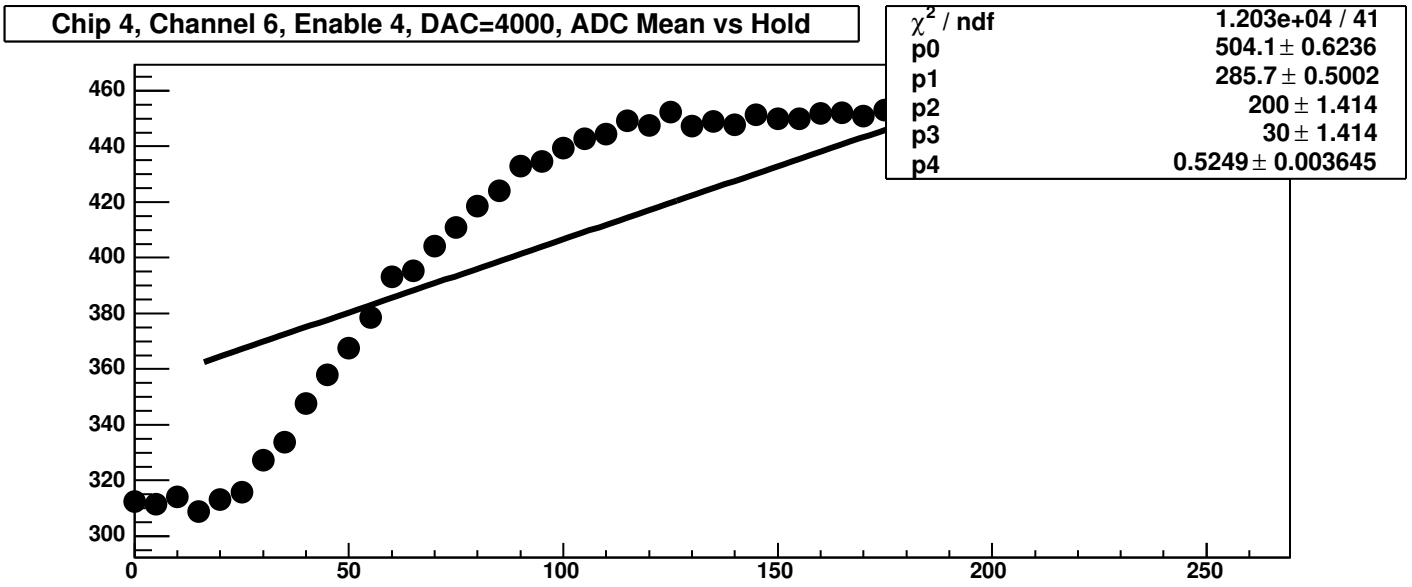


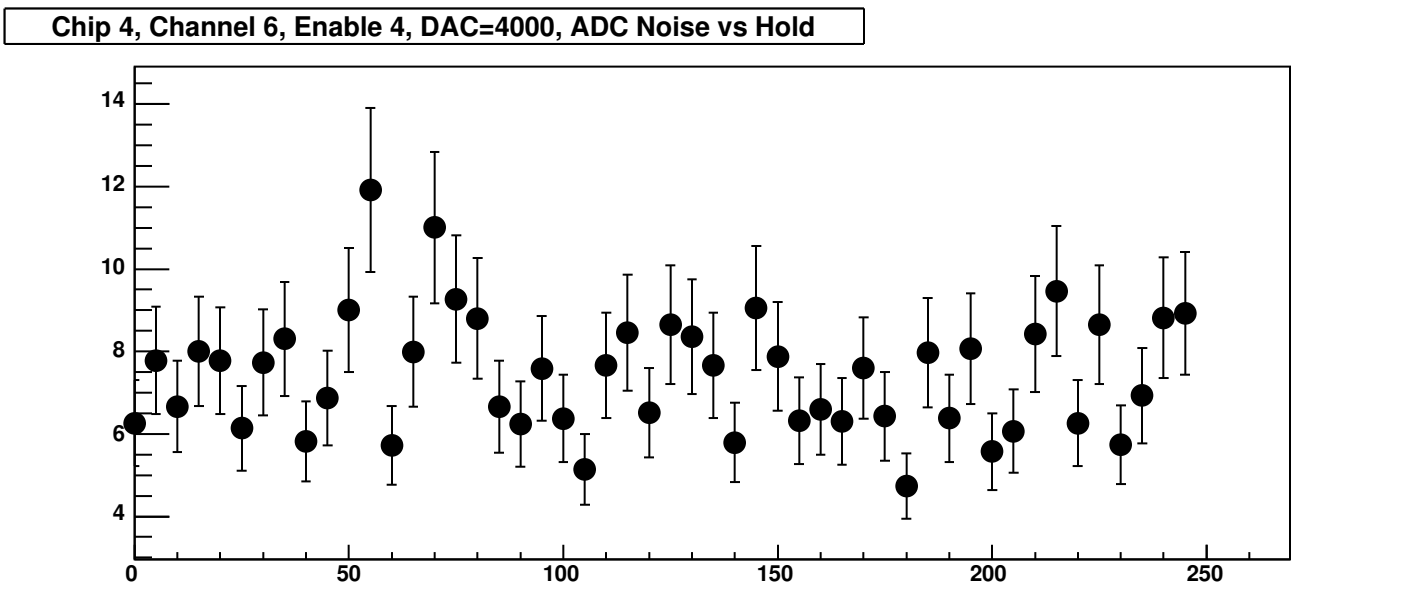
Chip 4, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold

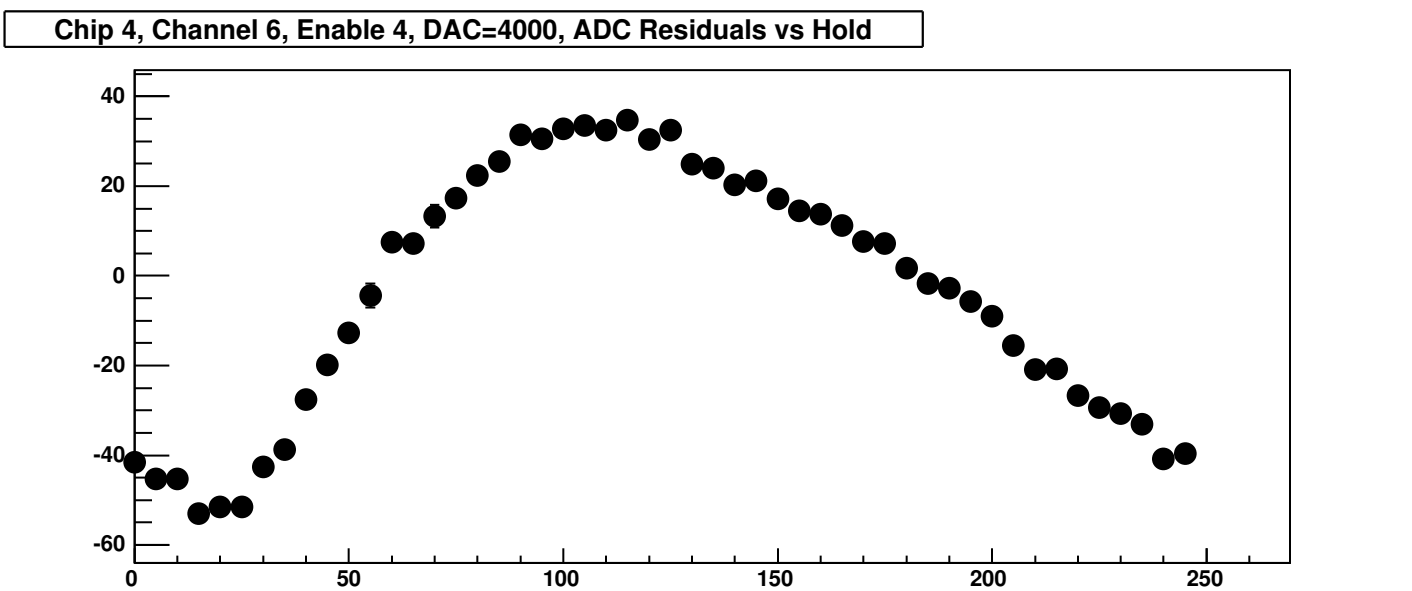


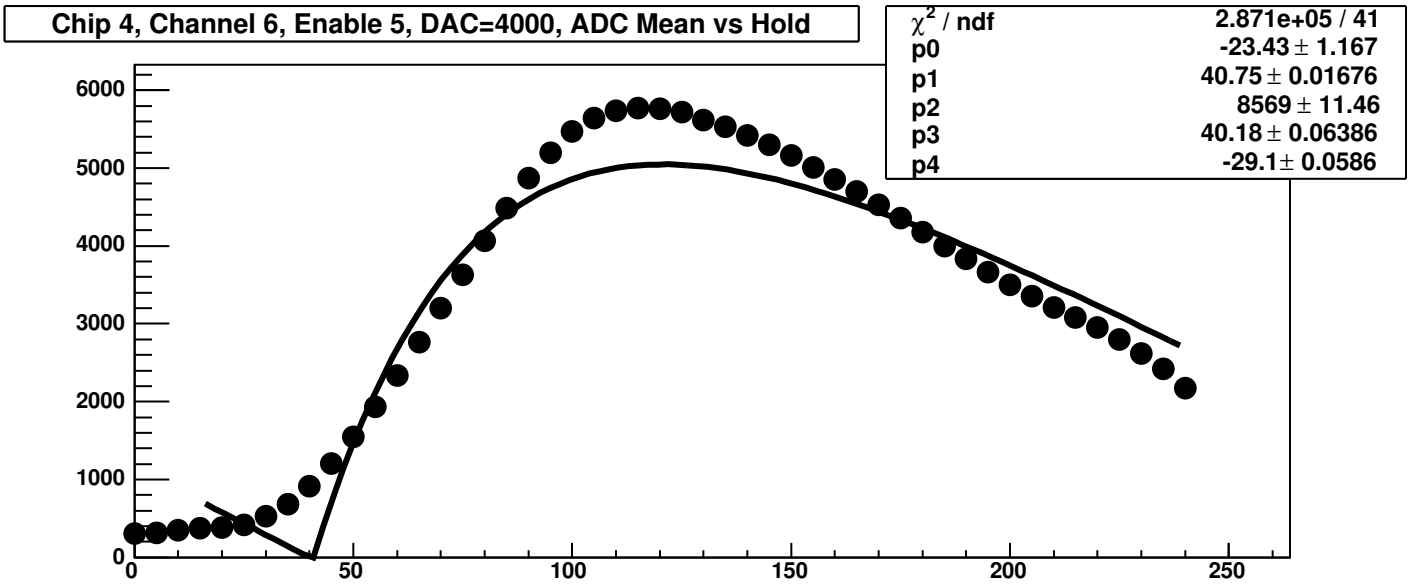
Chip 4, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

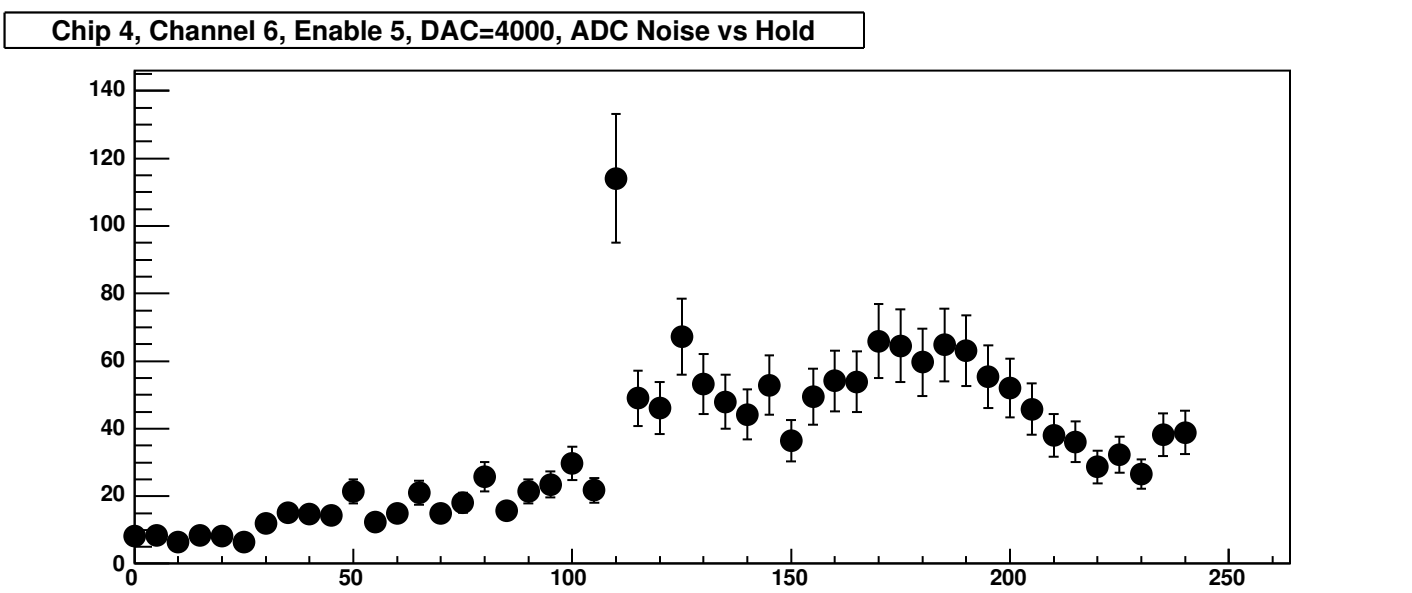


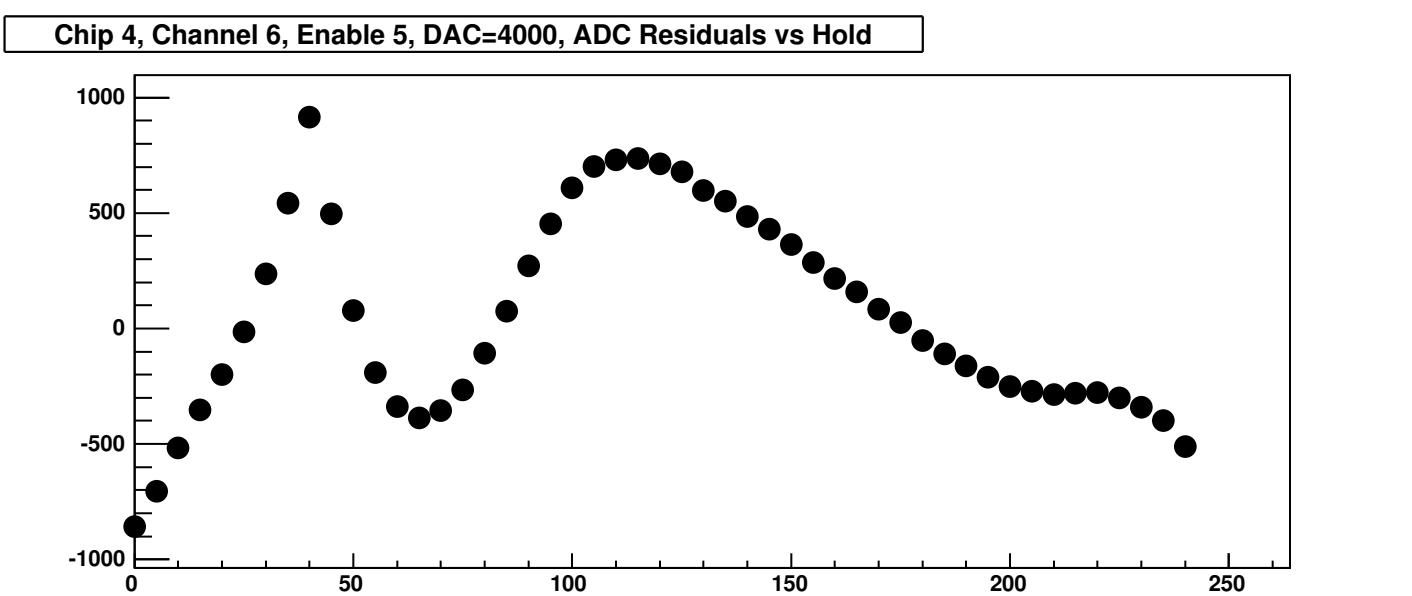

Chip 4, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.203e+04 / 41
p0 504.1 ± 0.6236
p1 285.7 ± 0.5002
p2 200 ± 1.414
p3 30 ± 1.414
p4 0.5249 ± 0.003645
460
440
420
400
380
360
340
320
300
0
50
100
150
200
250


Chip 4, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

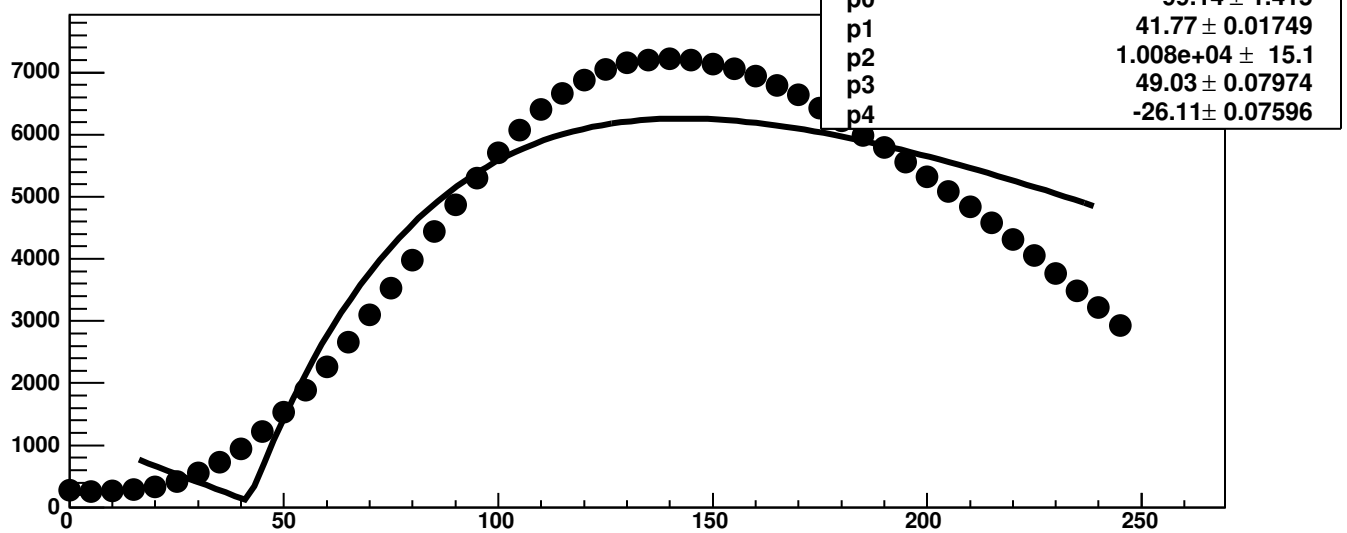

Chip 4, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
0
50
100
150
200
250


Chip 4, Channel 6, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.871e+05 / 41
p0 -23.43 ± 1.167
p1 40.75 ± 0.01676
p2 8569 ± 11.46
p3 40.18 ± 0.06386
p4 -29.1 ± 0.0586
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

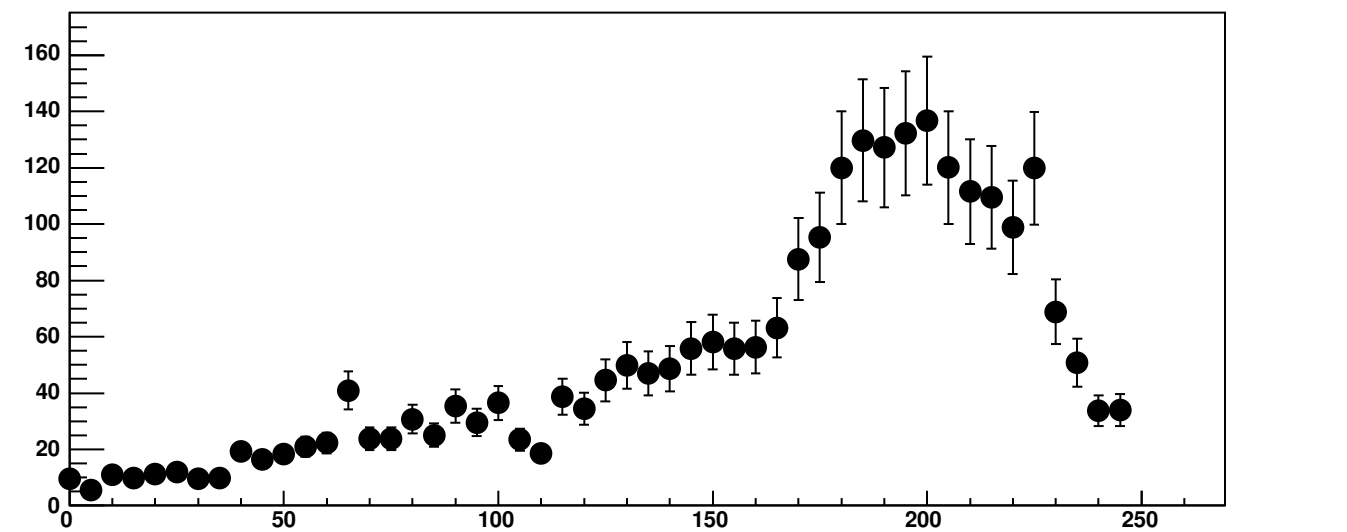

Chip 4, Channel 6, Enable 5, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 4, Channel 6, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
0
50
100
150
200
250

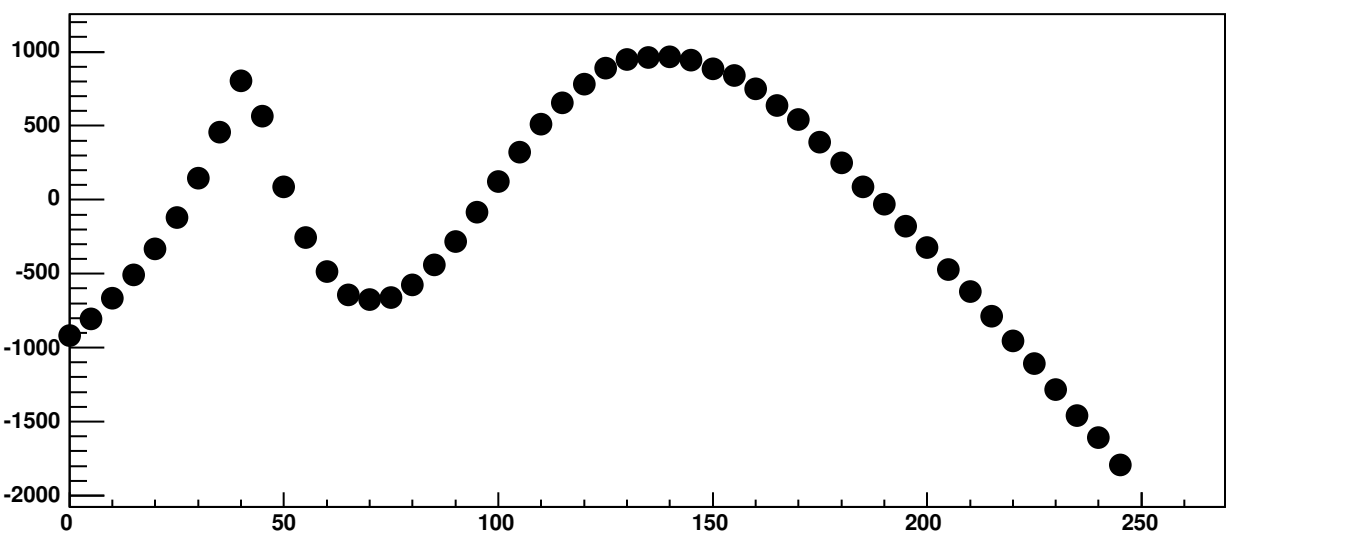
Chip 4, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

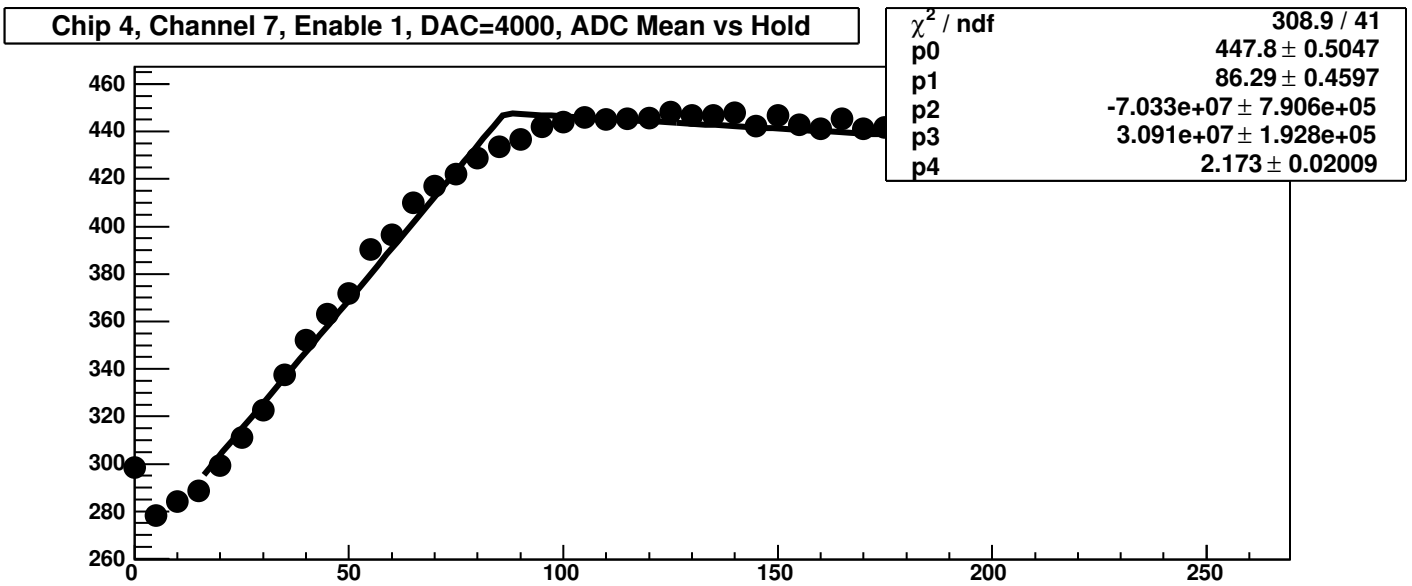


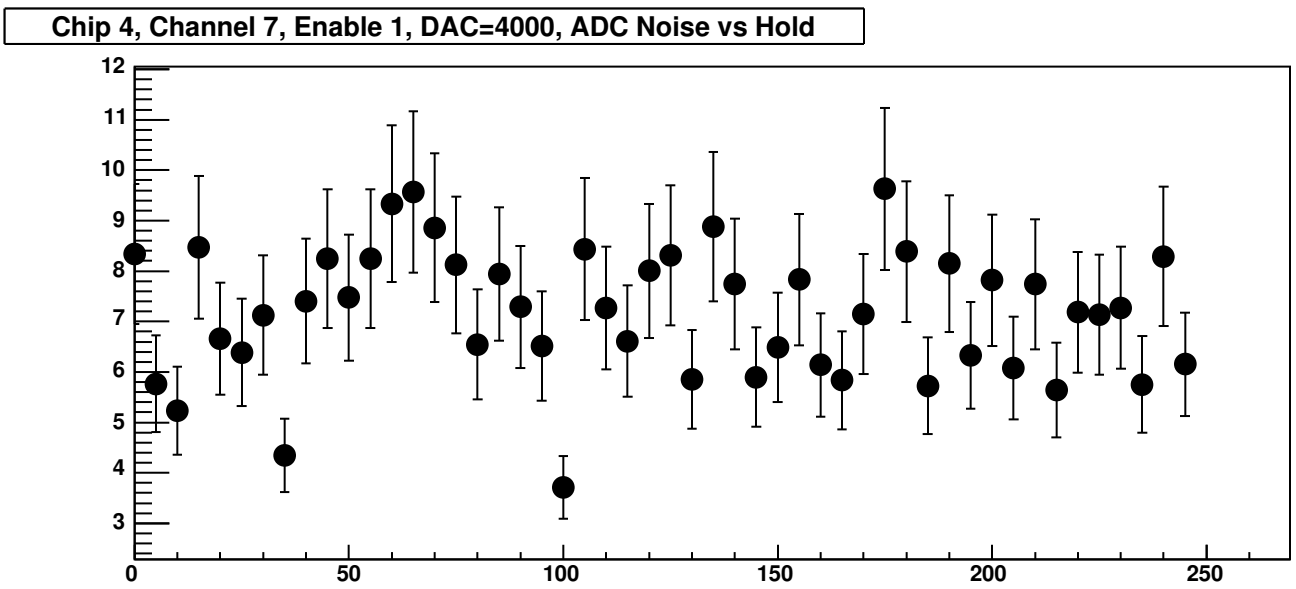
Chip 4, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold

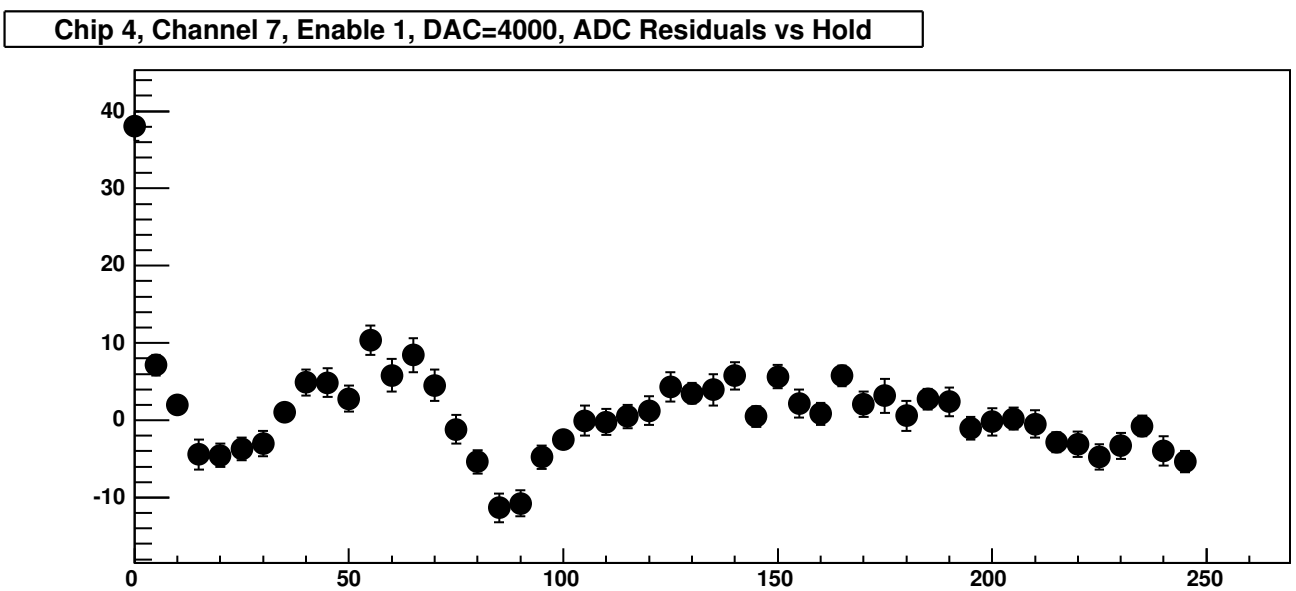


Chip 4, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

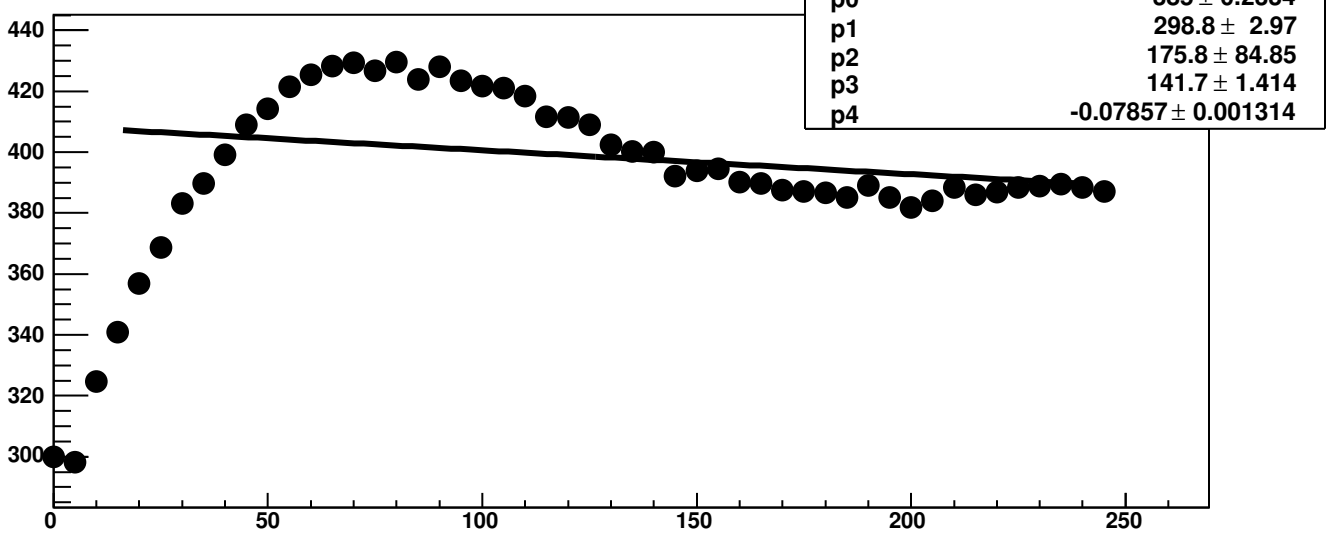



Chip 4, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 308.9 / 41
p0 447.8 ± 0.5047
p1 86.29 ± 0.4597
p2 -7.033e+07 ± 7.906e+05
p3 3.091e+07 ± 1.928e+05
p4 2.173 ± 0.02009
460
440
420
400
380
360
340
320
300
280
260
0
50
100
150
200
250

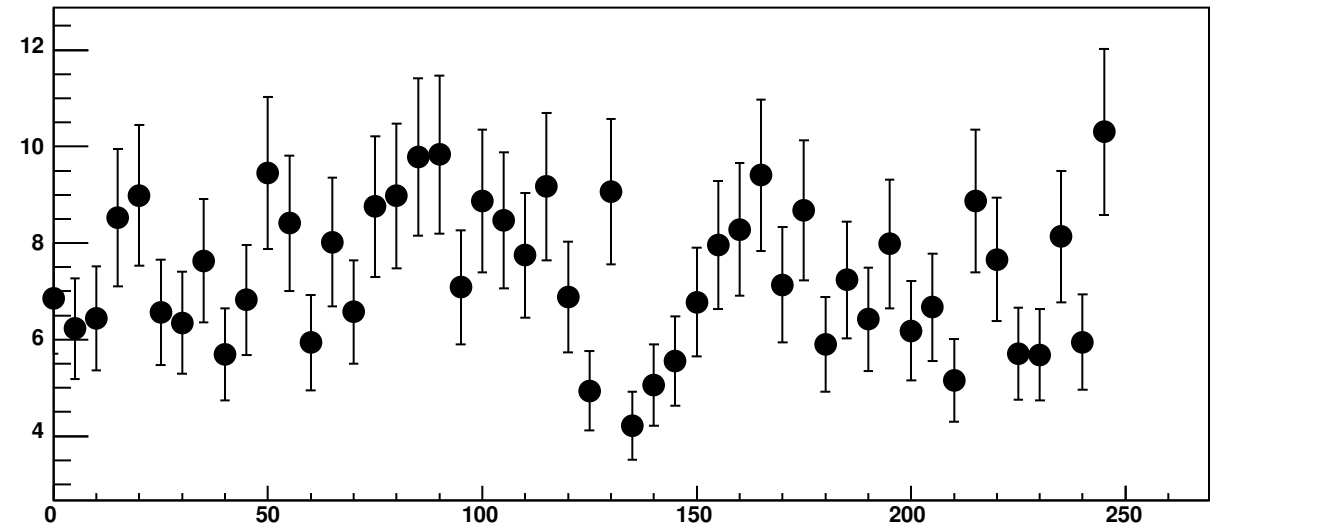

Chip 4, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 4, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

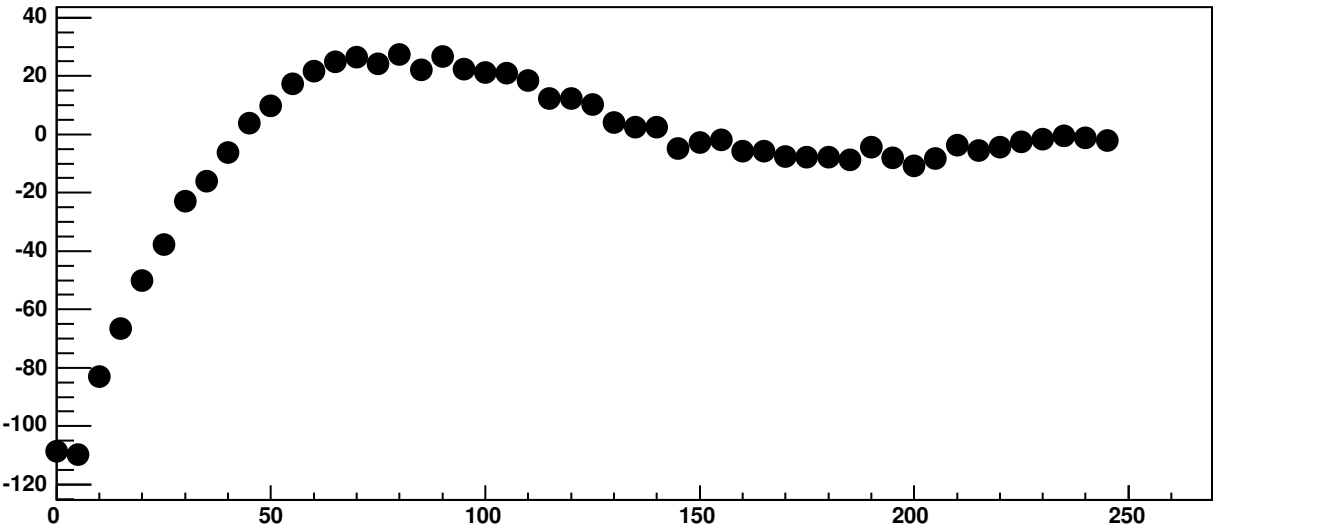
Chip 4, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold



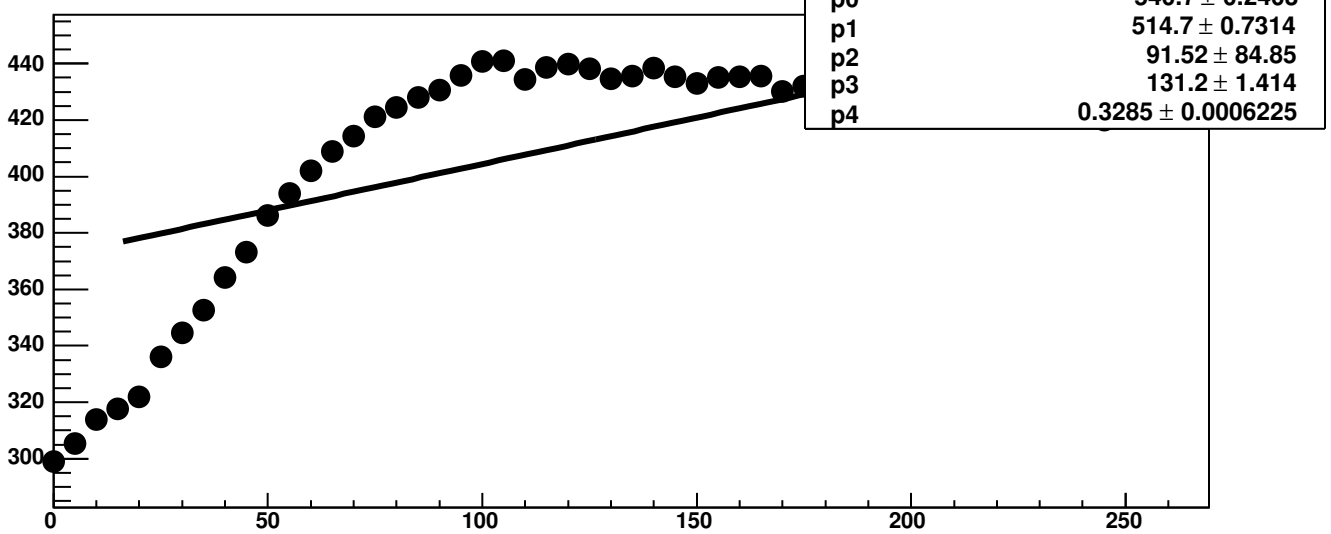
Chip 4, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold



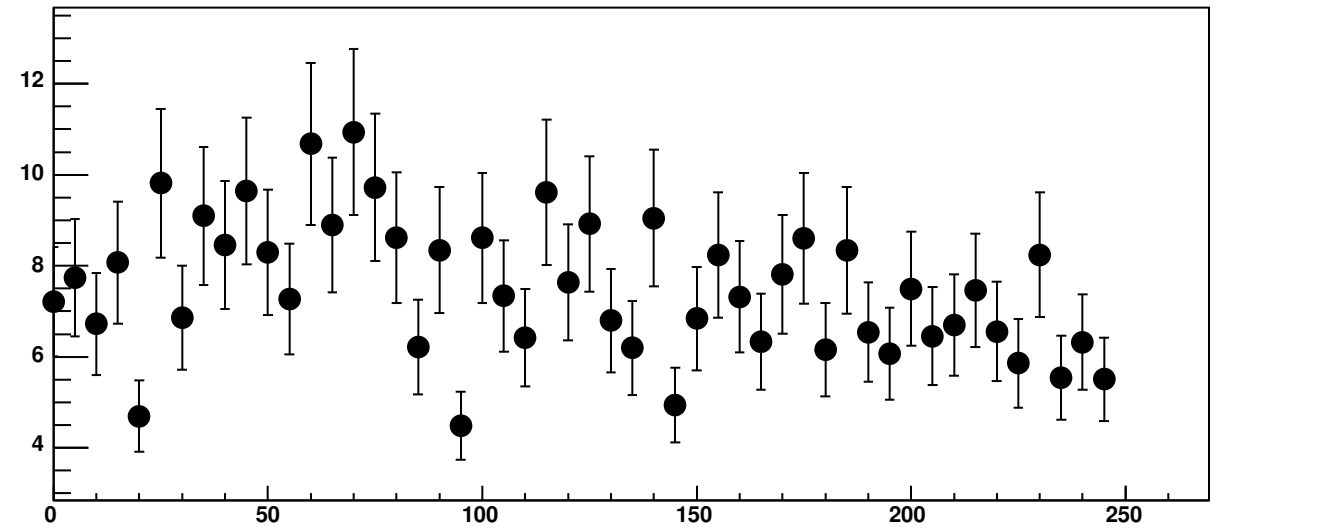
Chip 4, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold



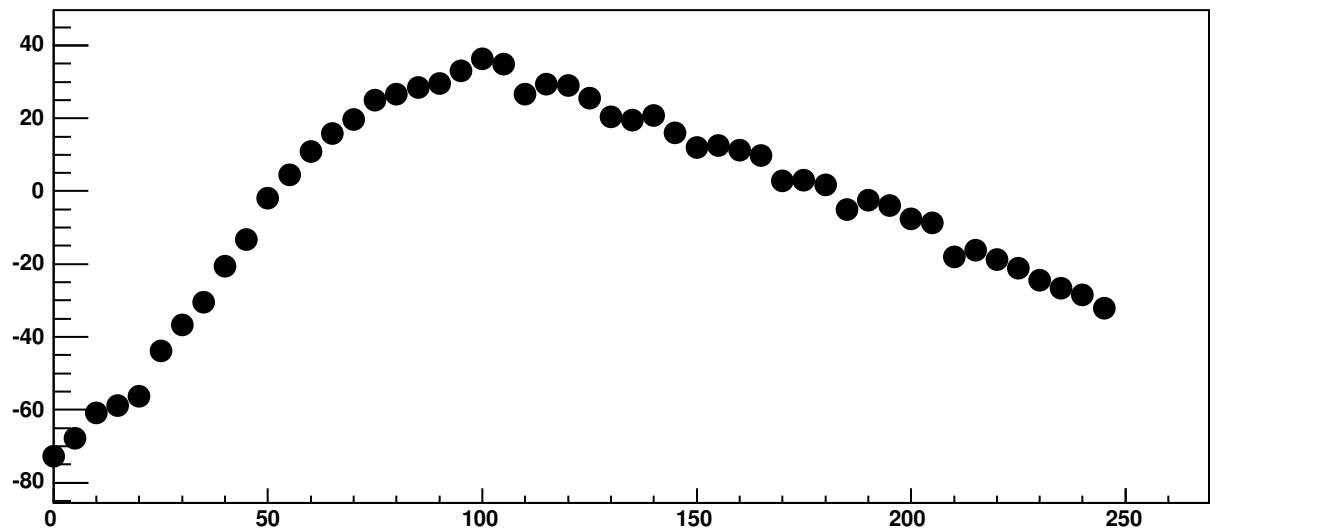
Chip 4, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

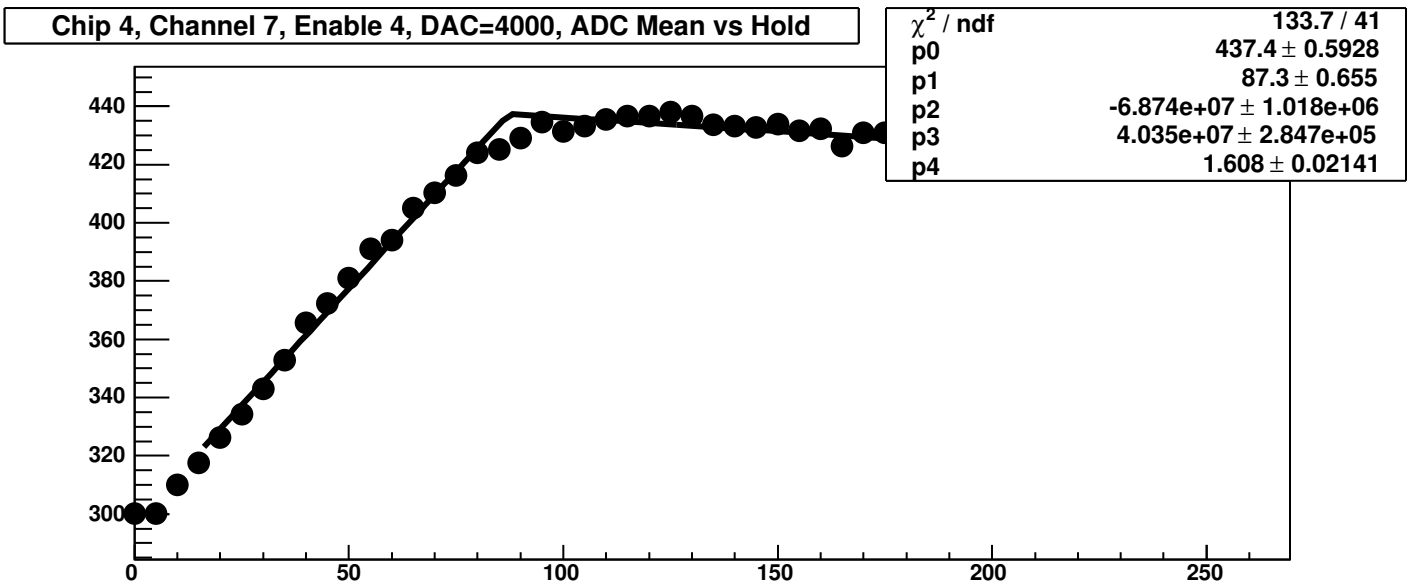


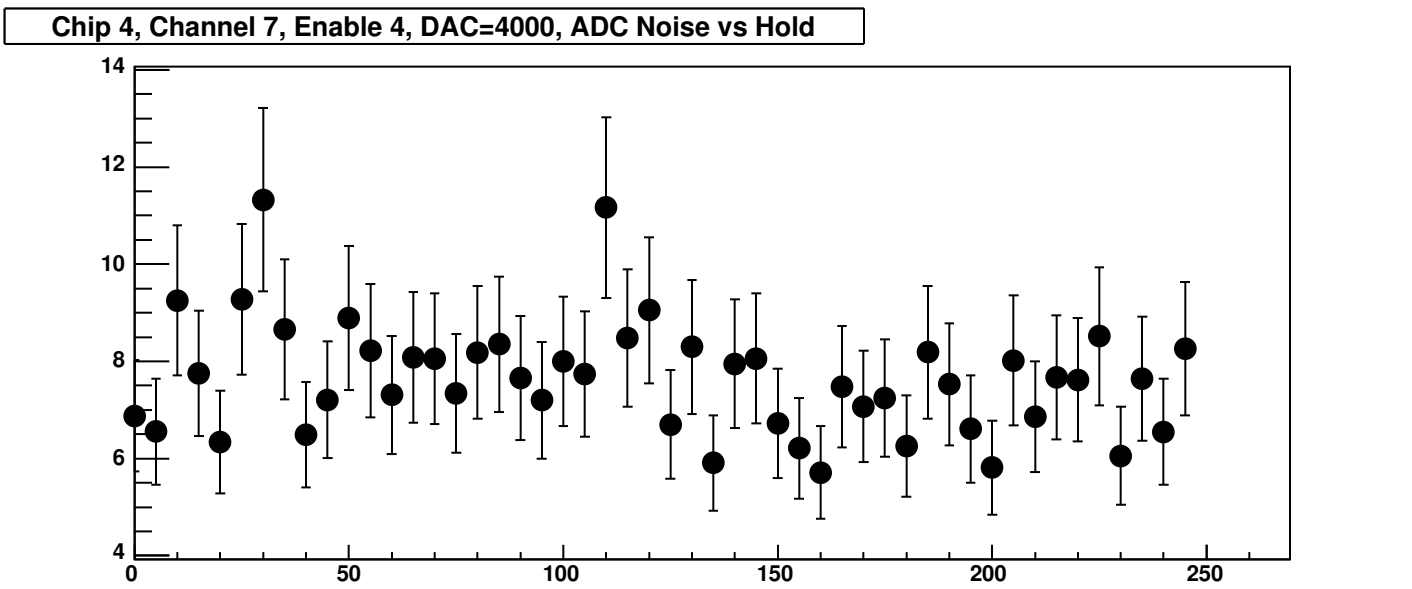
Chip 4, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold

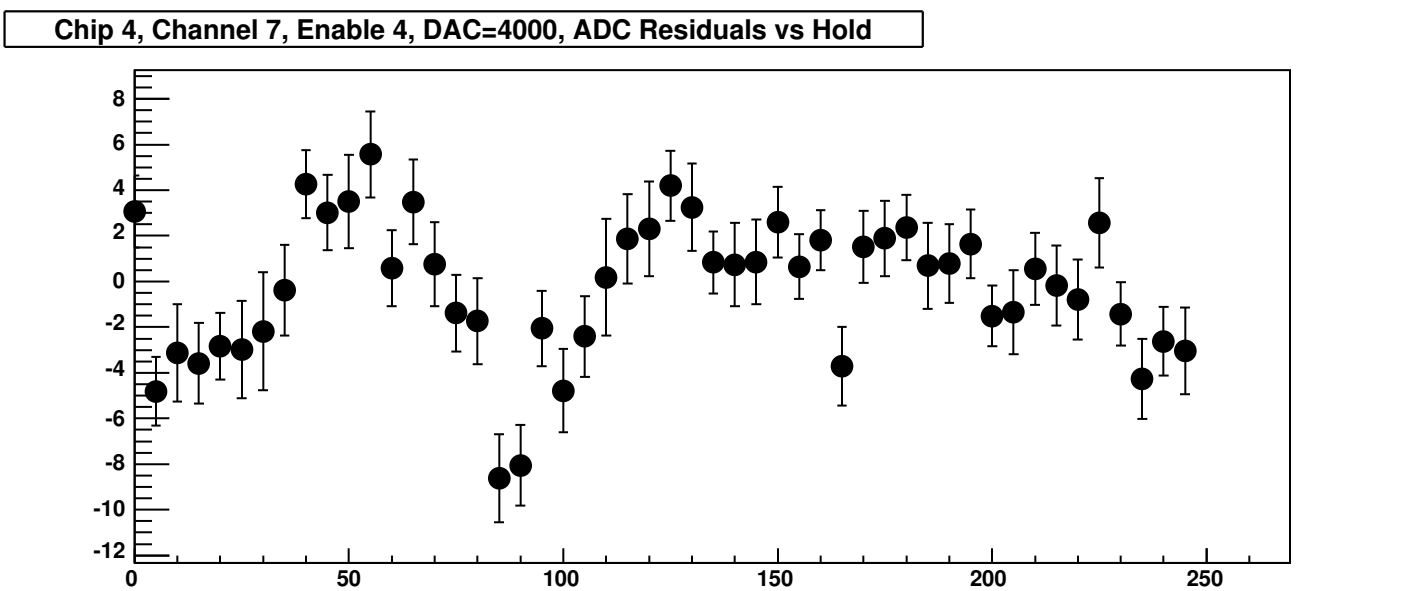


Chip 4, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

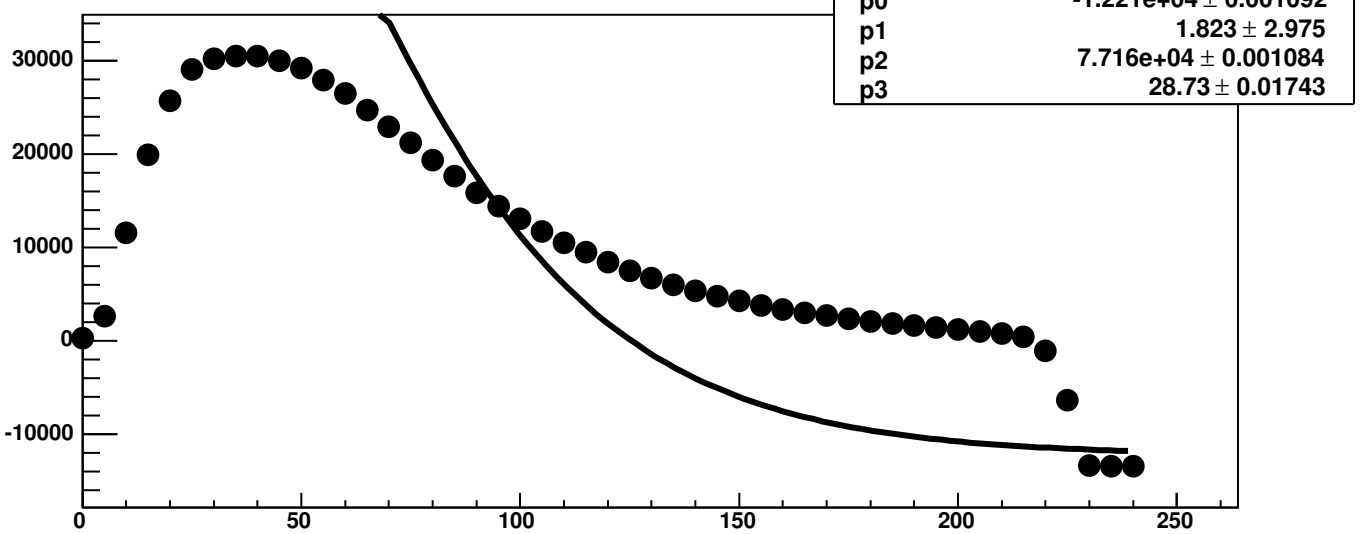



Chip 4, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 133.7 / 41
p0 437.4 ± 0.5928
p1 87.3 ± 0.655
p2 -6.874e+07 ± 1.018e+06
p3 4.035e+07 ± 2.847e+05
p4 1.608 ± 0.02141
440
420
400
380
360
340
320
300
0
50
100
150
200
250

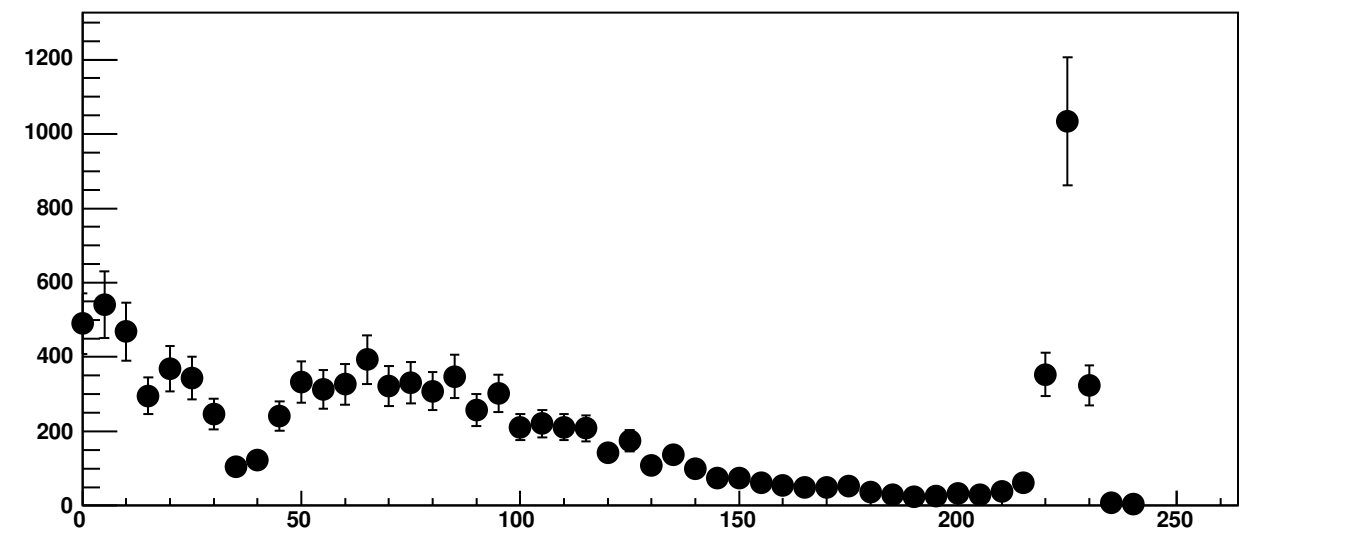

Chip 4, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 4, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
-12
0
50
100
150
200
250

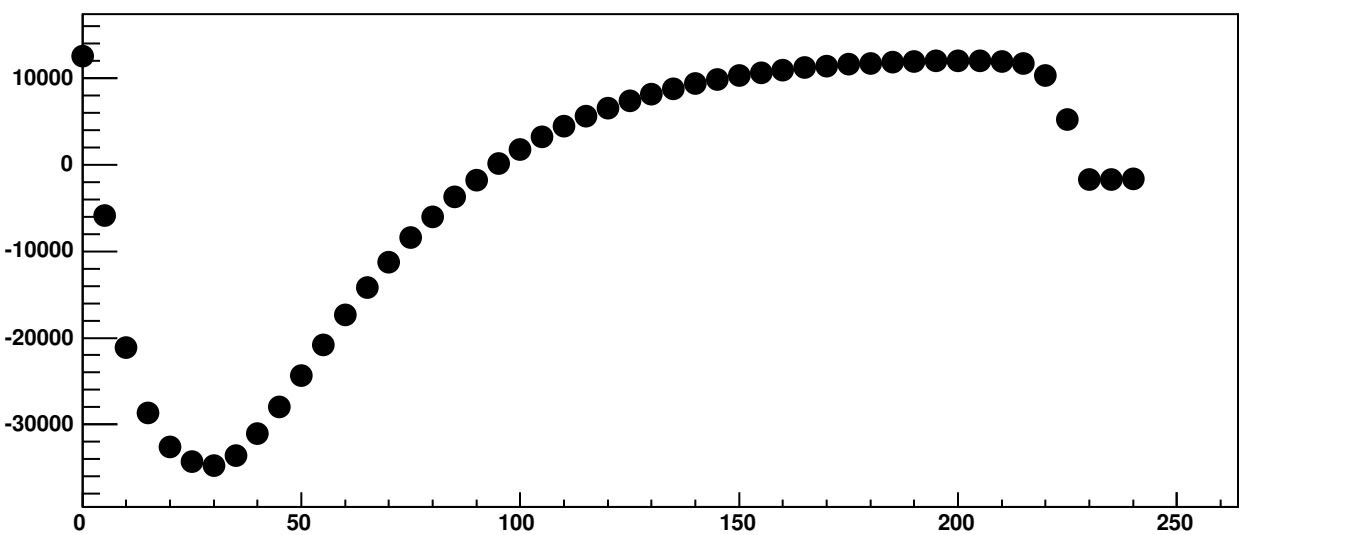
Chip 4, Channel 7, Enable 5!, DAC=4000, ADC Mean vs Hold



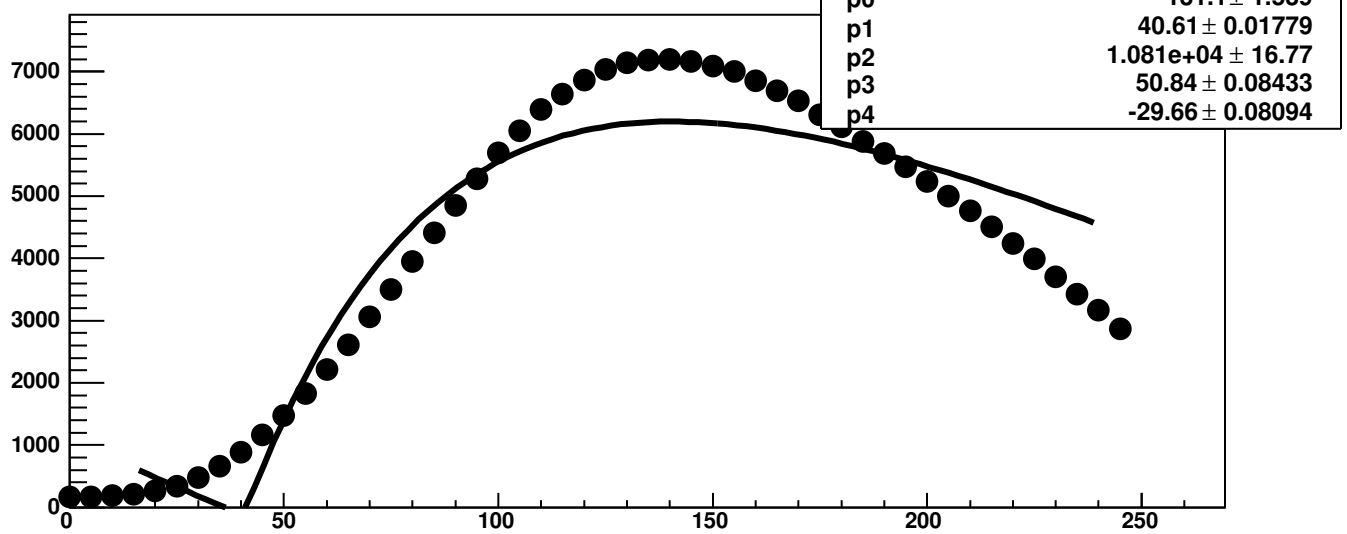
Chip 4, Channel 7, Enable 5!, DAC=4000, ADC Noise vs Hold



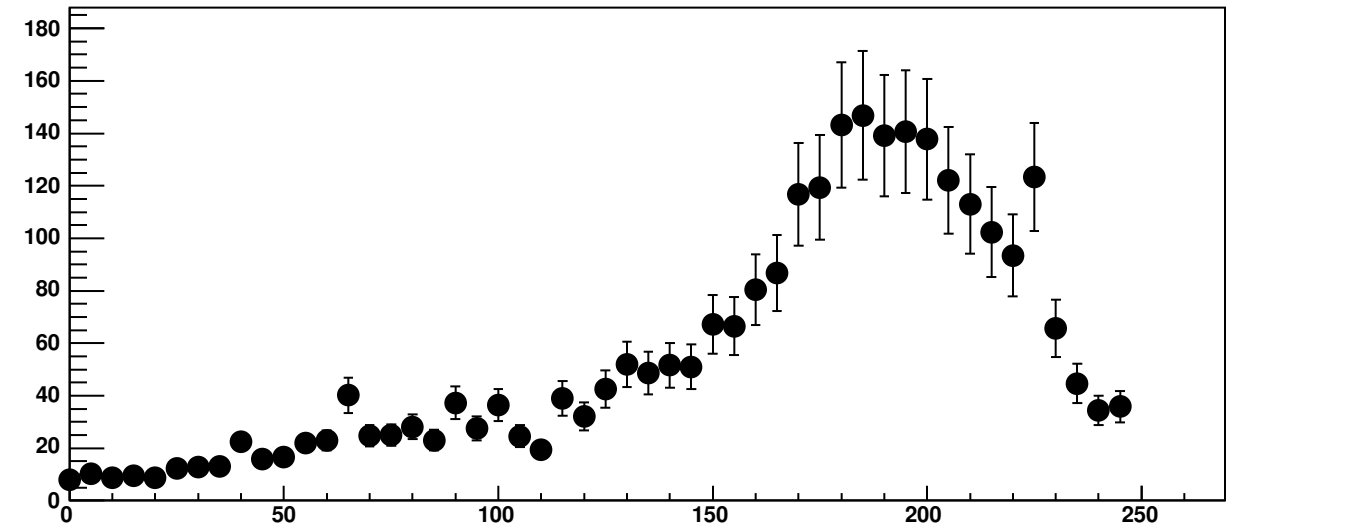
Chip 4, Channel 7, Enable 5!, DAC=4000, ADC Residuals vs Hold



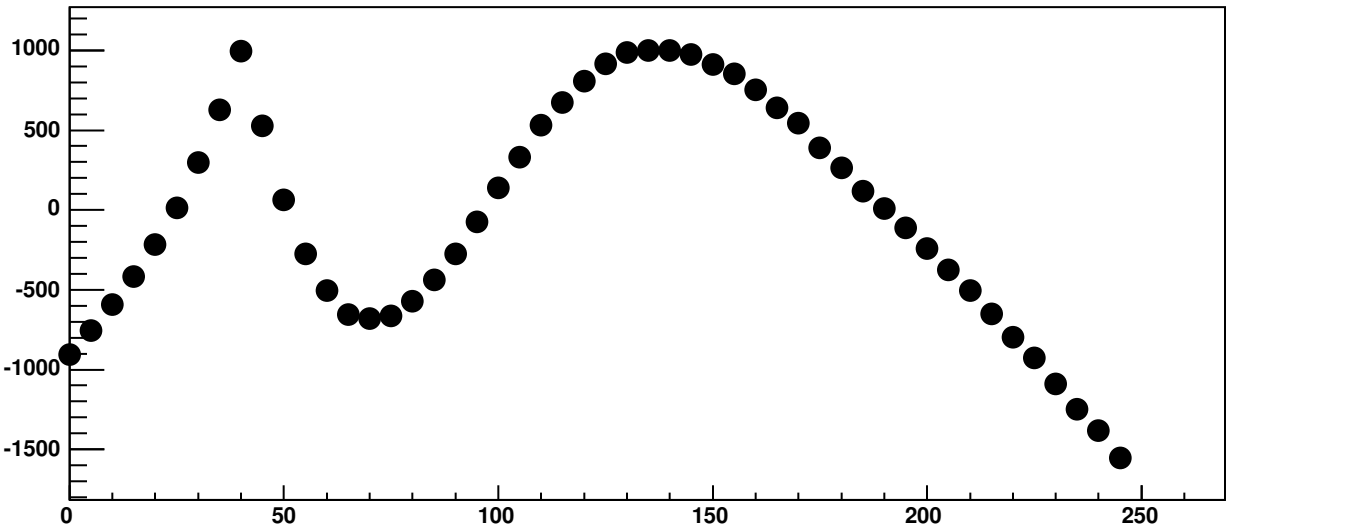
Chip 4, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

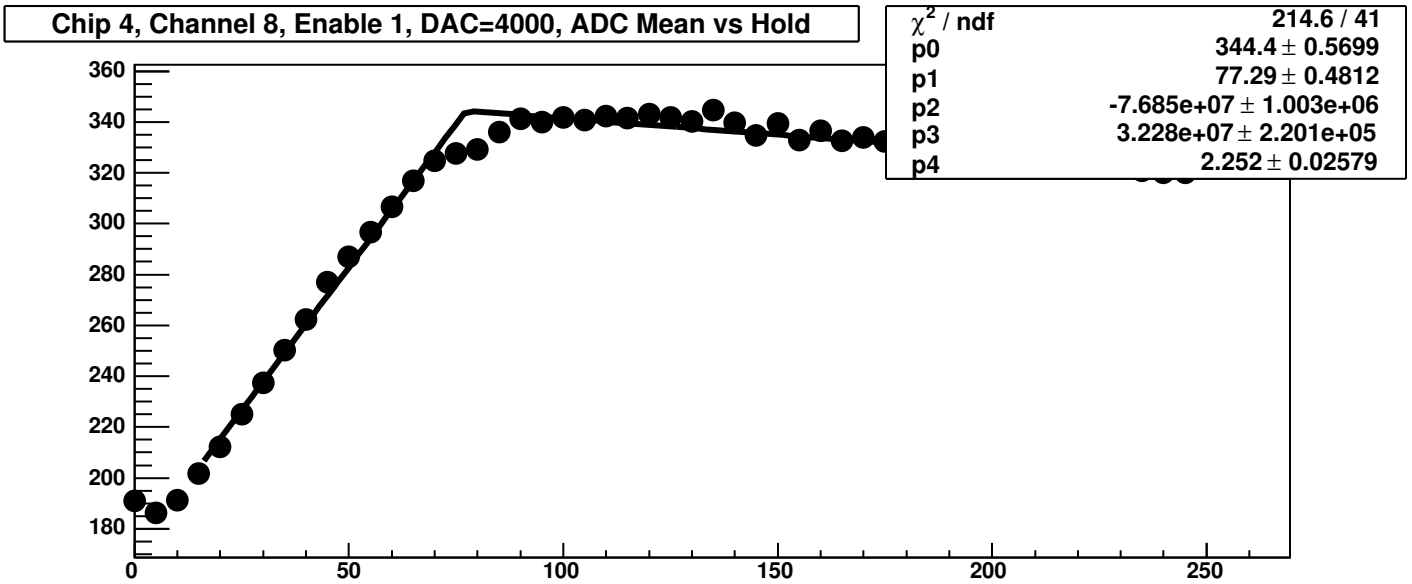


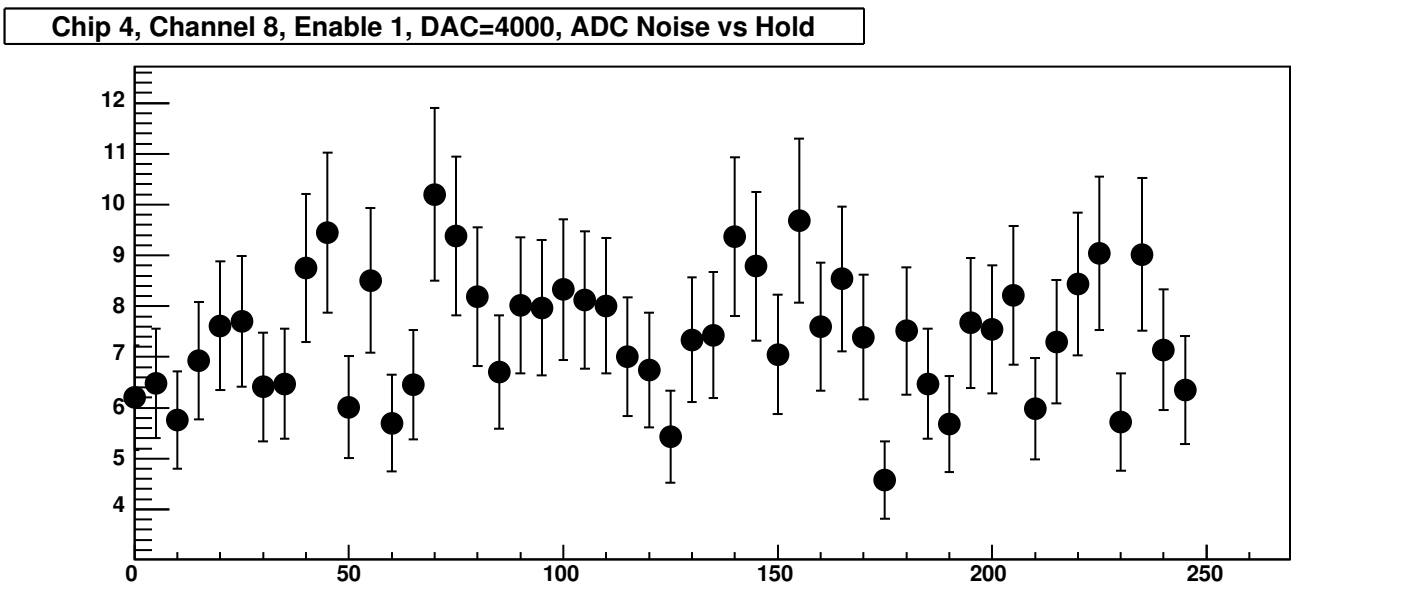
Chip 4, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold

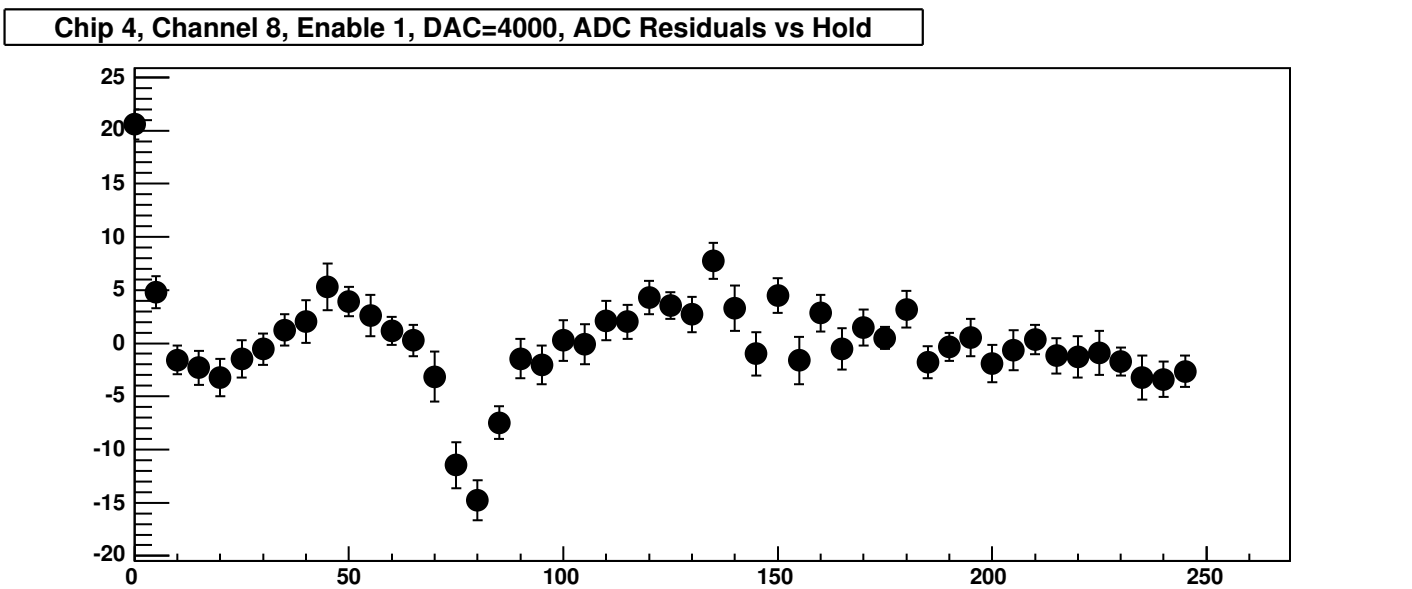


Chip 4, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

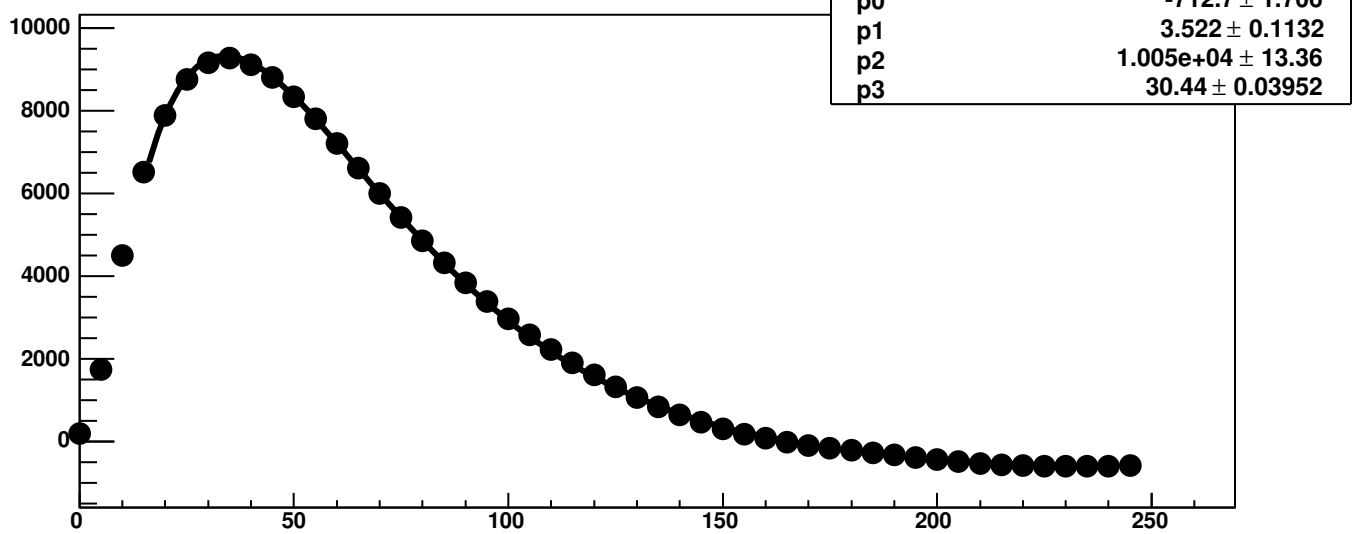



Chip 4, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 214.6 / 41
p0 344.4 ± 0.5699
p1 77.29 ± 0.4812
p2 -7.685e+07 ± 1.003e+06
p3 3.228e+07 ± 2.201e+05
p4 2.252 ± 0.02579
360
340
320
300
280
260
240
220
200
180
0
50
100
150
200
250

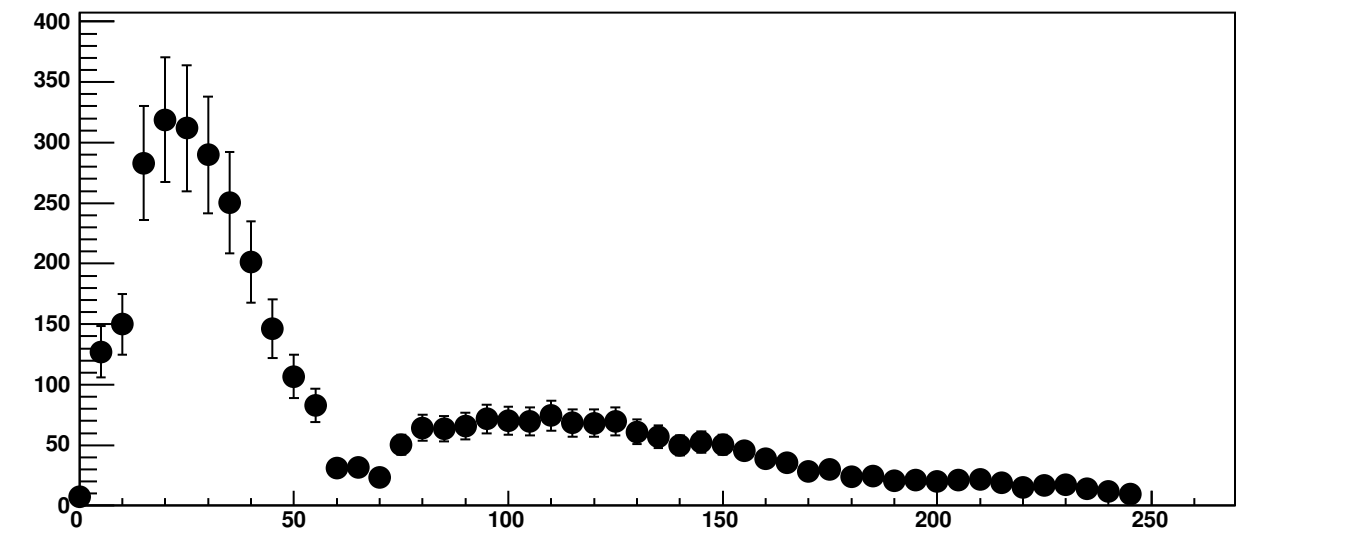

Chip 4, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 4, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
-15
-20
0
50
100
150
200
250

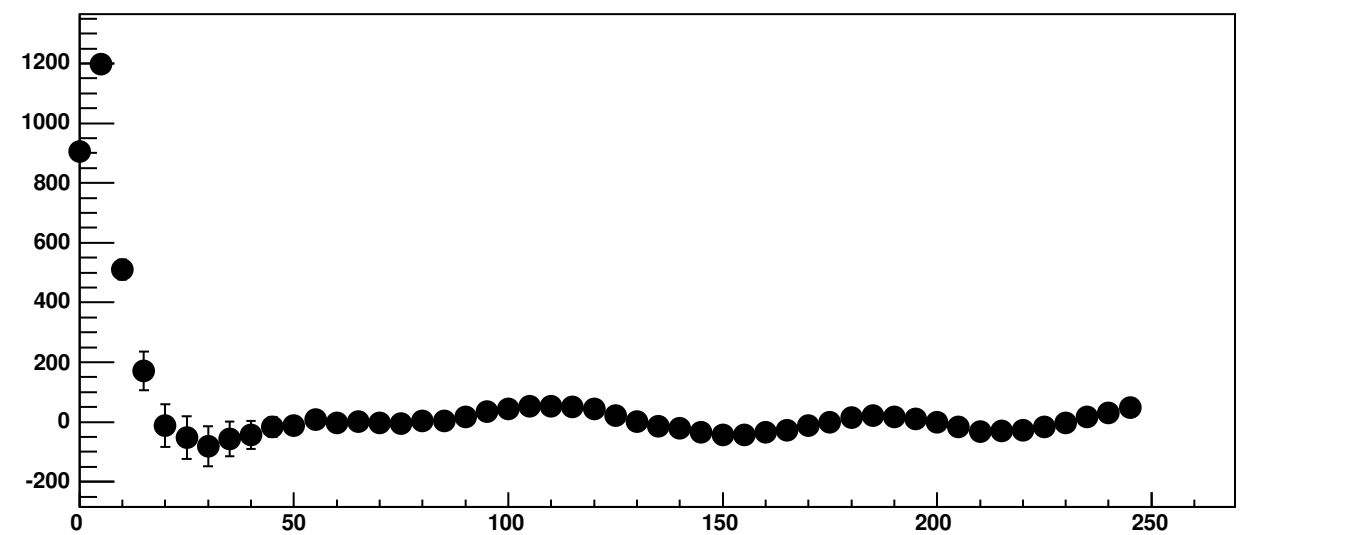
Chip 4, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold



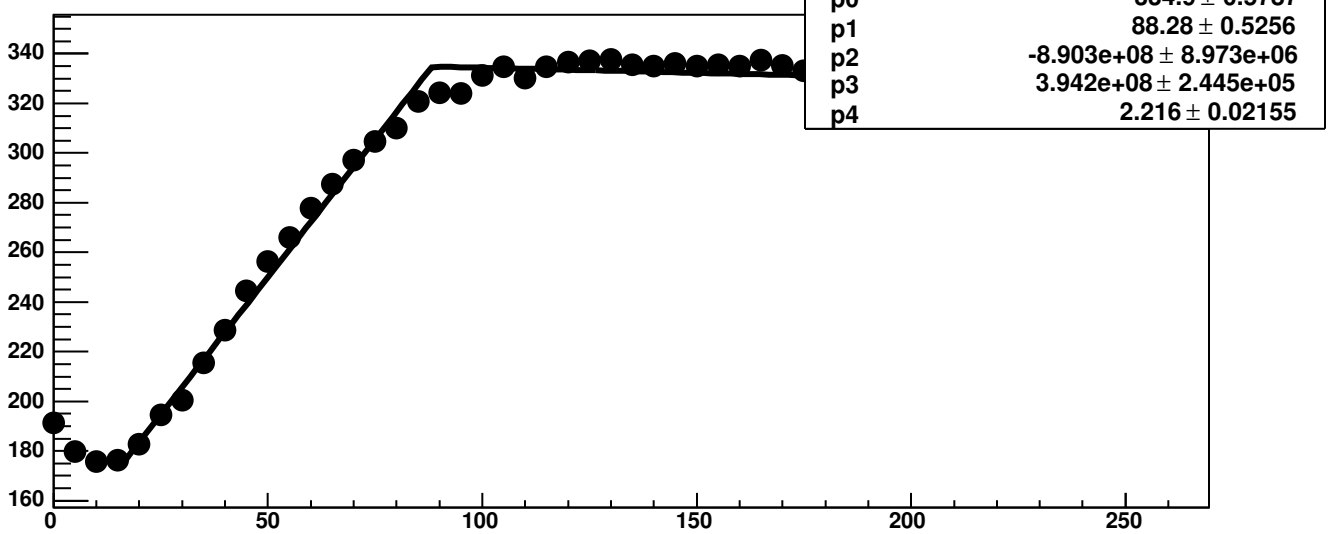
Chip 4, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold



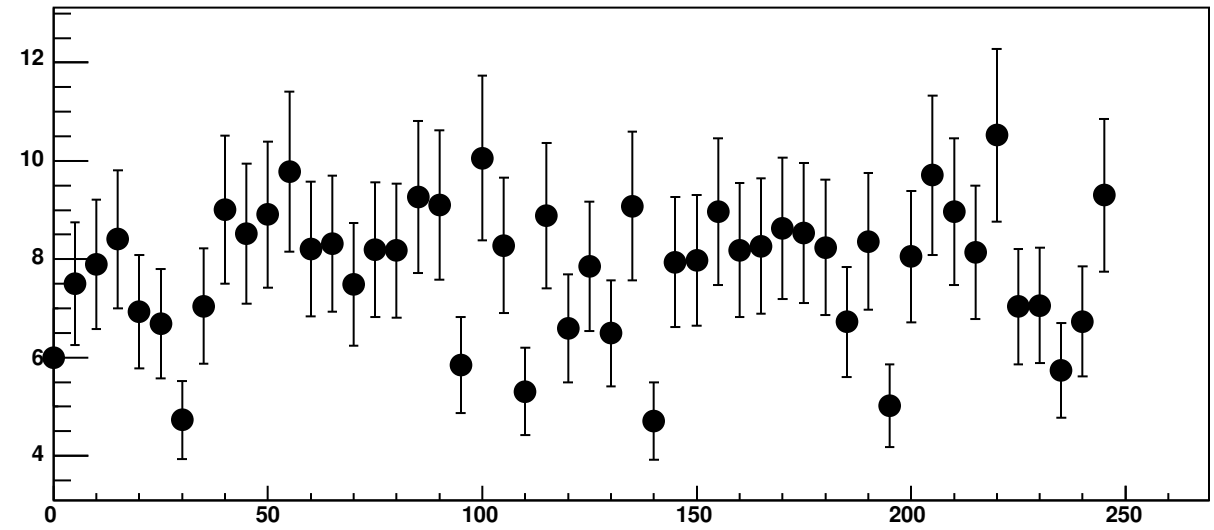
Chip 4, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold



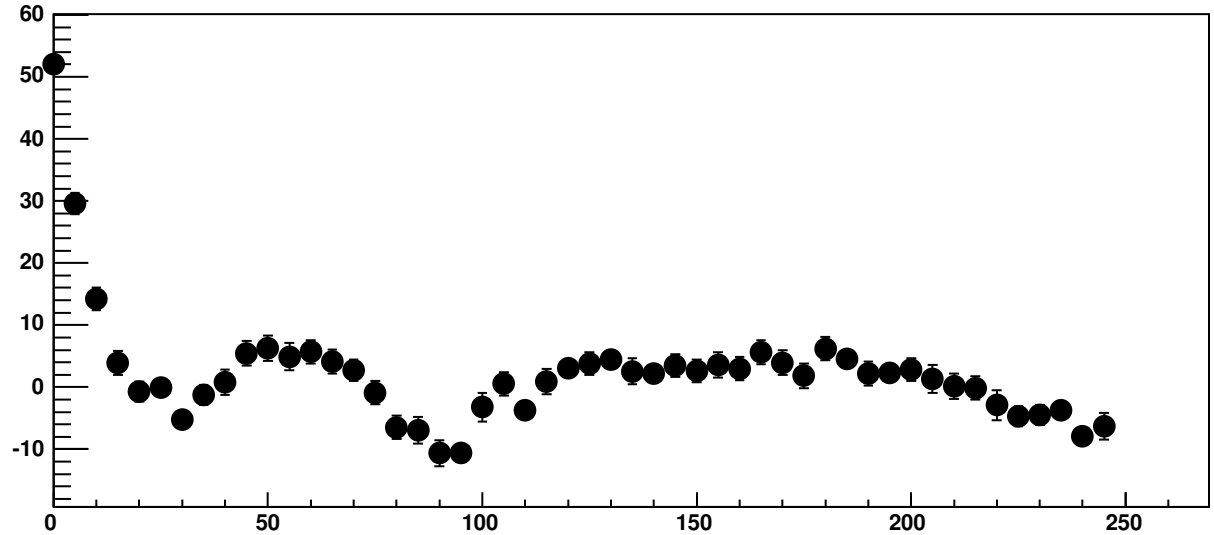
Chip 4, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold



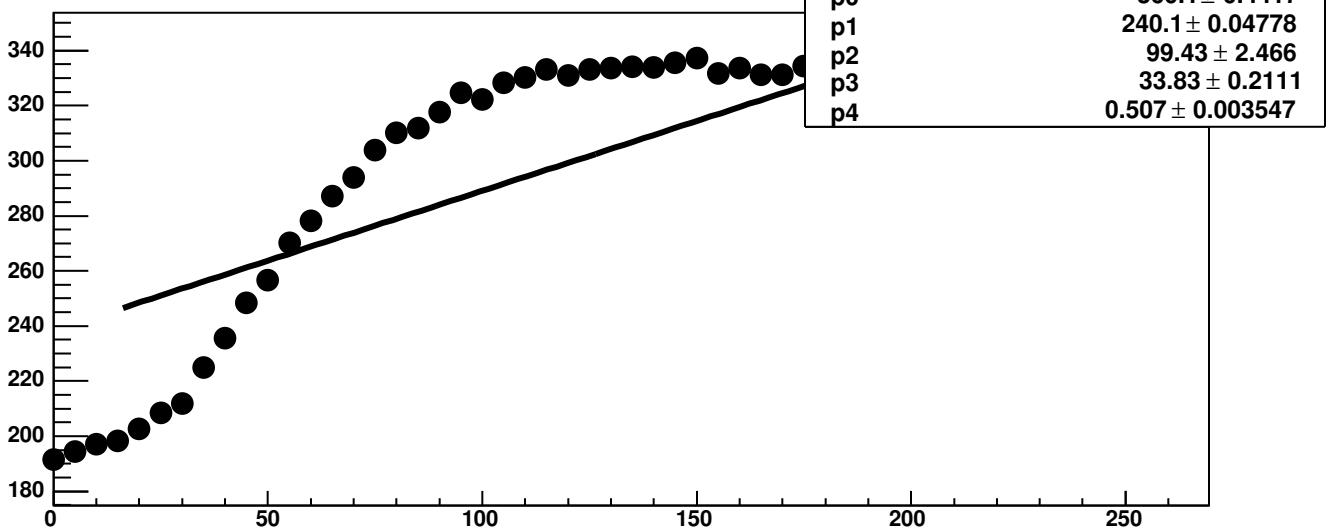
Chip 4, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



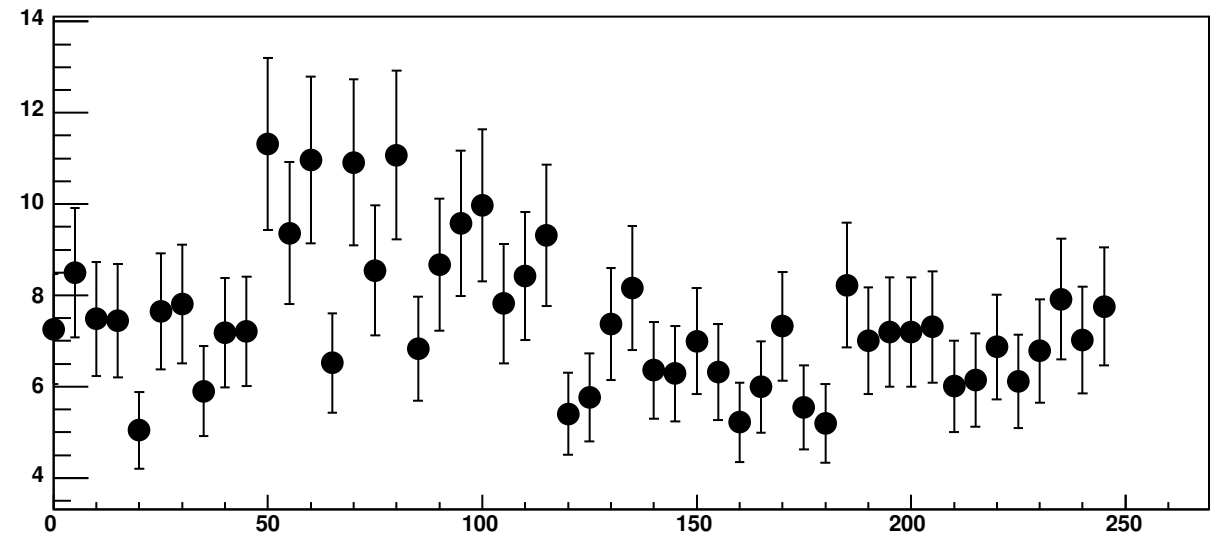
Chip 4, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold



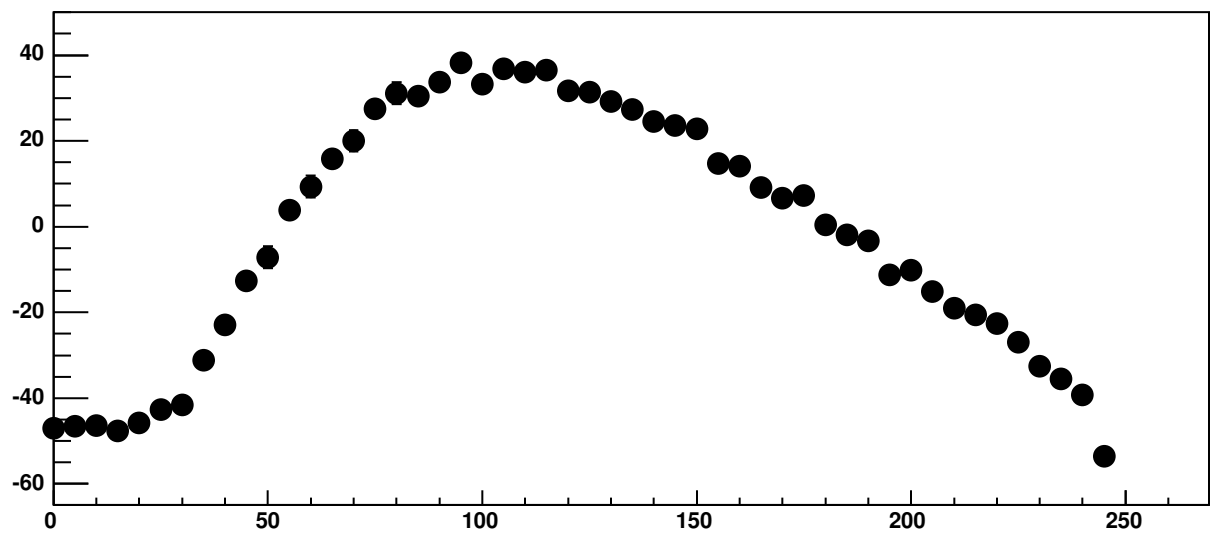
Chip 4, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold



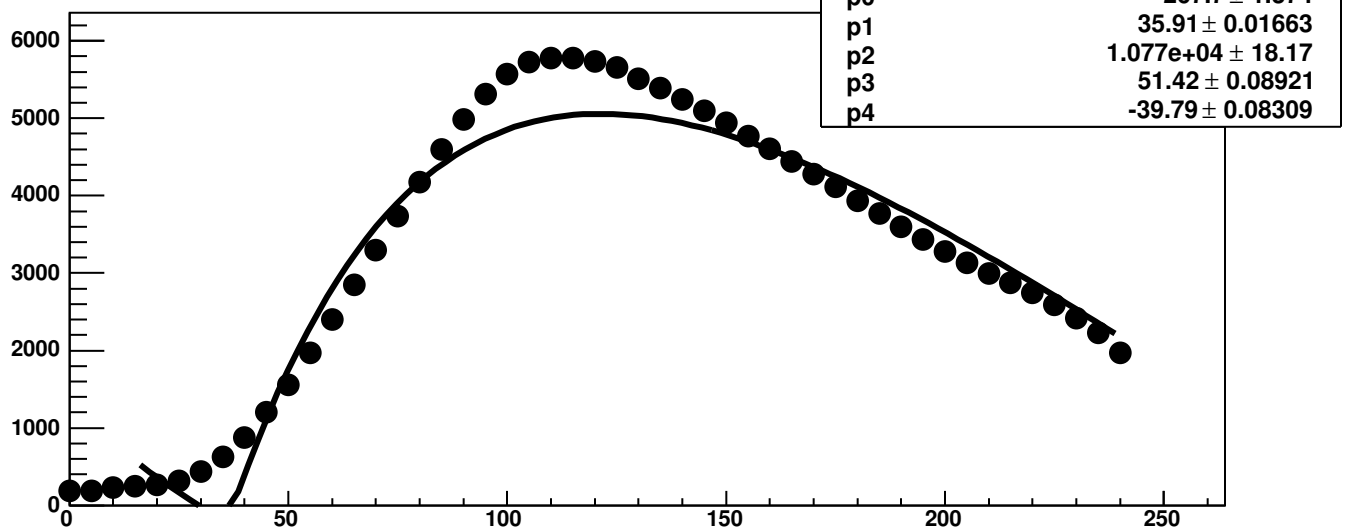
Chip 4, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



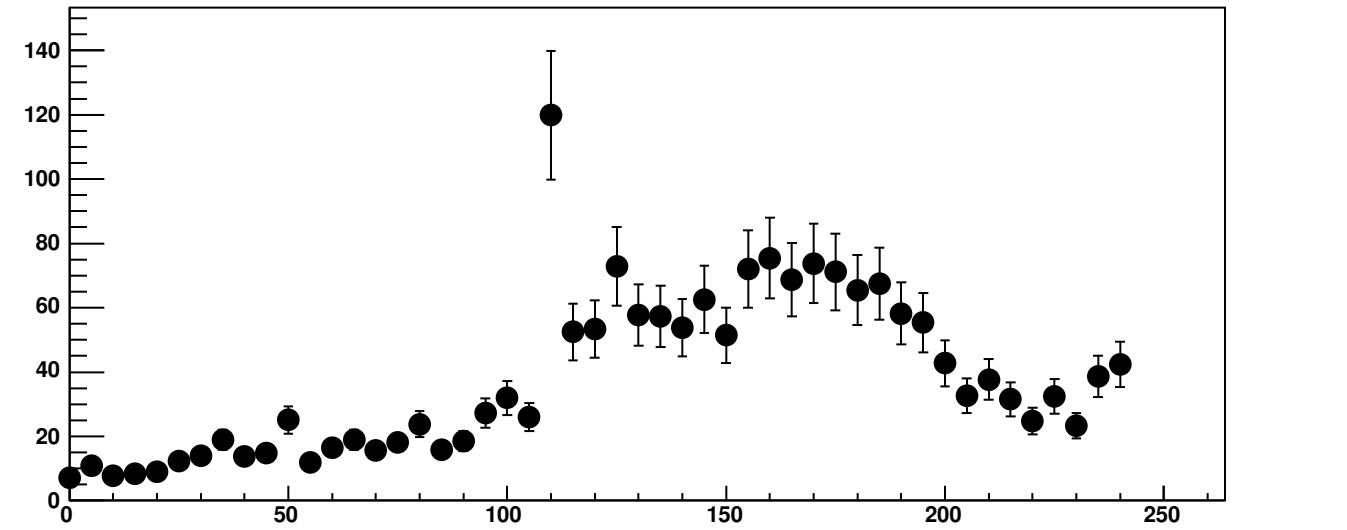
Chip 4, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold



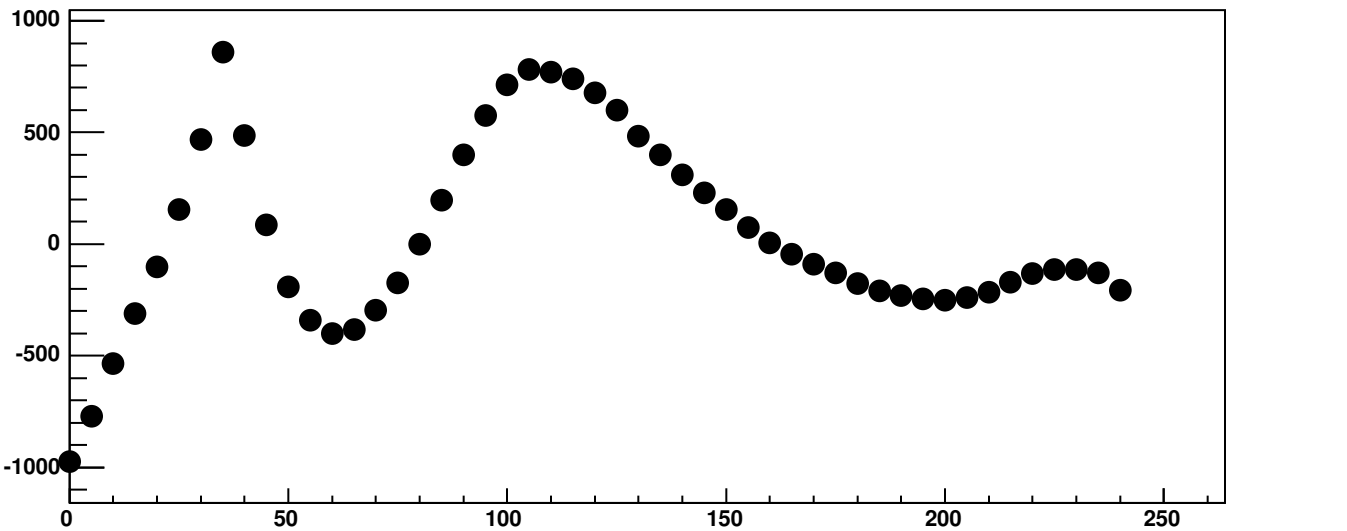
Chip 4, Channel 8, Enable 5, DAC=4000, ADC Mean vs Hold



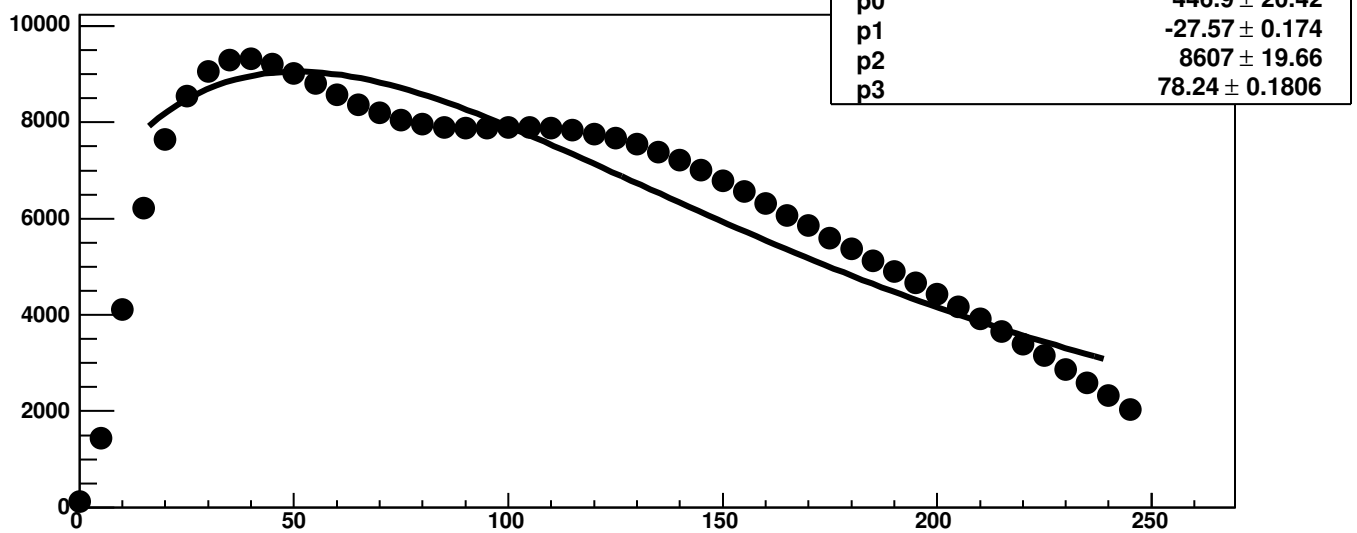
Chip 4, Channel 8, Enable 5, DAC=4000, ADC Noise vs Hold



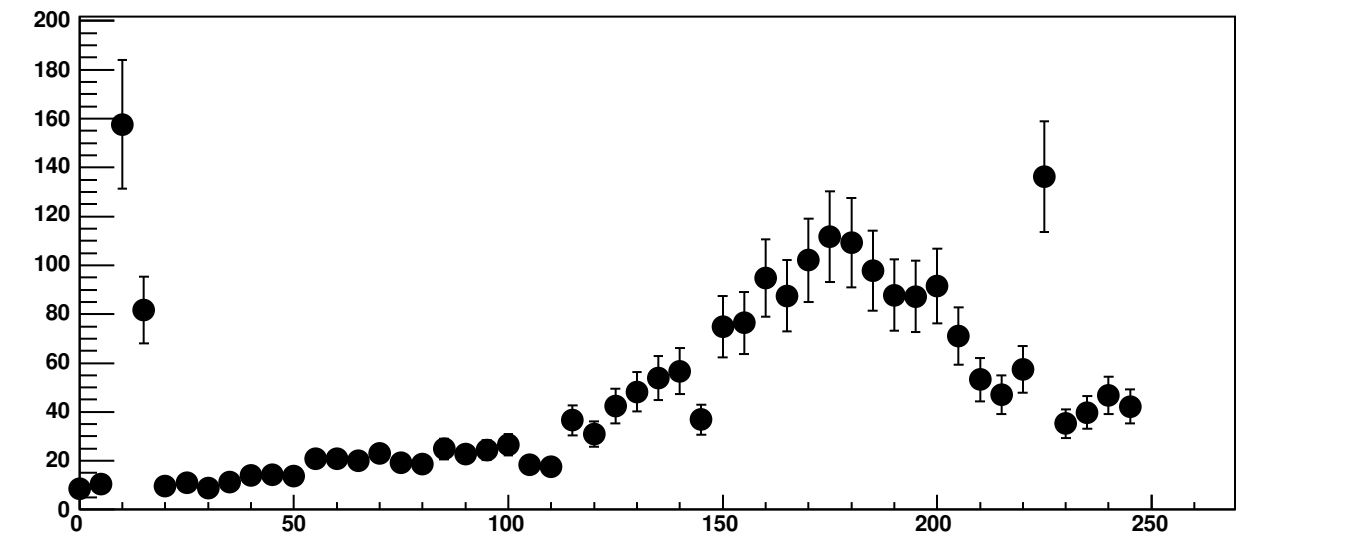
Chip 4, Channel 8, Enable 5, DAC=4000, ADC Residuals vs Hold



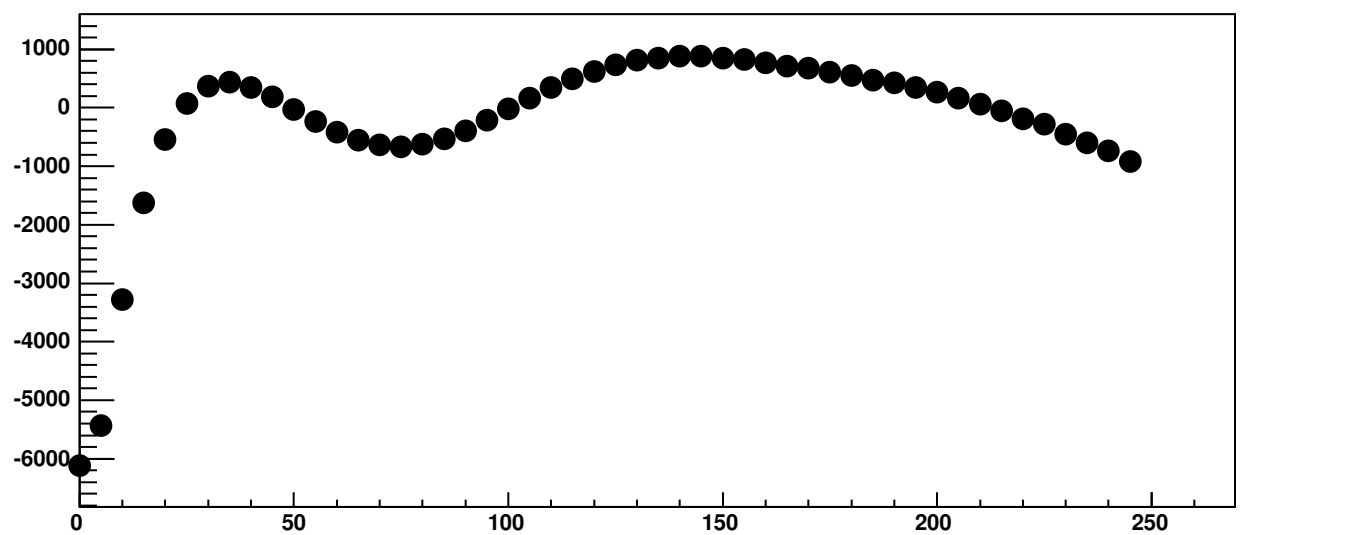
Chip 4, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold



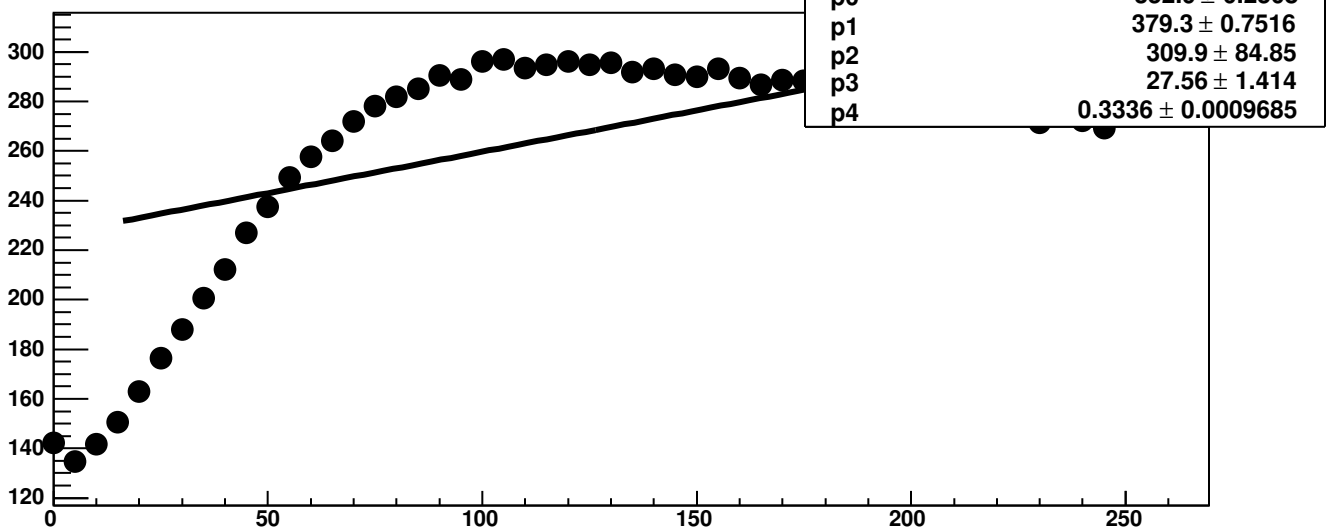
Chip 4, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold



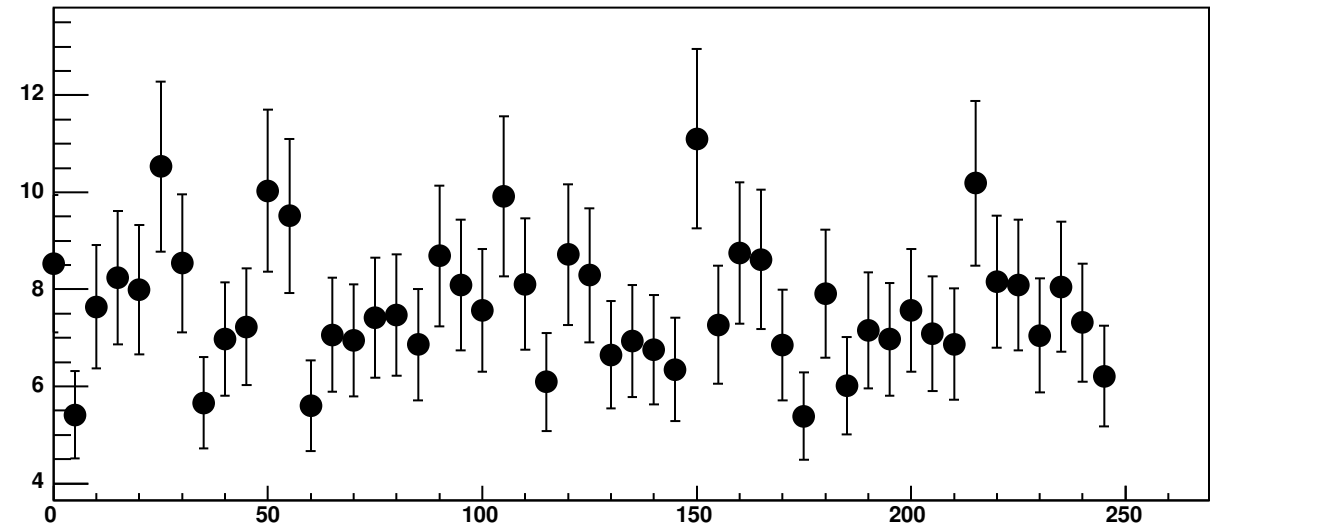
Chip 4, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold



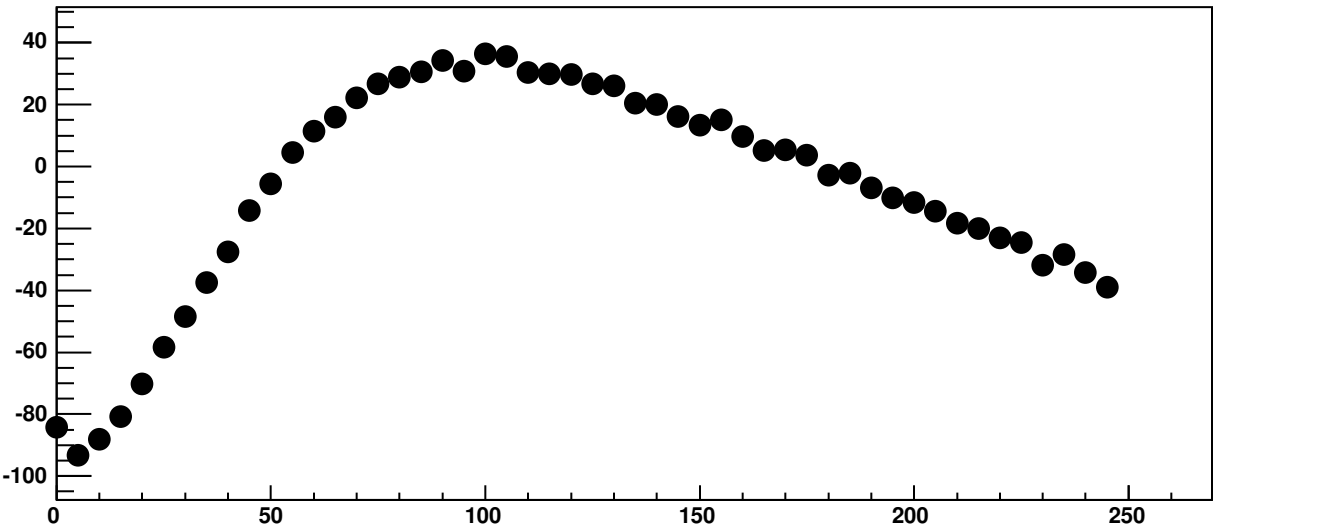
Chip 4, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

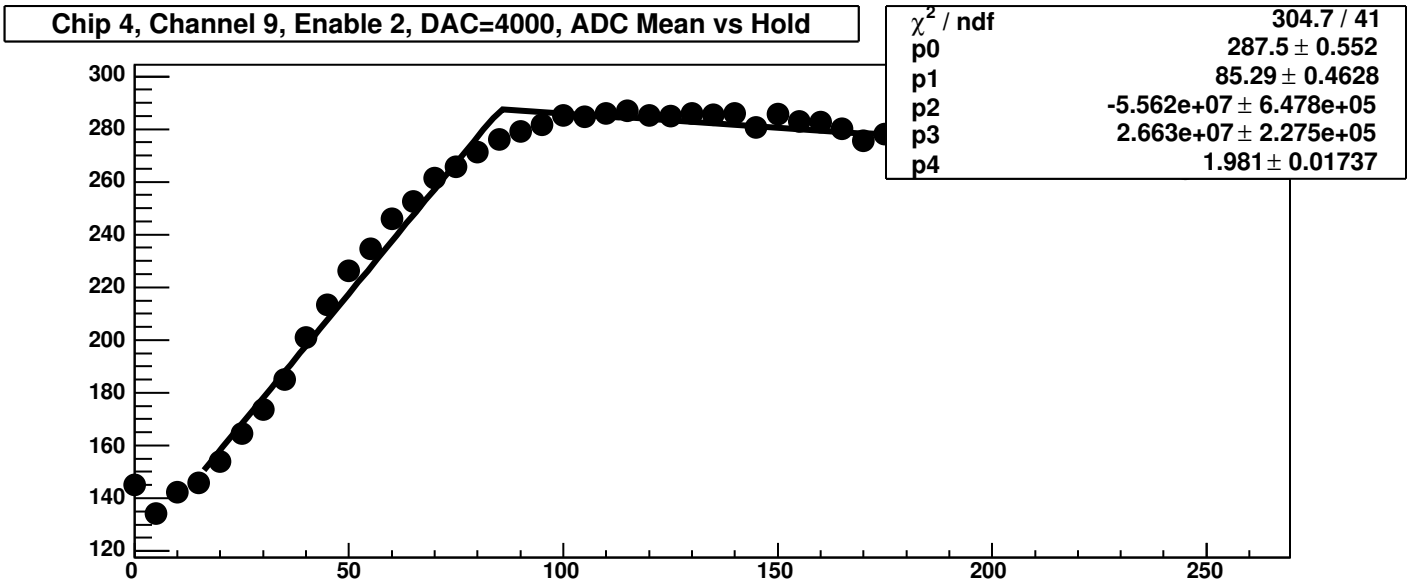


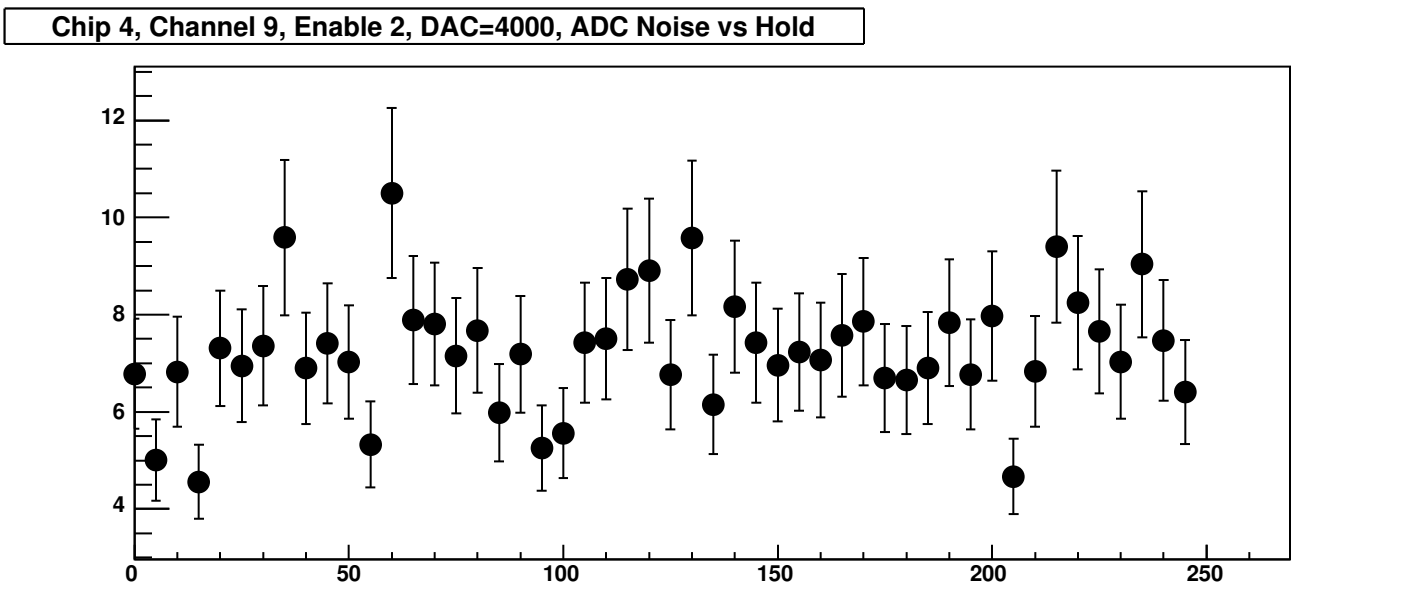
Chip 4, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold

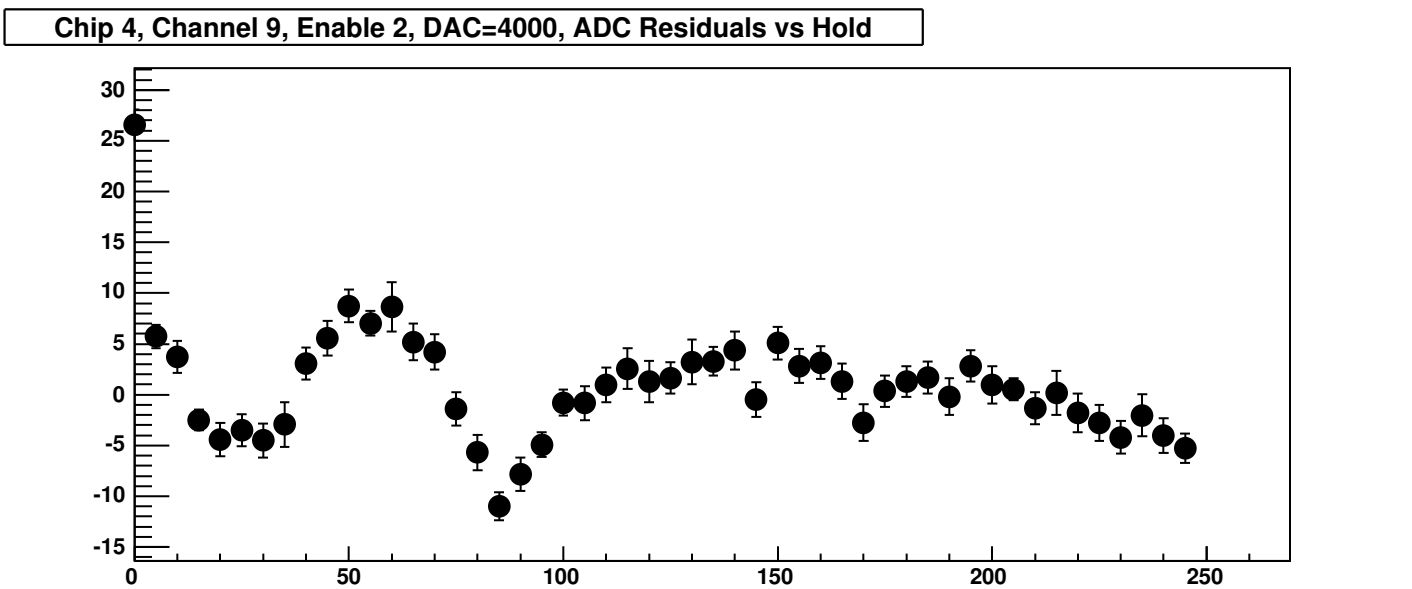


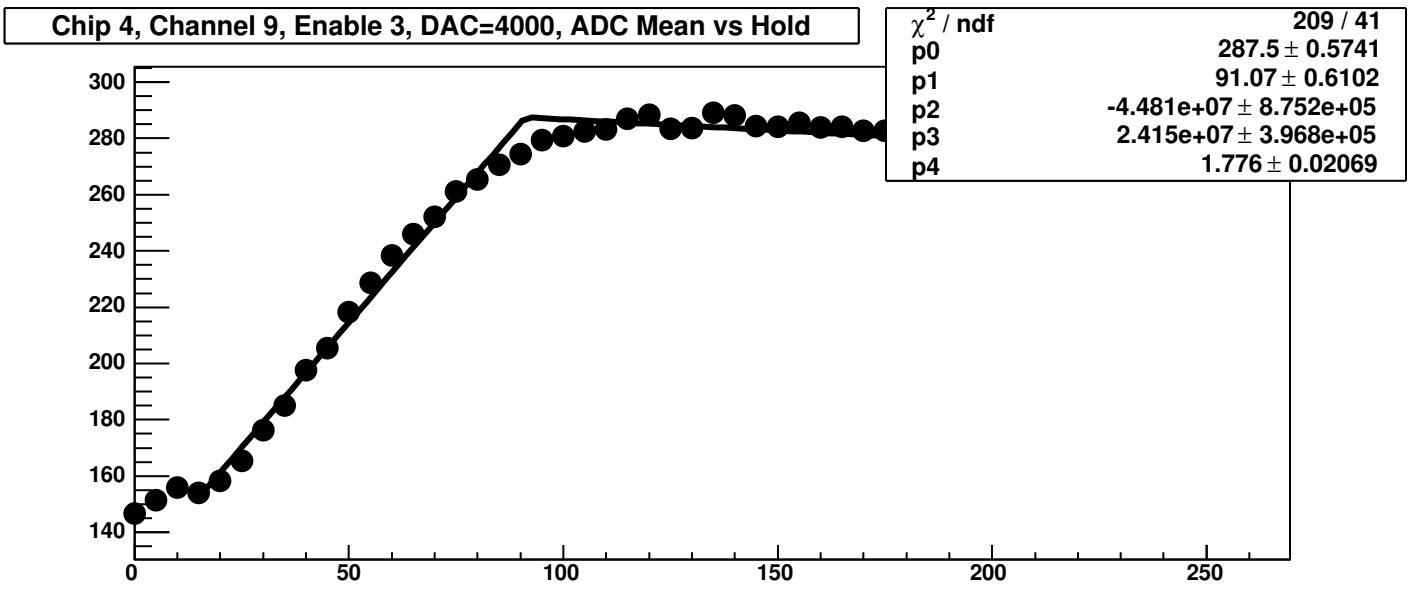
Chip 4, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

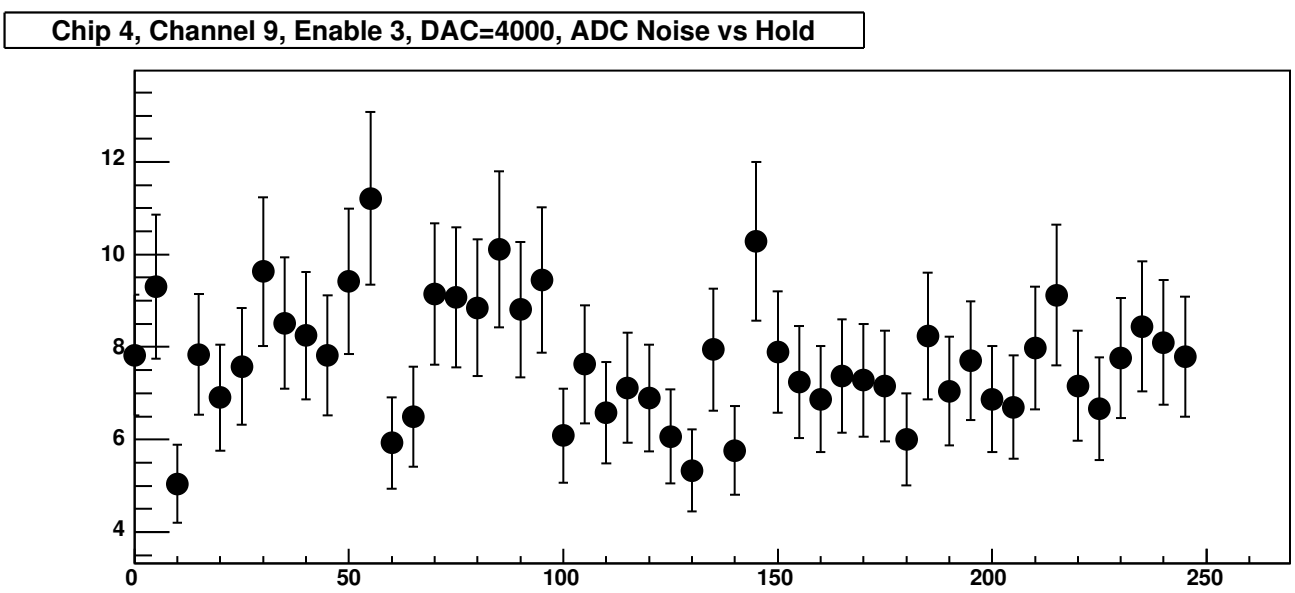


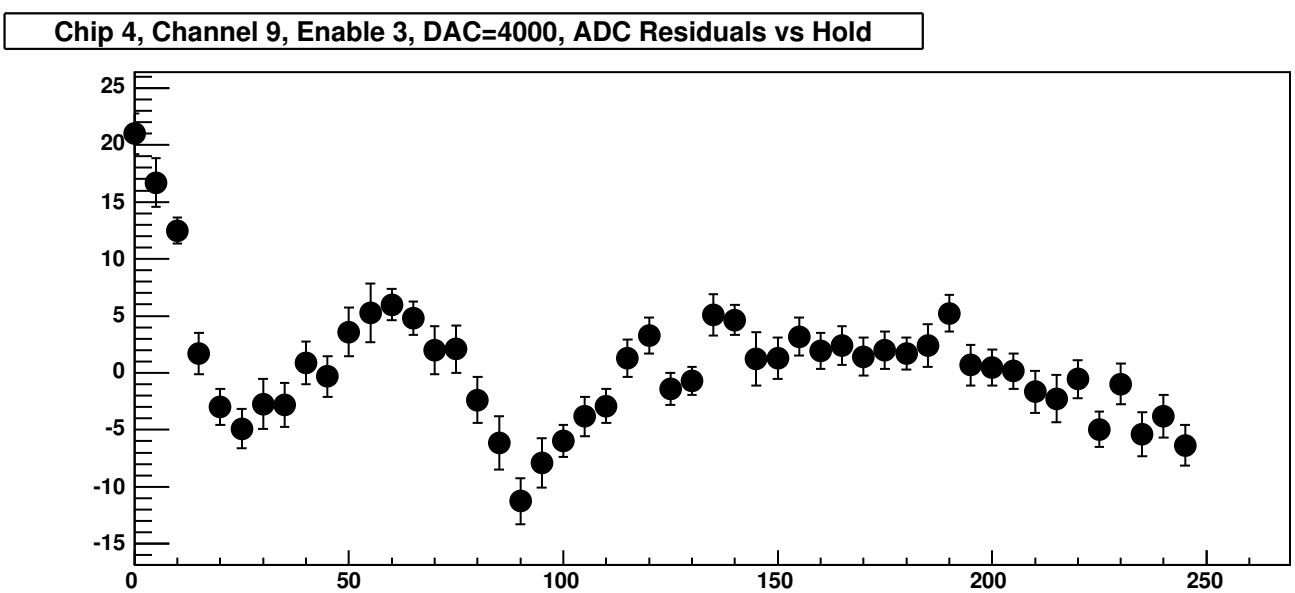

Chip 4, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 304.7 / 41
p0 287.5 ± 0.552
p1 85.29 ± 0.4628
p2 -5.562e+07 ± 6.478e+05
p3 2.663e+07 ± 2.275e+05
p4 1.981 ± 0.01737
300
280
260
240
220
200
180
160
140
120
0
50
100
150
200
250


Chip 4, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

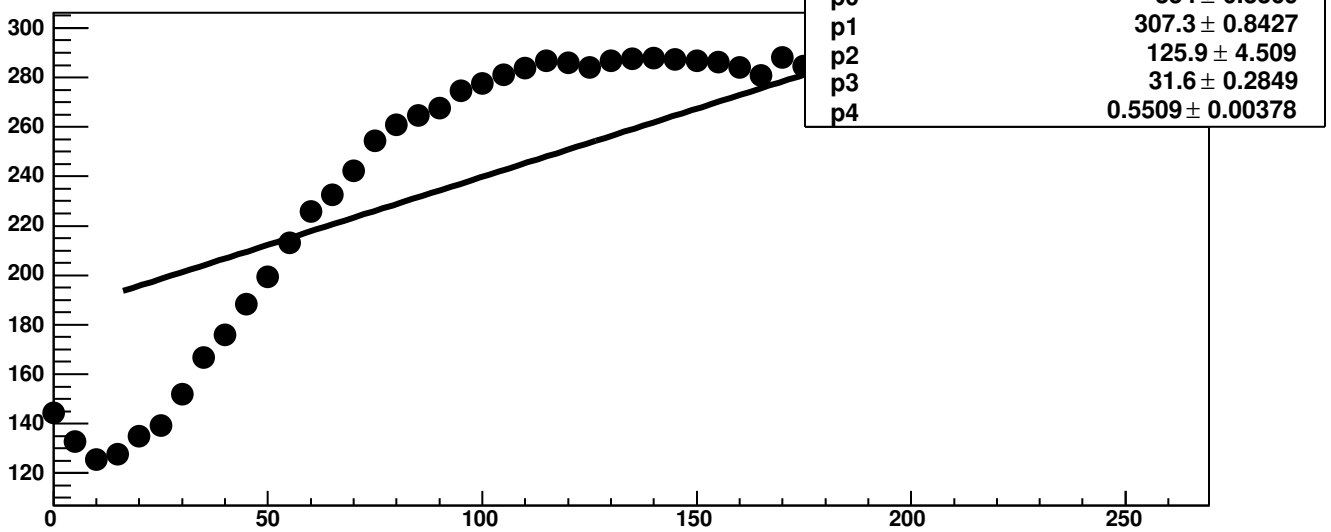

Chip 4, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250


Chip 4, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 209 / 41
p0 287.5 ± 0.5741
p1 91.07 ± 0.6102
p2 -4.481e+07 ± 8.752e+05
p3 2.415e+07 ± 3.968e+05
p4 1.776 ± 0.02069
300
280
260
240
220
200
180
160
140
0
50
100
150
200
250

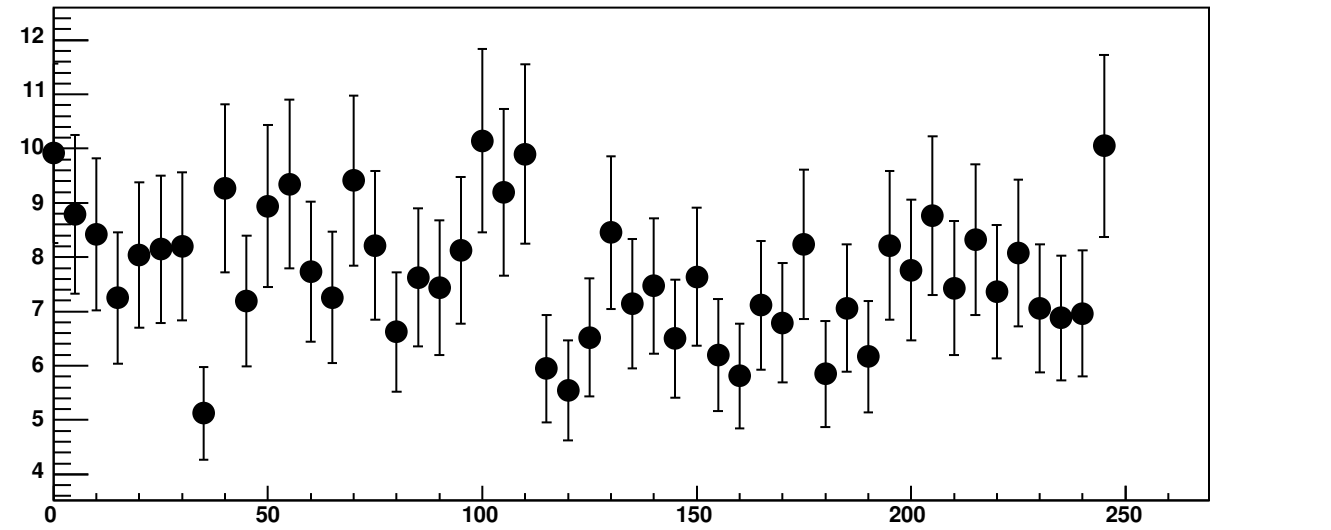

Chip 4, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 4, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

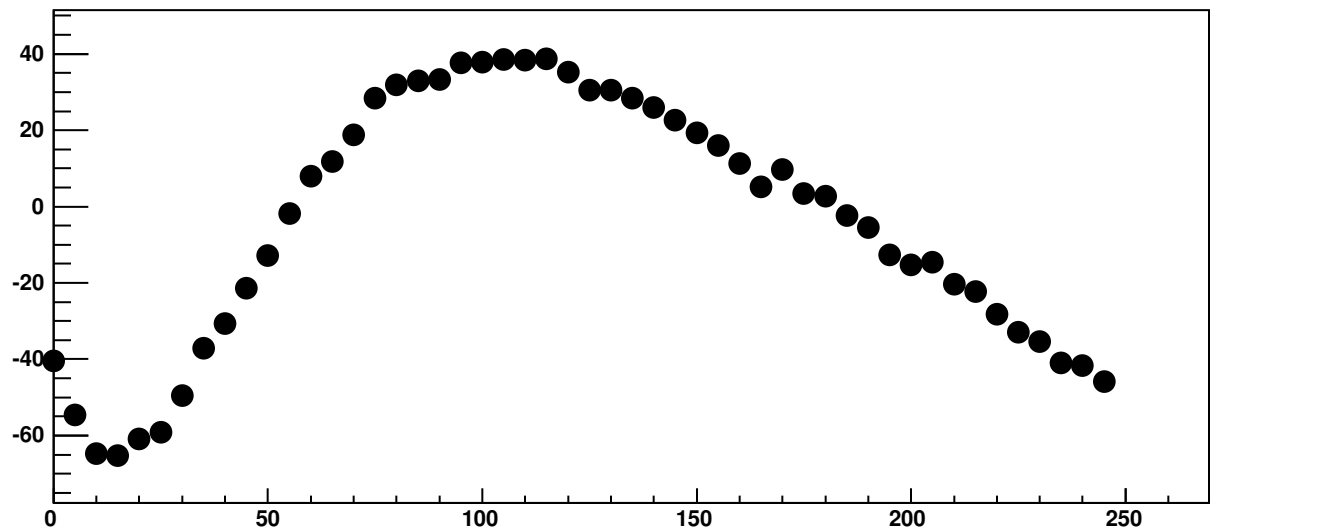
Chip 4, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold



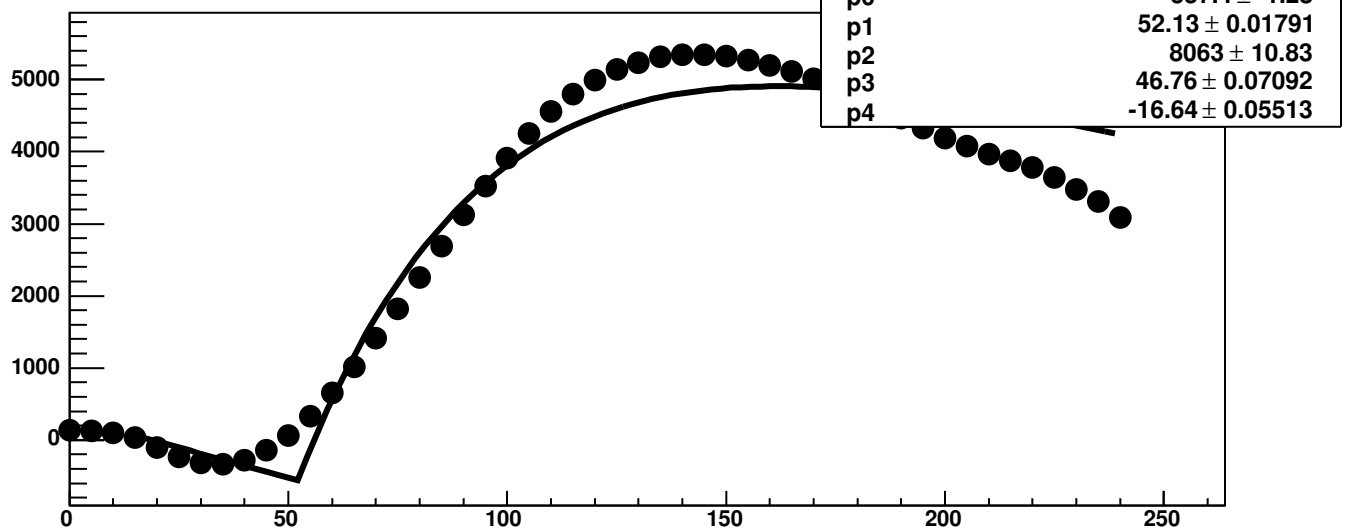
Chip 4, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



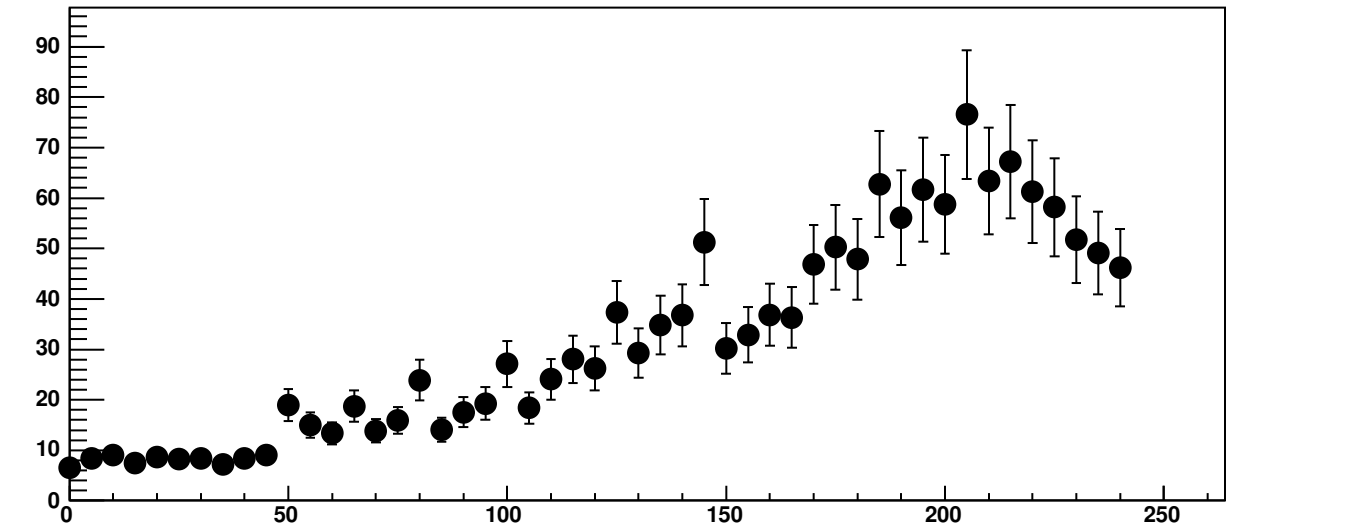
Chip 4, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold



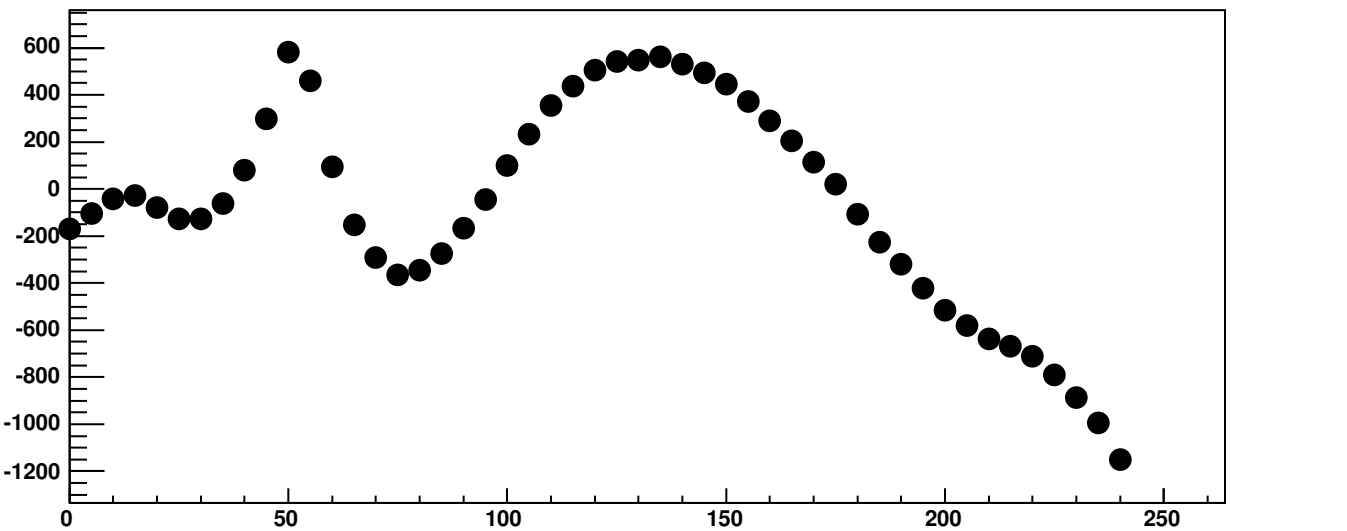
Chip 4, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold



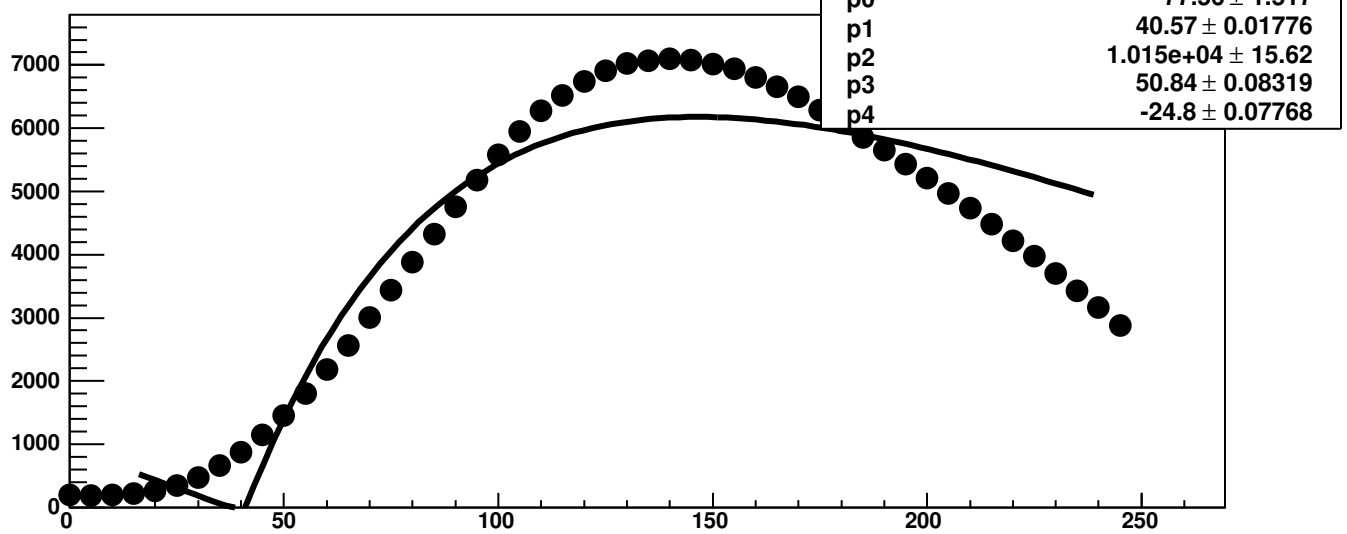
Chip 4, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



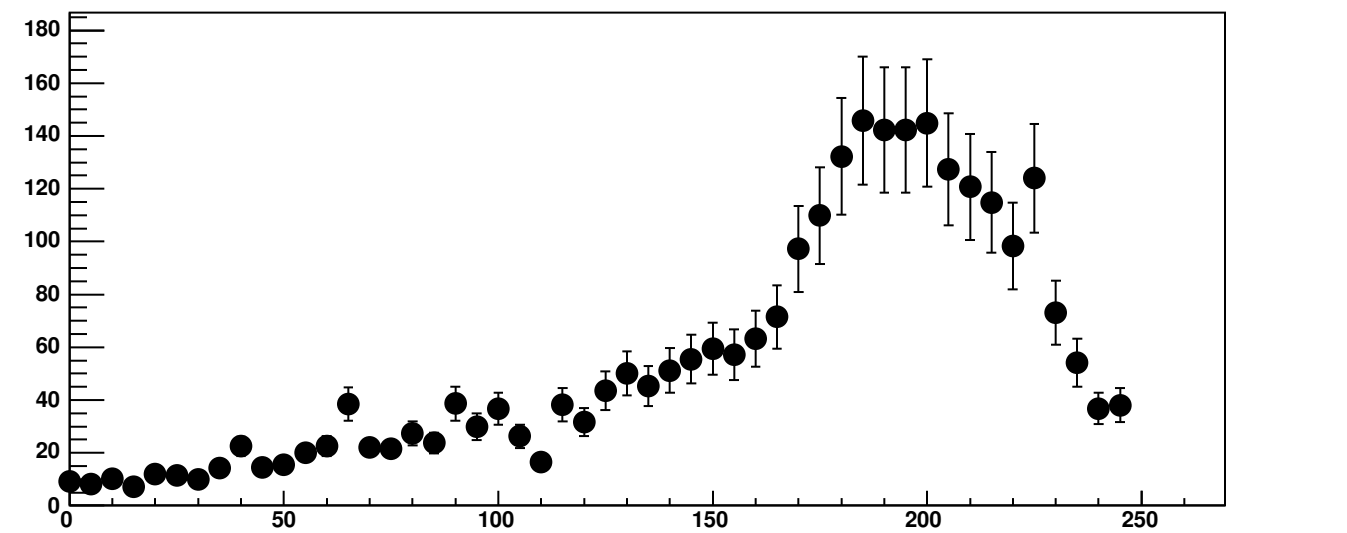
Chip 4, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold



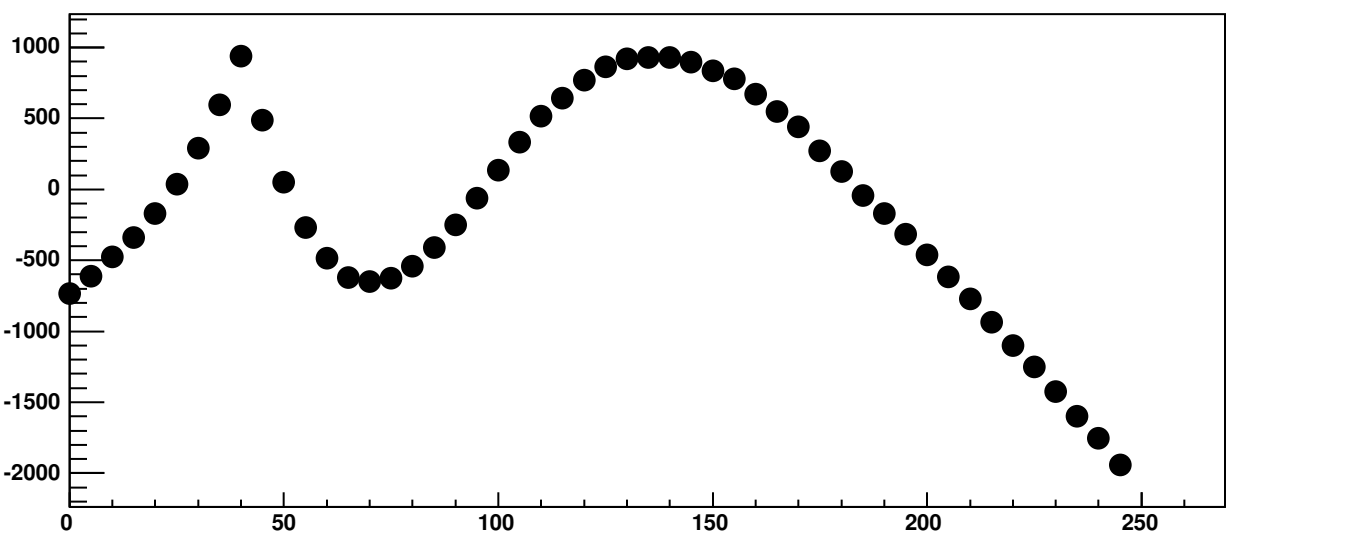
Chip 4, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold



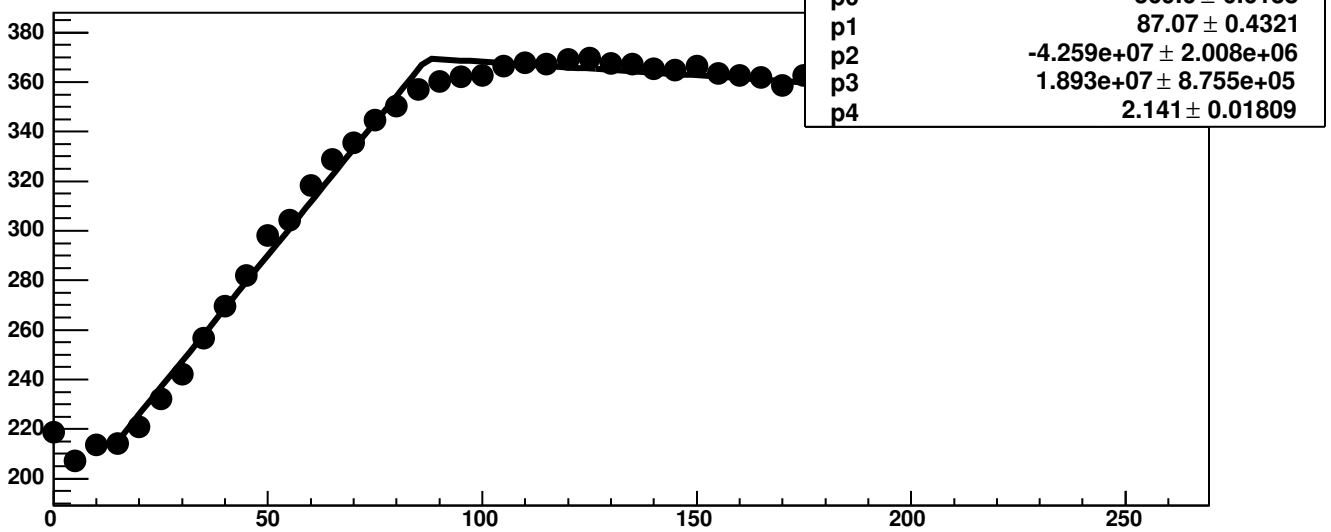
Chip 4, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold



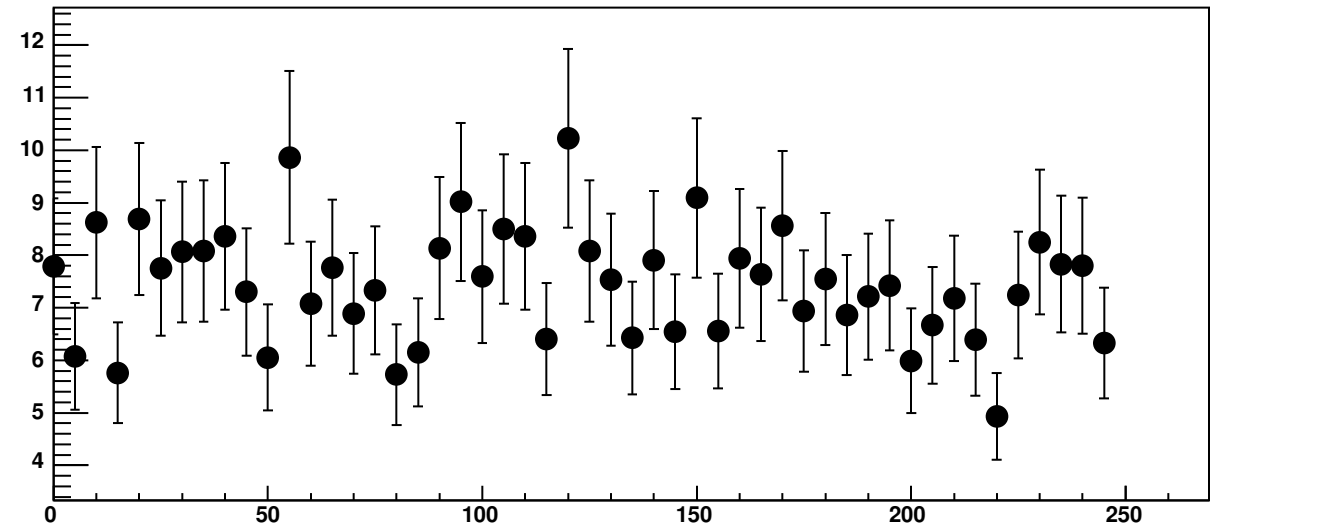
Chip 4, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold



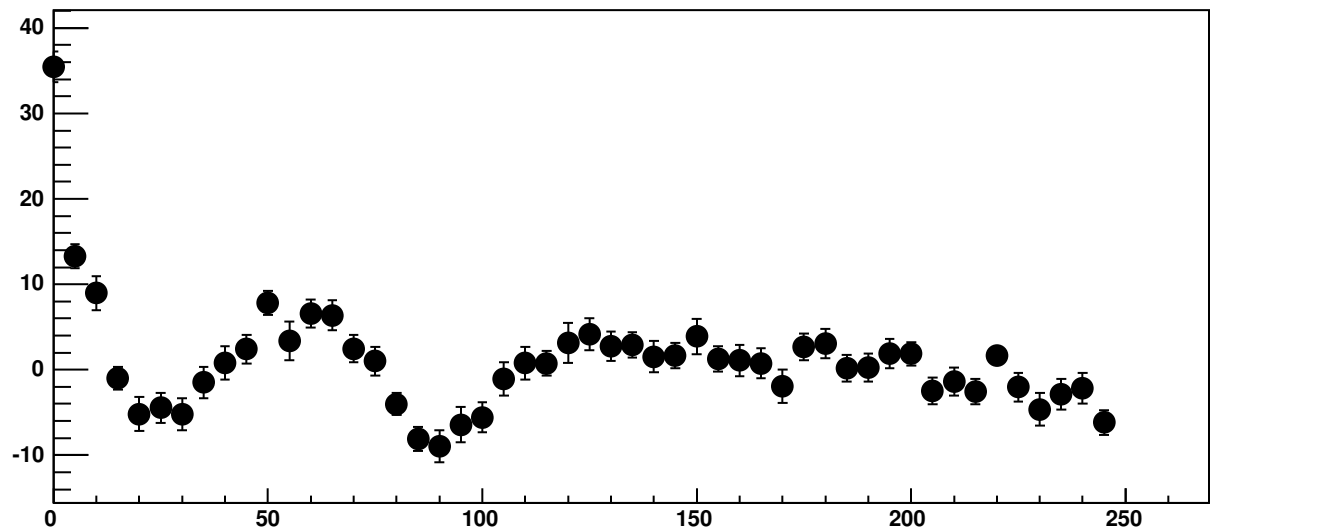
Chip 4, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold



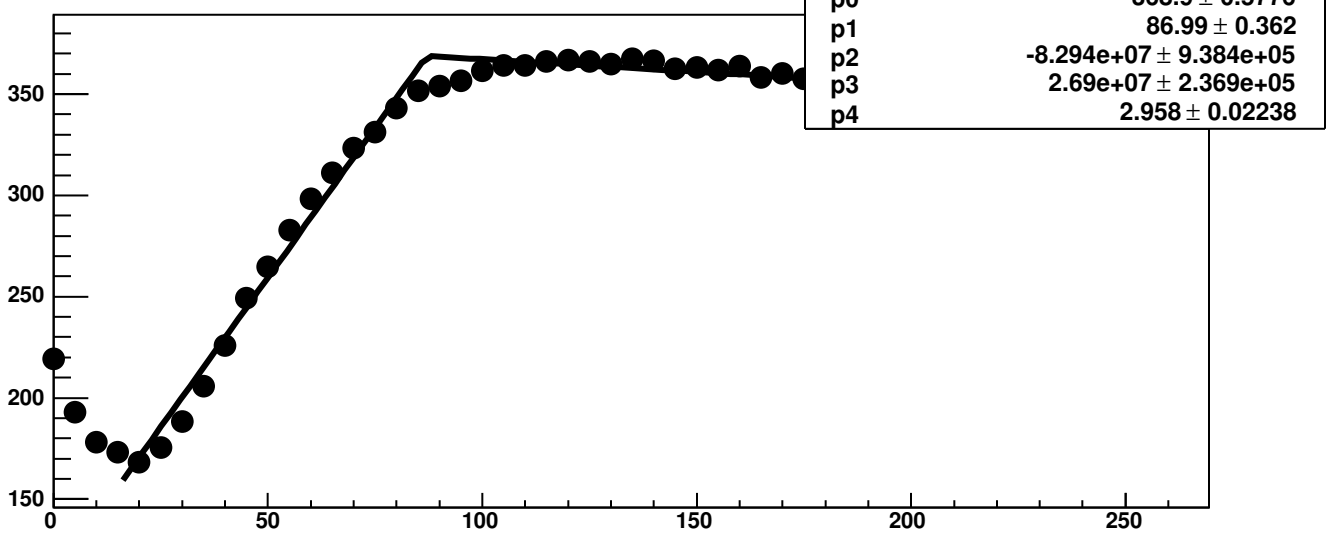
Chip 4, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



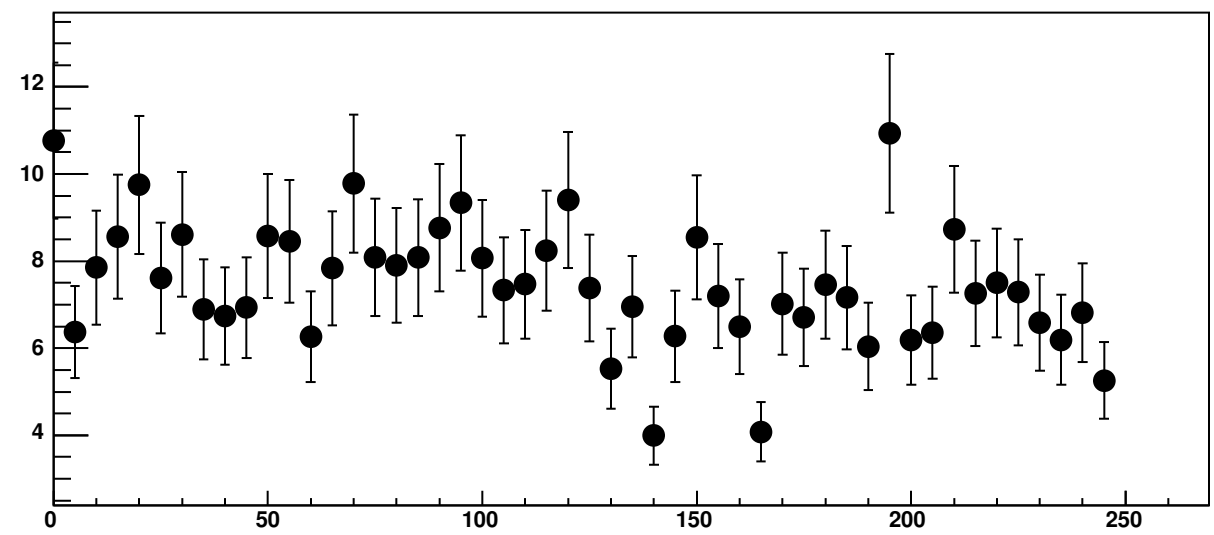
Chip 4, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold



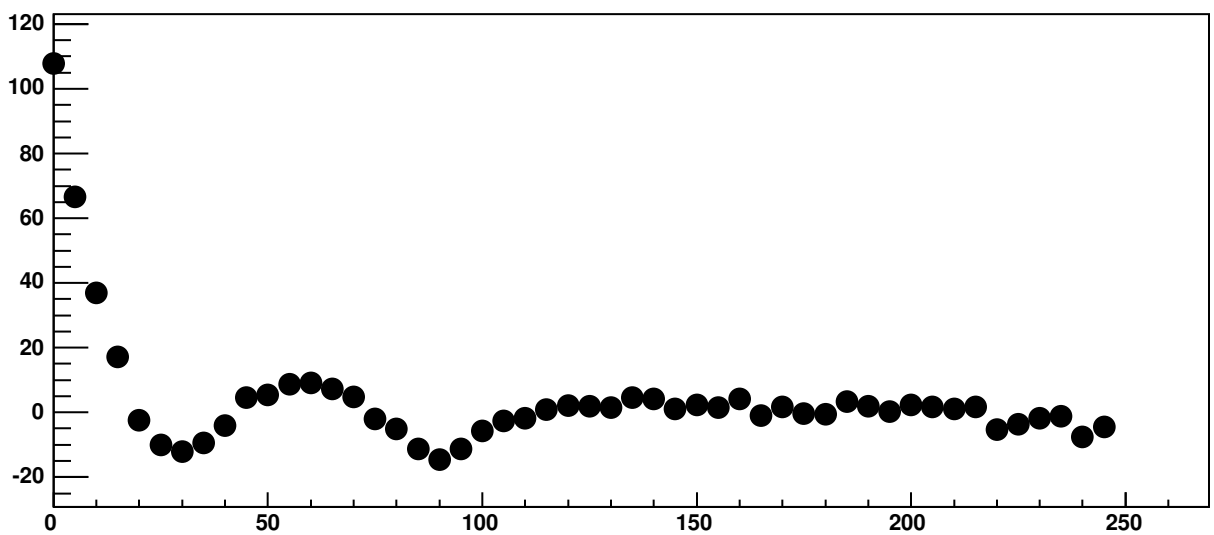
Chip 4, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

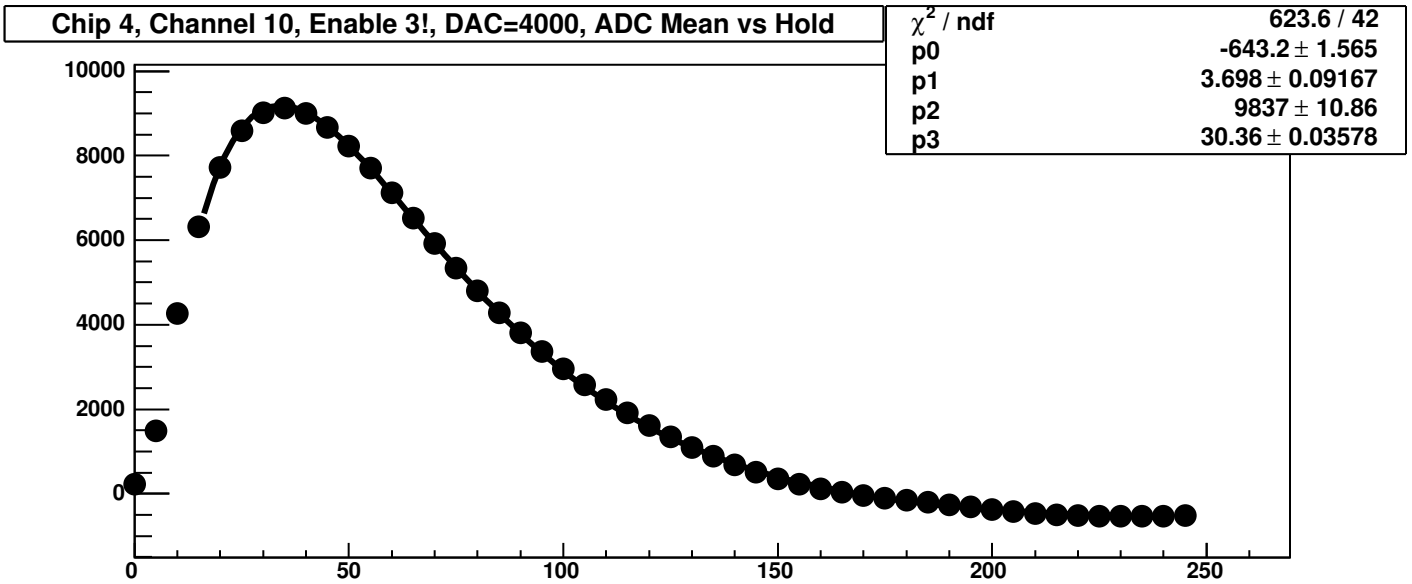


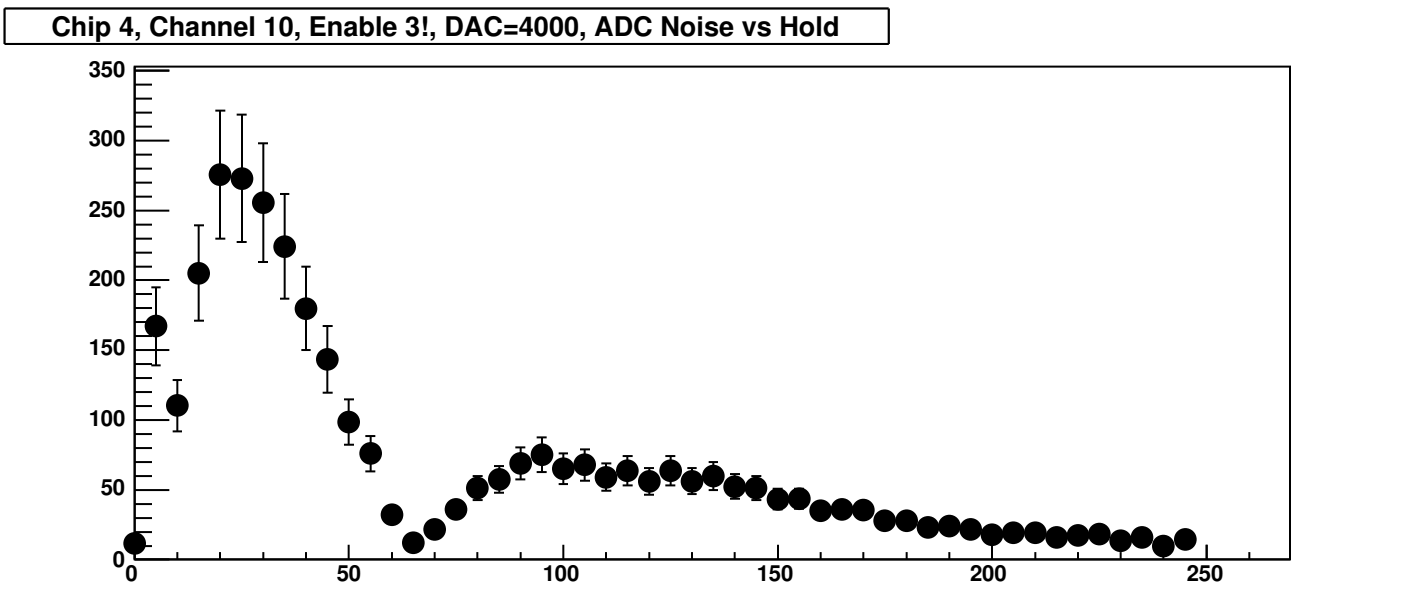
Chip 4, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold

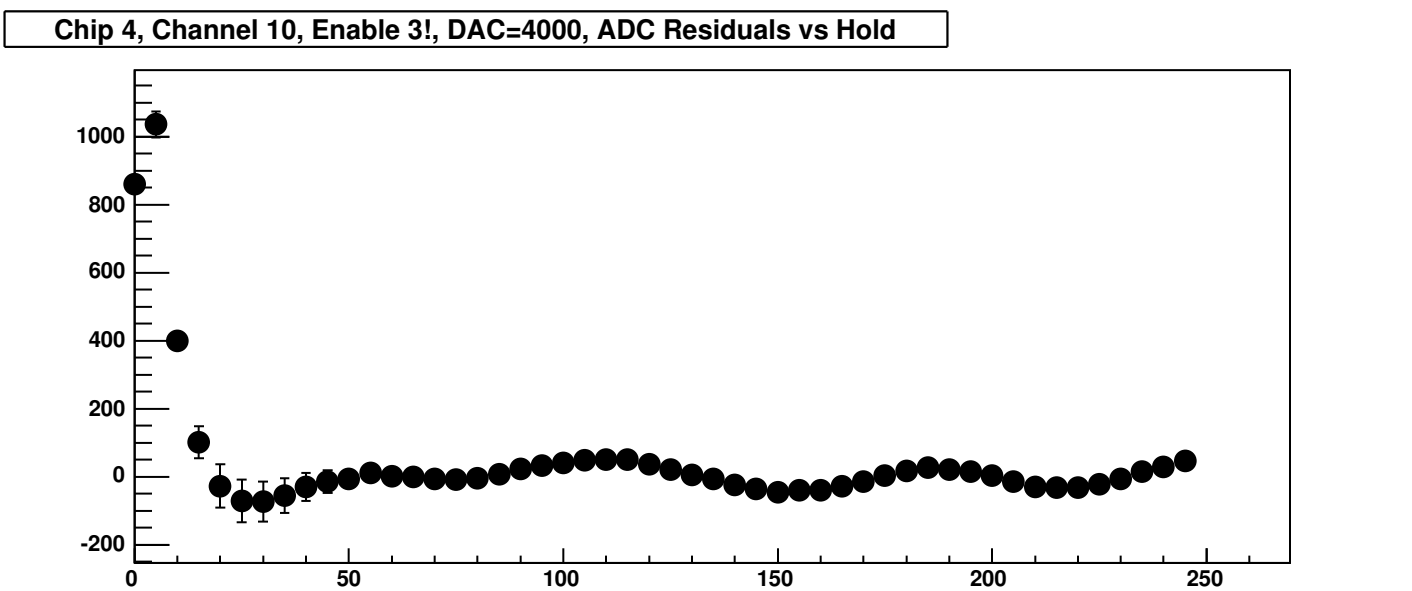


Chip 4, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

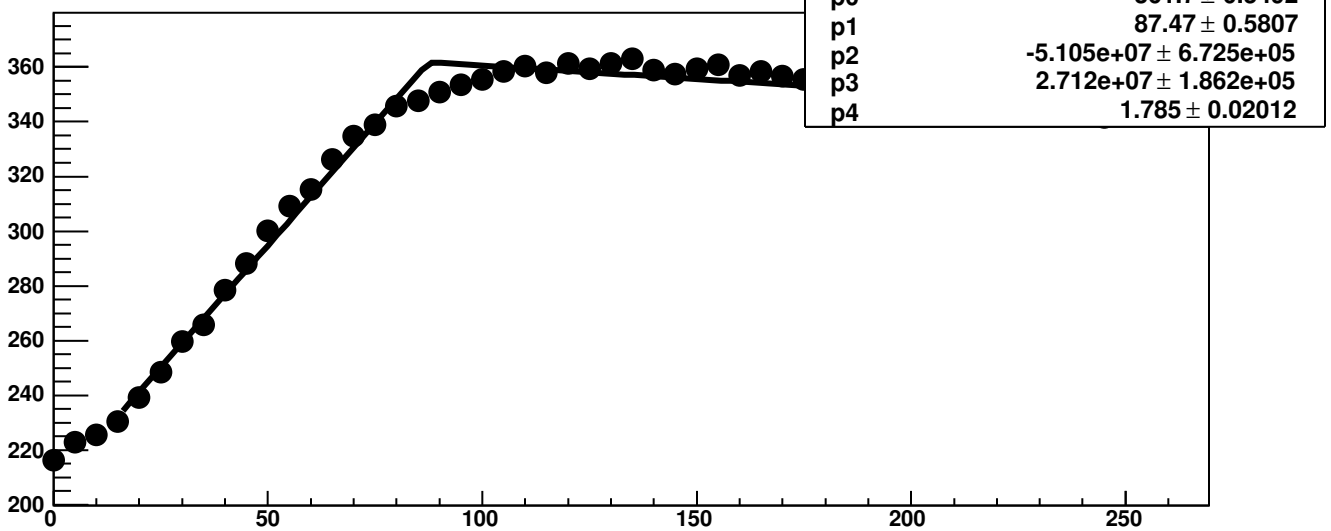



Chip 4, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 623.6 / 42
p0 -643.2 ± 1.565
p1 3.698 ± 0.09167
p2 9837 ± 10.86
p3 30.36 ± 0.03578
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

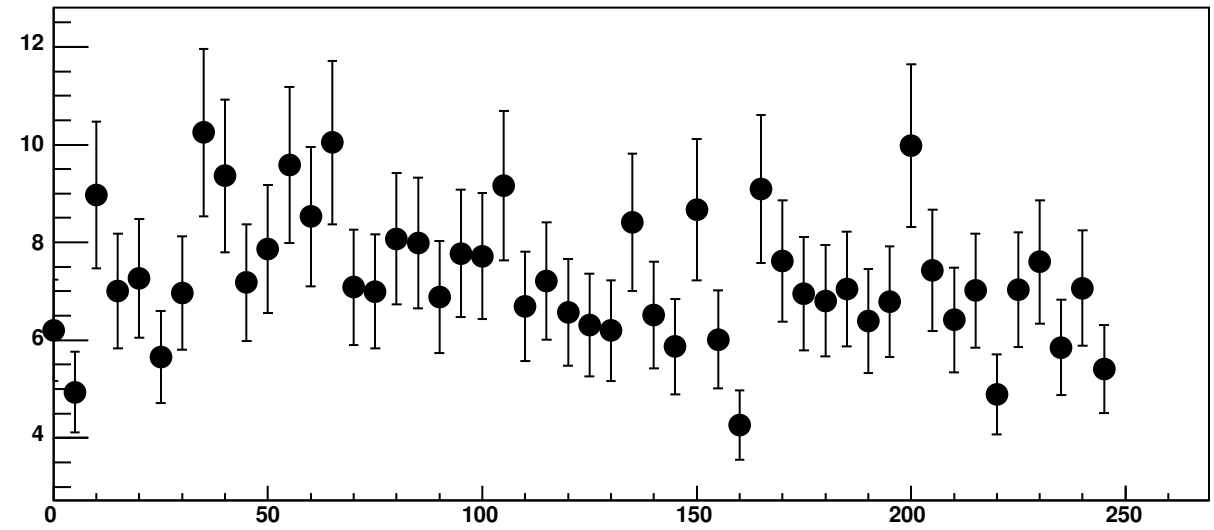

Chip 4, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold
350
300
250
200
150
100
50
0
0
50
100
150
200
250


Chip 4, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold
1000
800
600
400
200
0
-200
0
50
100
150
200
250

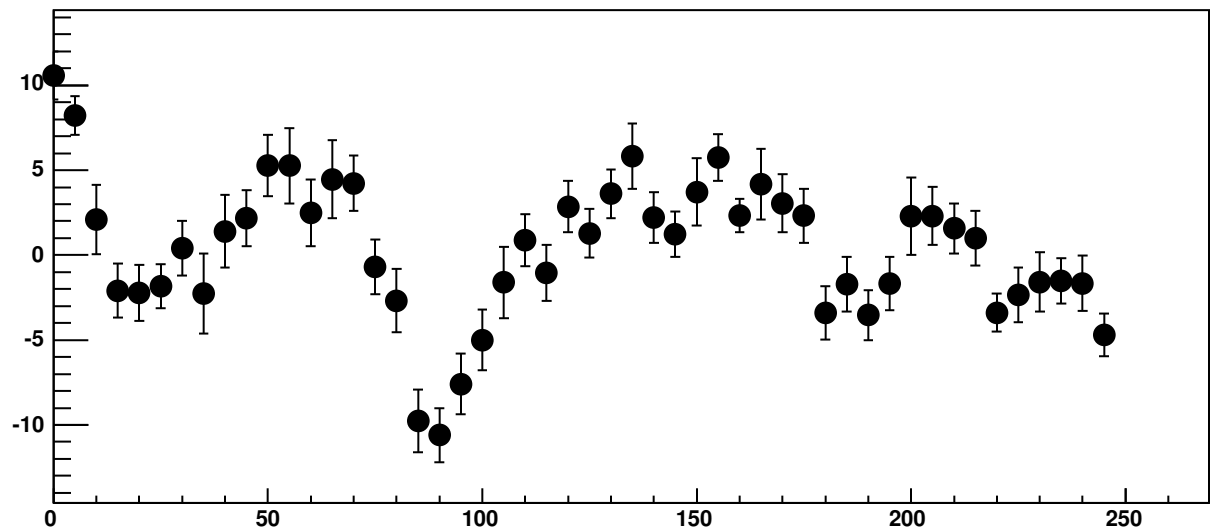
Chip 4, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold



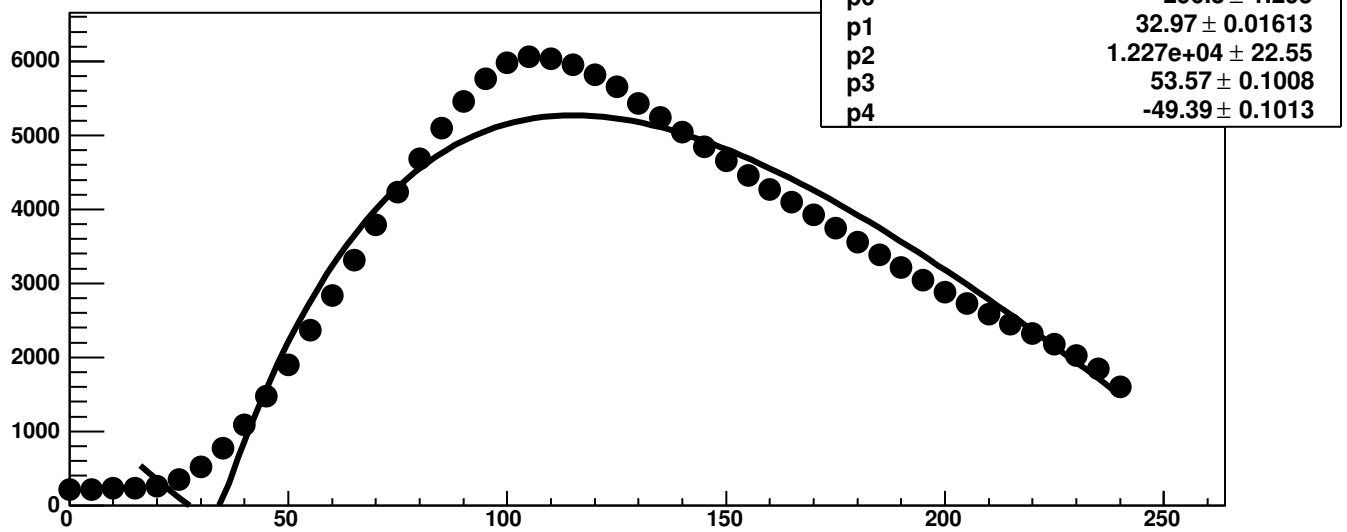
Chip 4, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



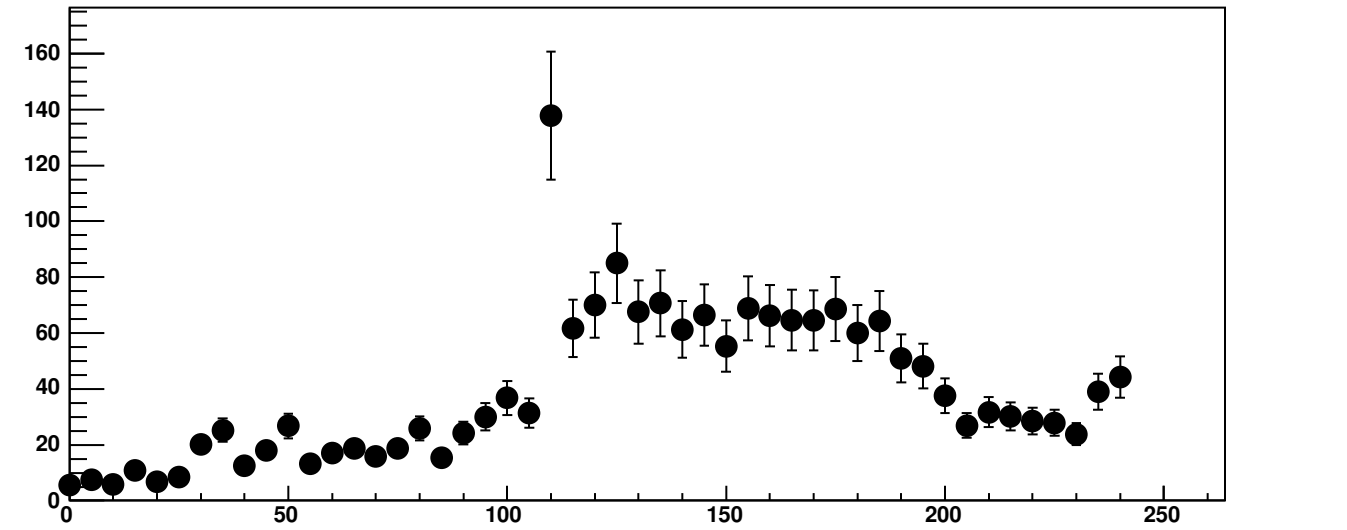
Chip 4, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold



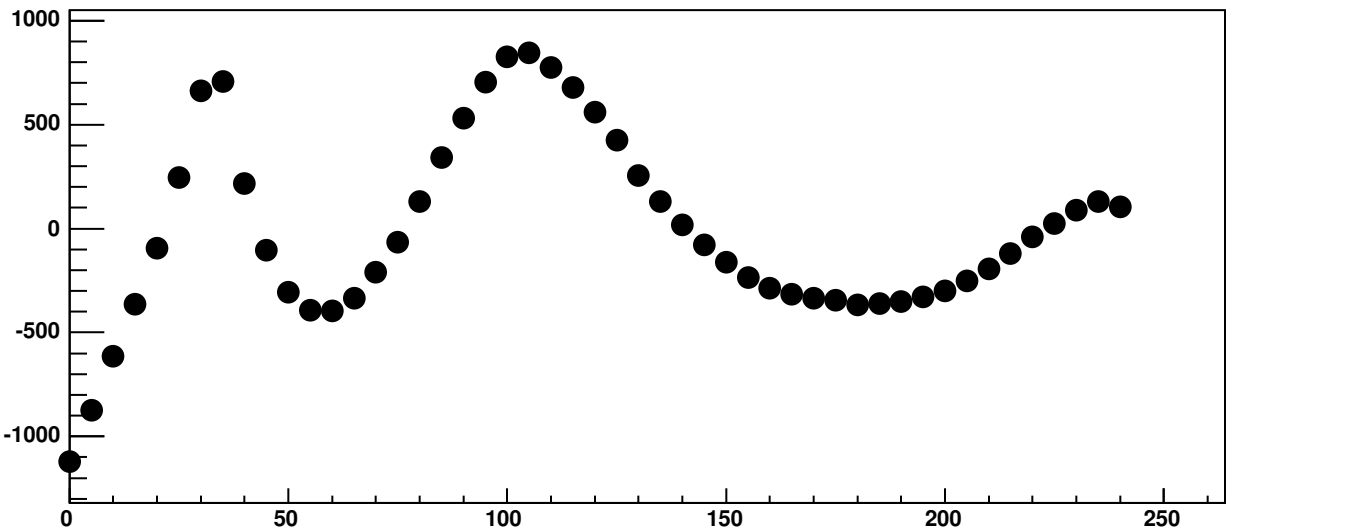
Chip 4, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold



Chip 4, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold

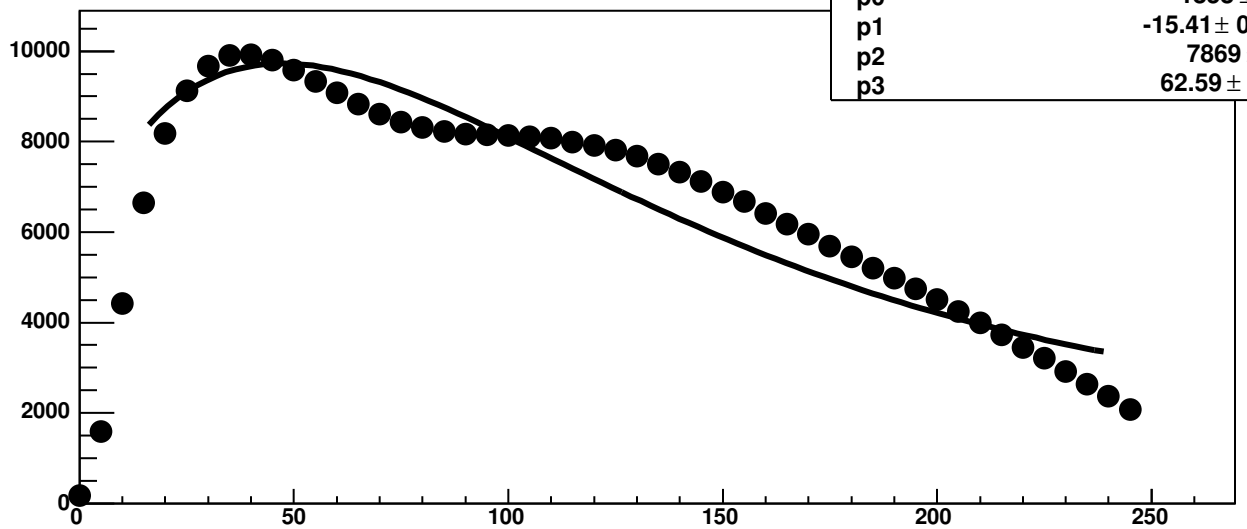


Chip 4, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

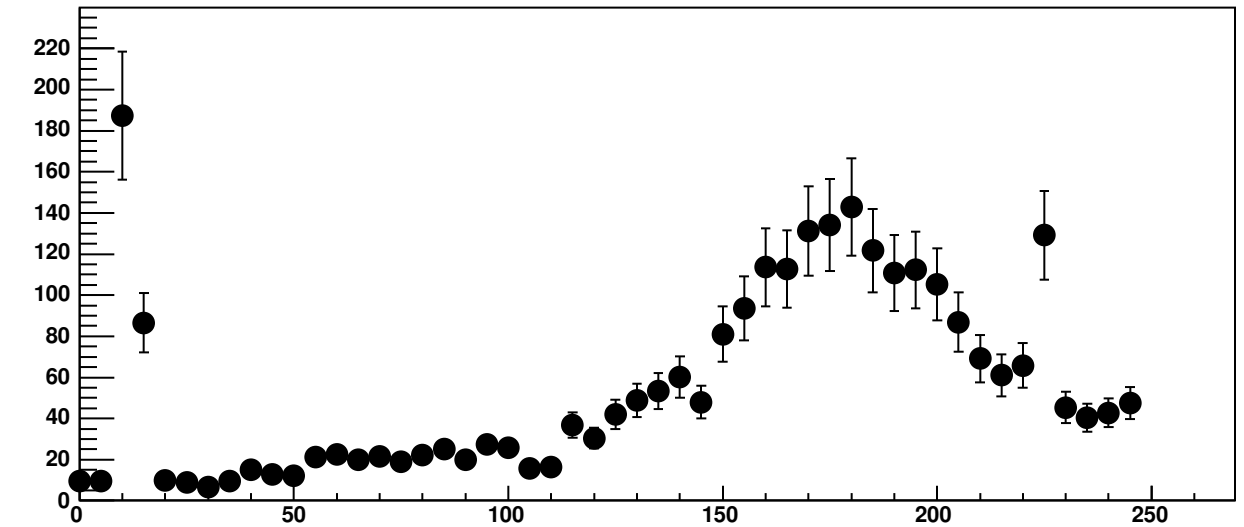


Chip 4, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold

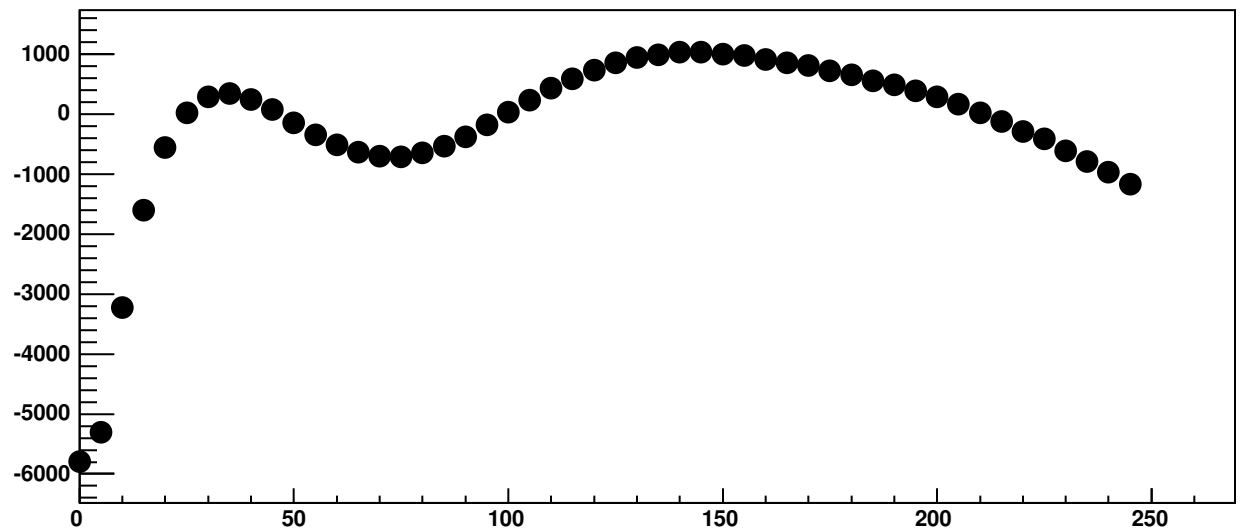
χ^2 / ndf	3.532e+05 / 42
p0	1858 ± 12.42
p1	-15.41 ± 0.09821
p2	7869 ± 11.9
p3	62.59 ± 0.1017

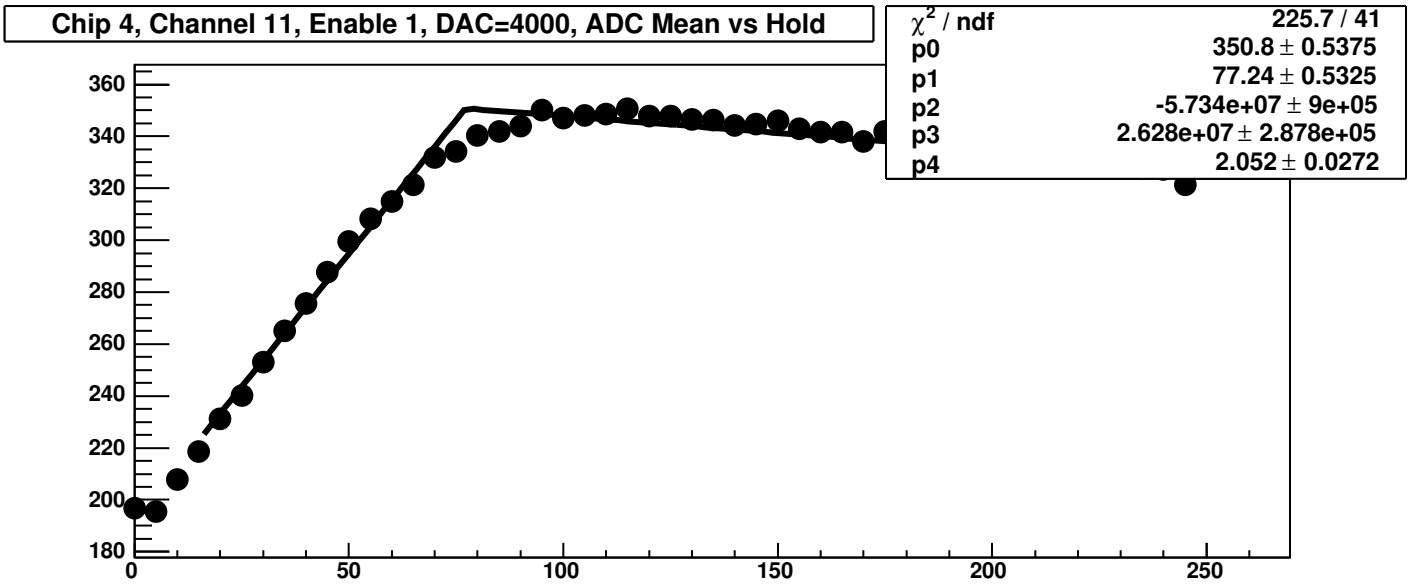


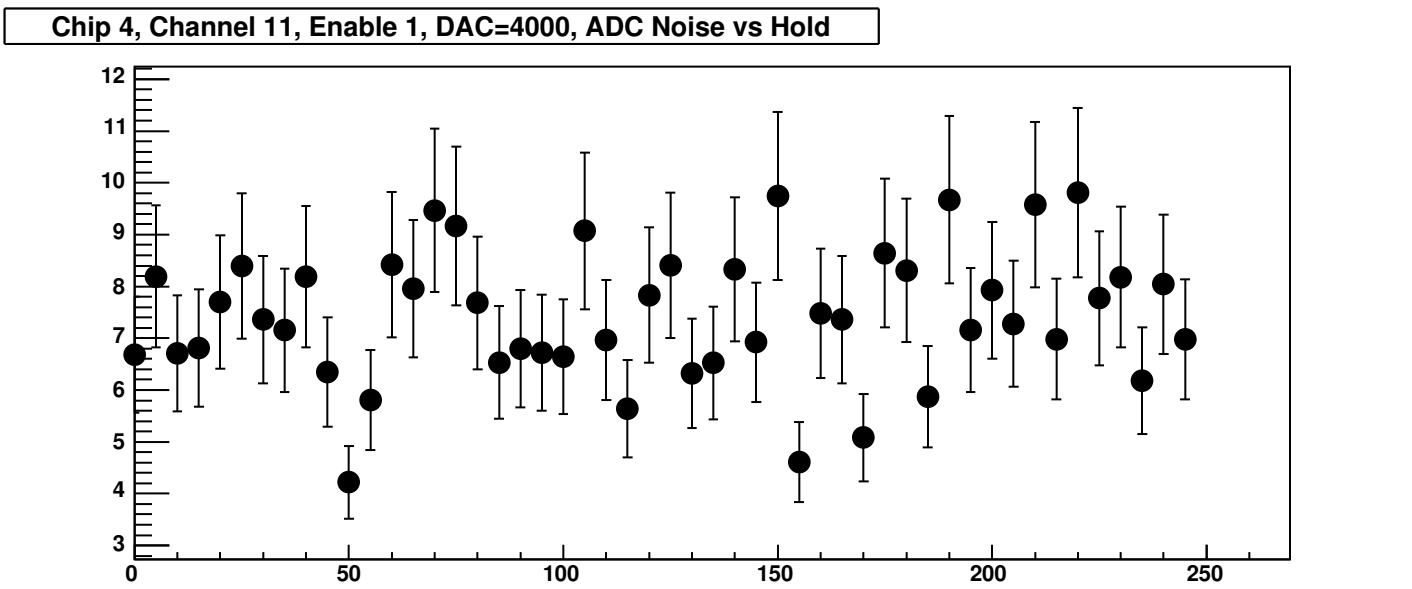
Chip 4, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold

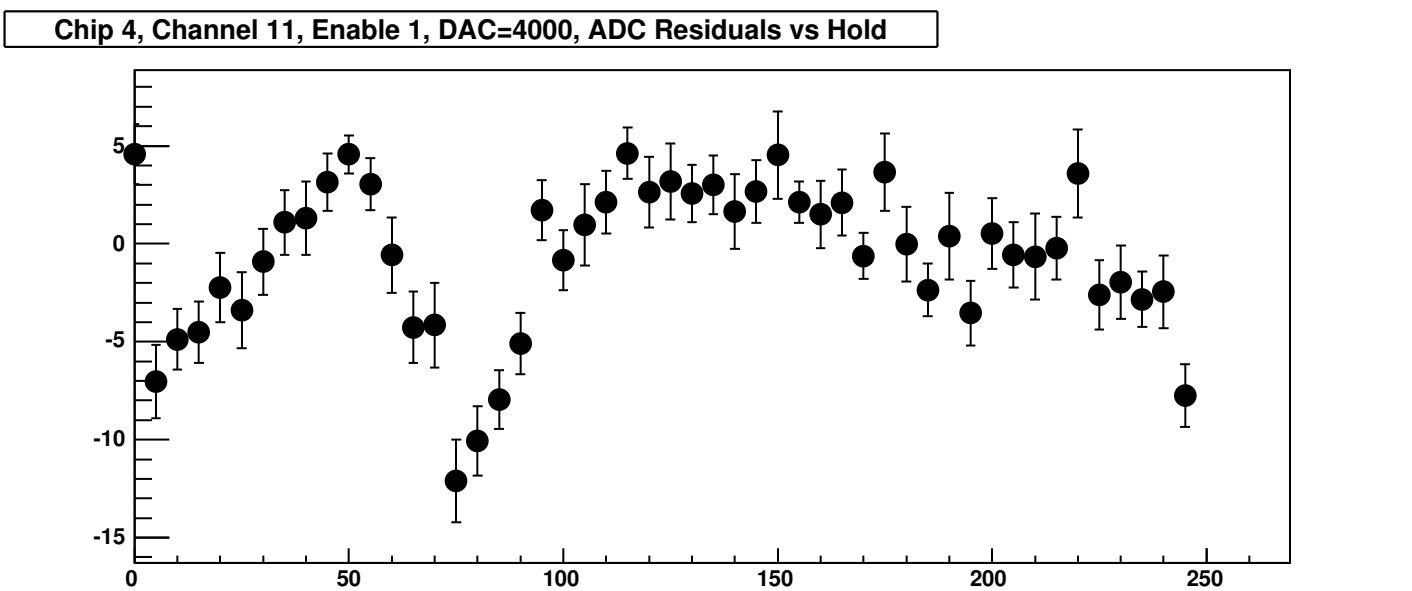


Chip 4, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold

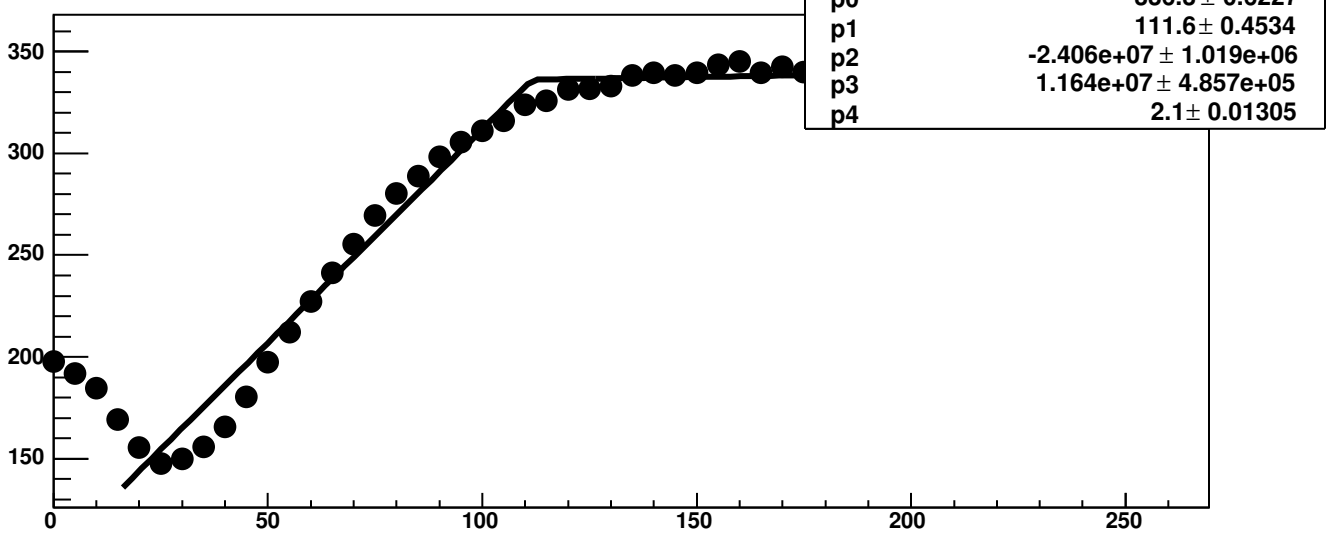



Chip 4, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 225.7 / 41
p0 350.8 ± 0.5375
p1 77.24 ± 0.5325
p2 -5.734e+07 ± 9e+05
p3 2.628e+07 ± 2.878e+05
p4 2.052 ± 0.0272
360
340
320
300
280
260
240
220
200
180
0
50
100
150
200
250

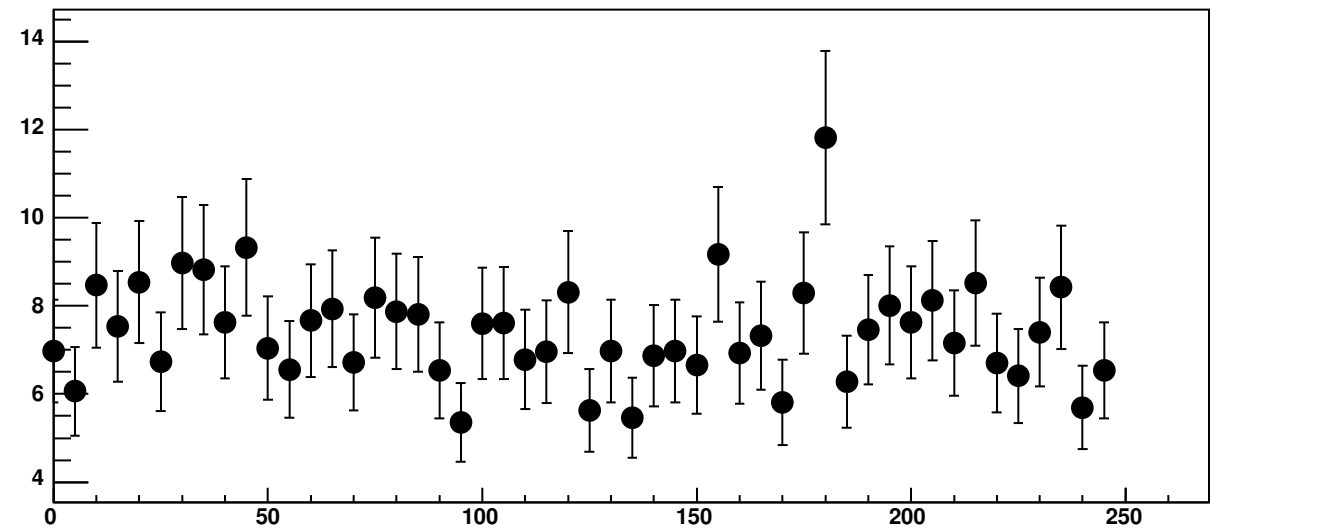

Chip 4, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 4, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold
5
0
-5
-10
-15
0
50
100
150
200
250

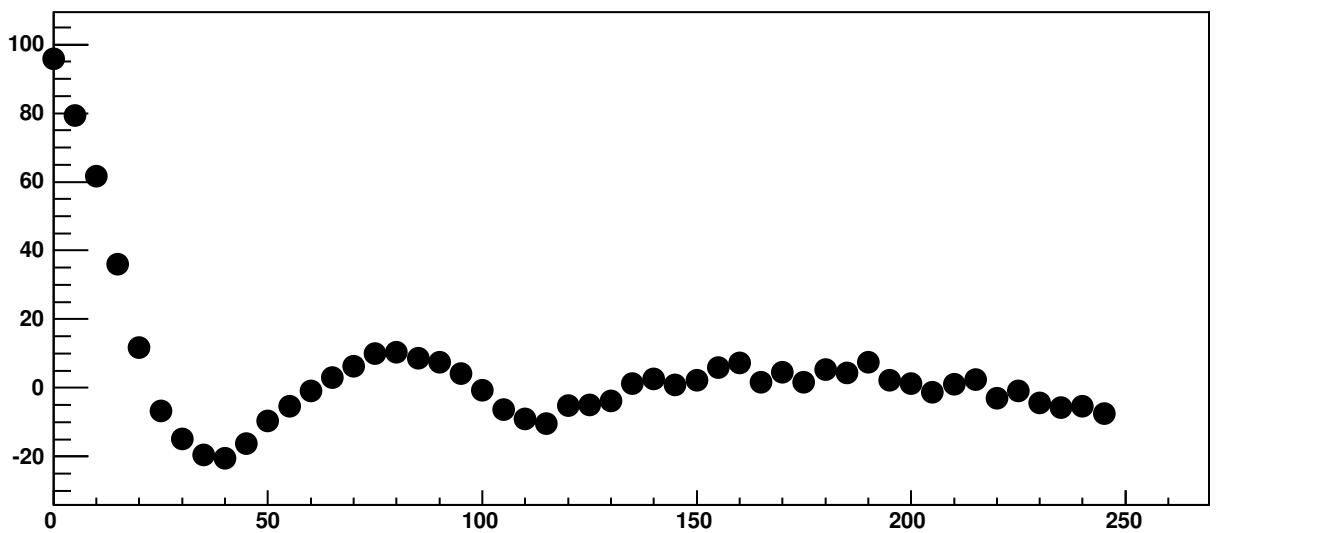
Chip 4, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold



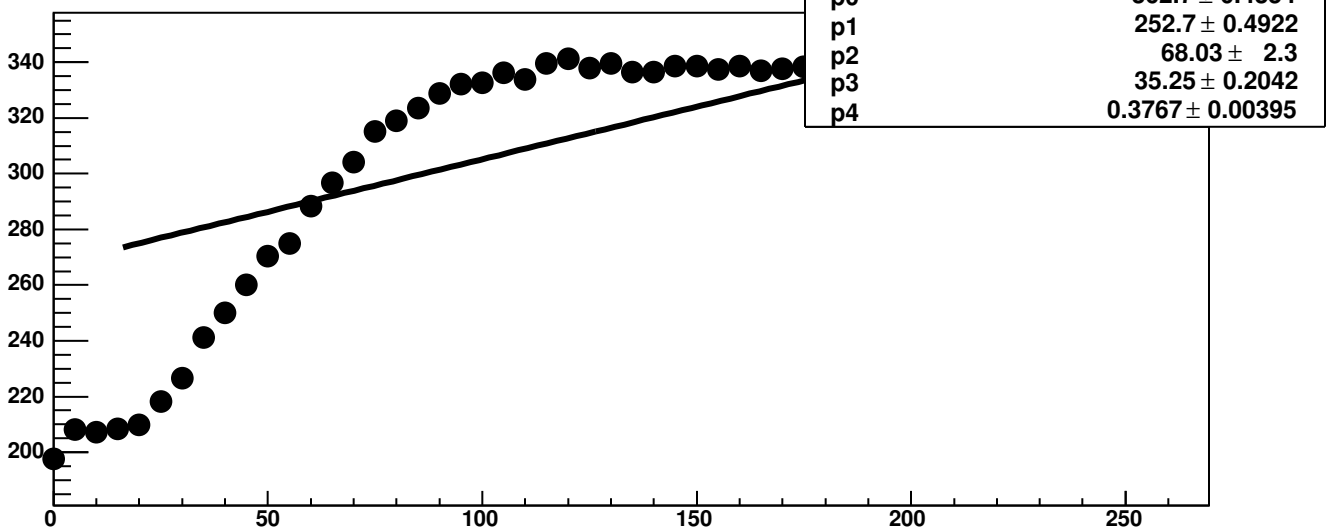
Chip 4, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



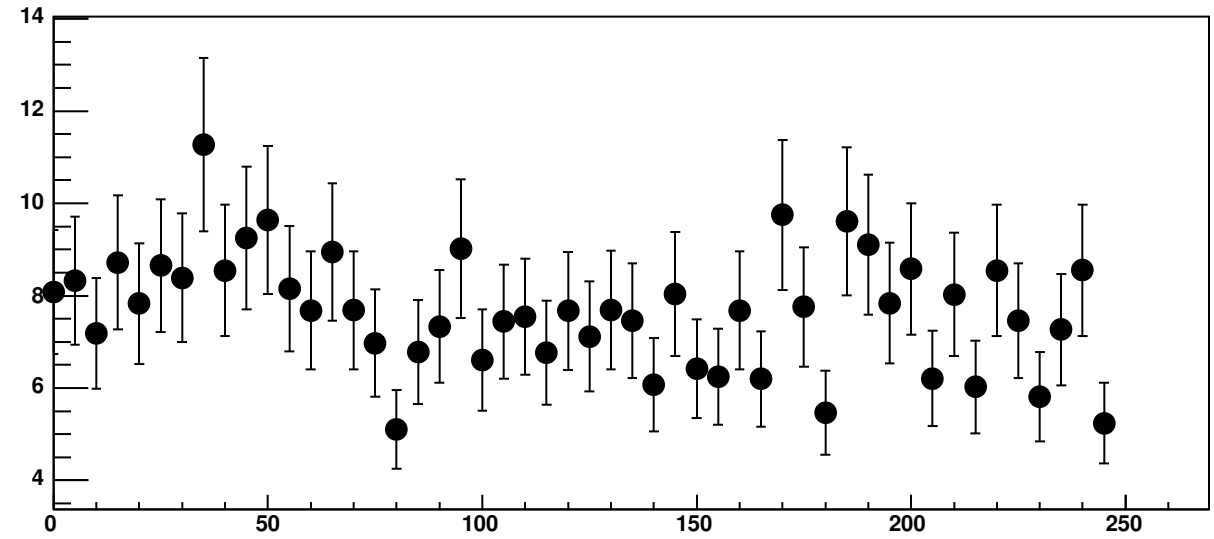
Chip 4, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold



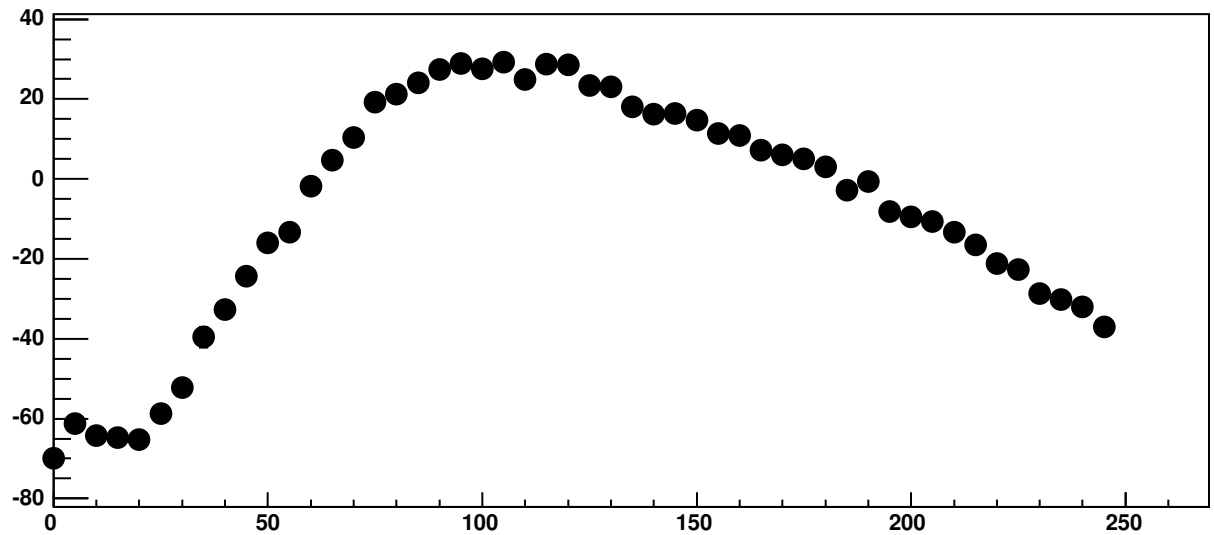
Chip 4, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold



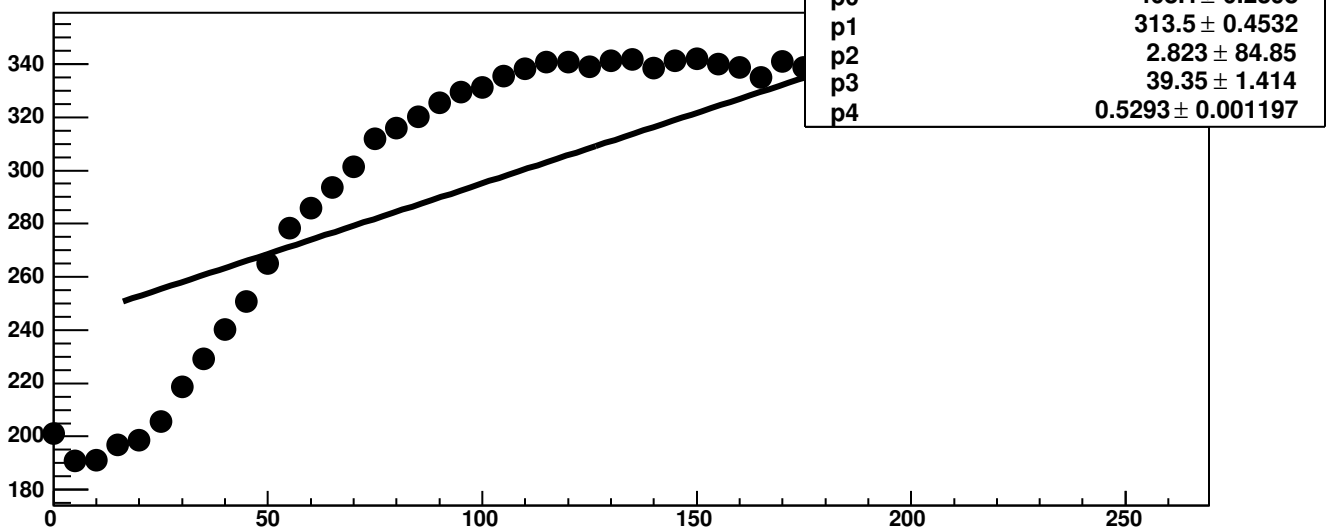
Chip 4, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold



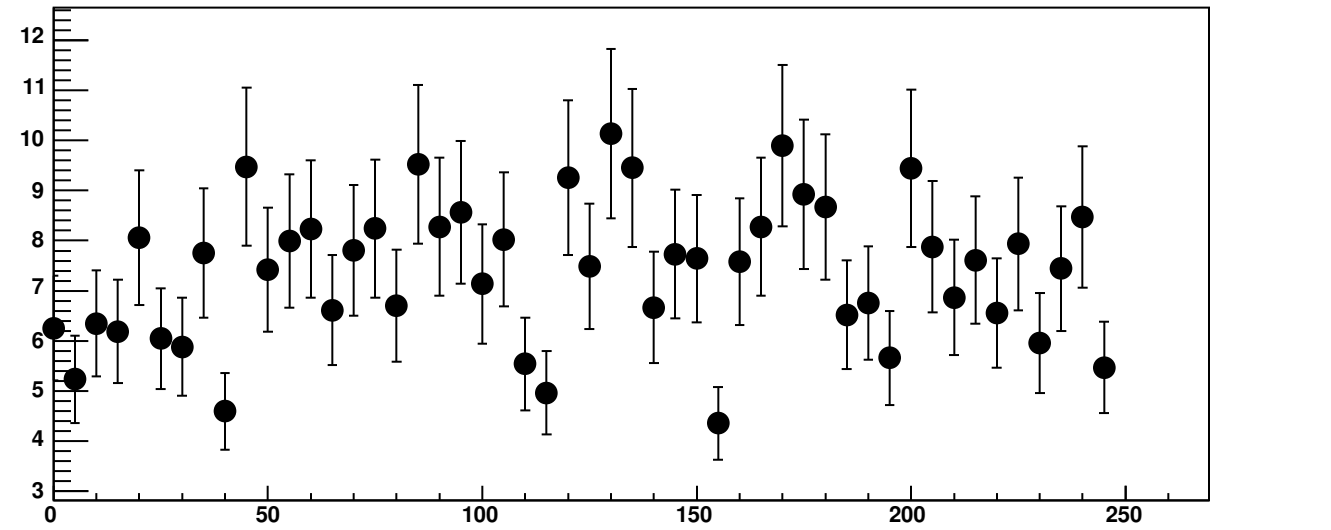
Chip 4, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold



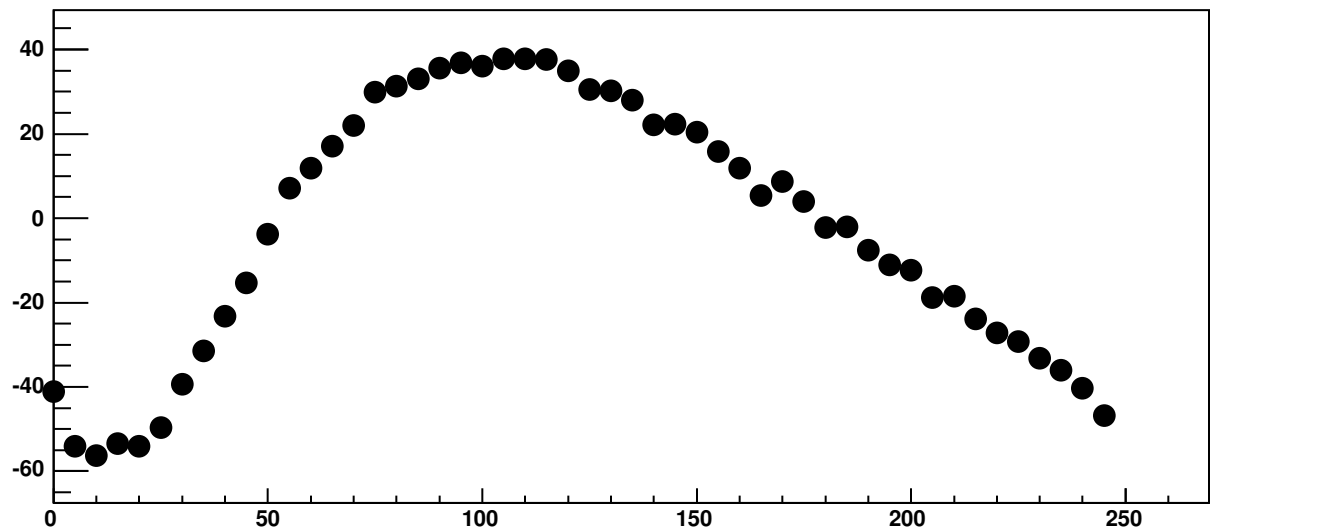
Chip 4, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

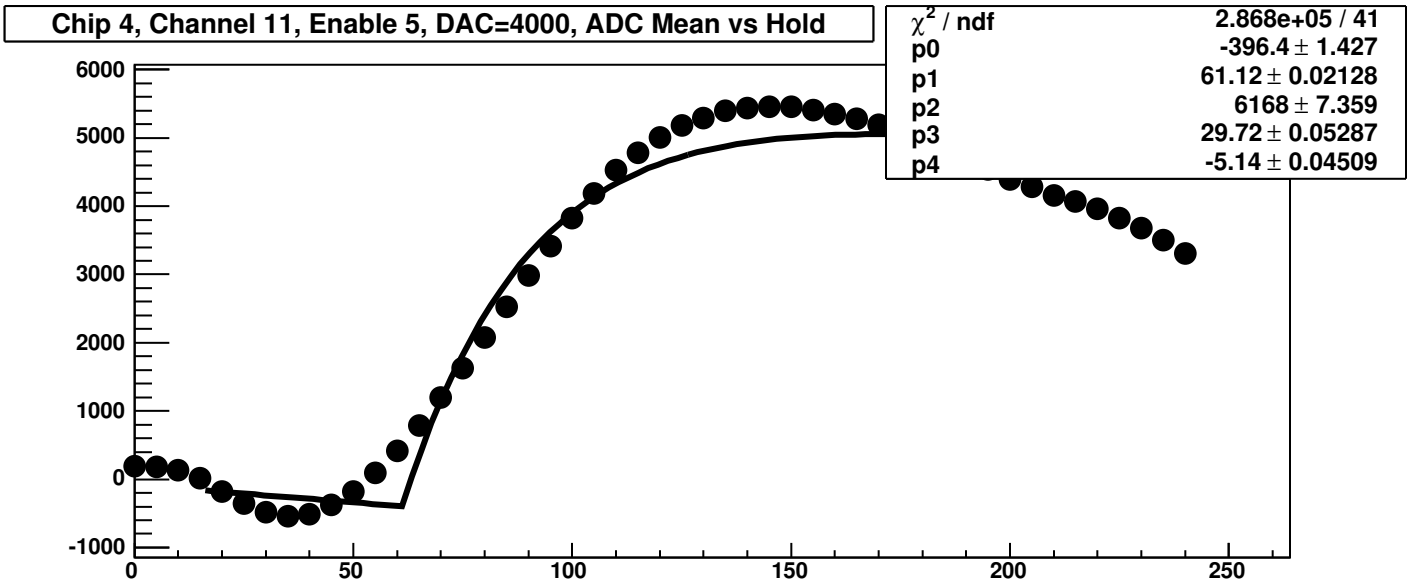


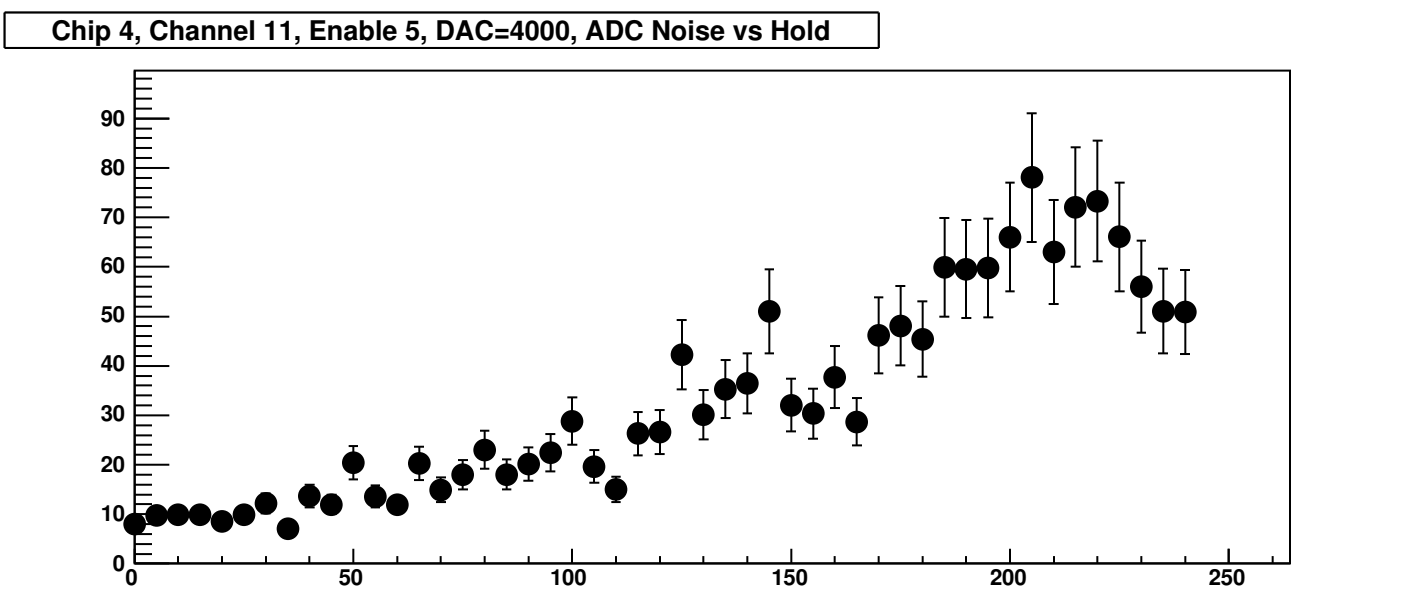
Chip 4, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold

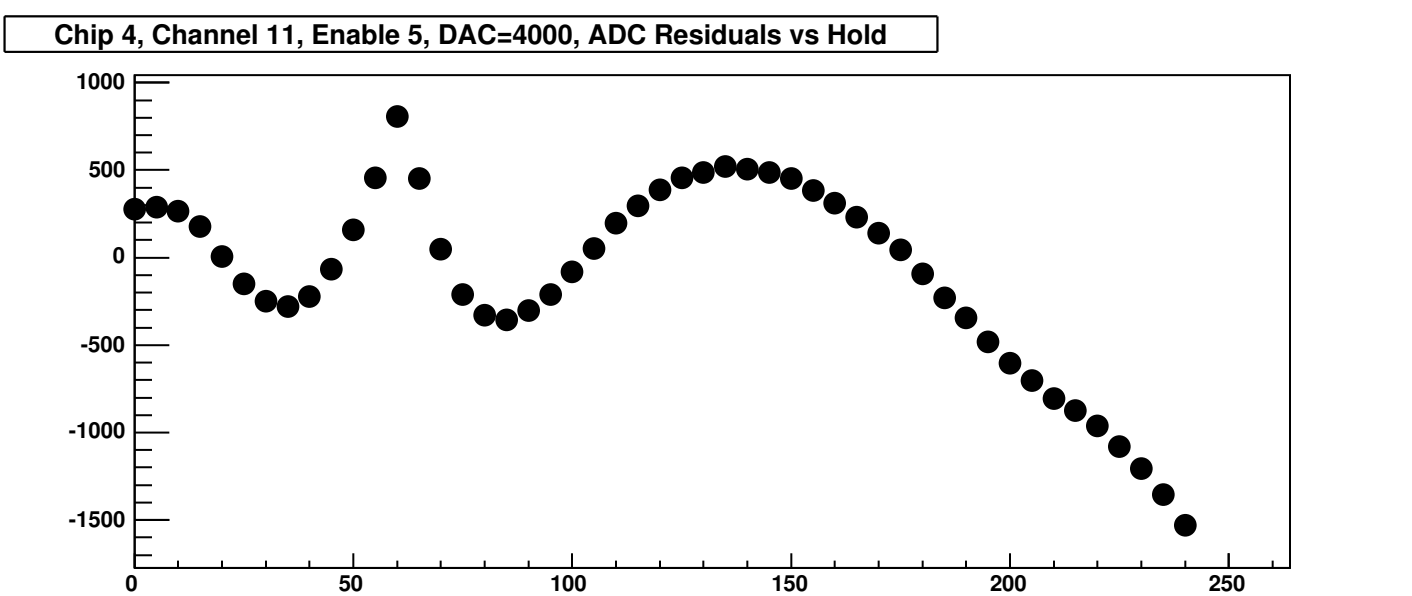


Chip 4, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

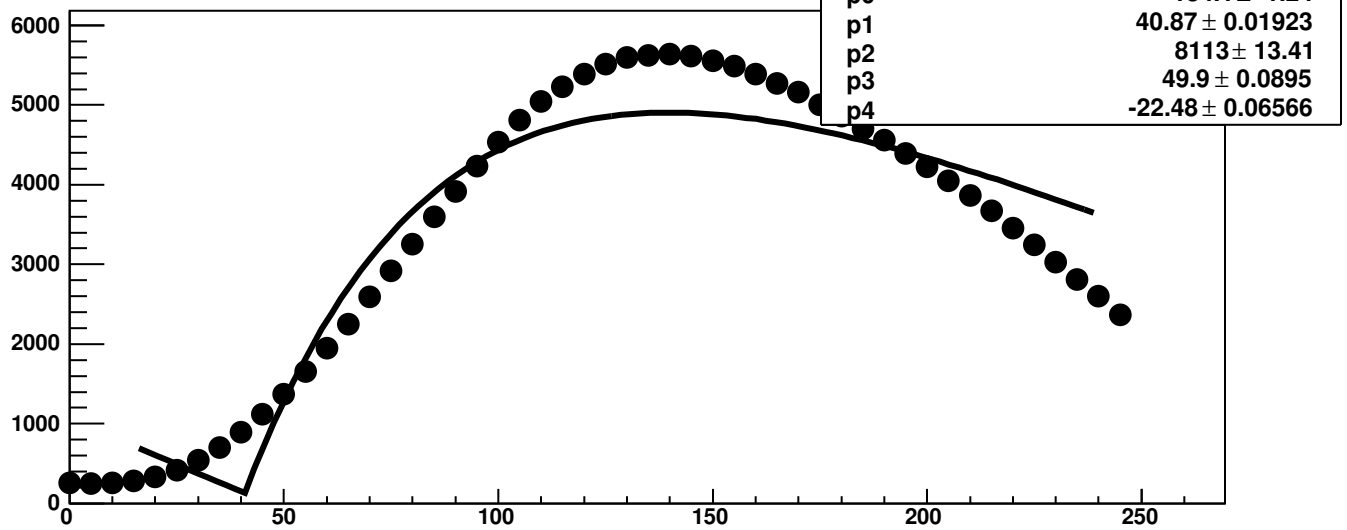



Chip 4, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.868e+05 / 41
p0 -396.4 ± 1.427
p1 61.12 ± 0.02128
p2 6168 ± 7.359
p3 29.72 ± 0.05287
p4 -5.14 ± 0.04509
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

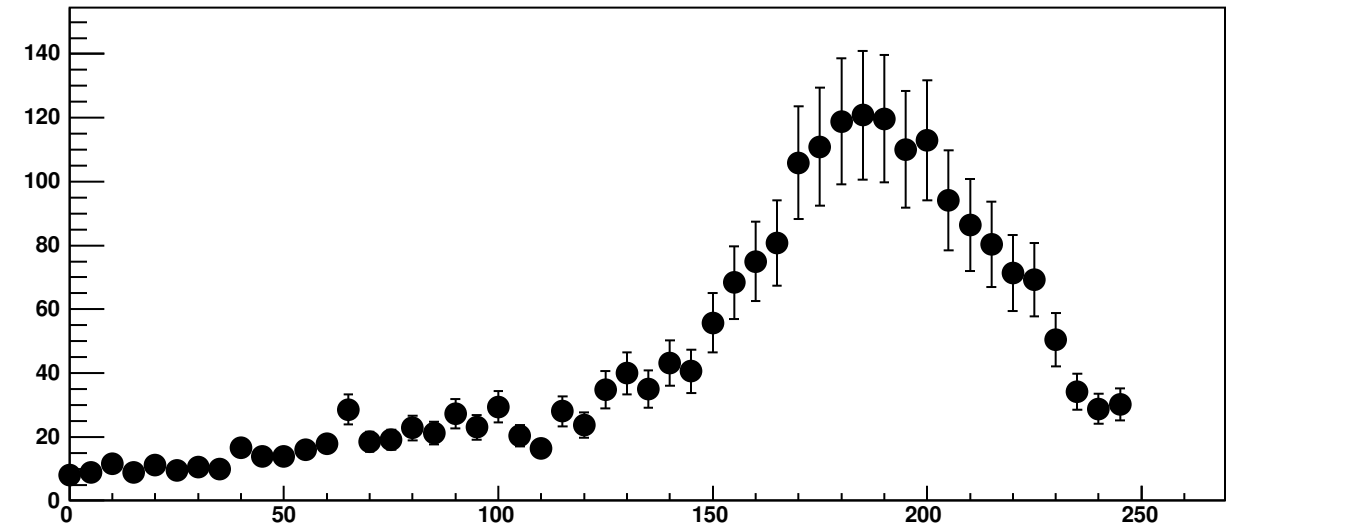

Chip 4, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold
90
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250


Chip 4, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

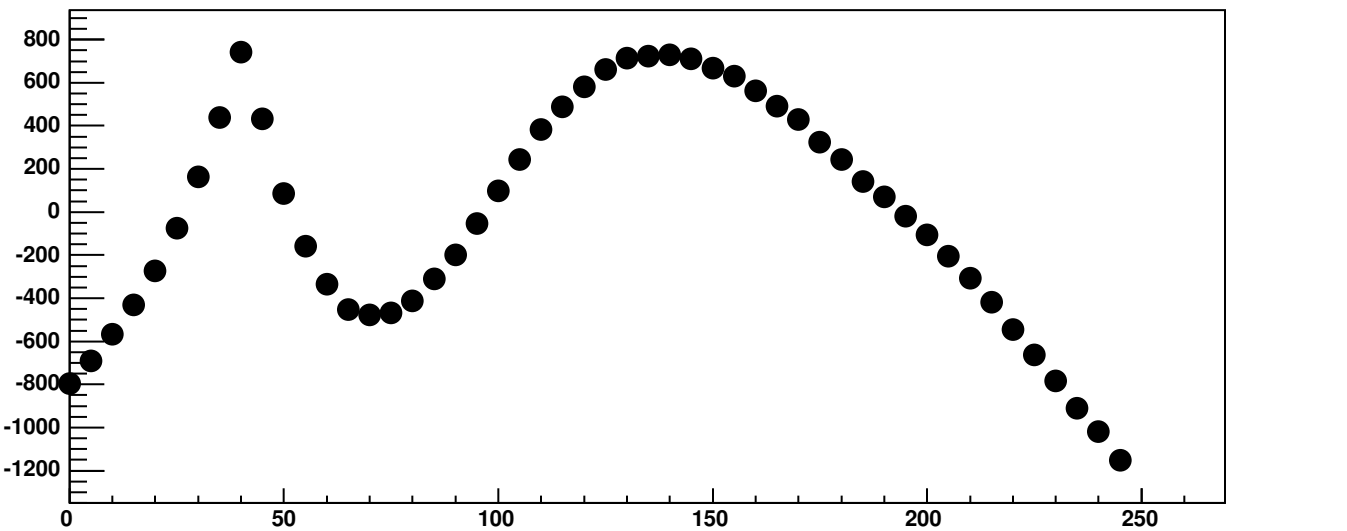
Chip 4, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

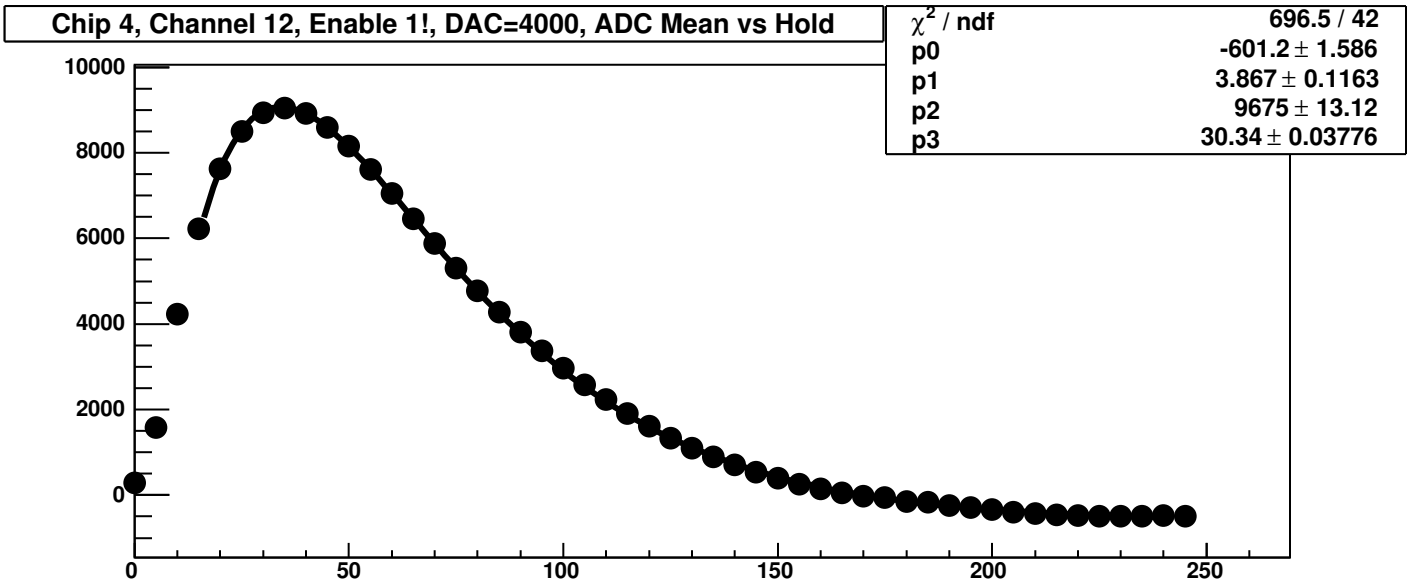


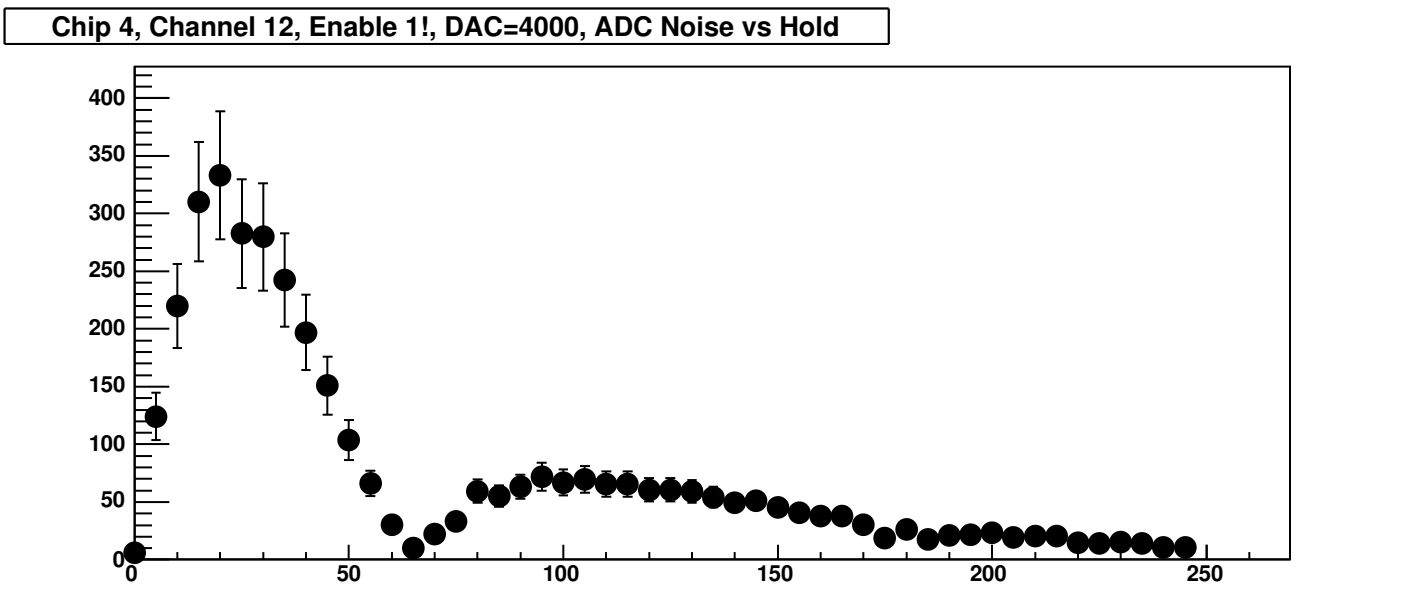
Chip 4, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold

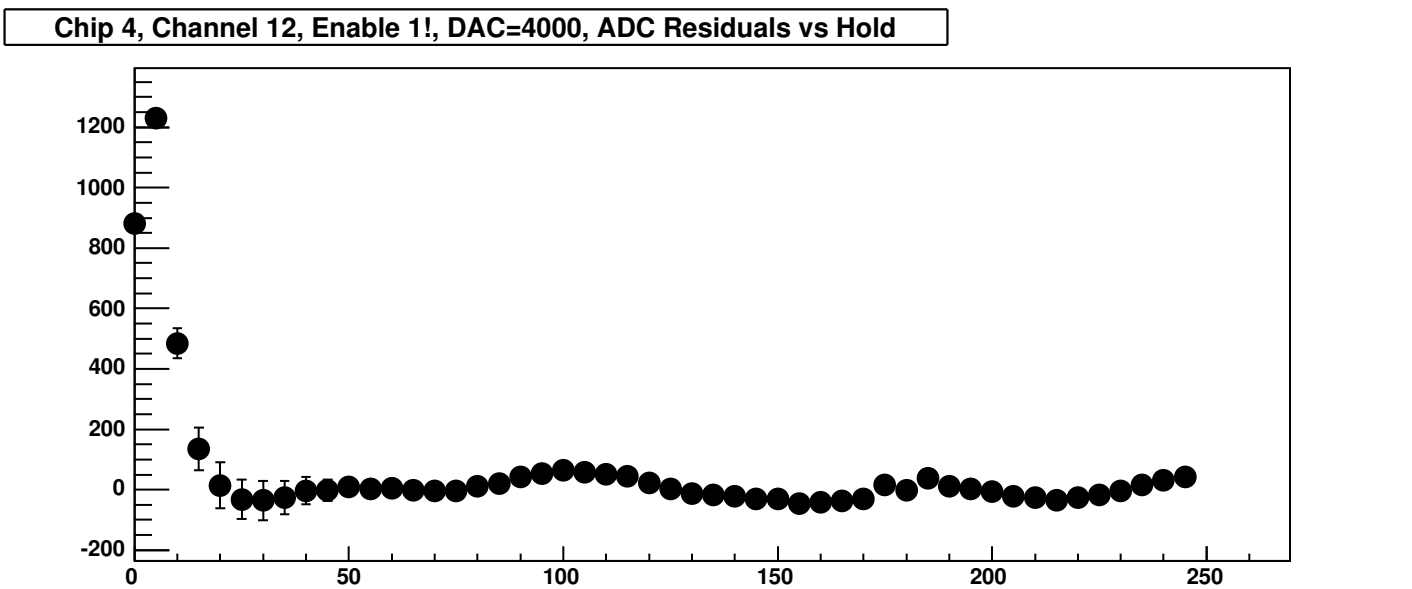


Chip 4, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

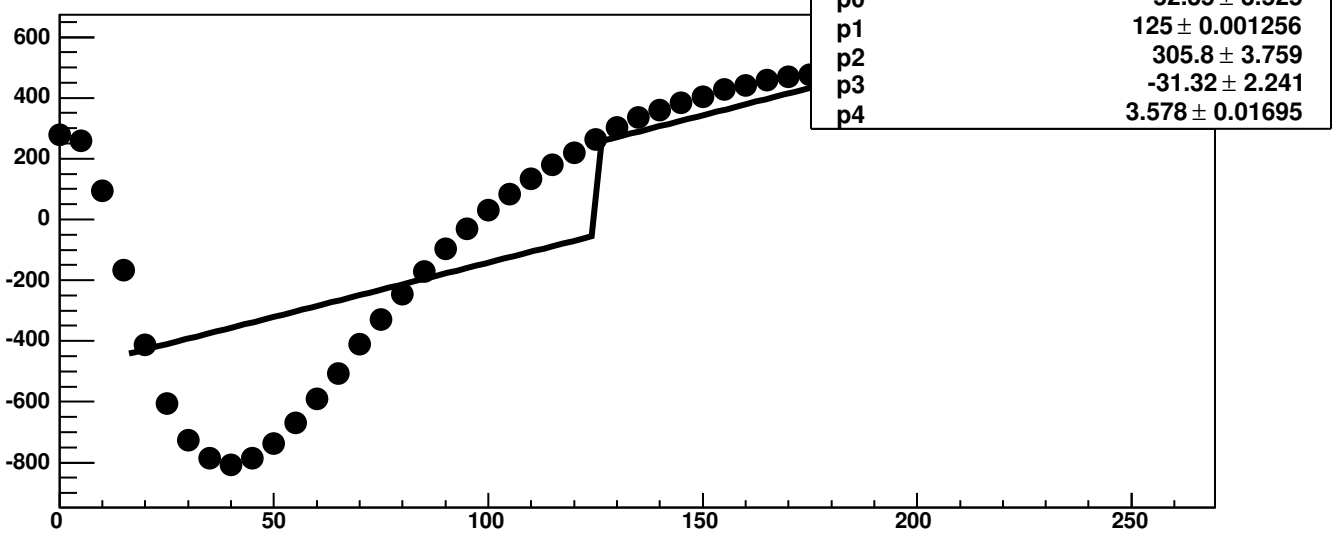



Chip 4, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 696.5 / 42
p0 -601.2 ± 1.586
p1 3.867 ± 0.1163
p2 9675 ± 13.12
p3 30.34 ± 0.03776
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

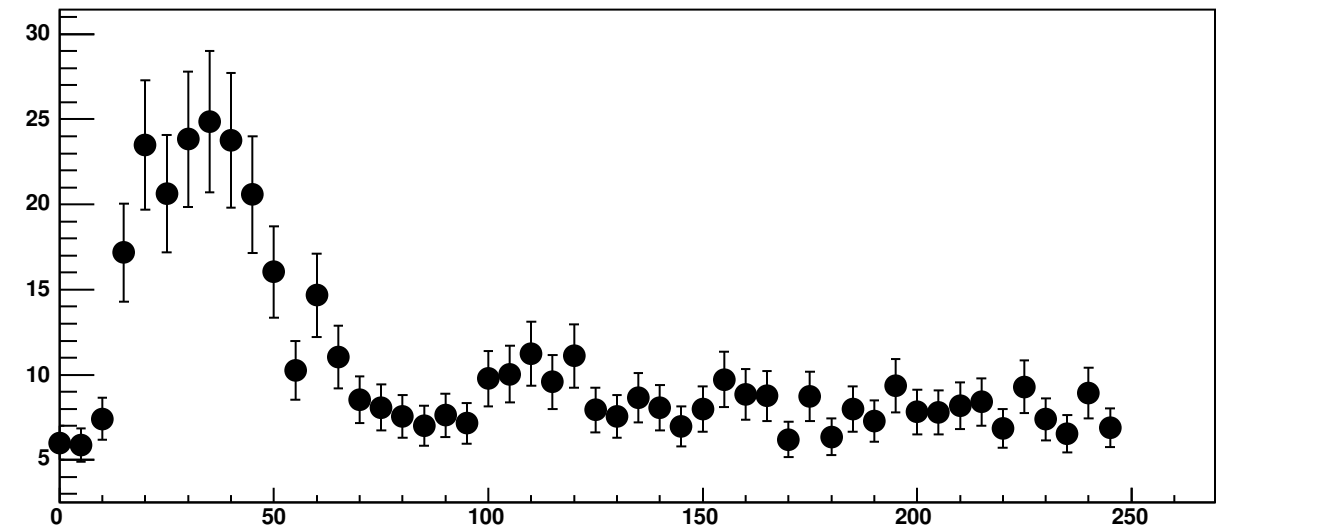

Chip 4, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold
400
350
300
250
200
150
100
50
0
0
50
100
150
200
250


Chip 4, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

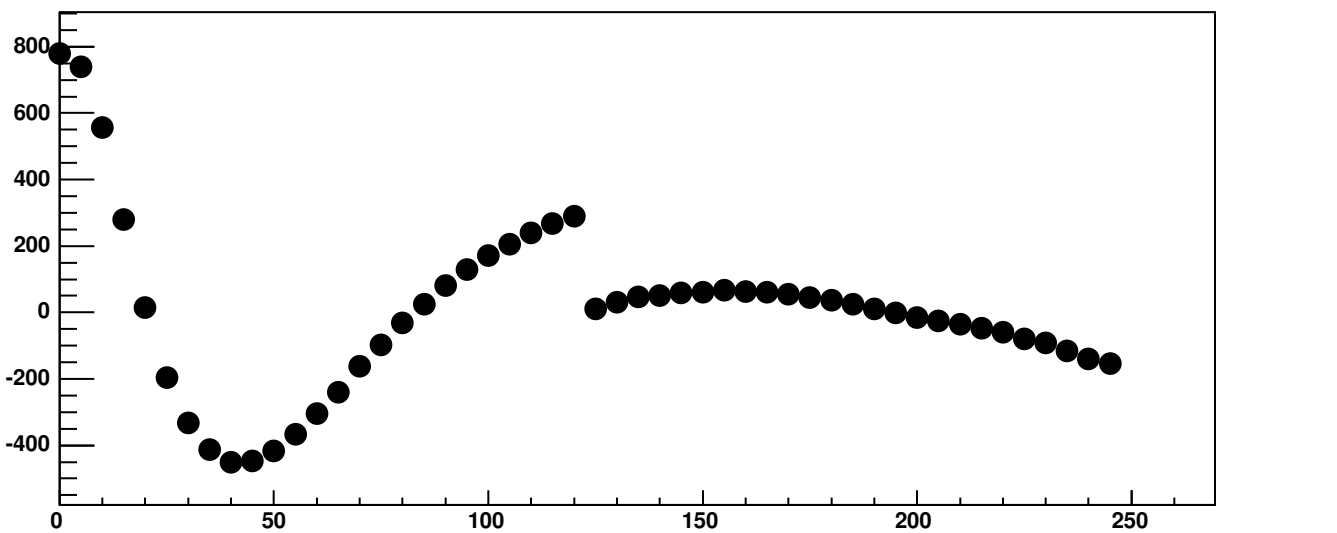
Chip 4, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold



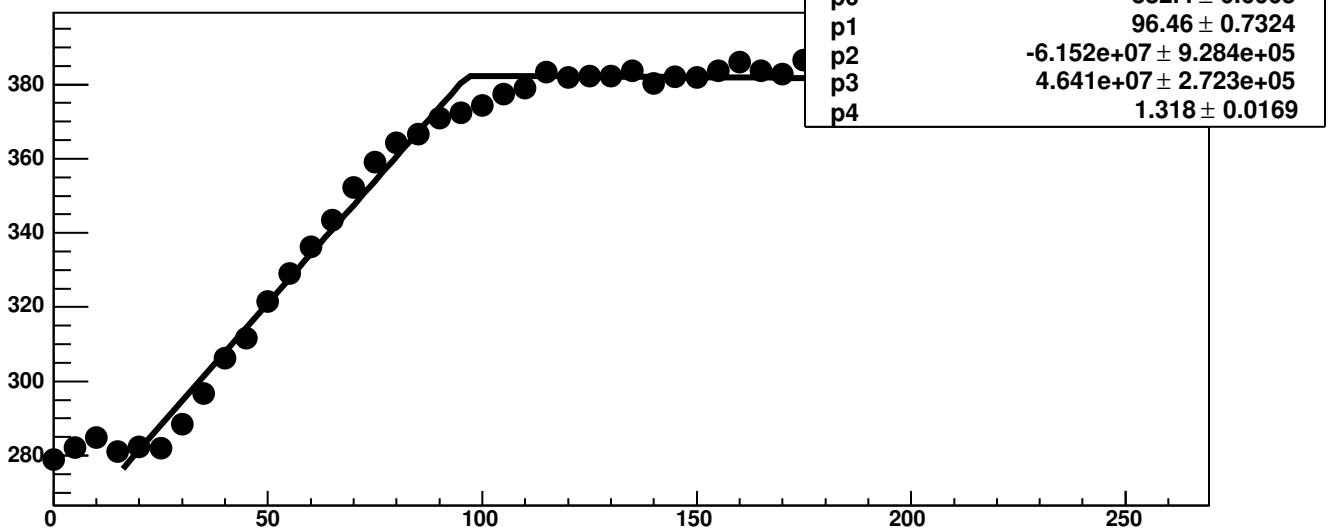
Chip 4, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



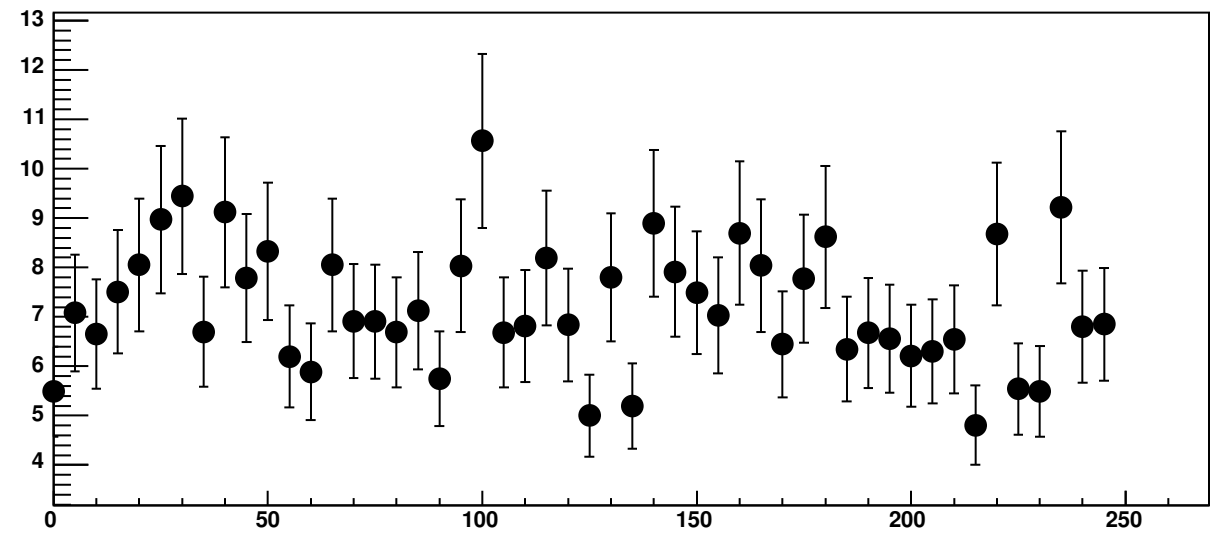
Chip 4, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold



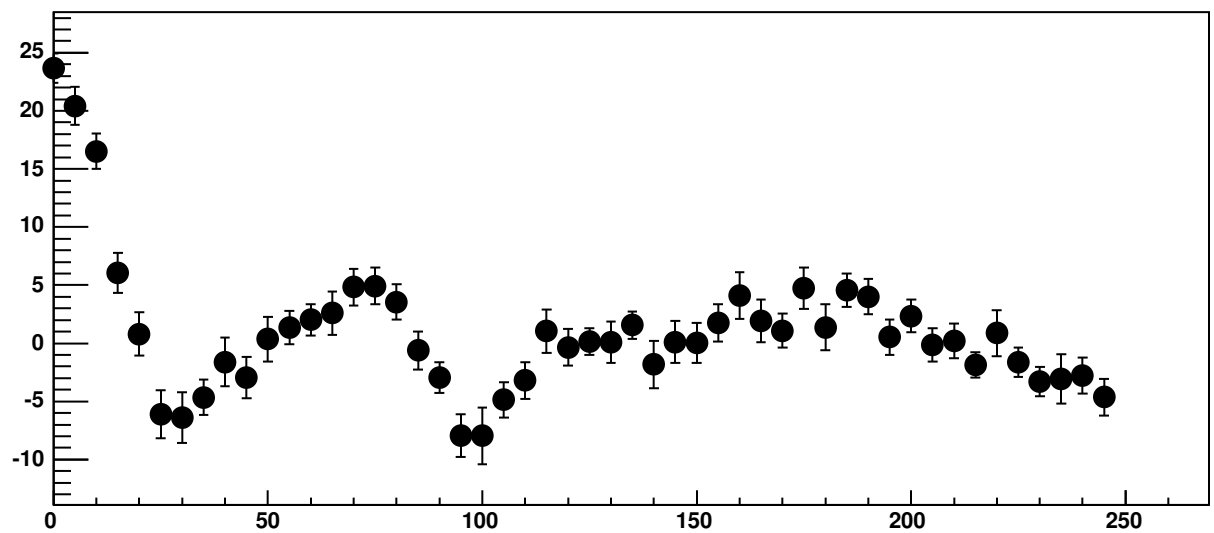
Chip 4, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

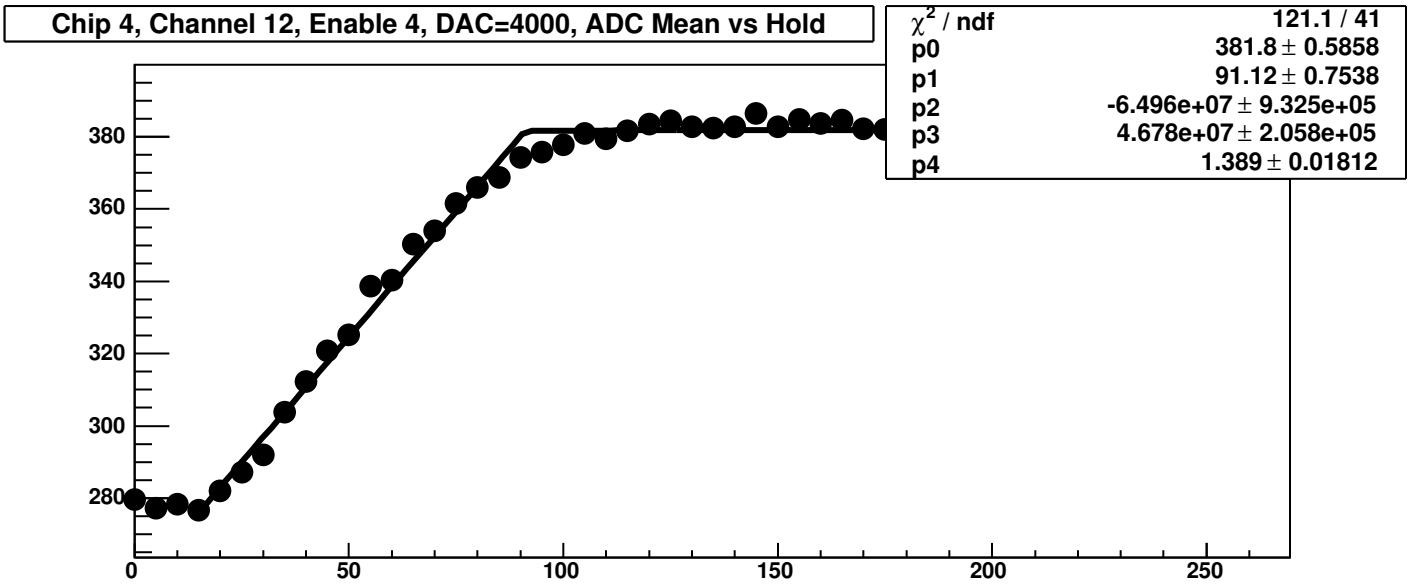


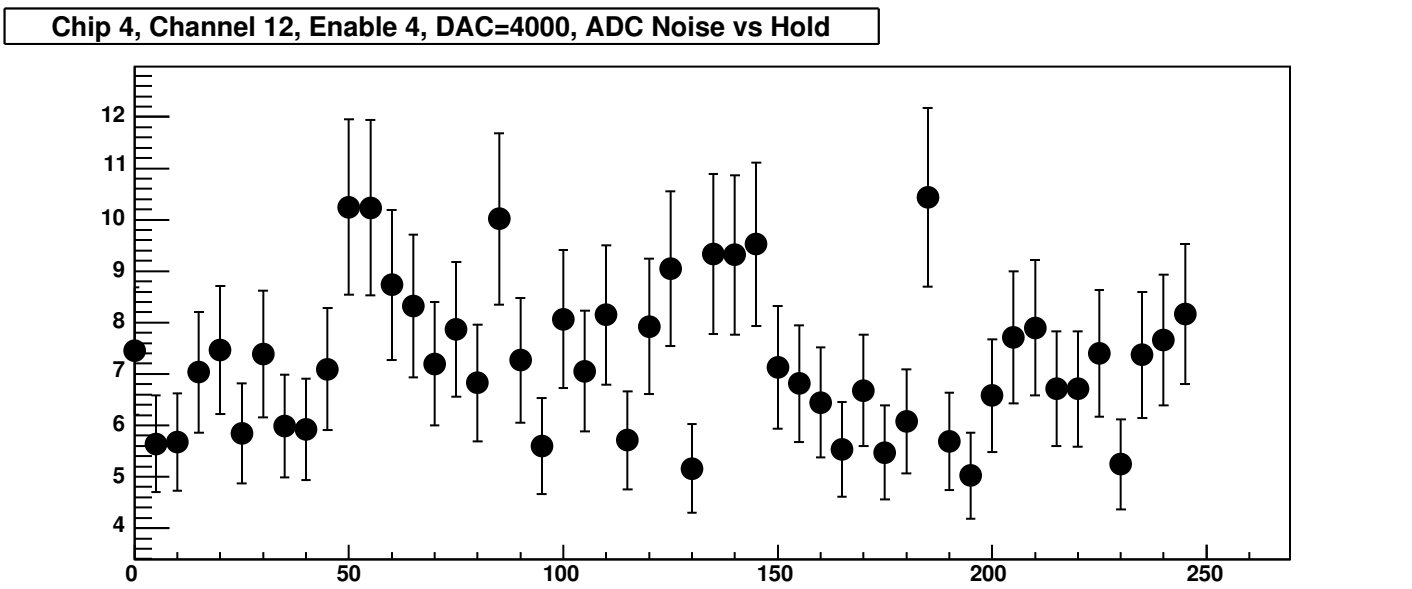
Chip 4, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold

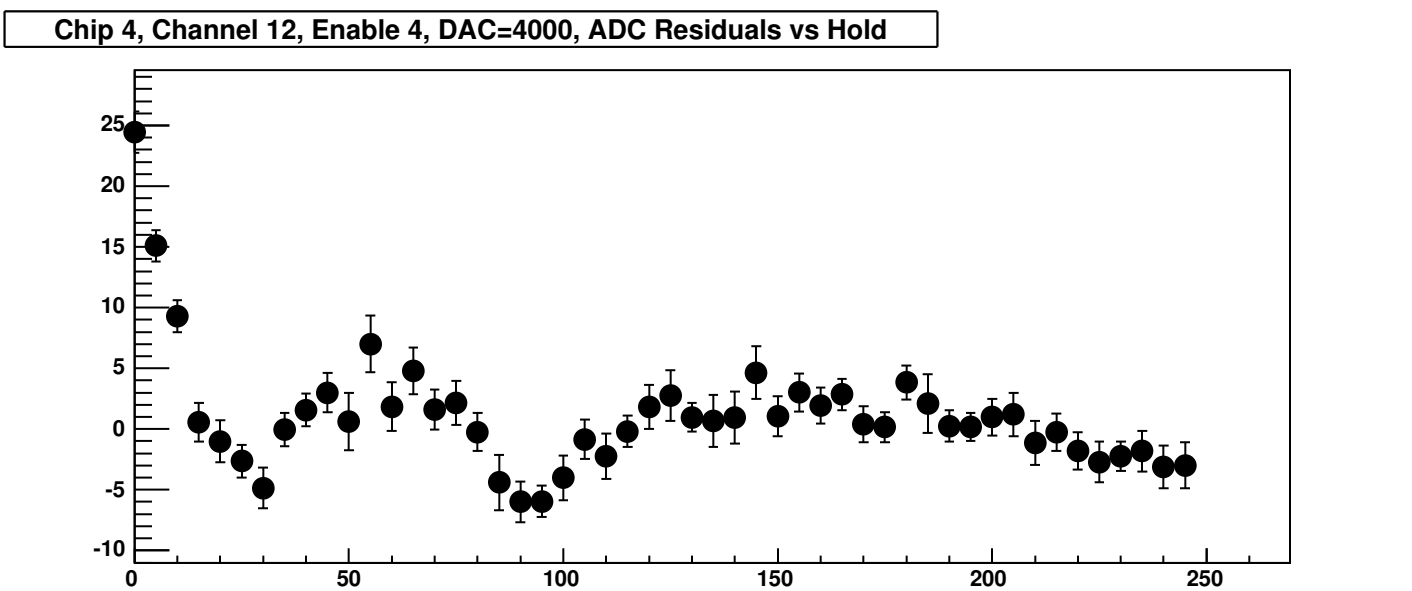


Chip 4, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

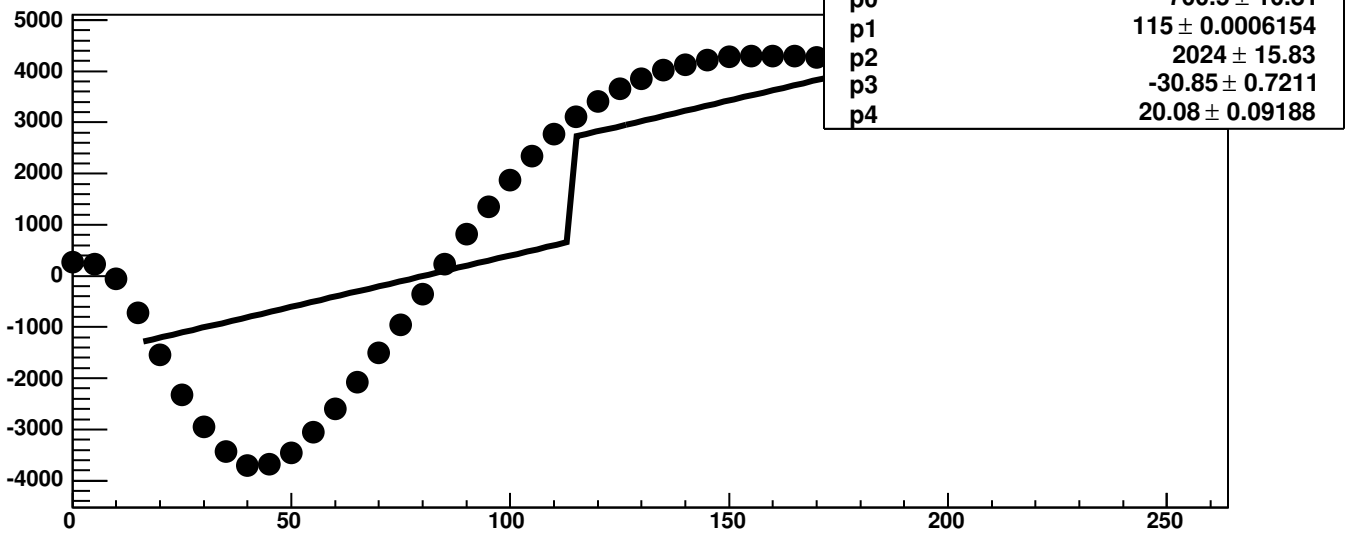



Chip 4, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 121.1 / 41
p0 381.8 ± 0.5858
p1 91.12 ± 0.7538
p2 -6.496e+07 ± 9.325e+05
p3 4.678e+07 ± 2.058e+05
p4 1.389 ± 0.01812
380
360
340
320
300
280
0
50
100
150
200
250

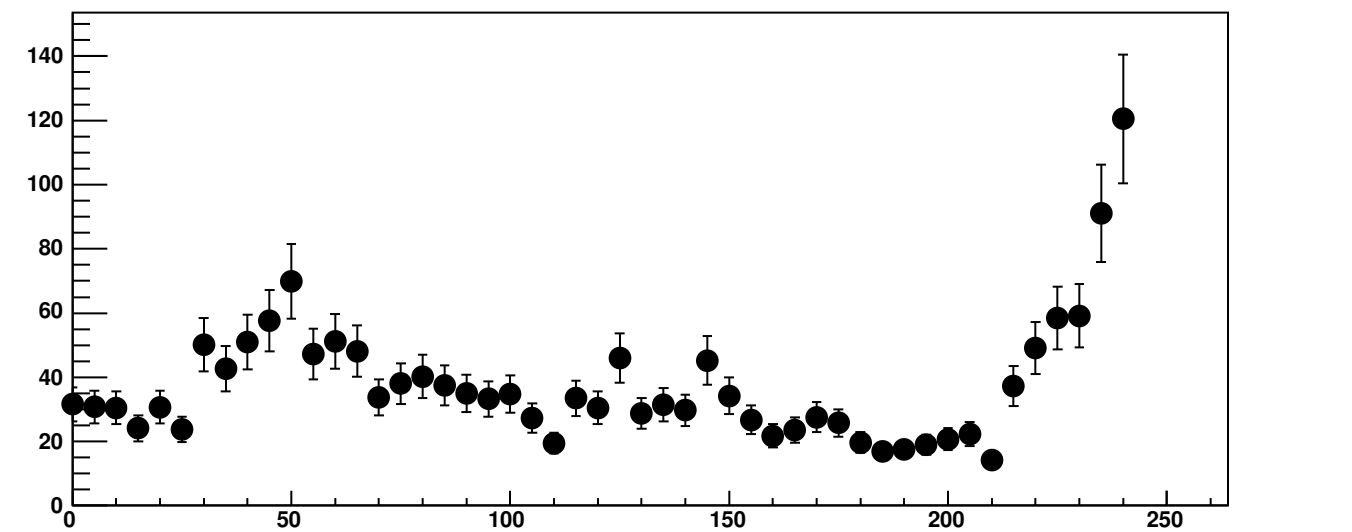

Chip 4, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 4, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
0
50
100
150
200
250

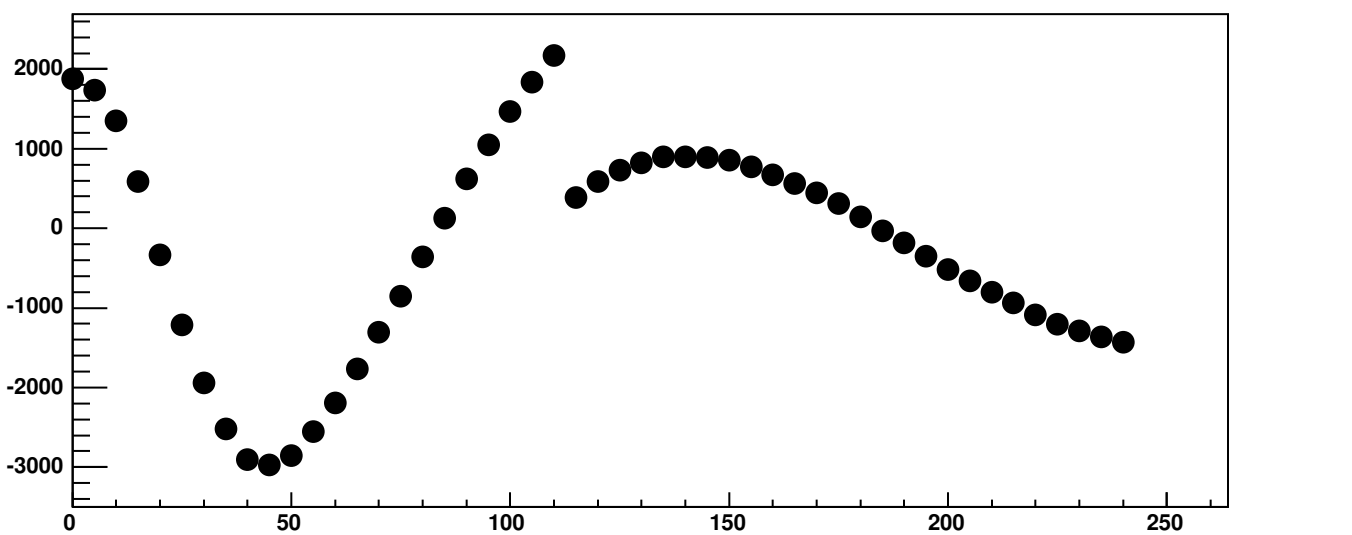
Chip 4, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold



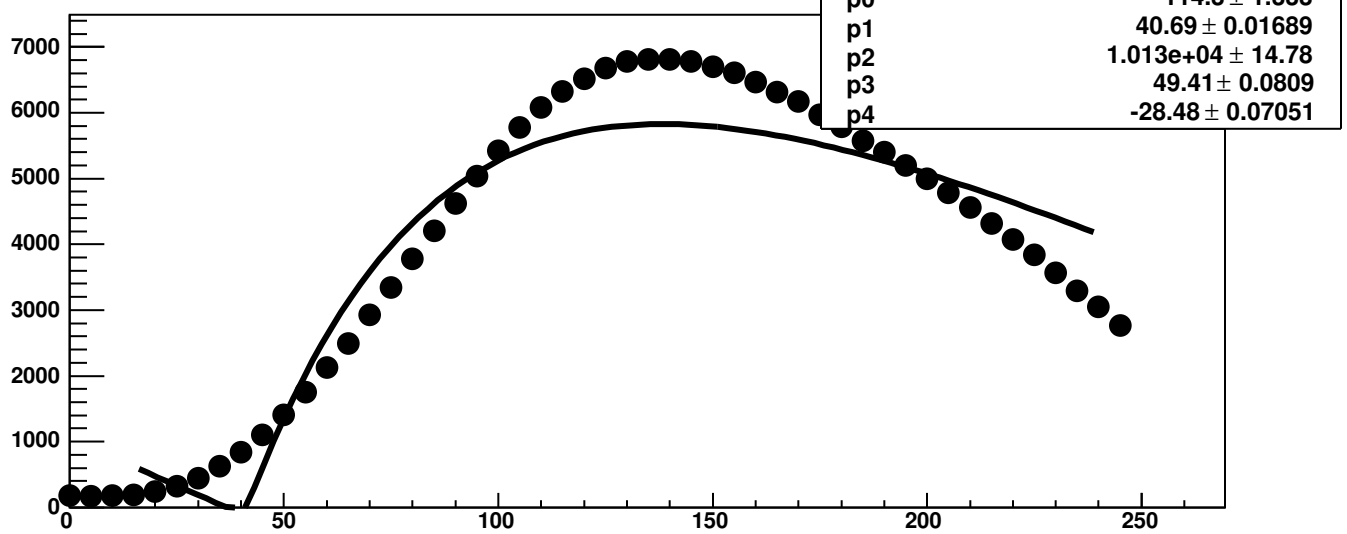
Chip 4, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold



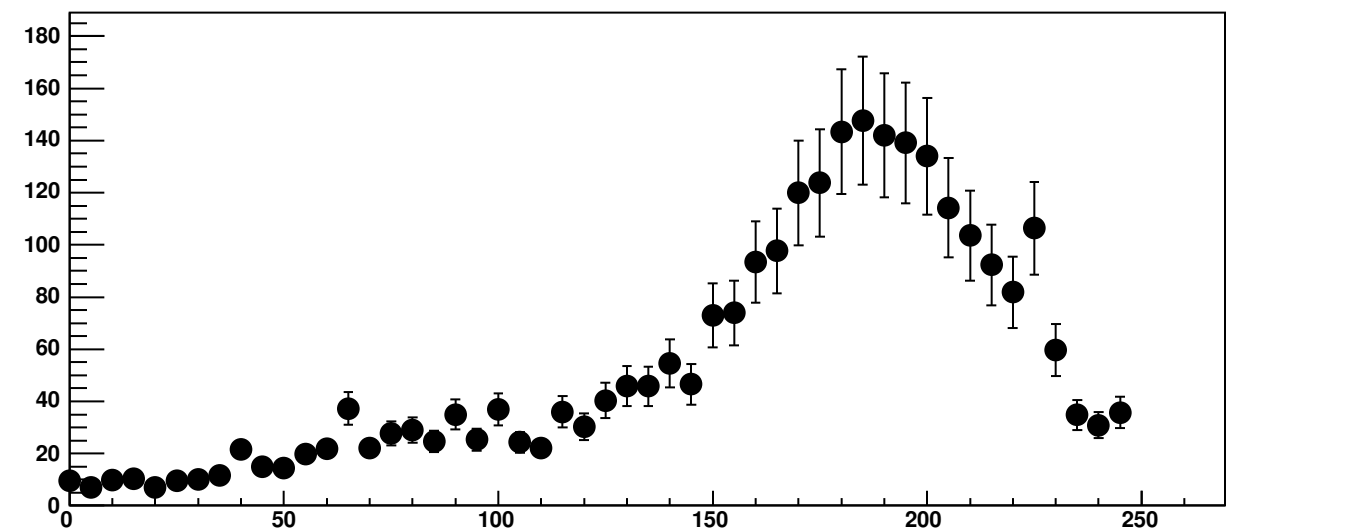
Chip 4, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold



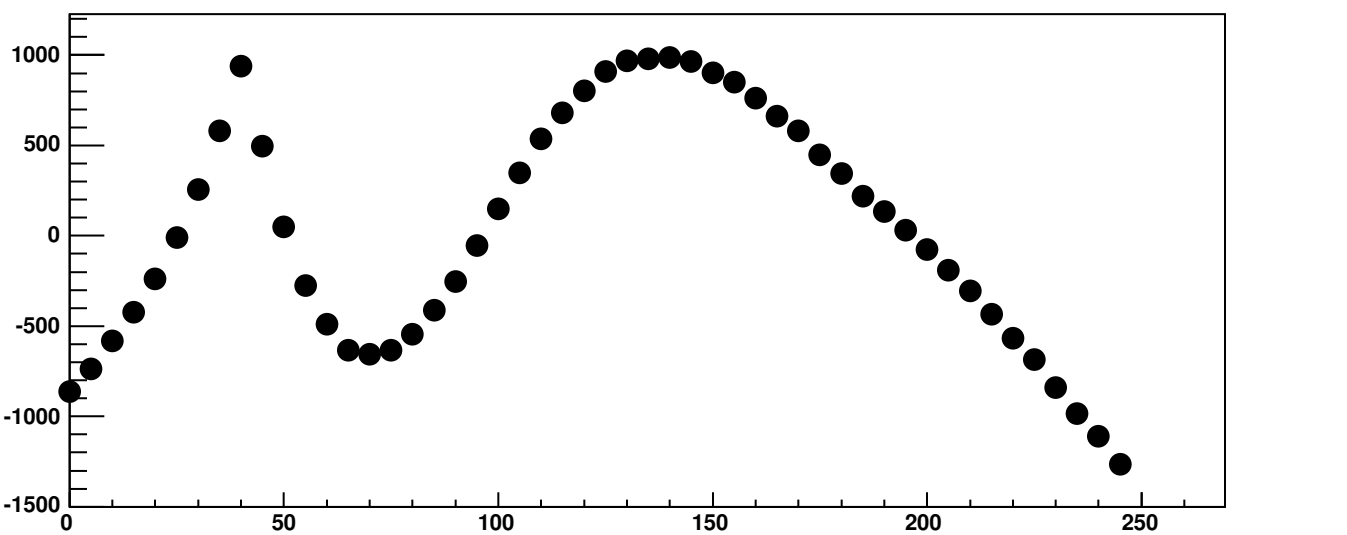
Chip 4, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold



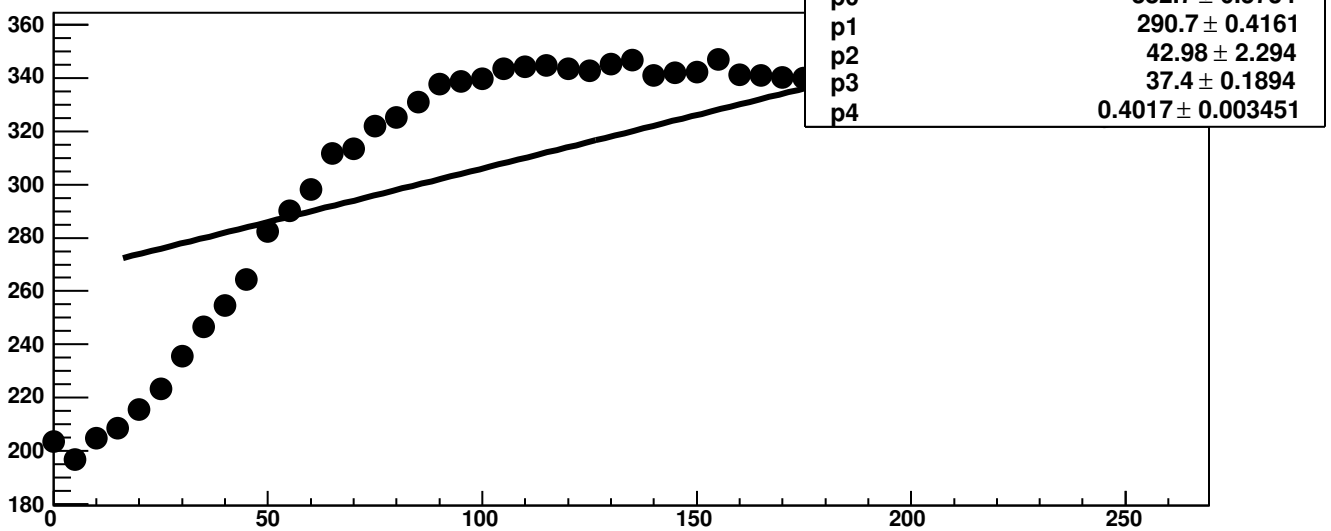
Chip 4, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



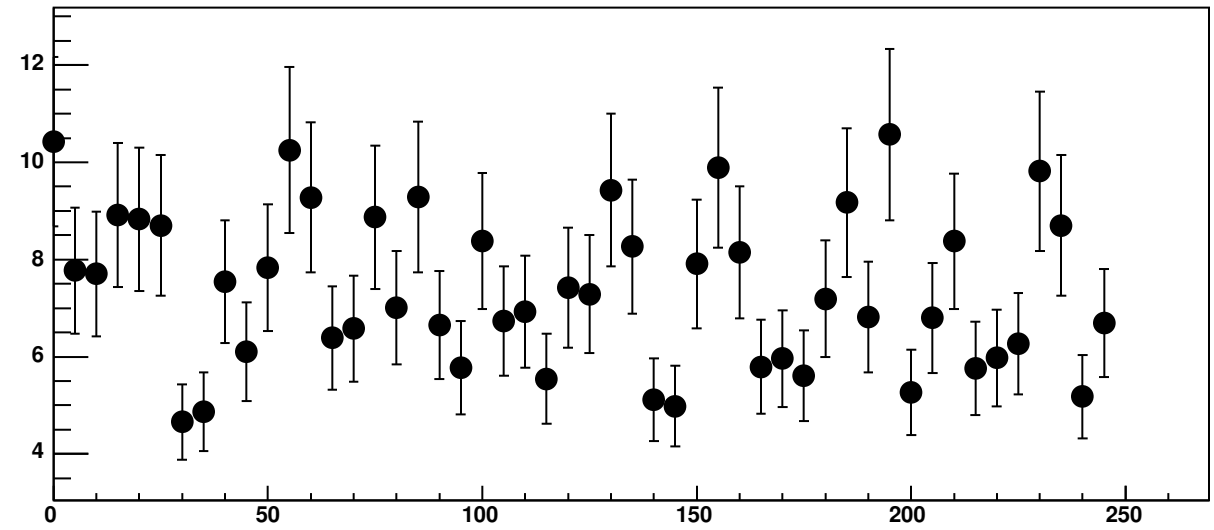
Chip 4, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold



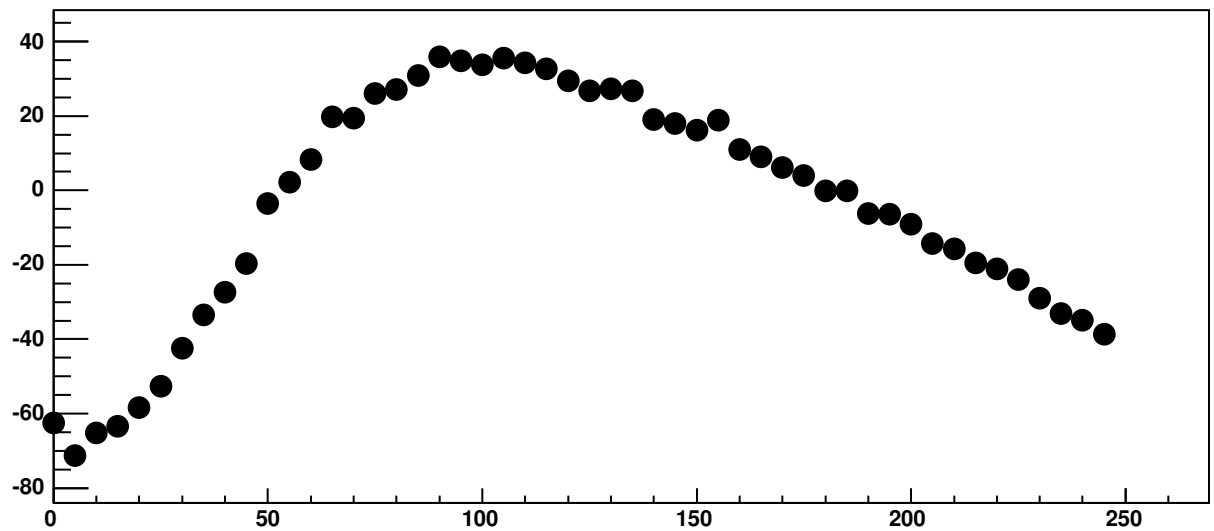
Chip 4, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold



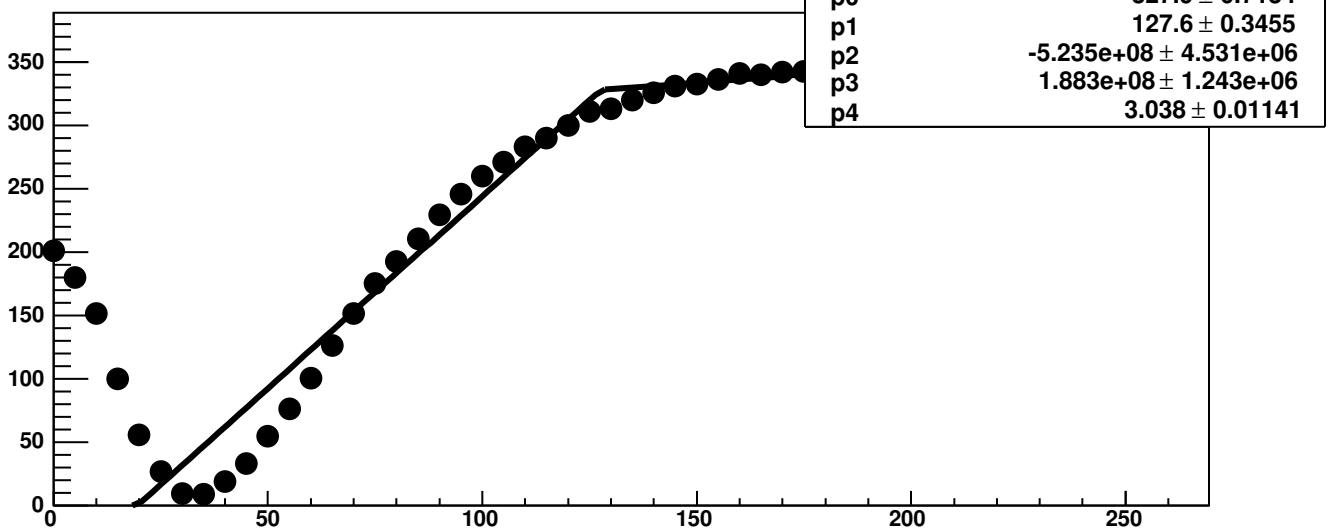
Chip 4, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold



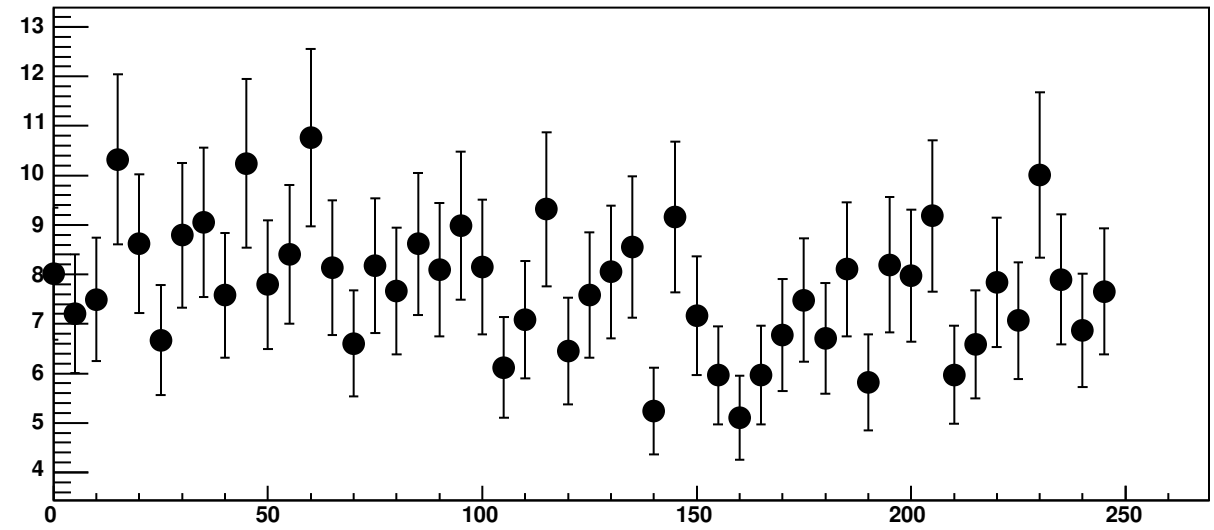
Chip 4, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold



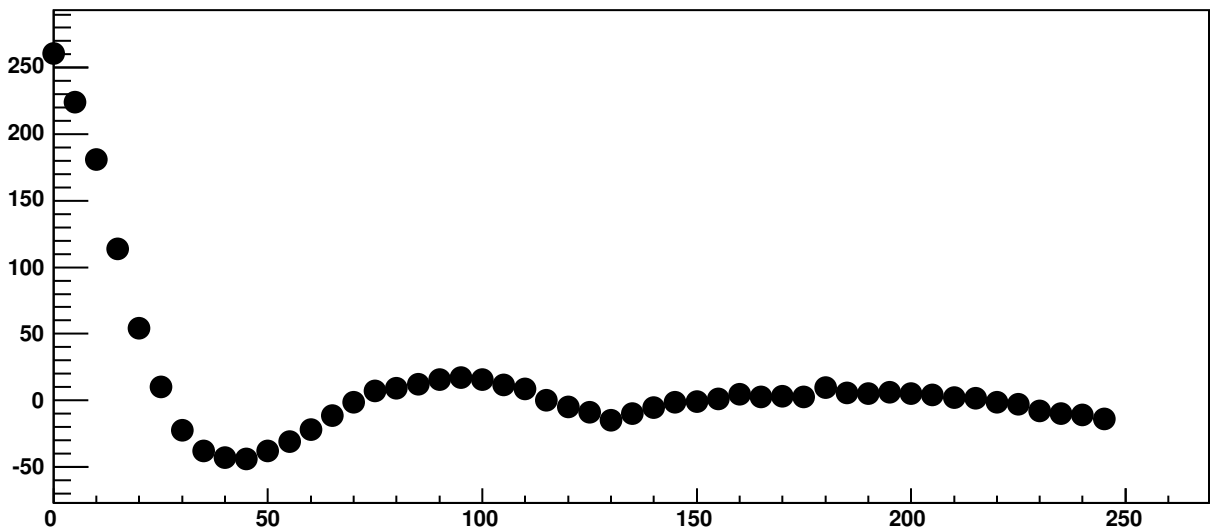
Chip 4, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold



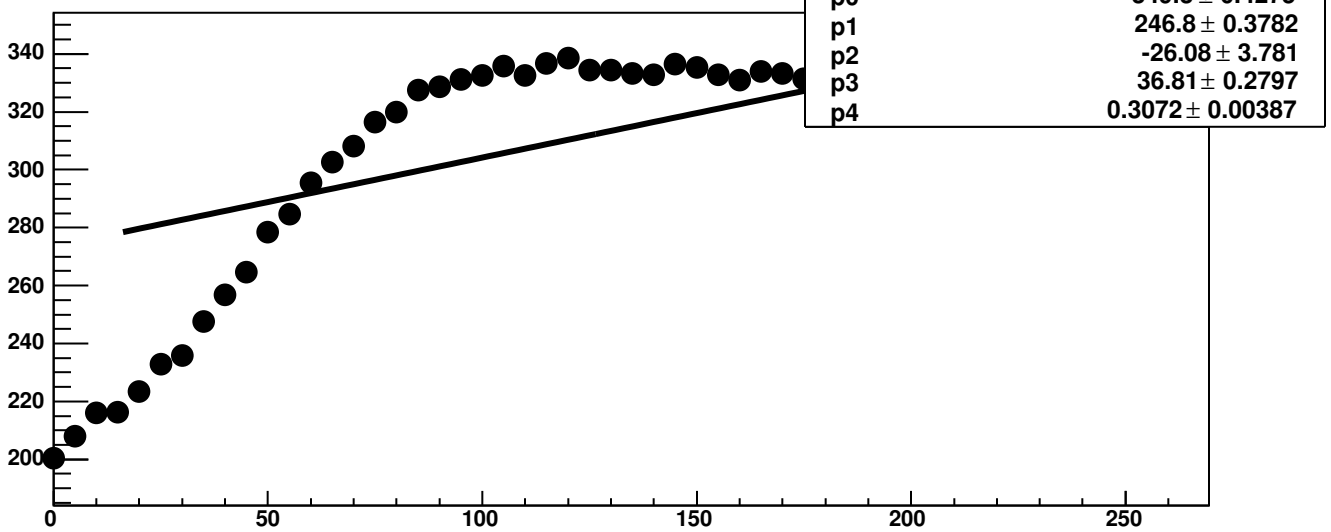
Chip 4, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



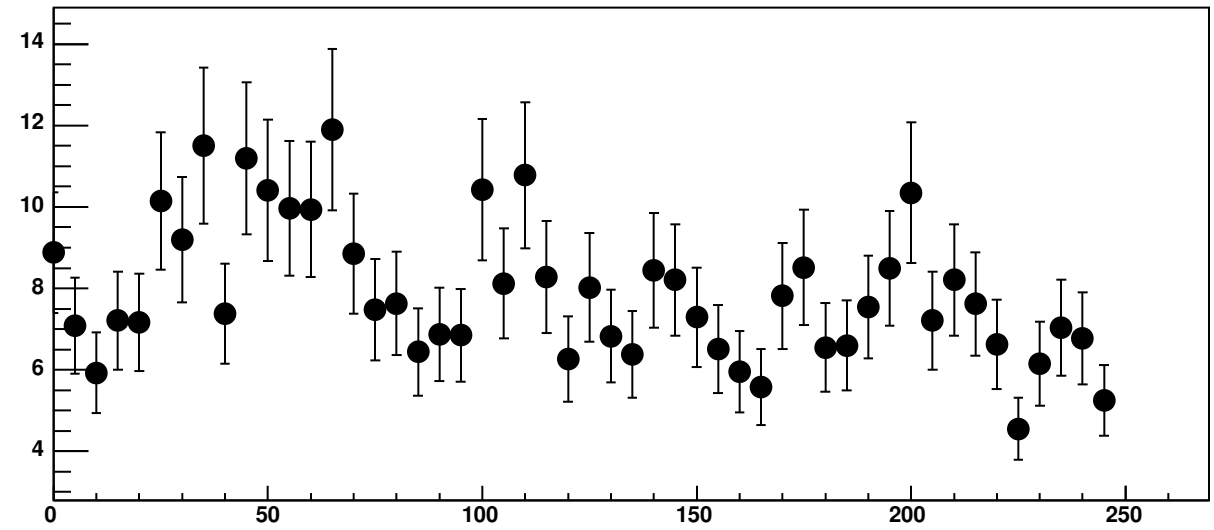
Chip 4, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold



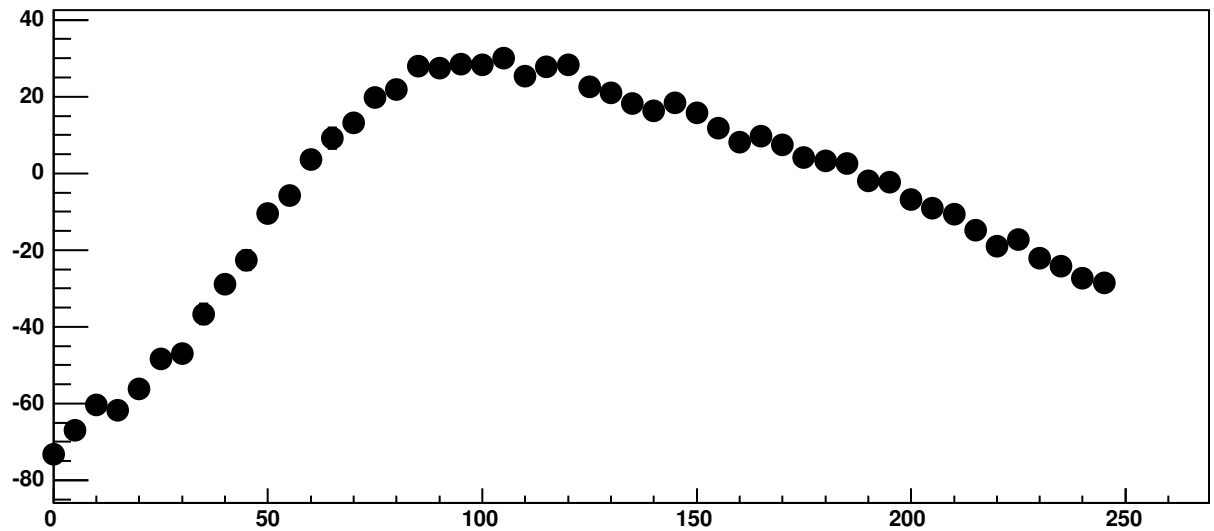
Chip 4, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold



Chip 4, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold

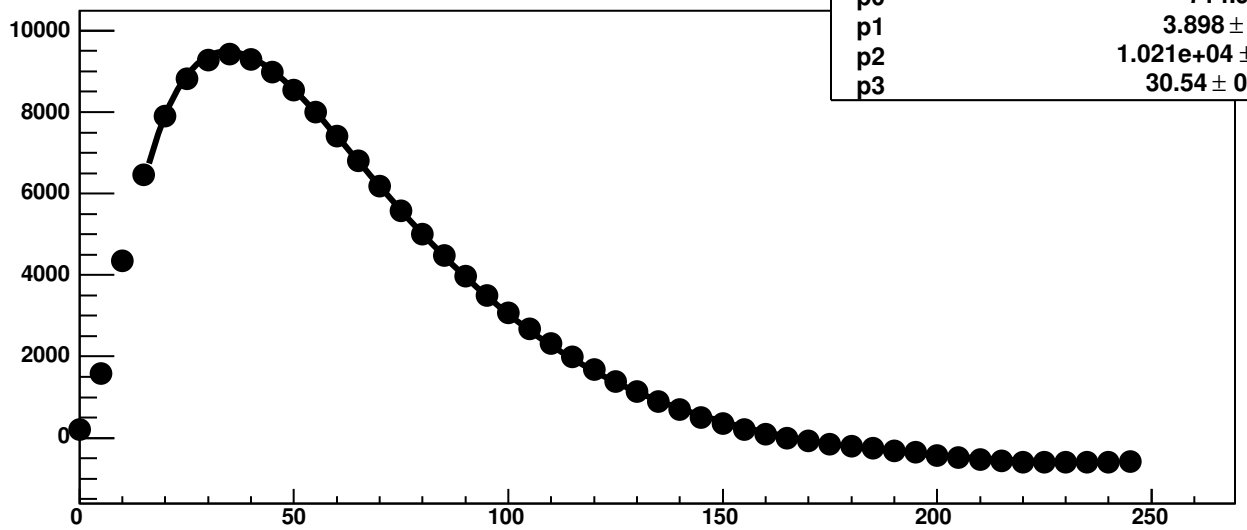


Chip 4, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

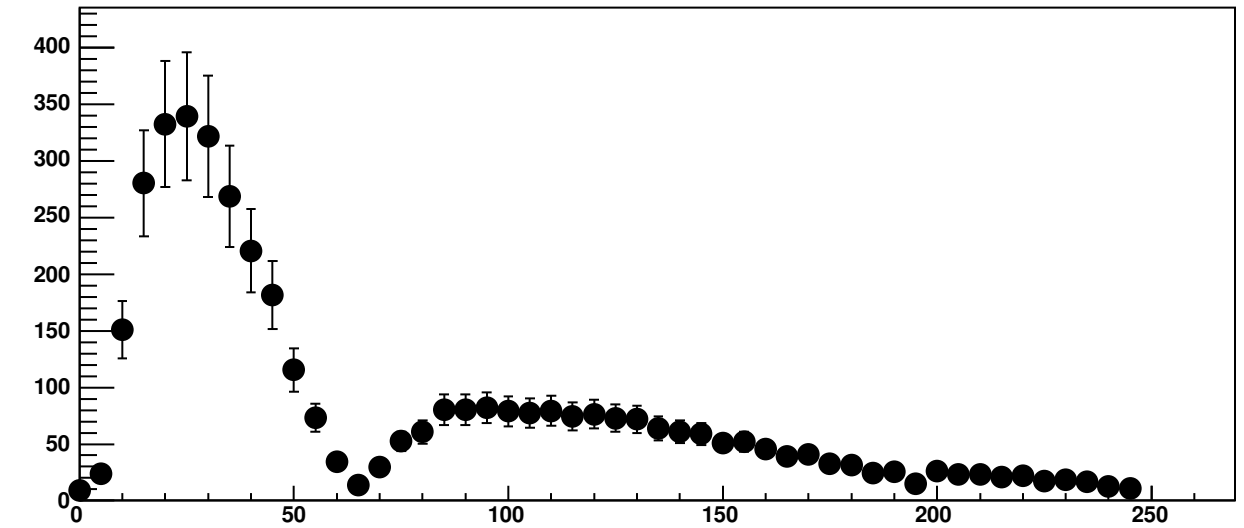


Chip 4, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold

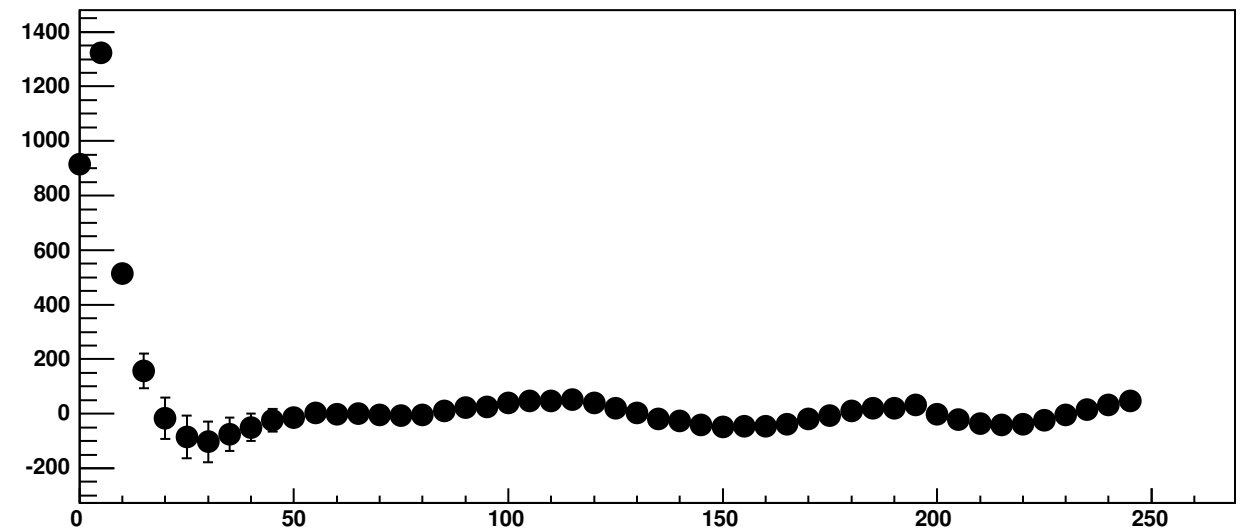
χ^2 / ndf	632.5 / 42
p0	-714.9 ± 1.9
p1	3.898 ± 0.1115
p2	1.021e+04 ± 13.32
p3	30.54 ± 0.04133

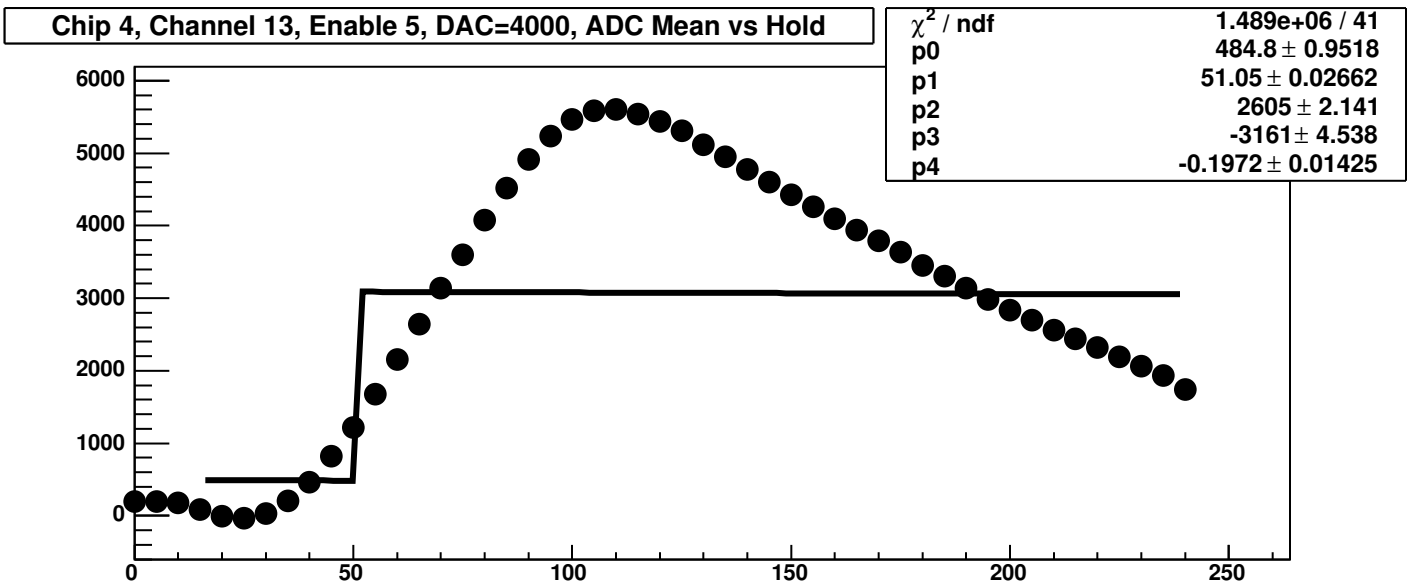


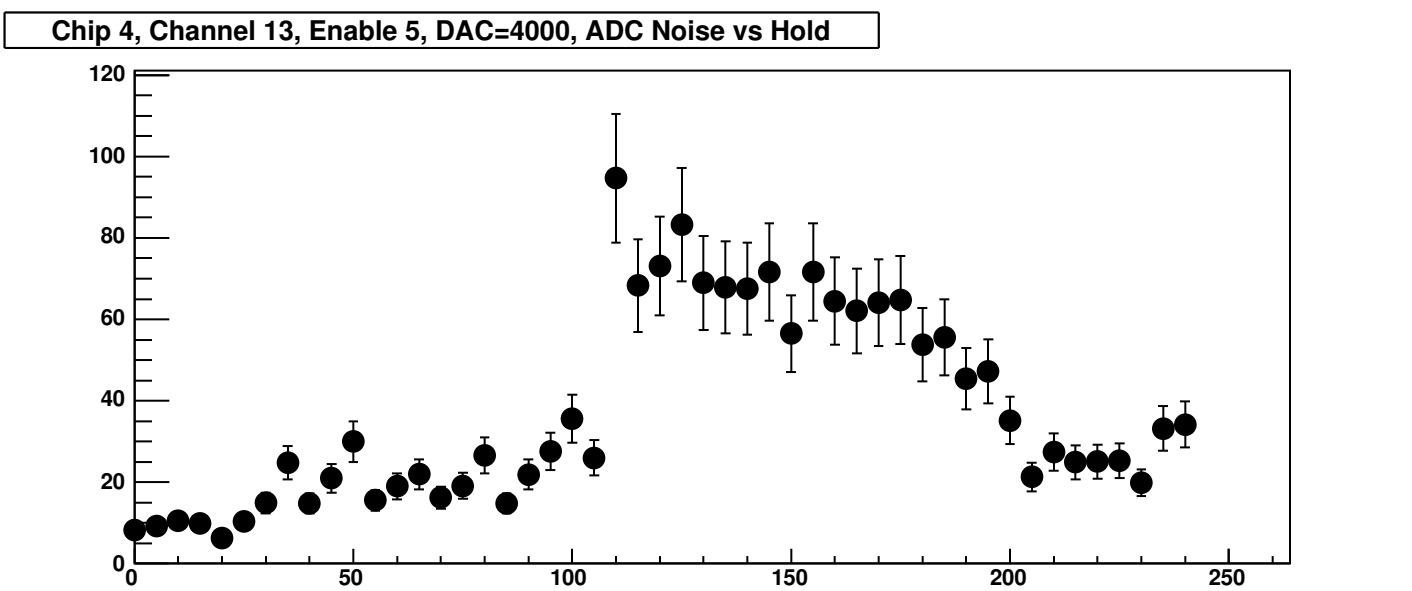
Chip 4, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold

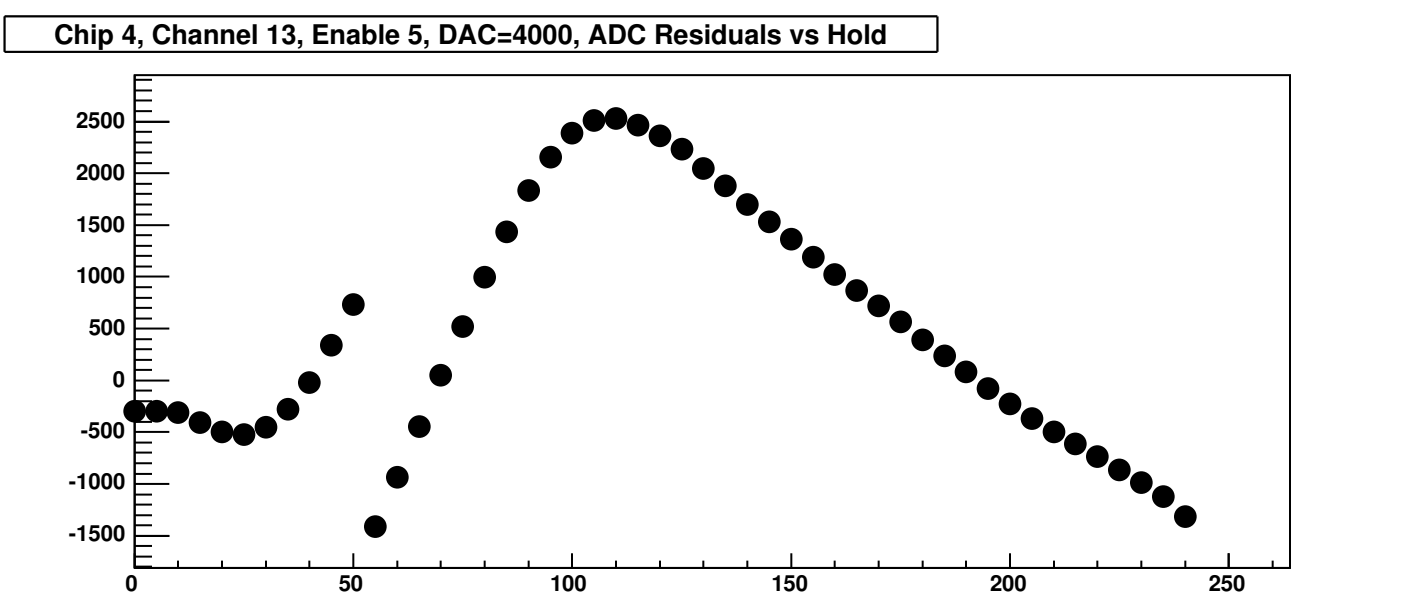


Chip 4, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold

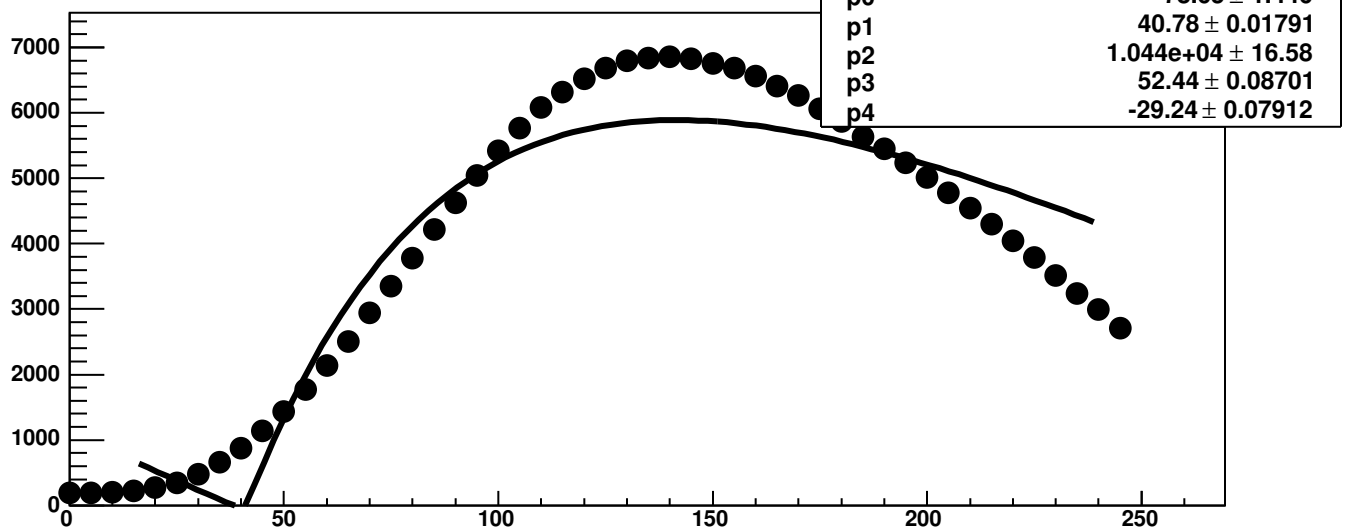



Chip 4, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.489e+06 / 41
p0 484.8 ± 0.9518
p1 51.05 ± 0.02662
p2 2605 ± 2.141
p3 -3161 ± 4.538
p4 -0.1972 ± 0.01425
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

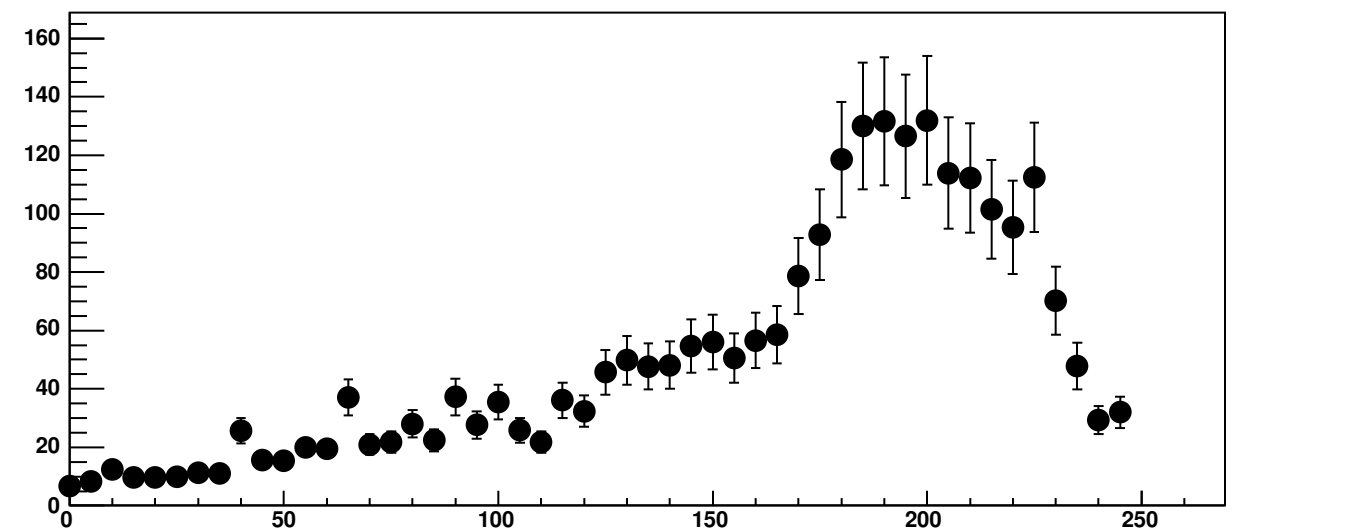

Chip 4, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 4, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold
2500
2000
1500
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

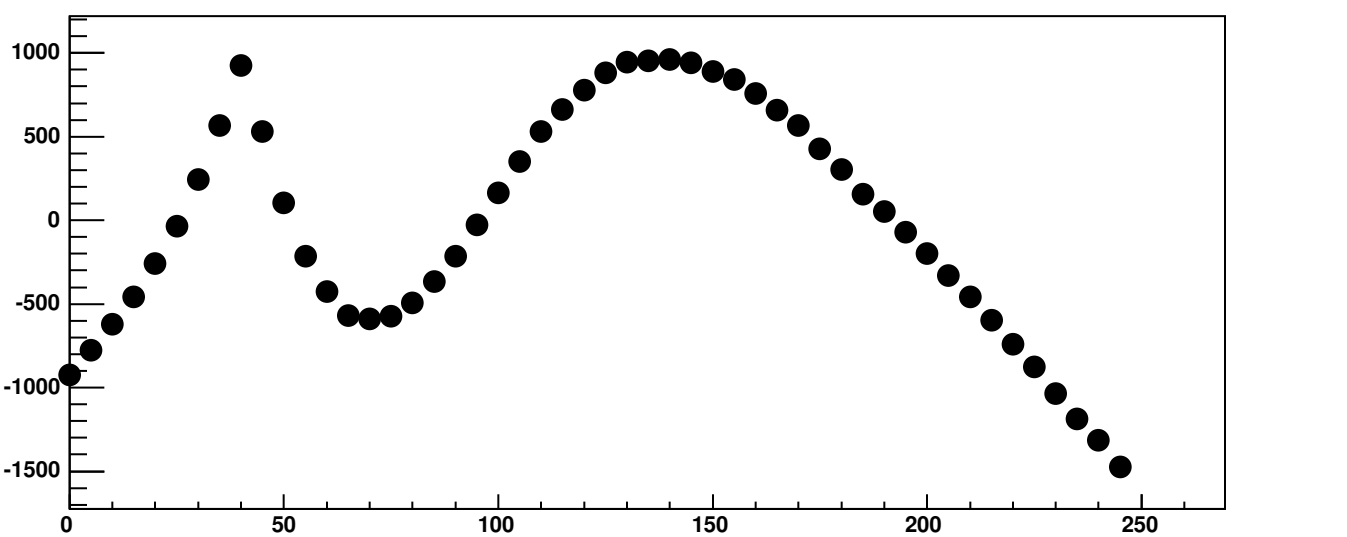
Chip 4, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold



Chip 4, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold

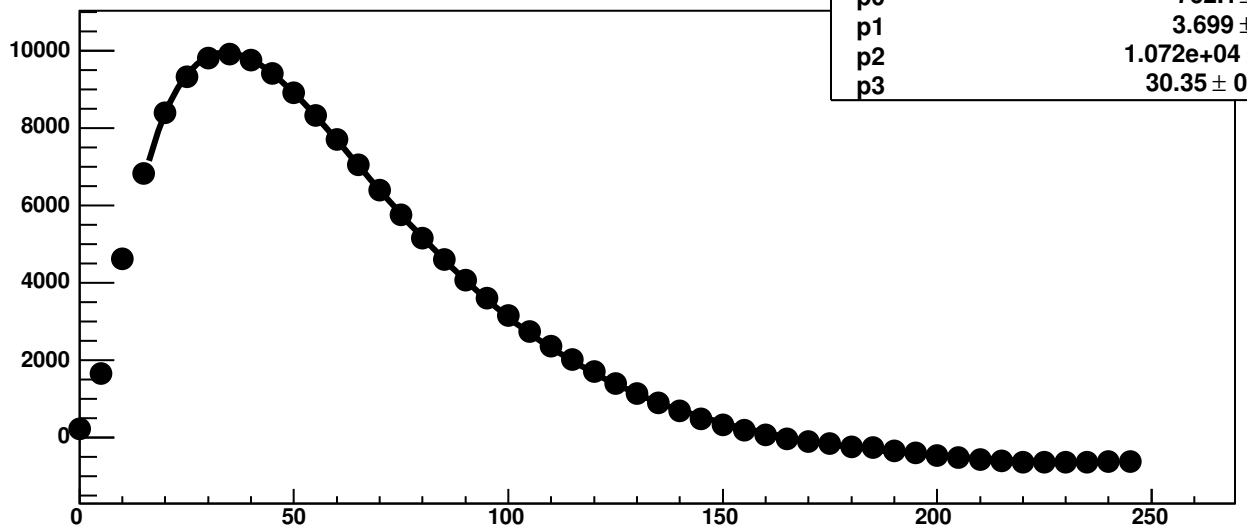


Chip 4, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

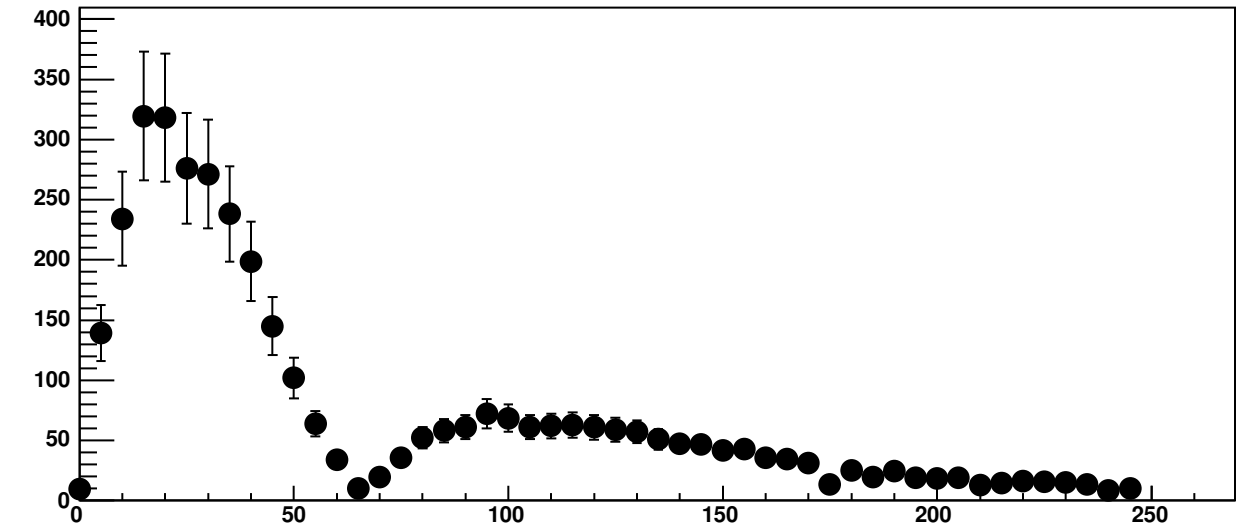


Chip 4, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold

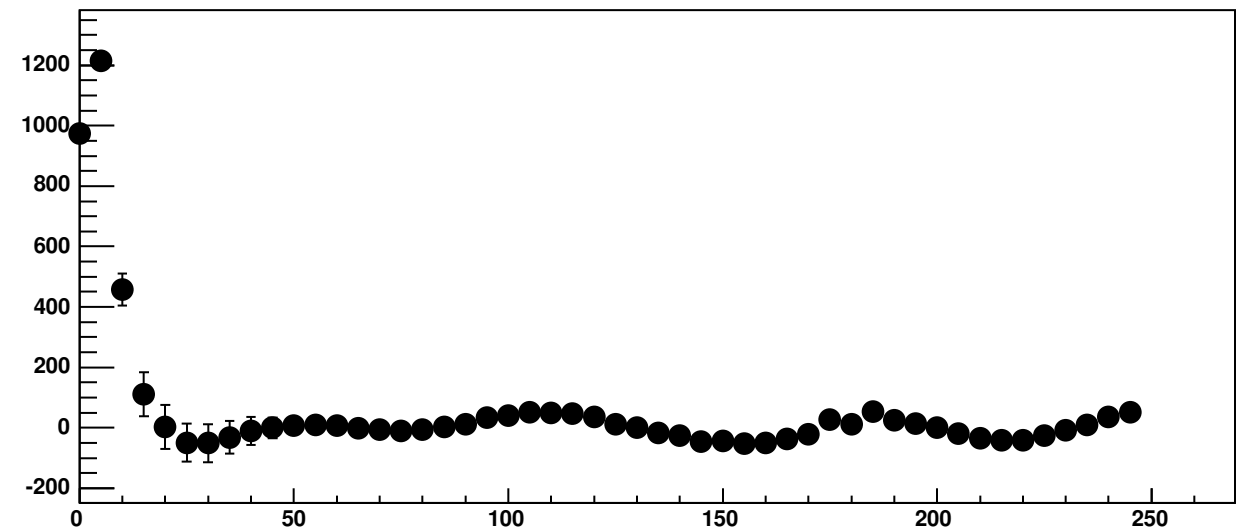
χ^2 / ndf	1253 / 42
p0	-762.1 ± 1.472
p1	3.699 ± 0.105
p2	1.072e+04 ± 13.2
p3	30.35 ± 0.03297



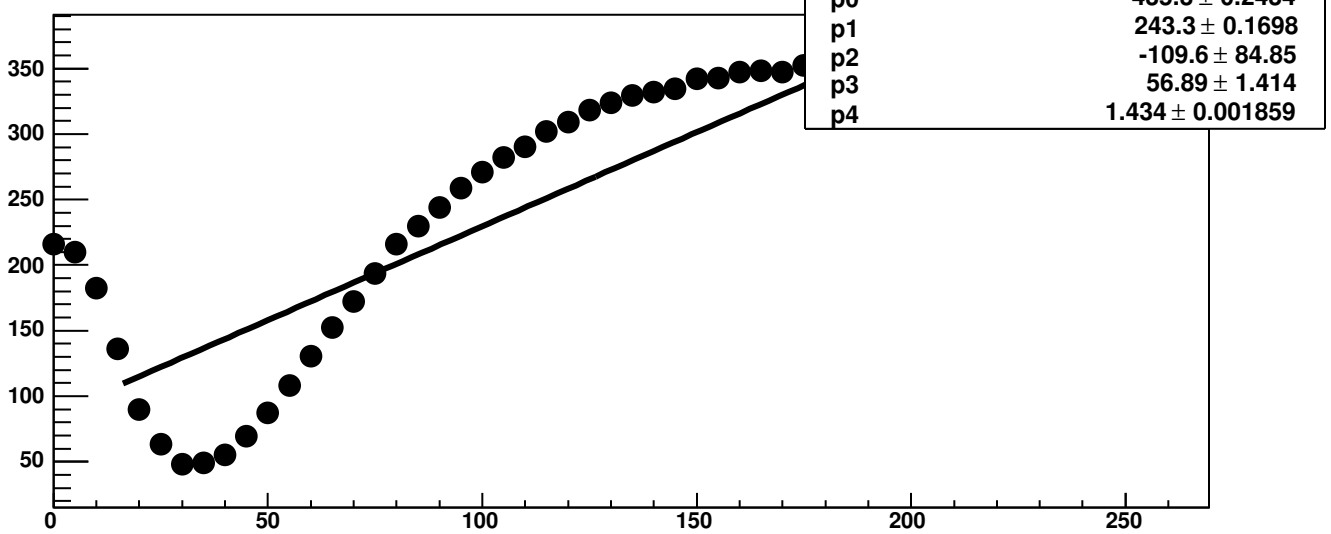
Chip 4, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold



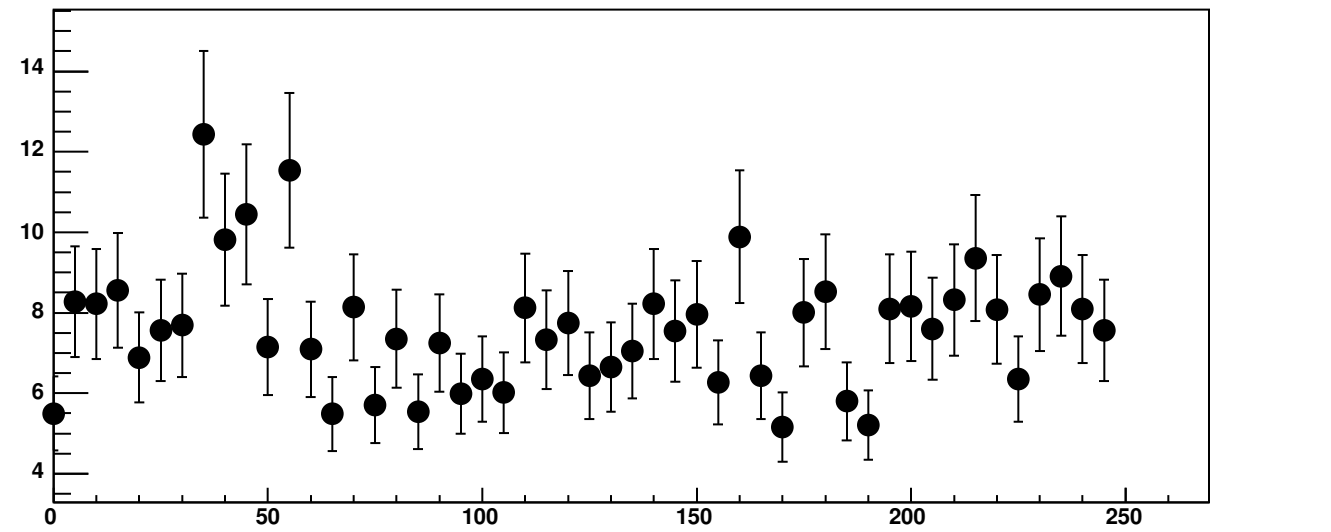
Chip 4, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold



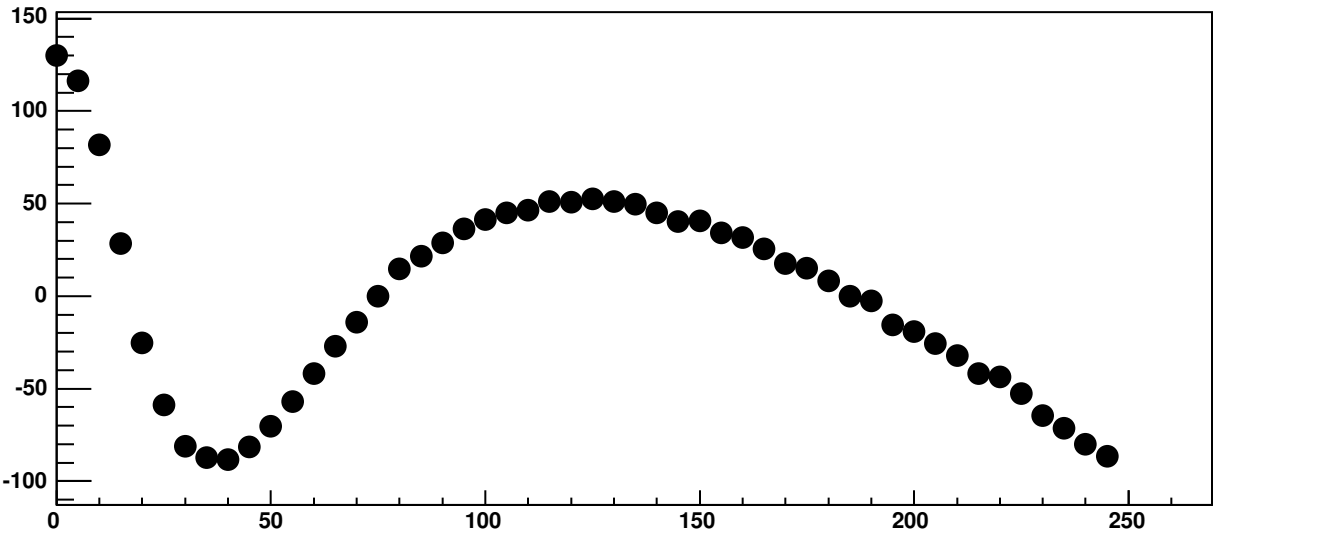
Chip 4, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold



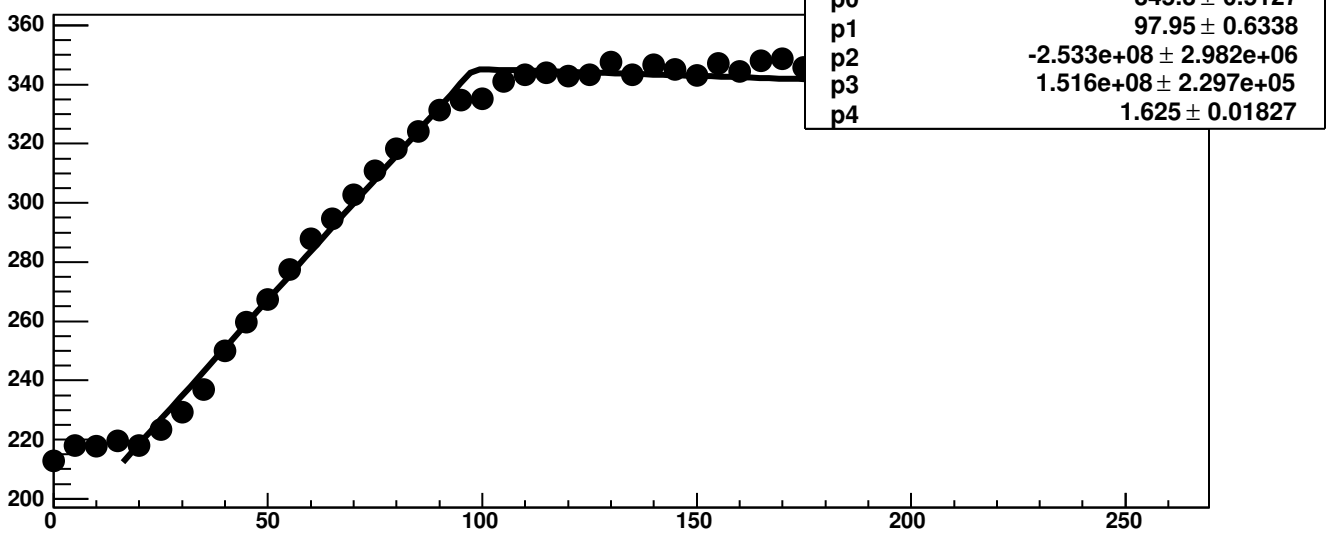
Chip 4, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



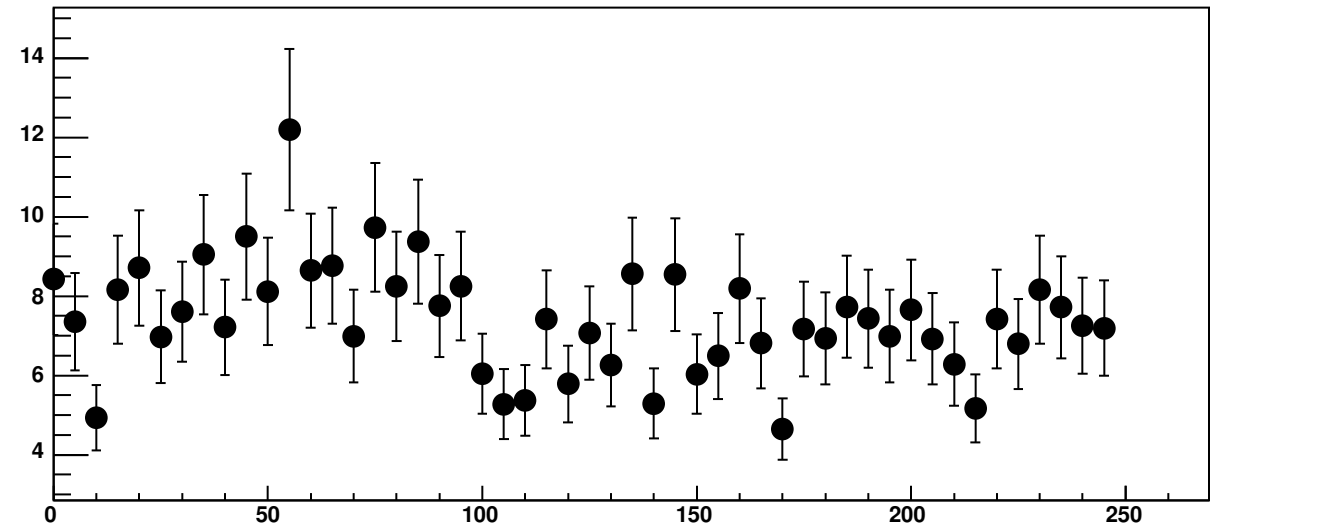
Chip 4, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold



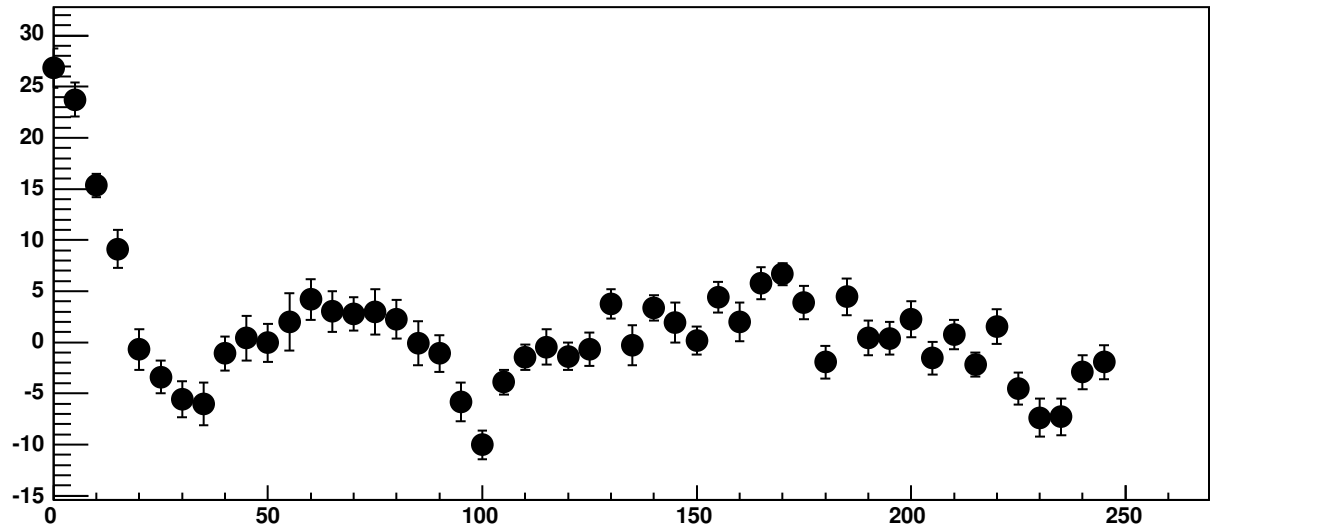
Chip 4, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold



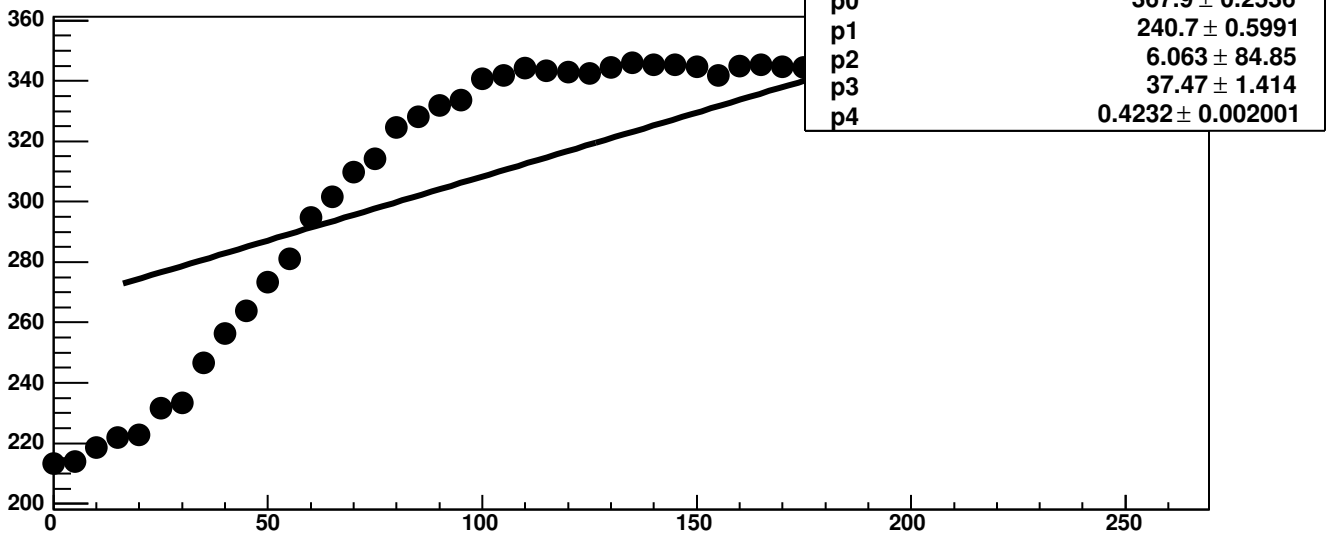
Chip 4, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



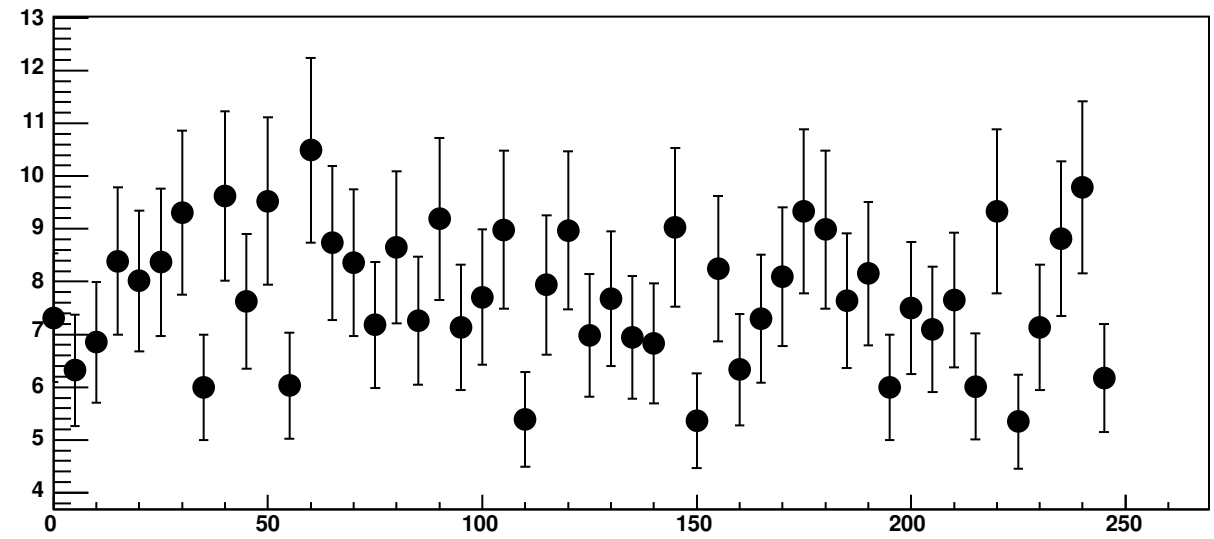
Chip 4, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold



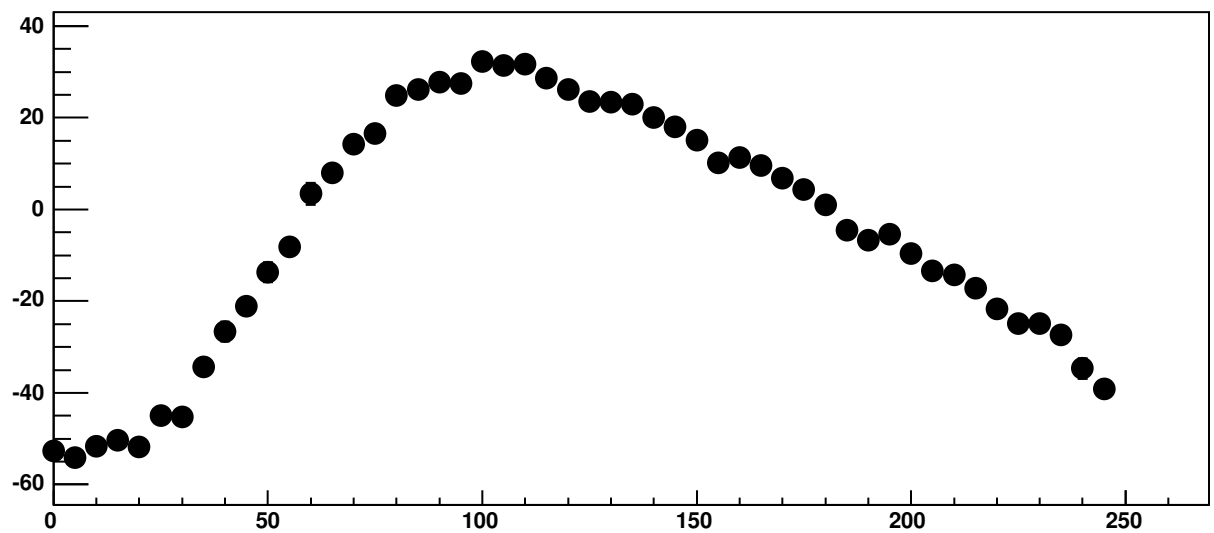
Chip 4, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold



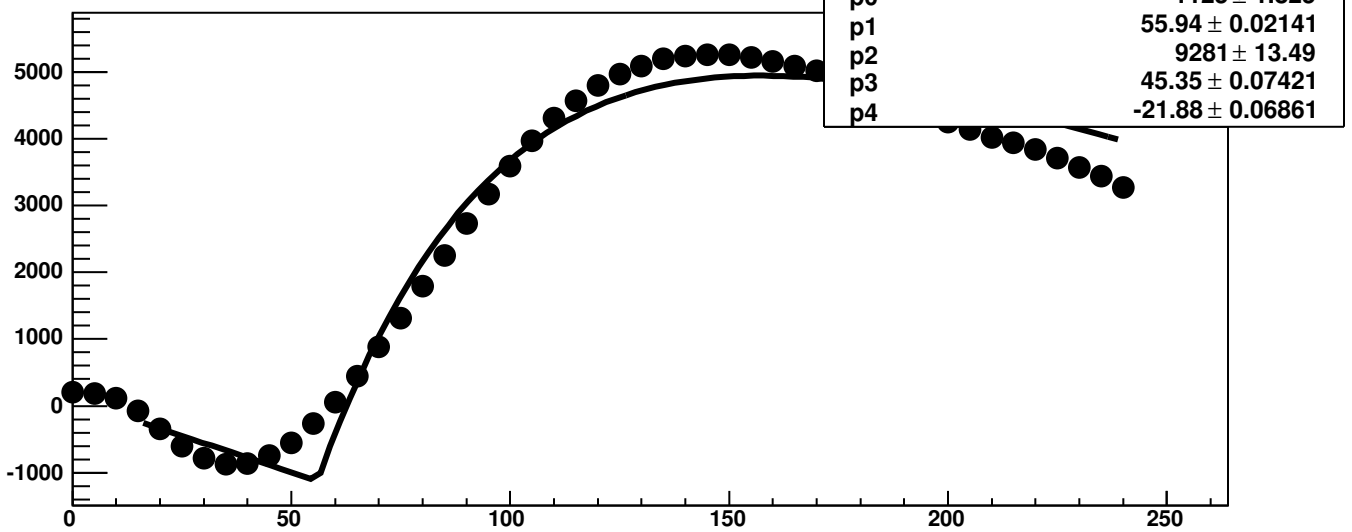
Chip 4, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold



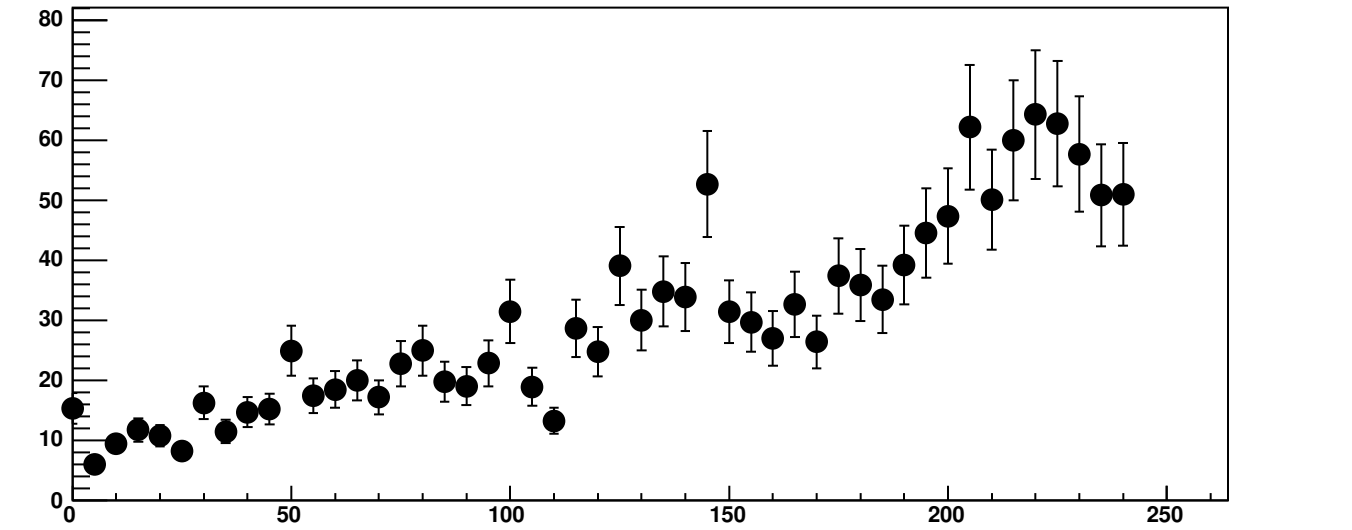
Chip 4, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold



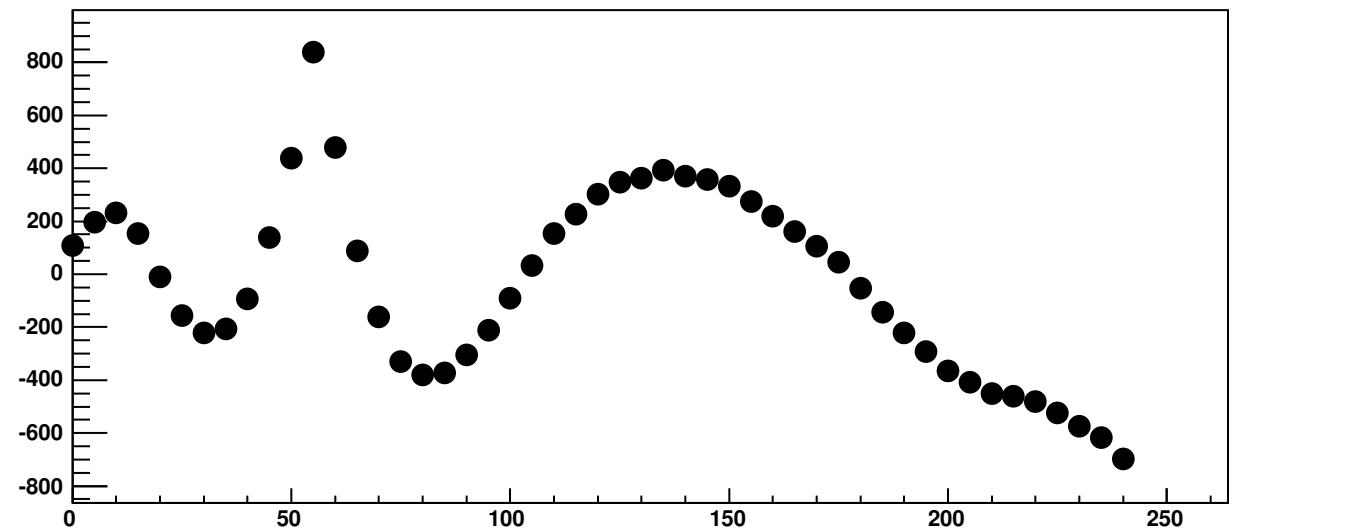
Chip 4, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

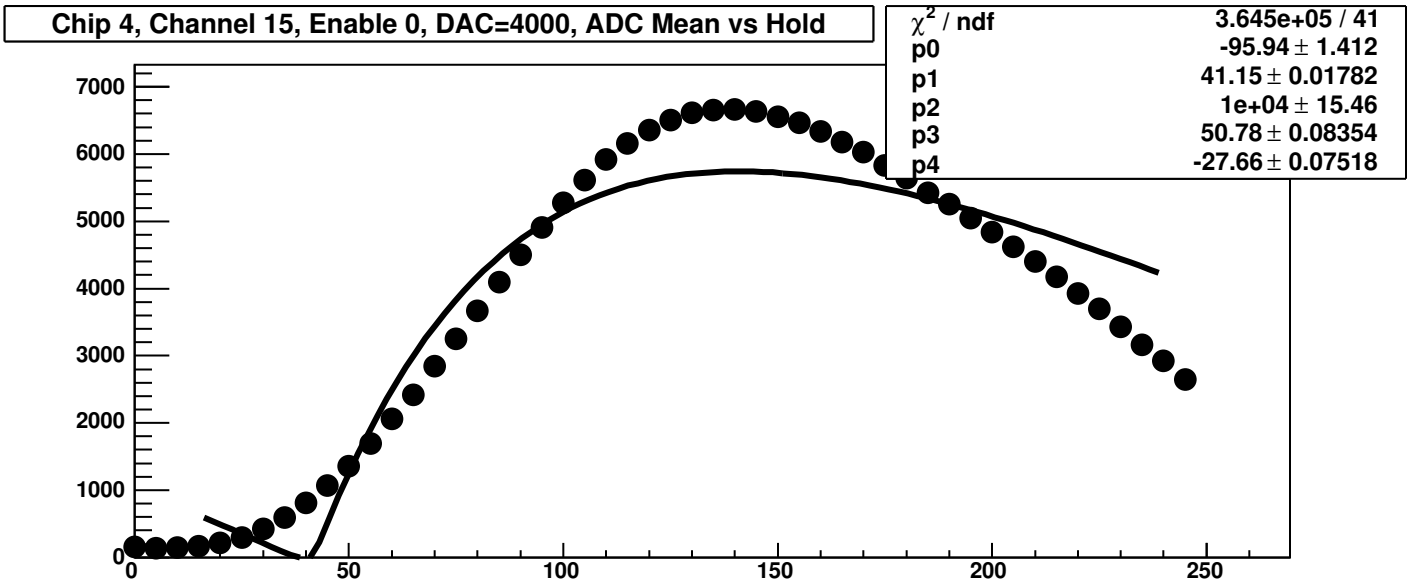


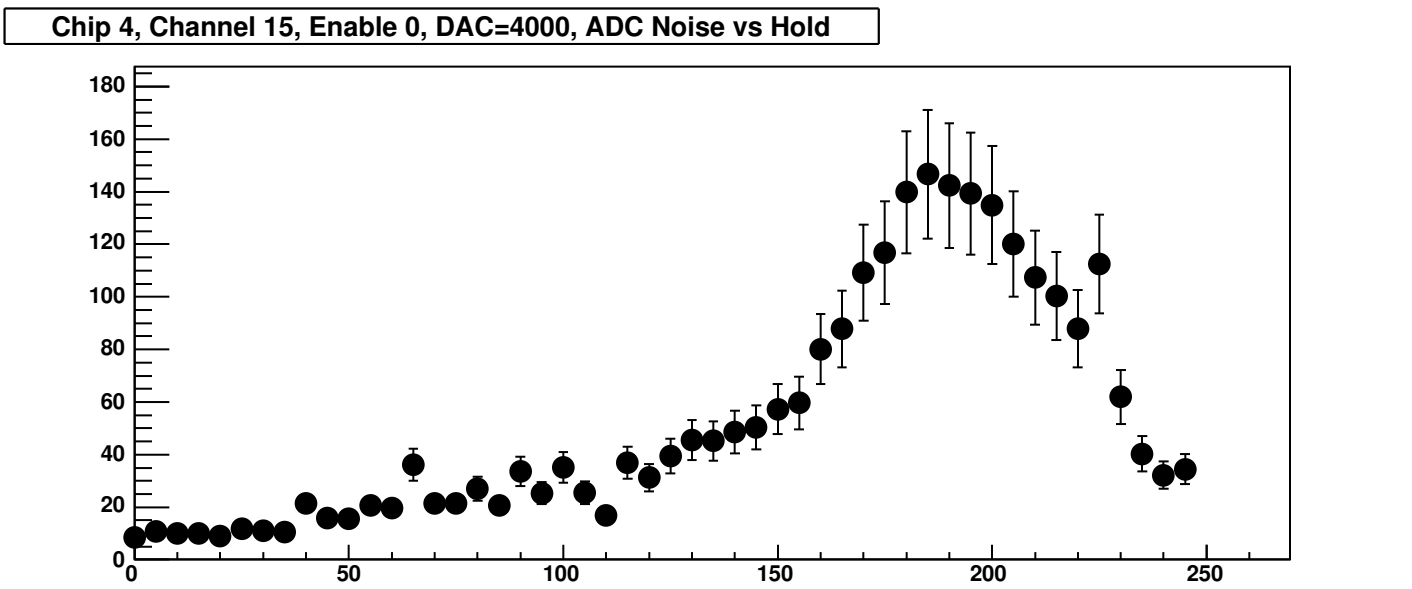
Chip 4, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold

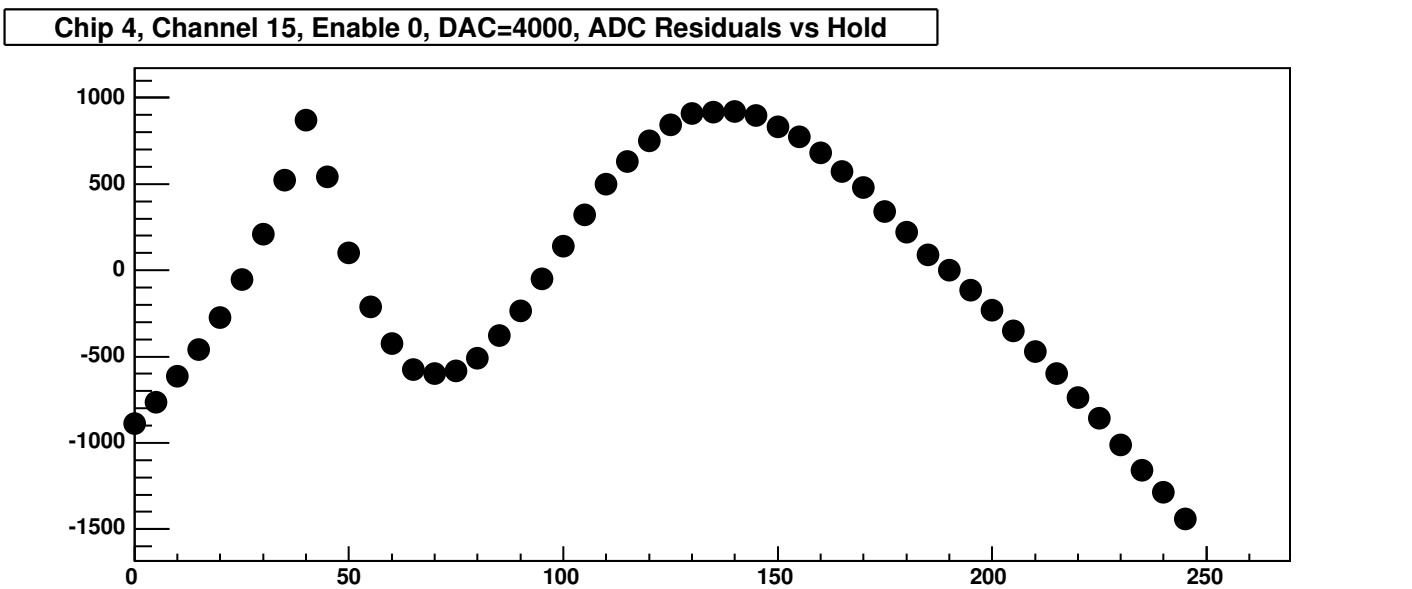


Chip 4, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold

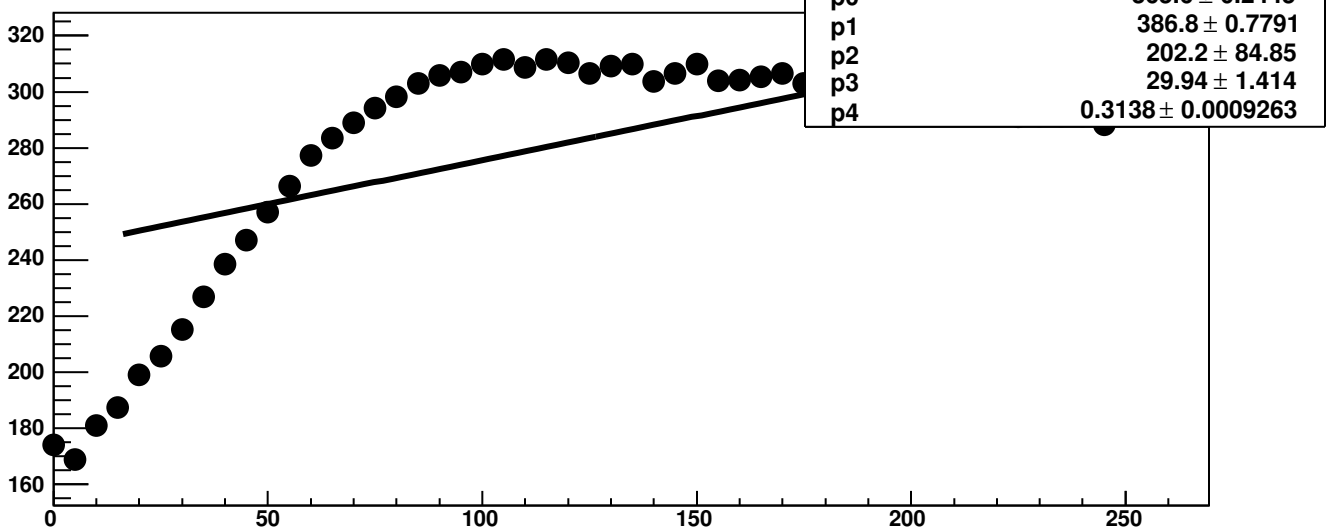



Chip 4, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.645e+05 / 41
p0 -95.94 ± 1.412
p1 41.15 ± 0.01782
p2 1e+04 ± 15.46
p3 50.78 ± 0.08354
p4 -27.66 ± 0.07518
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

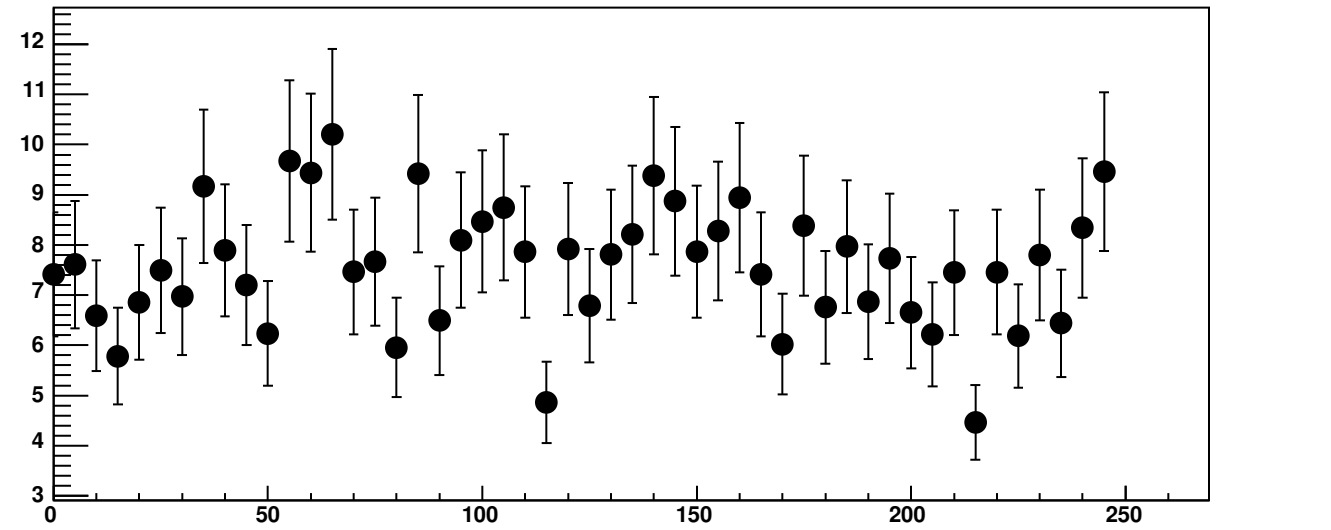

Chip 4, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 4, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

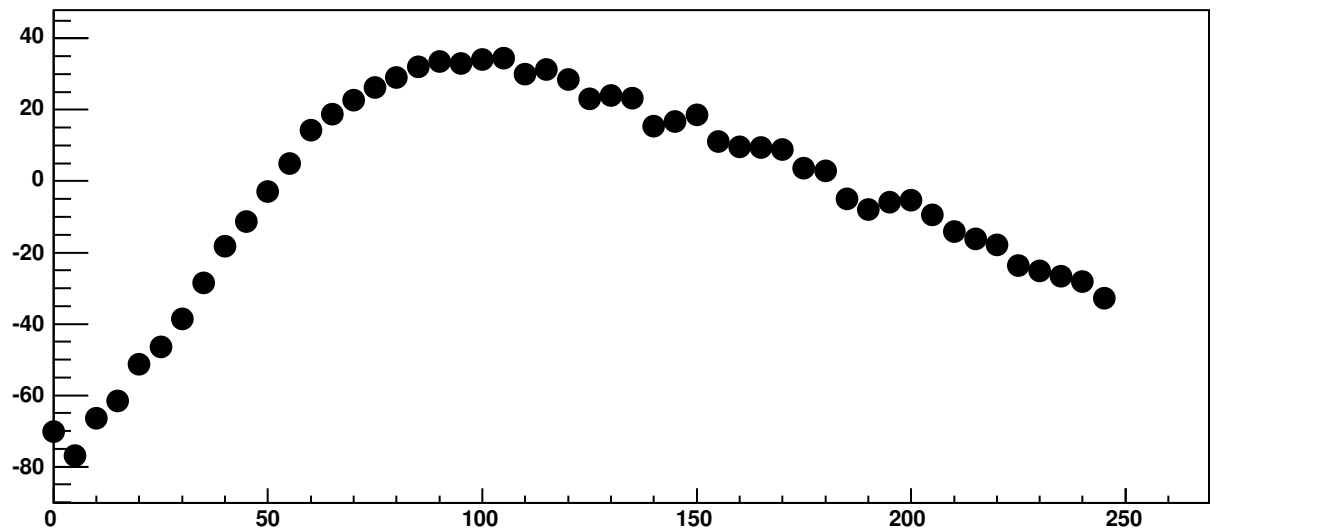
Chip 4, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 4, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold

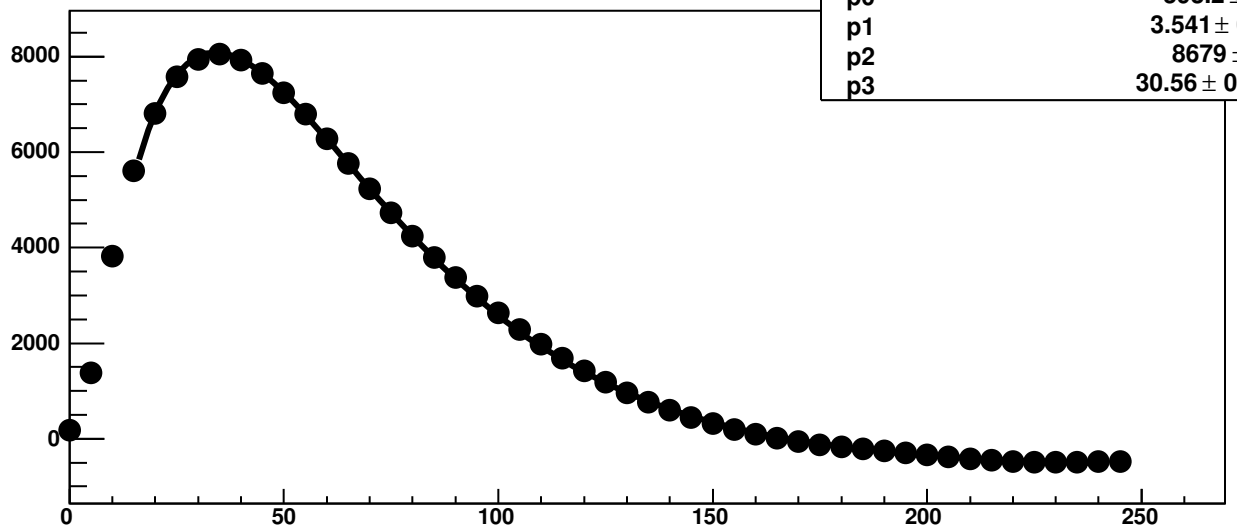


Chip 4, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

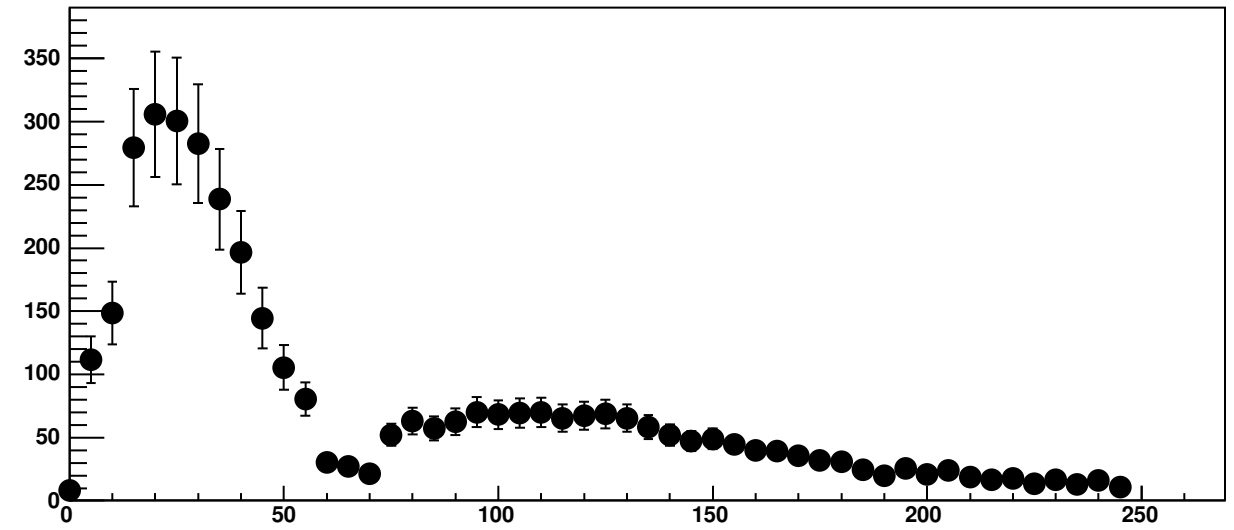


Chip 4, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold

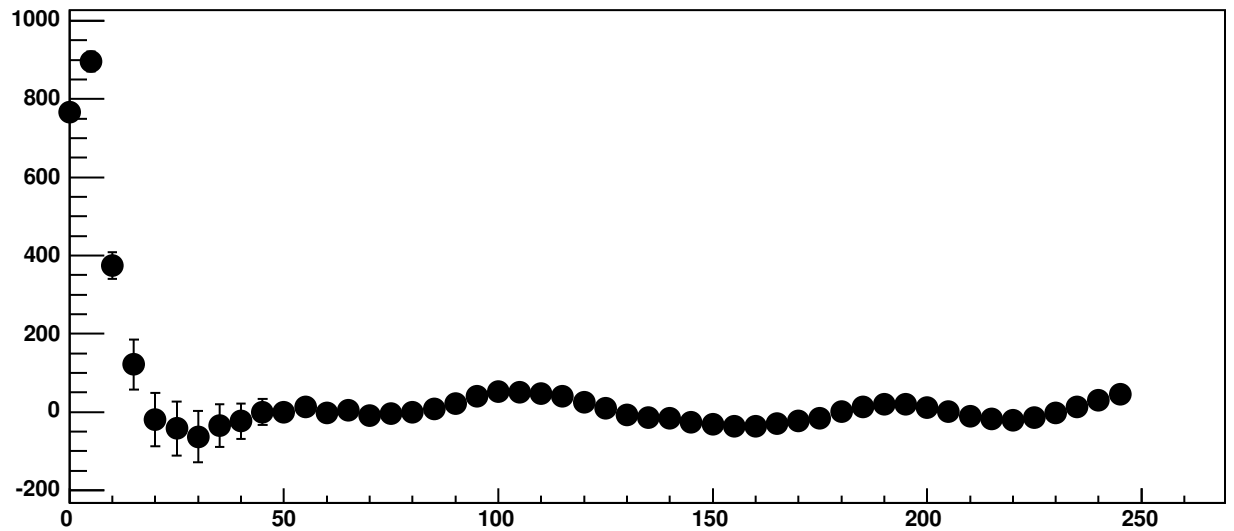
χ^2 / ndf	334.8 / 42
p0	-593.2 ± 1.736
p1	3.541 ± 0.1286
p2	8679 ± 12.91
p3	30.56 ± 0.04589



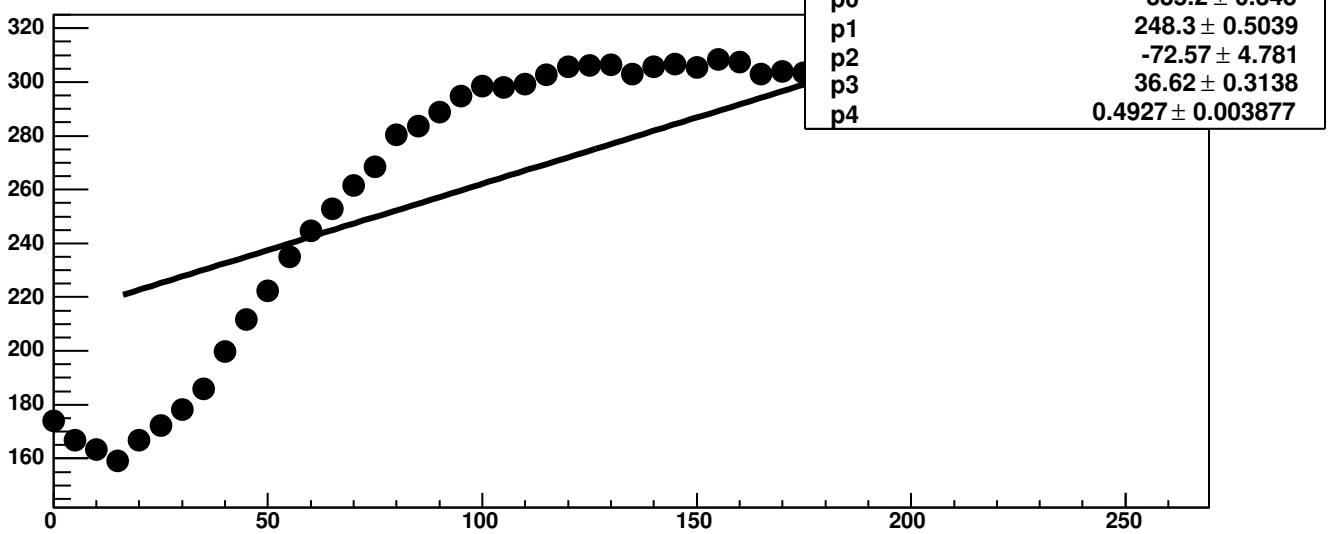
Chip 4, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold



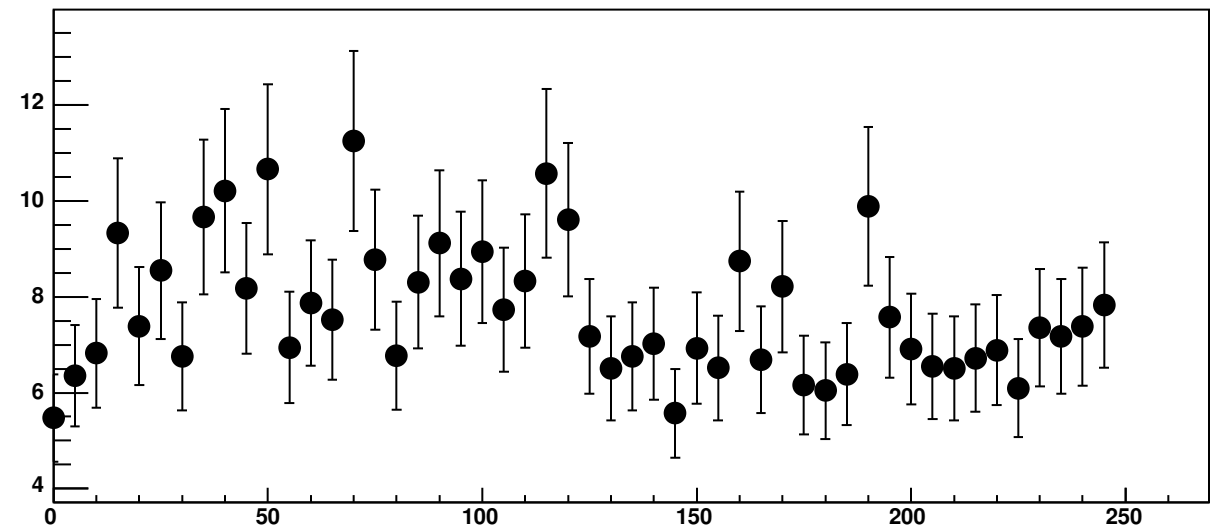
Chip 4, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold



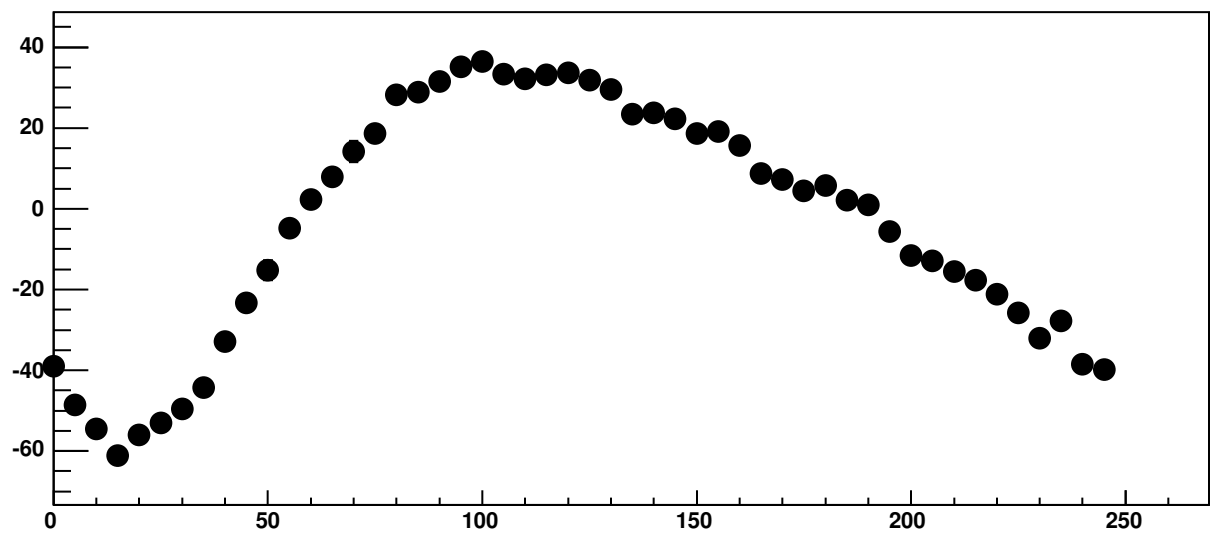
Chip 4, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold



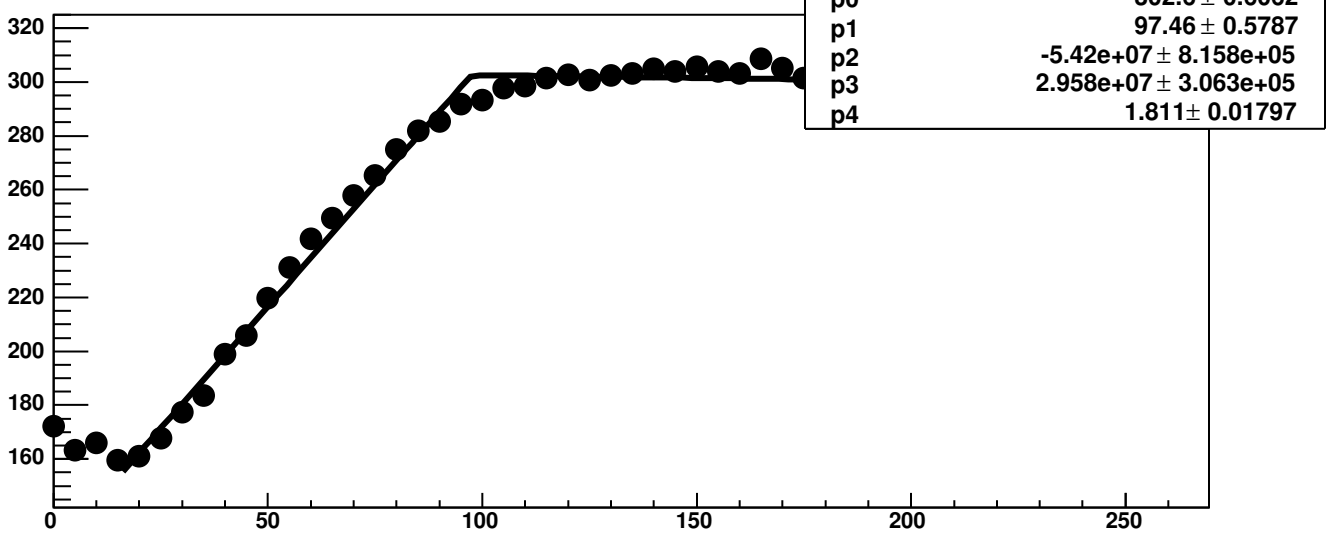
Chip 4, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



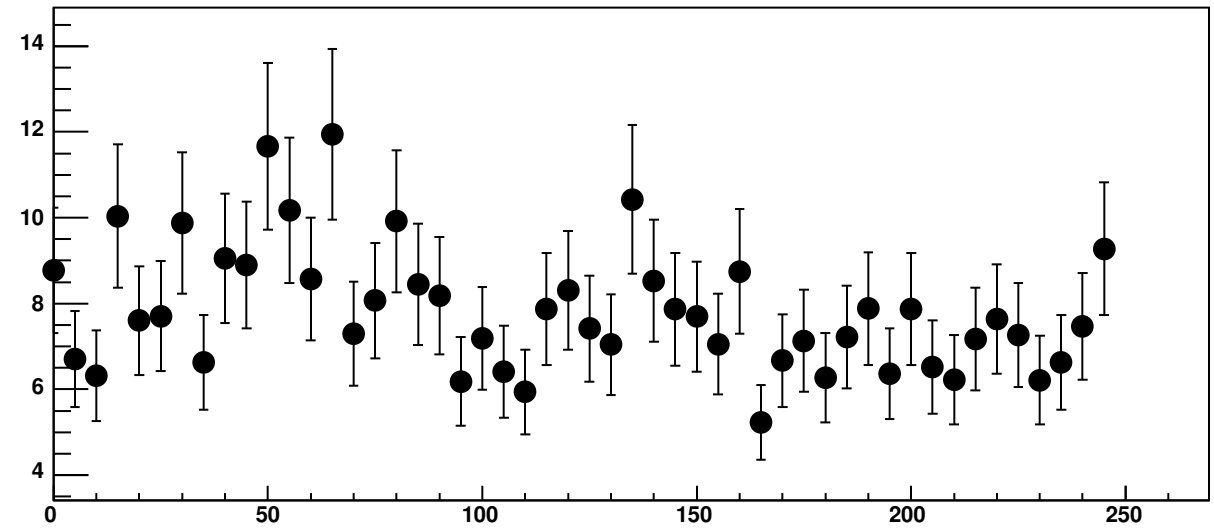
Chip 4, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold



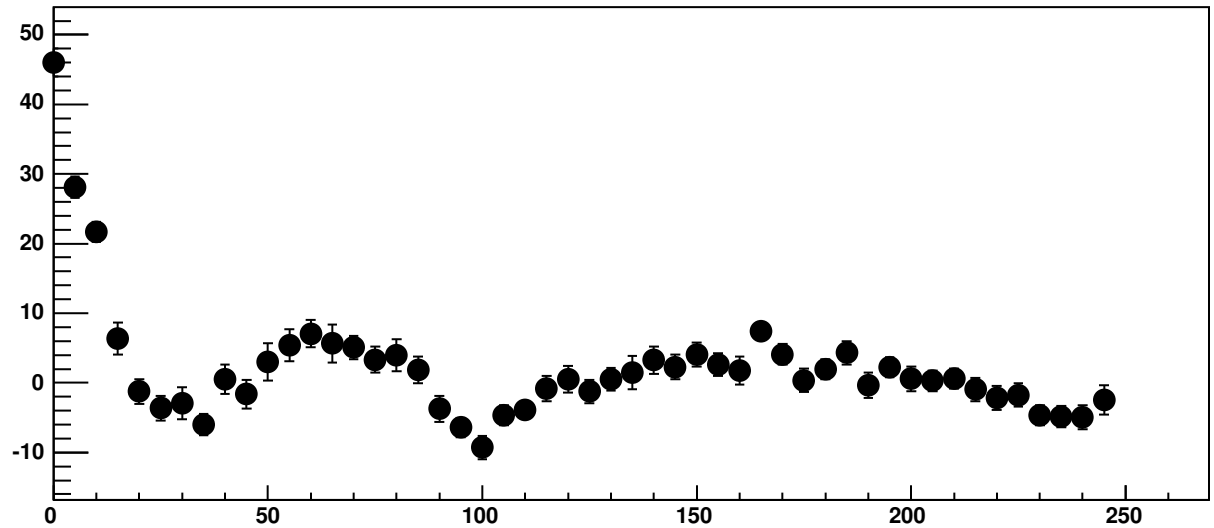
Chip 4, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

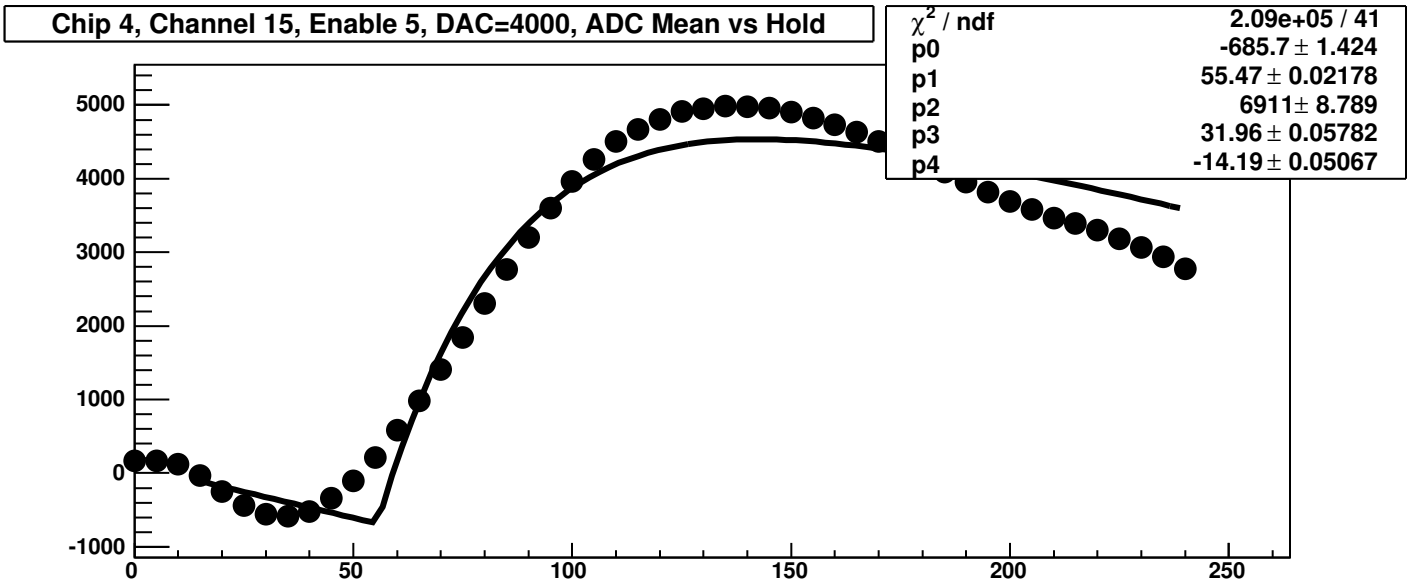


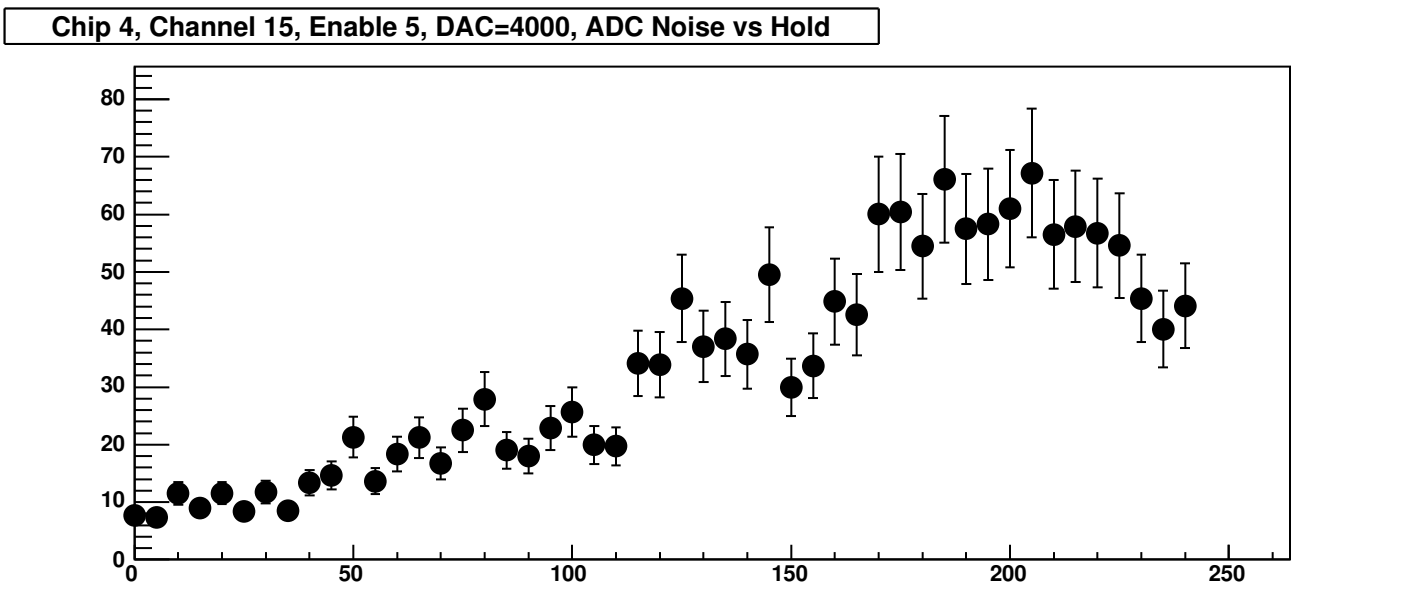
Chip 4, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold

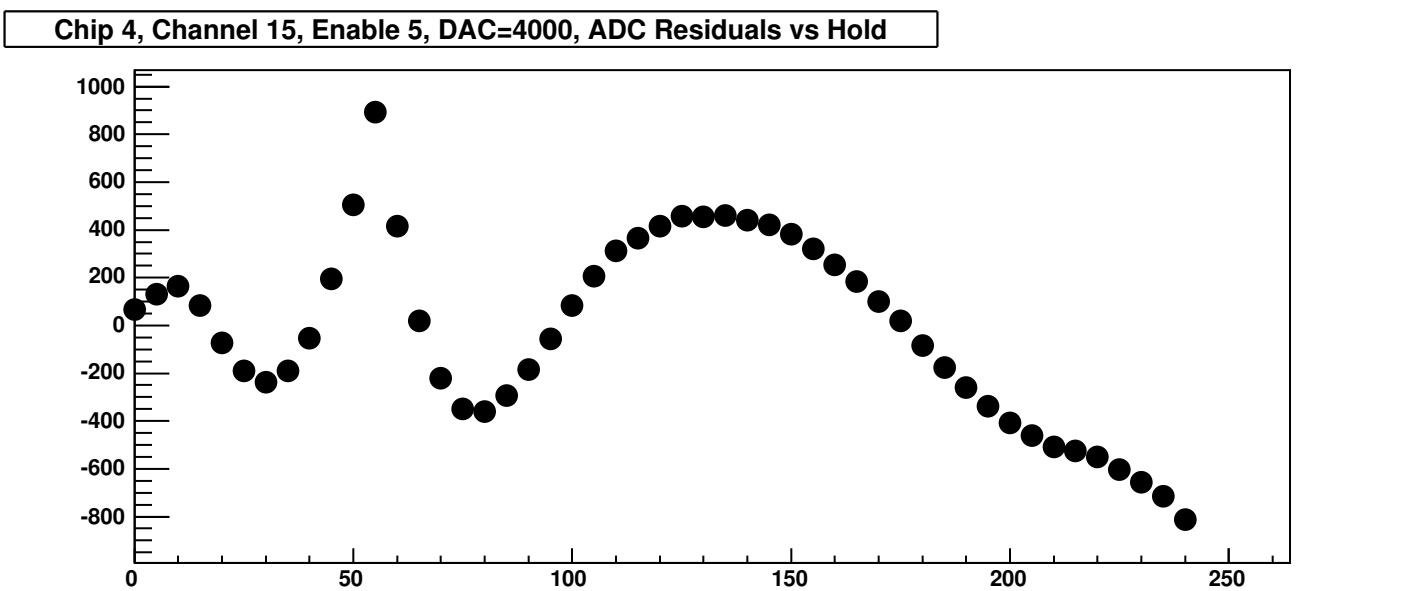


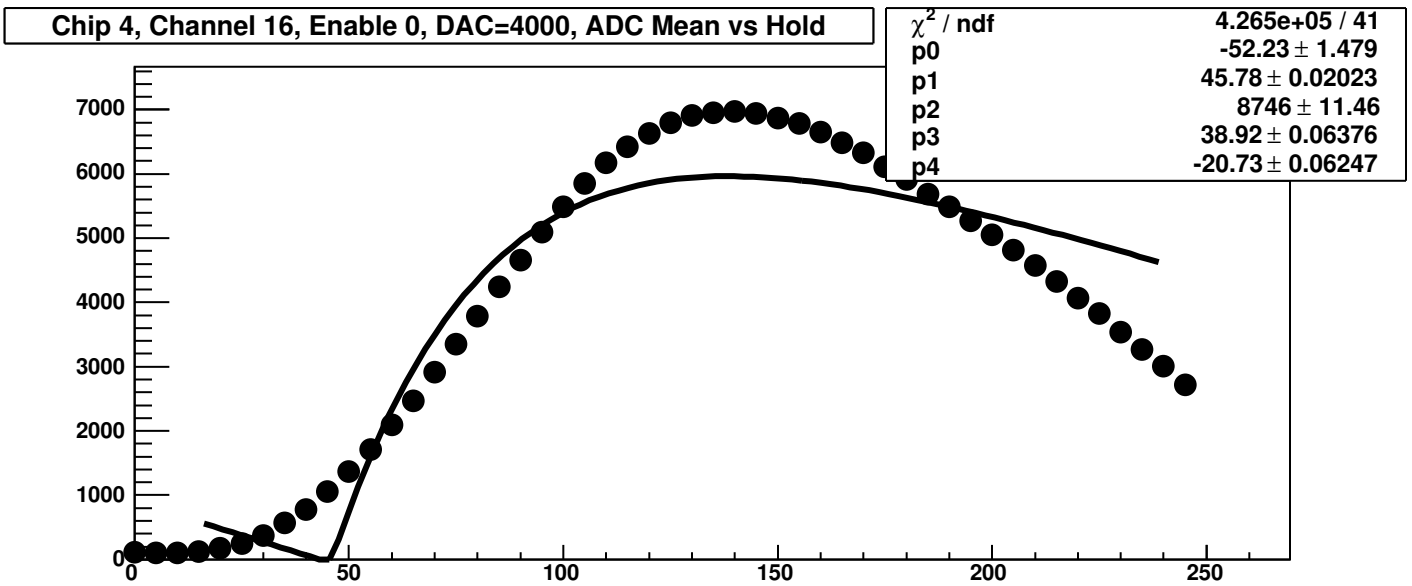
Chip 4, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

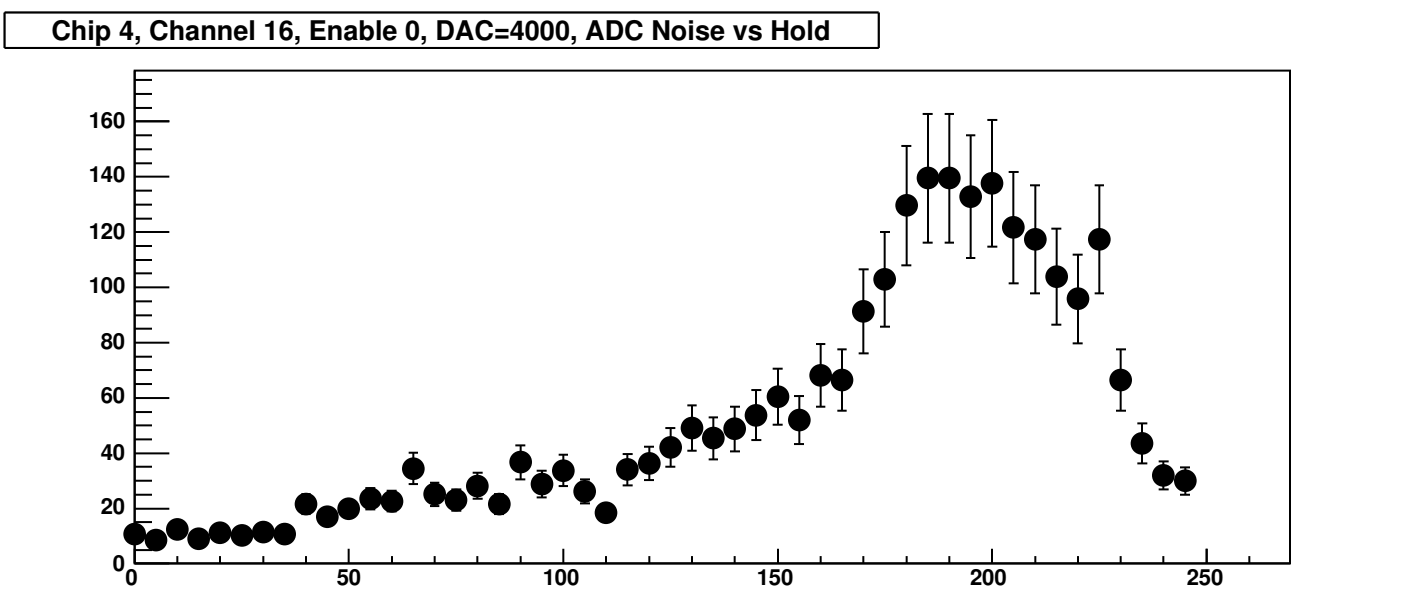


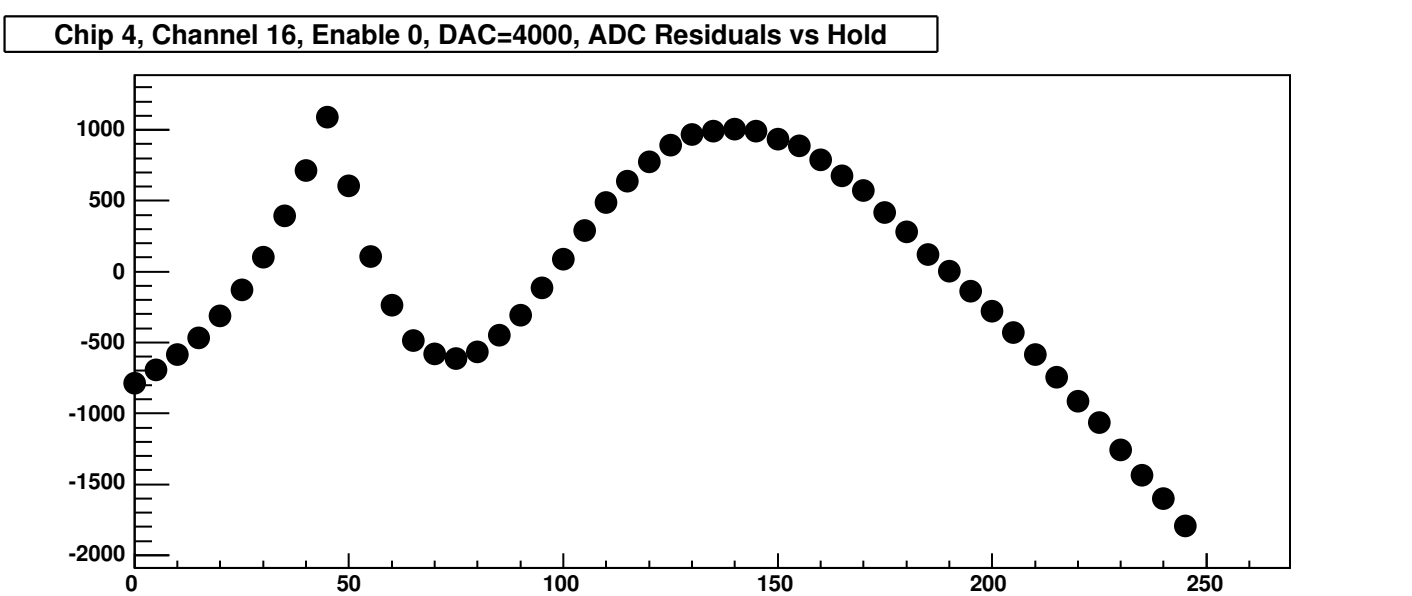

Chip 4, Channel 15, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.09e+05 / 41
p0 -685.7 ± 1.424
p1 55.47 ± 0.02178
p2 6911 ± 8.789
p3 31.96 ± 0.05782
p4 -14.19 ± 0.05067
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

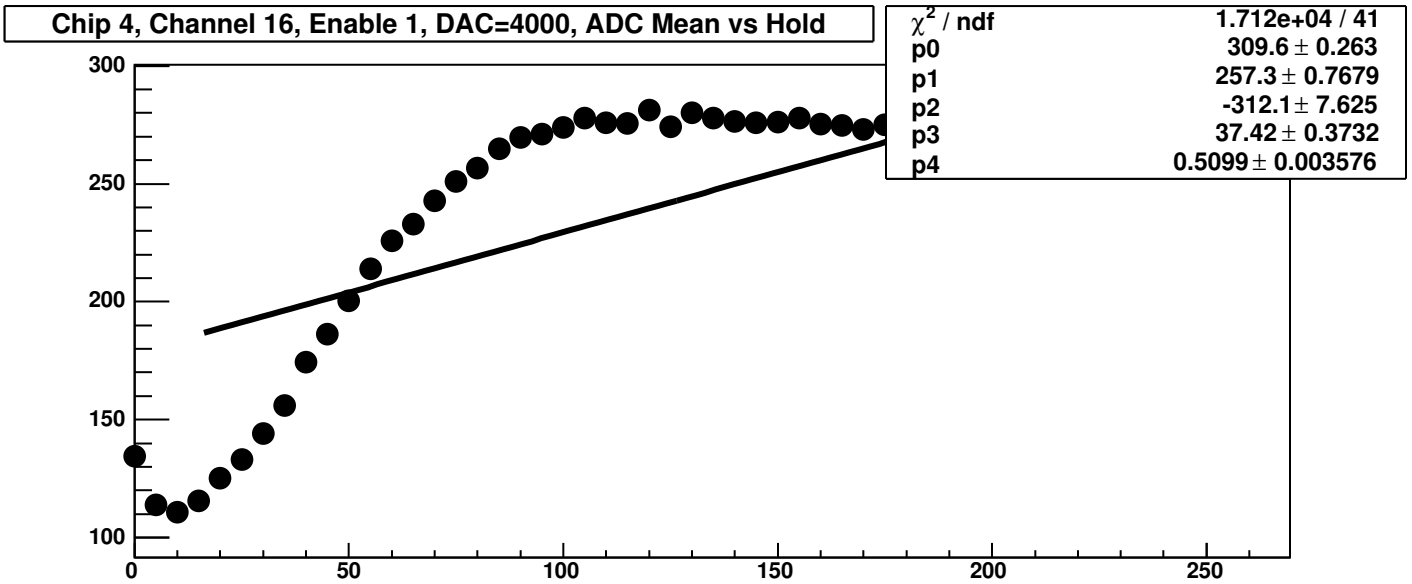

Chip 4, Channel 15, Enable 5, DAC=4000, ADC Noise vs Hold
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250

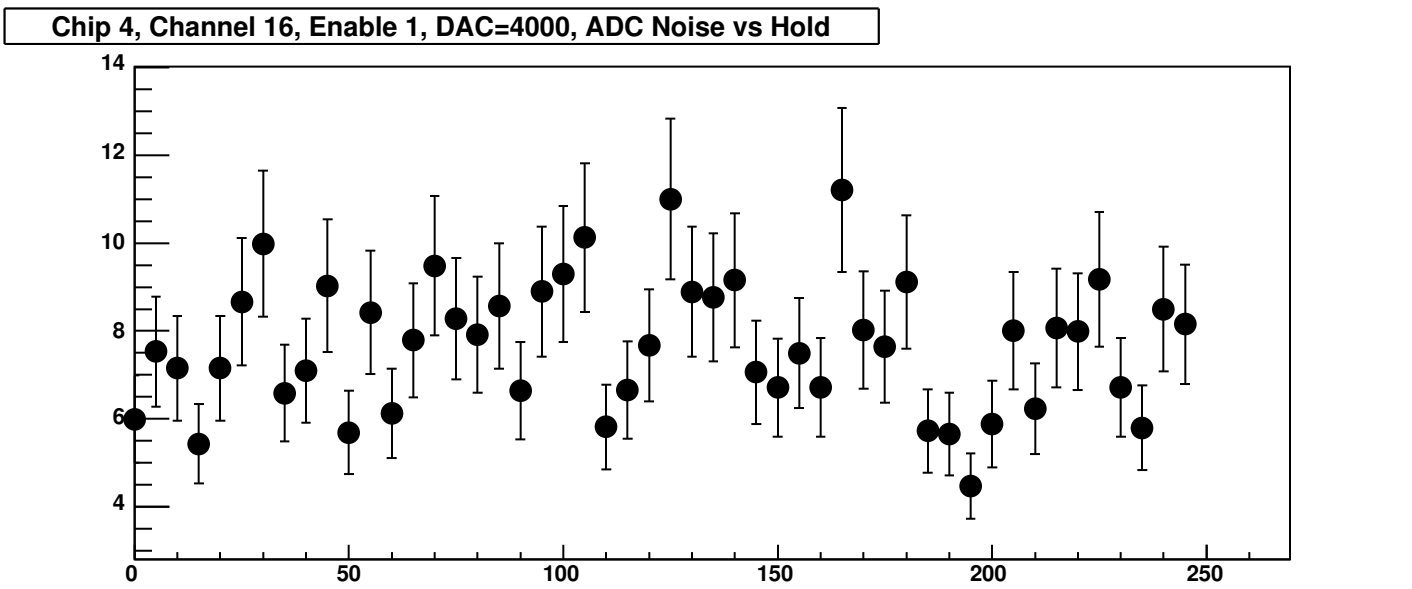

Chip 4, Channel 15, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
800
600
400
200
0
-200
-400
-600
-800
0
50
100
150
200
250

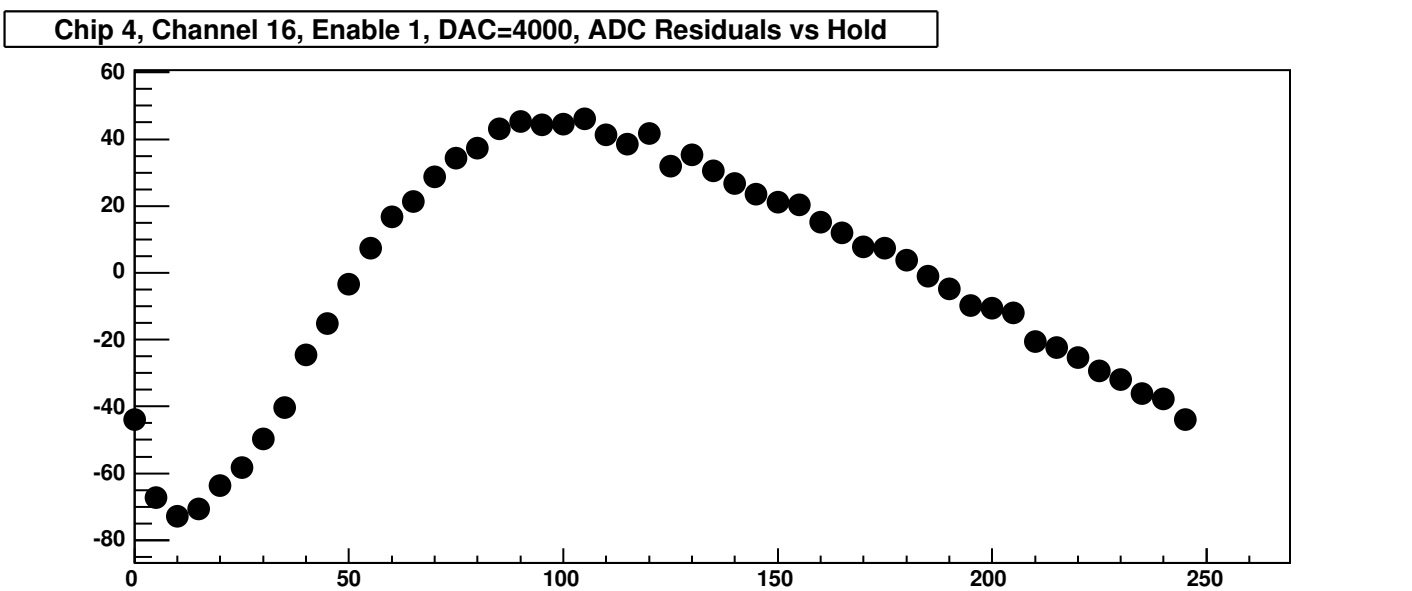

Chip 4, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.265e+05 / 41
p0 -52.23 ± 1.479
p1 45.78 ± 0.02023
p2 8746 ± 11.46
p3 38.92 ± 0.06376
p4 -20.73 ± 0.06247
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 4, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

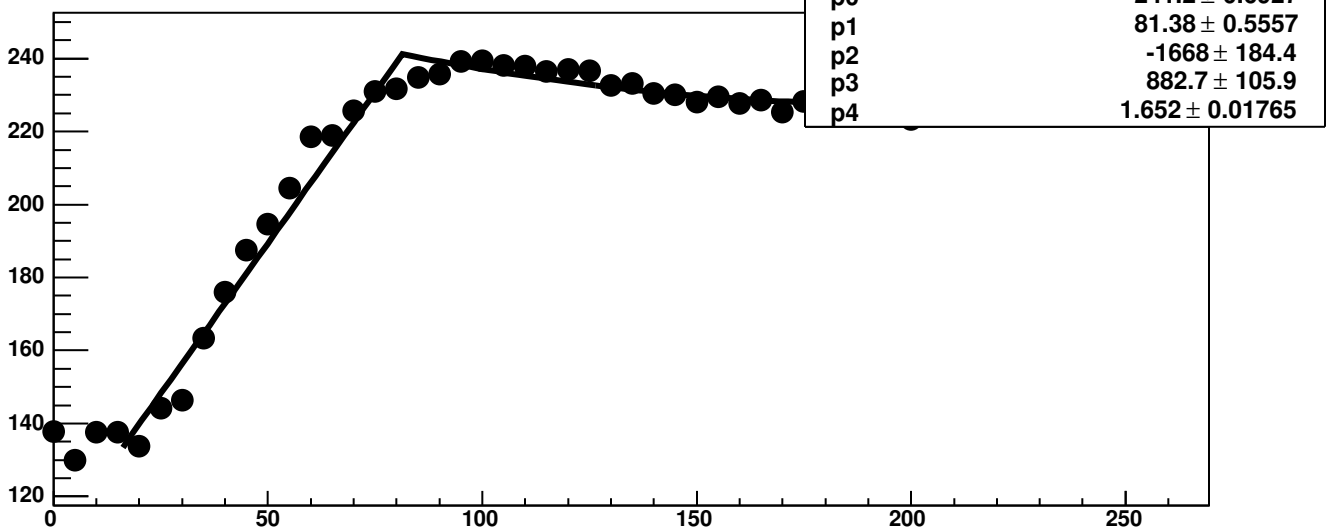

Chip 4, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250


Chip 4, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.712e+04 / 41
p0 309.6 ± 0.263
p1 257.3 ± 0.7679
p2 -312.1 ± 7.625
p3 37.42 ± 0.3732
p4 0.5099 ± 0.003576
300
250
200
150
100
0
50
100
150
200
250

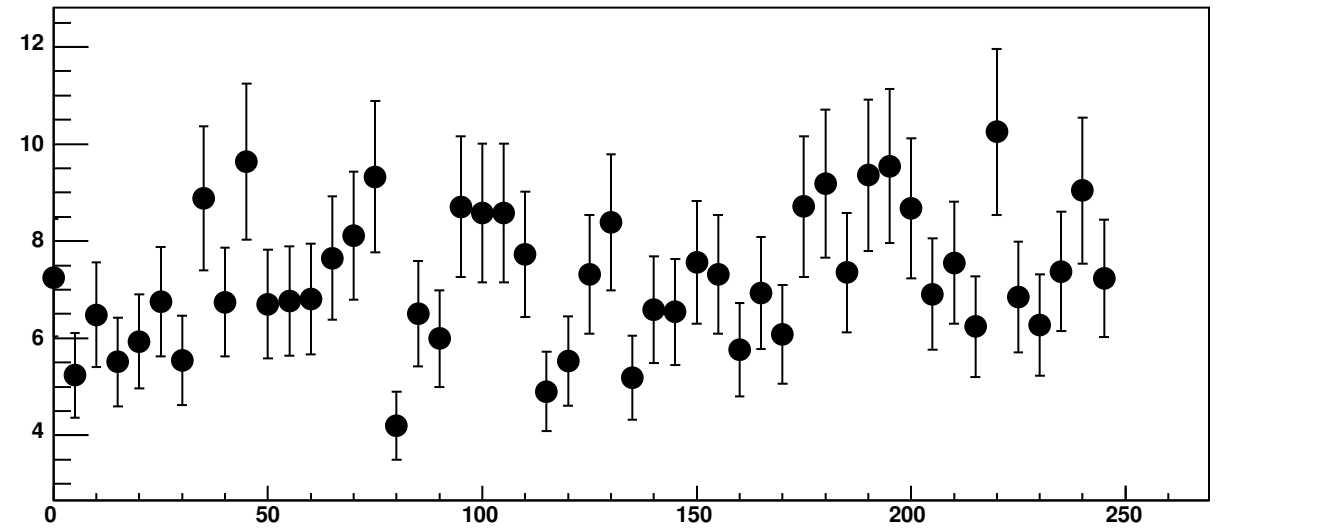

Chip 4, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 4, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold
60
40
20
0
-20
-40
-60
-80
0
50
100
150
200
250

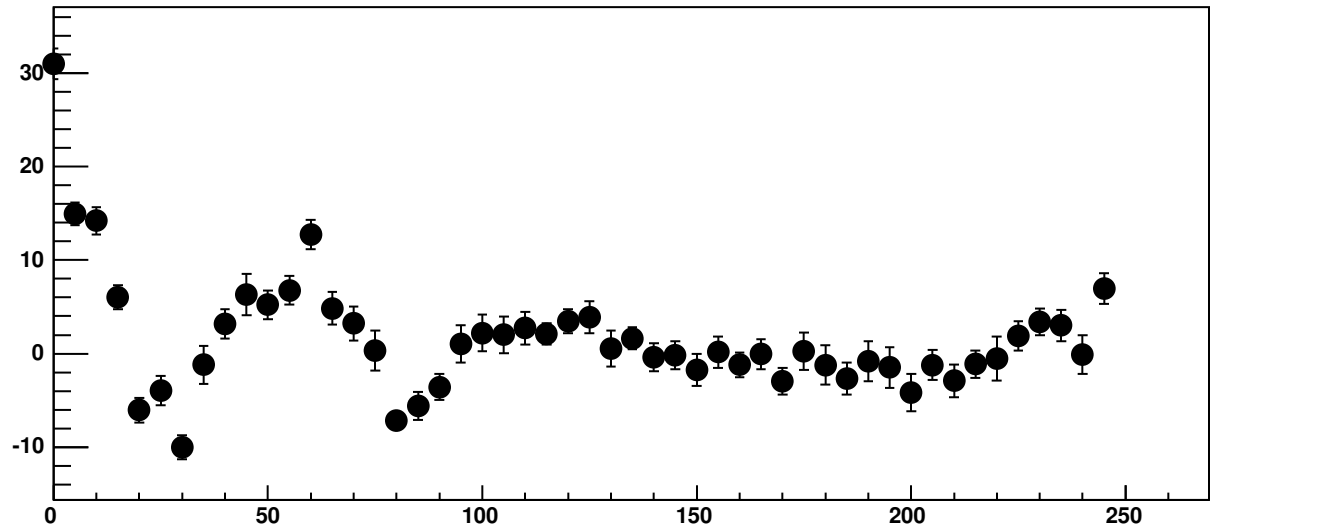
Chip 4, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold



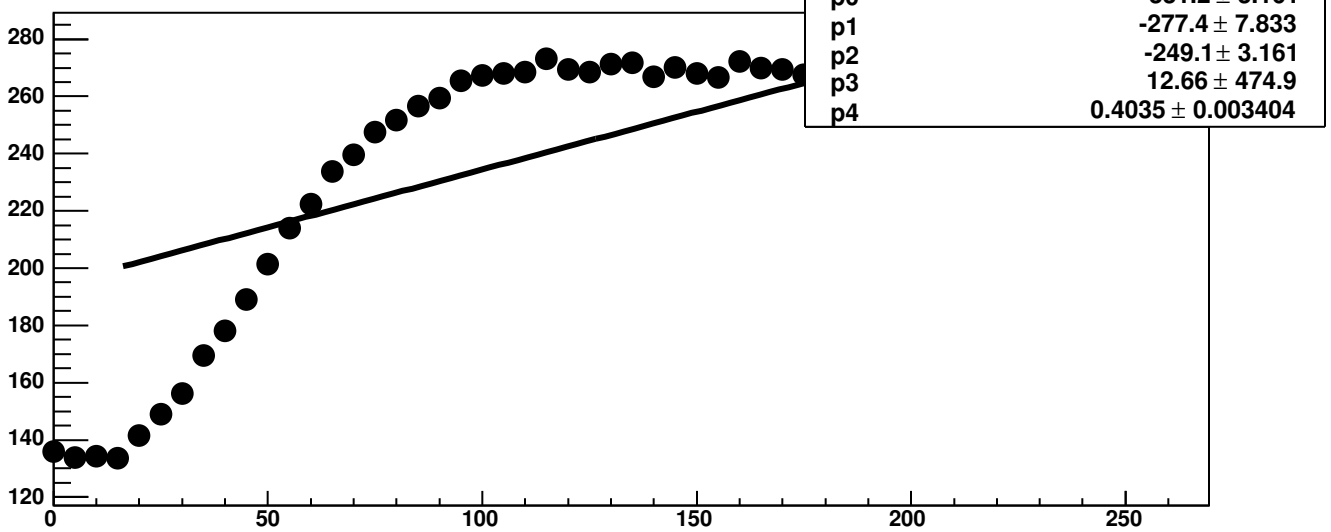
Chip 4, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold



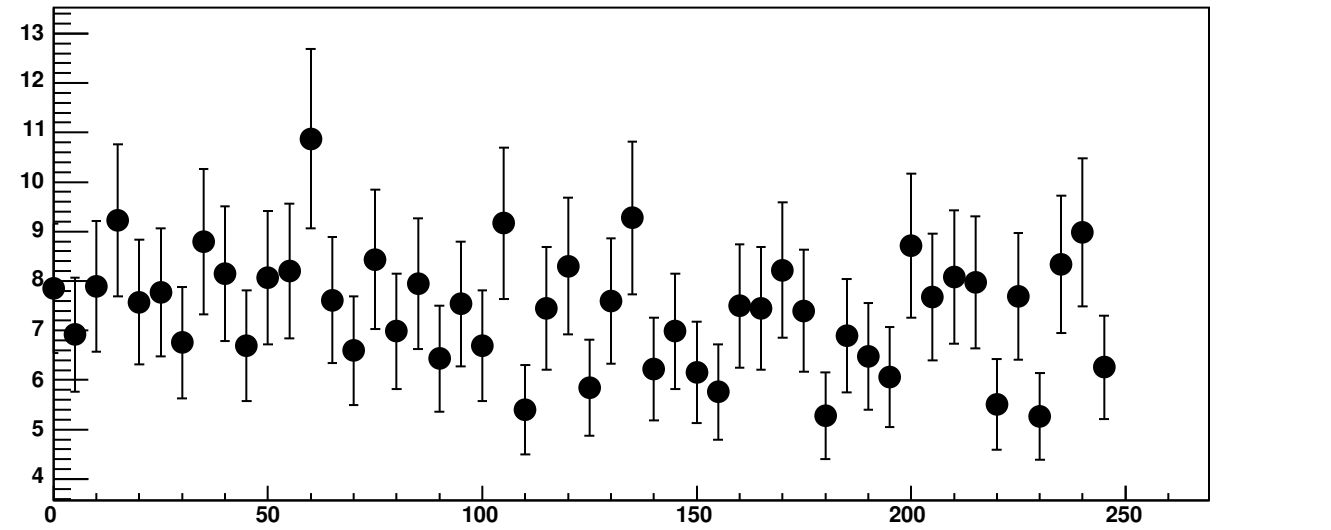
Chip 4, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold



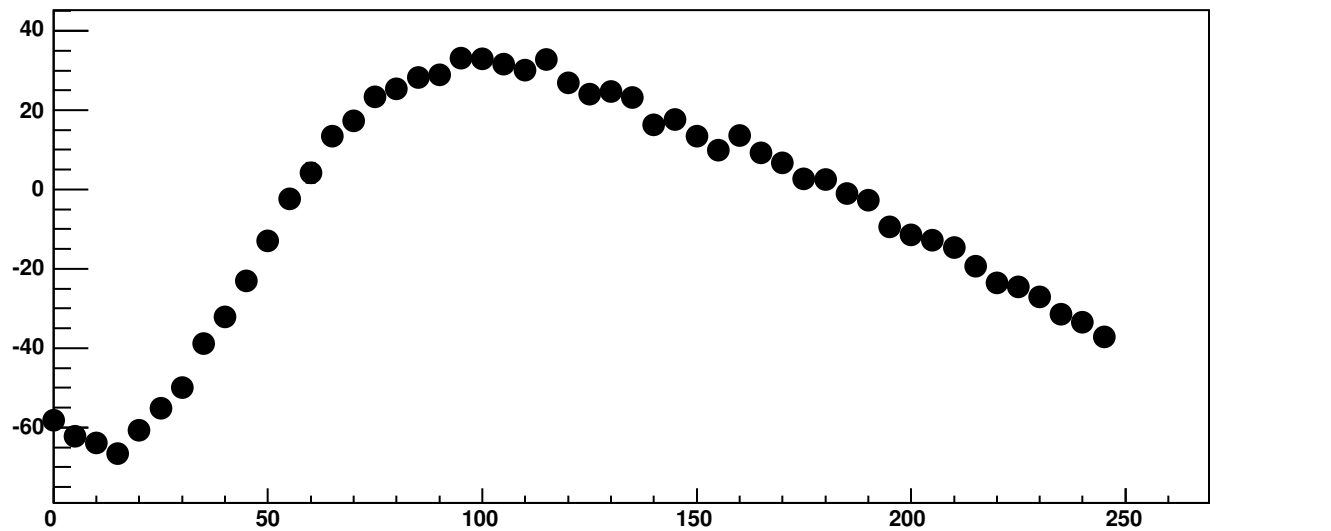
Chip 4, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold



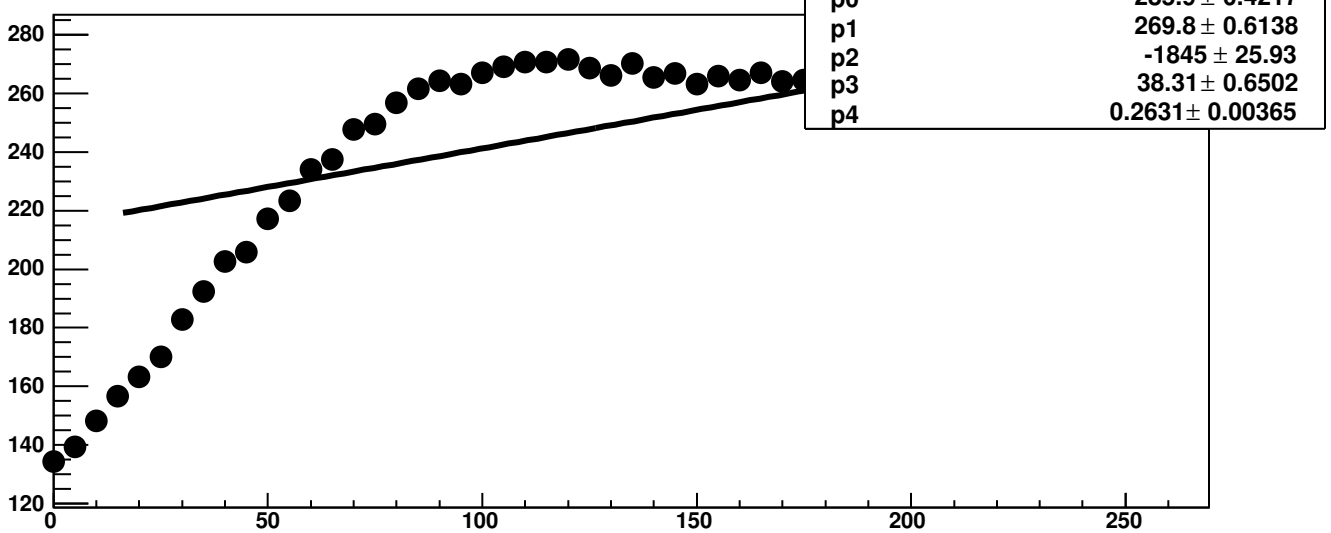
Chip 4, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



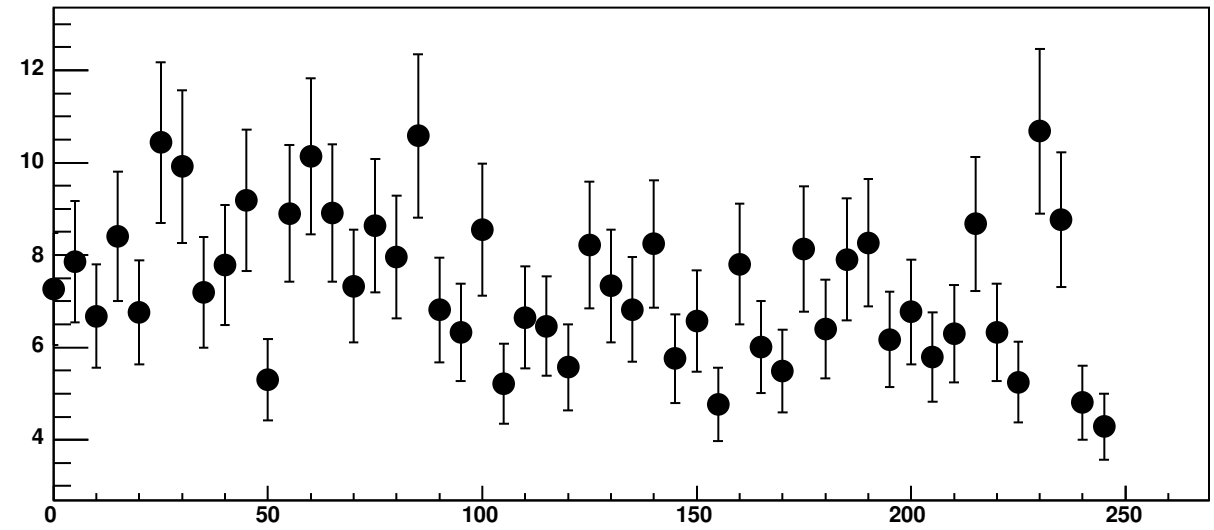
Chip 4, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold



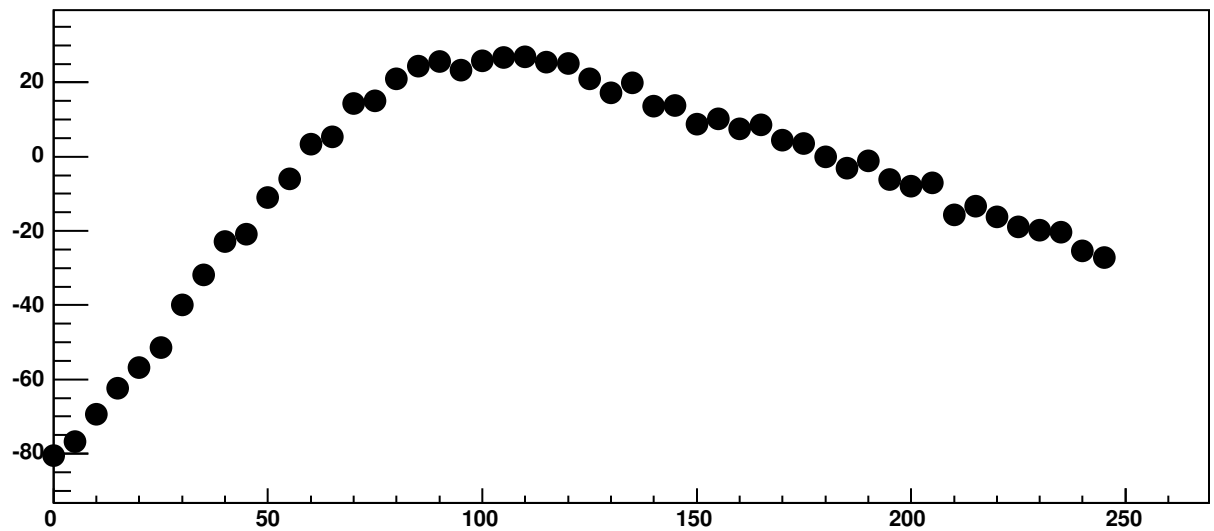
Chip 4, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold



Chip 4, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold

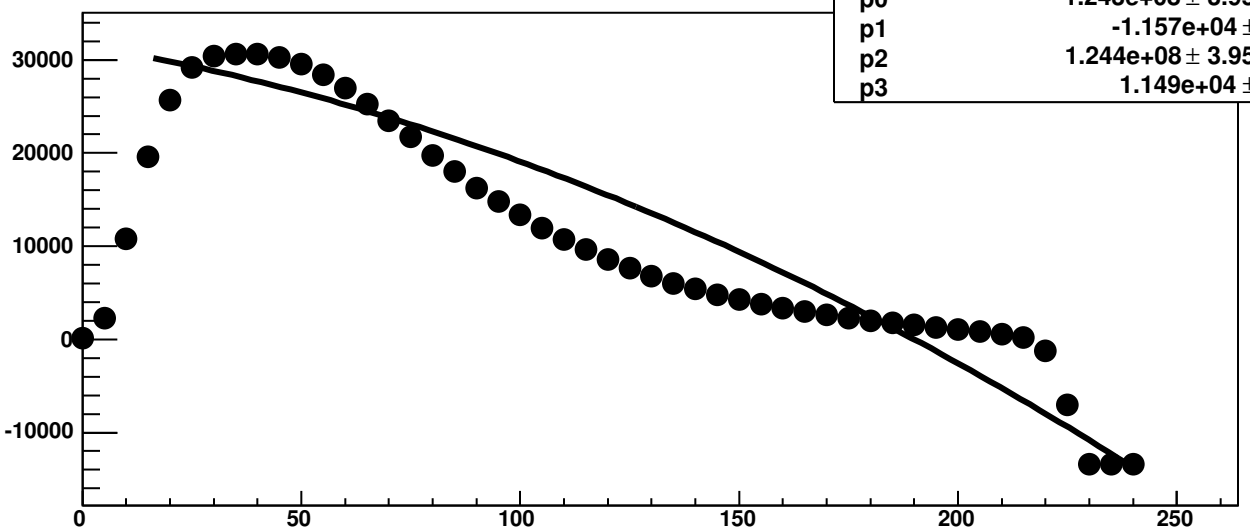


Chip 4, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

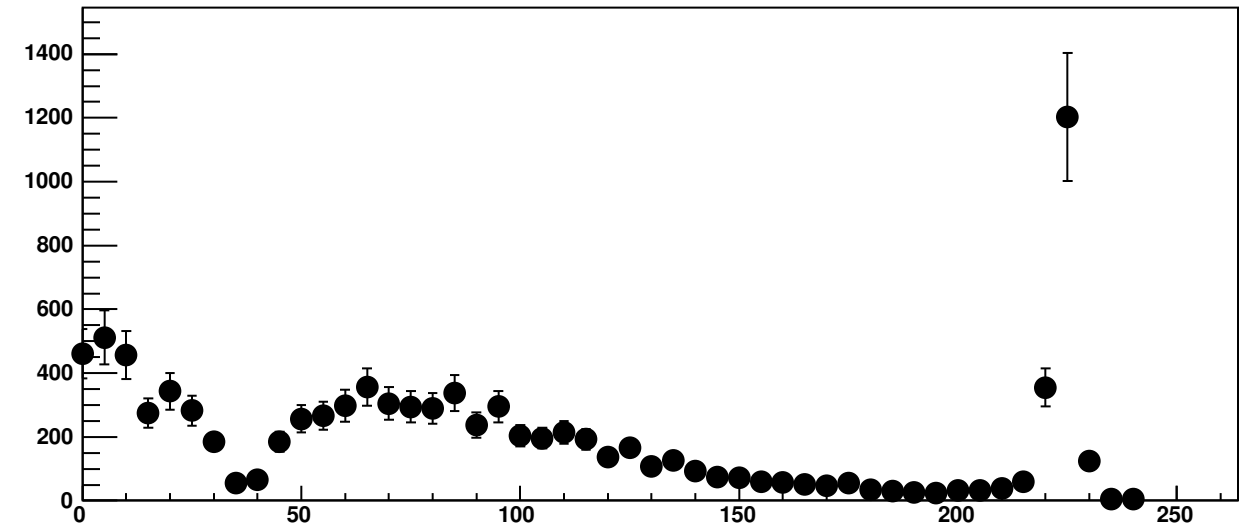


Chip 4, Channel 16, Enable 5!, DAC=4000, ADC Mean vs Hold

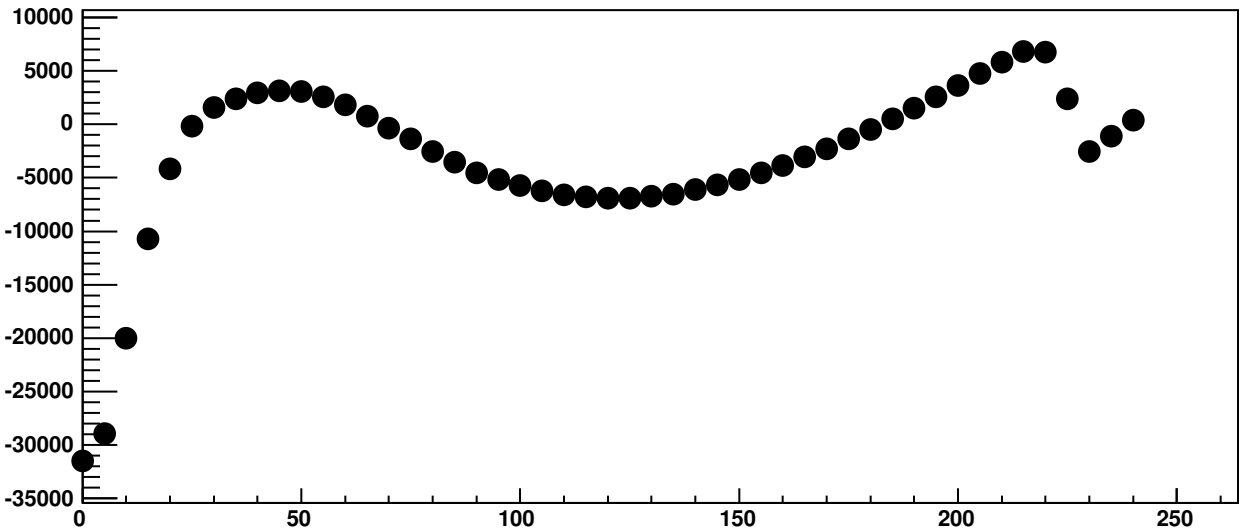
χ^2 / ndf	3.499e+06 / 42
p0	-1.243e+08 ± 3.953e+05
p1	-1.157e+04 ± 20.63
p2	1.244e+08 ± 3.953e+05
p3	1.149e+04 ± 20.52



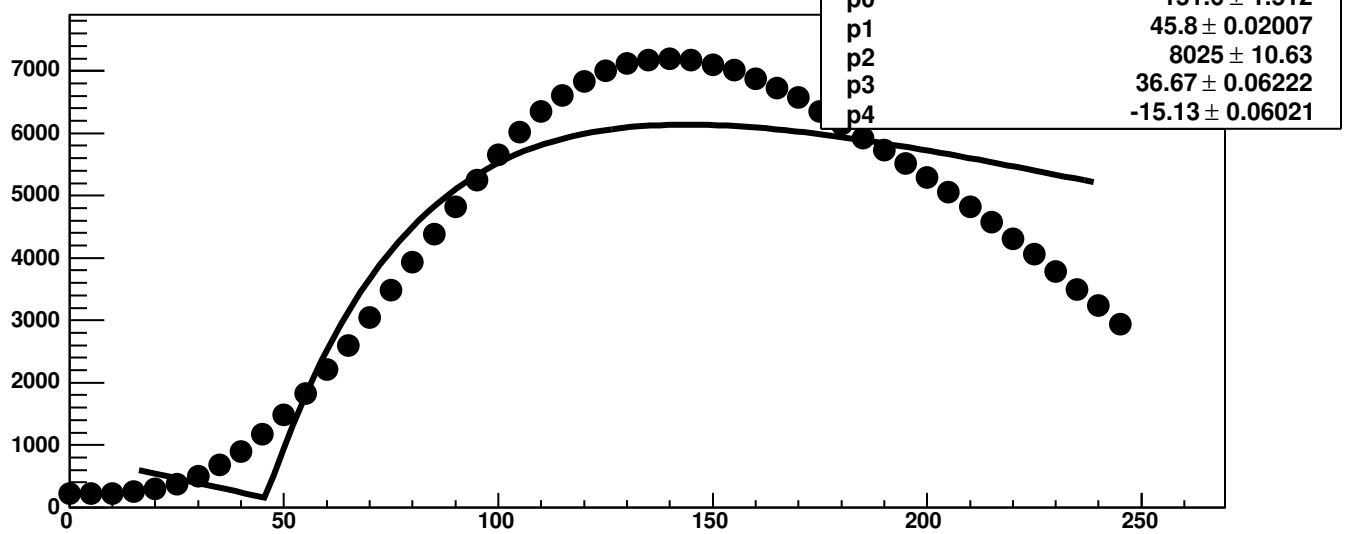
Chip 4, Channel 16, Enable 5!, DAC=4000, ADC Noise vs Hold



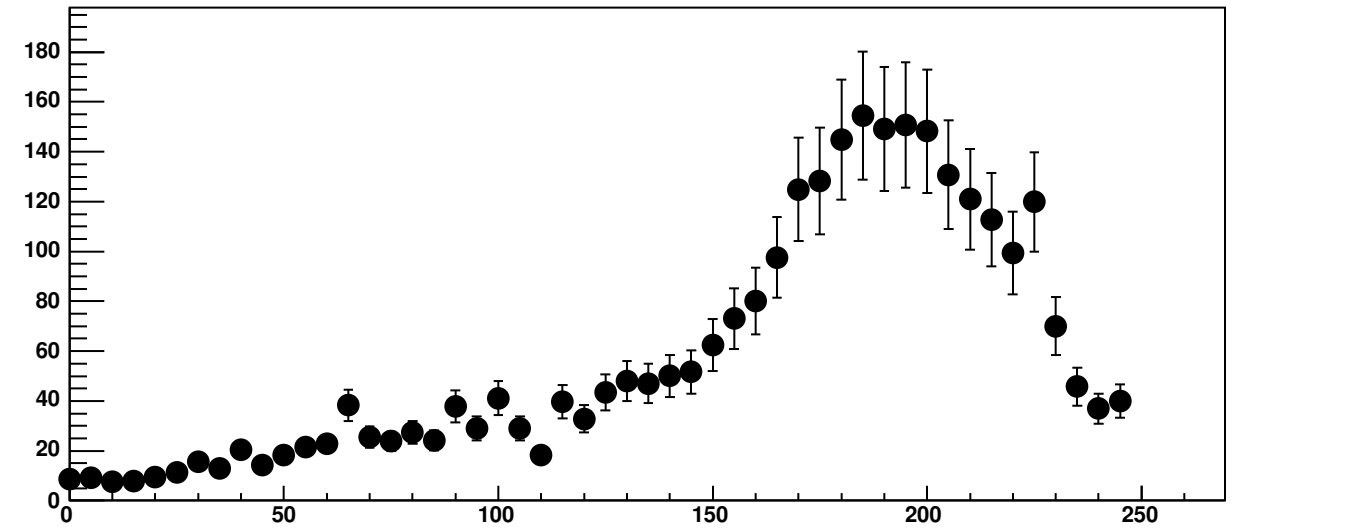
Chip 4, Channel 16, Enable 5!, DAC=4000, ADC Residuals vs Hold



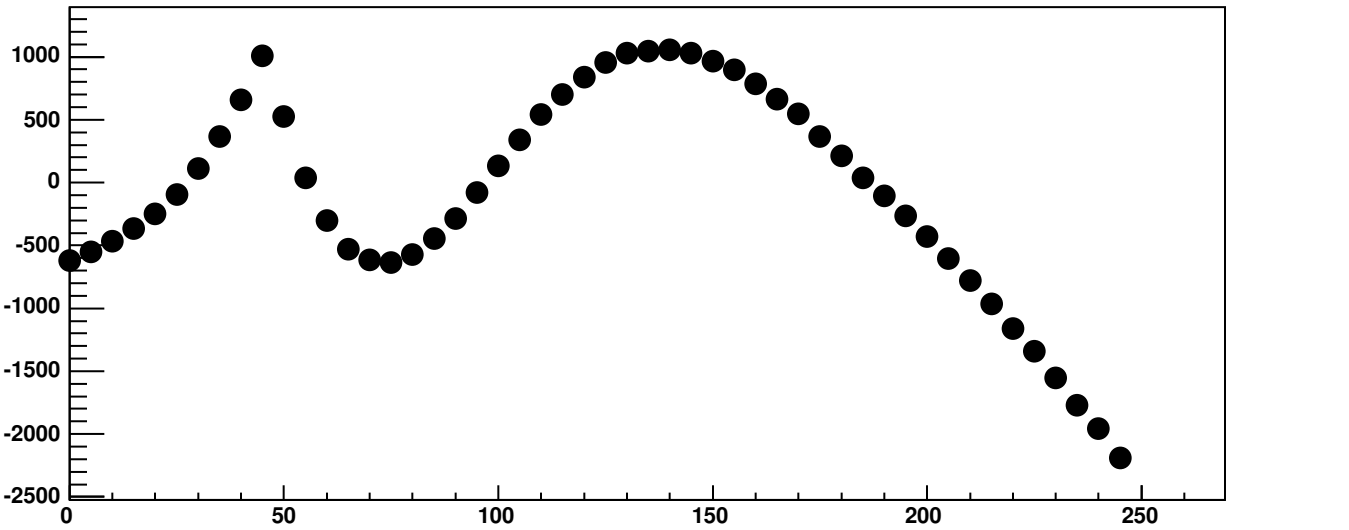
Chip 4, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold



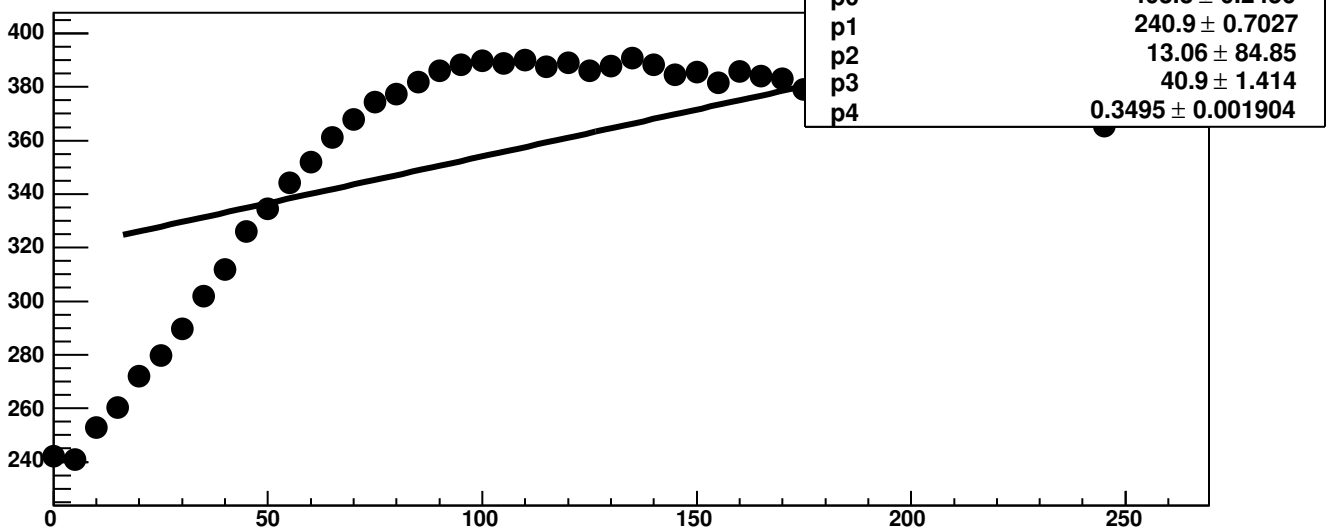
Chip 4, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



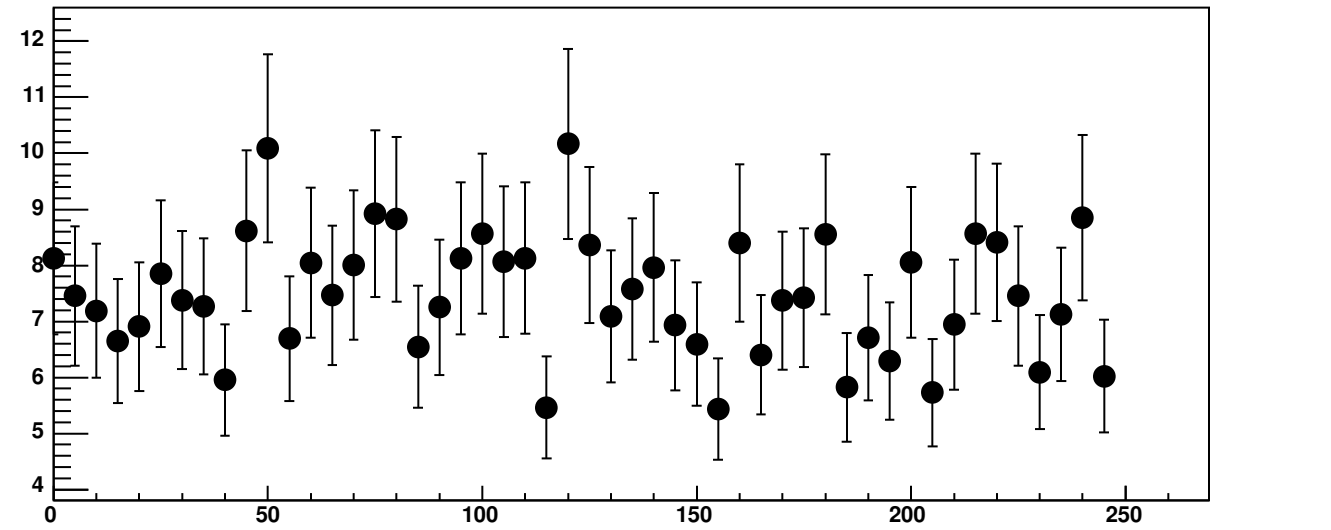
Chip 4, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold



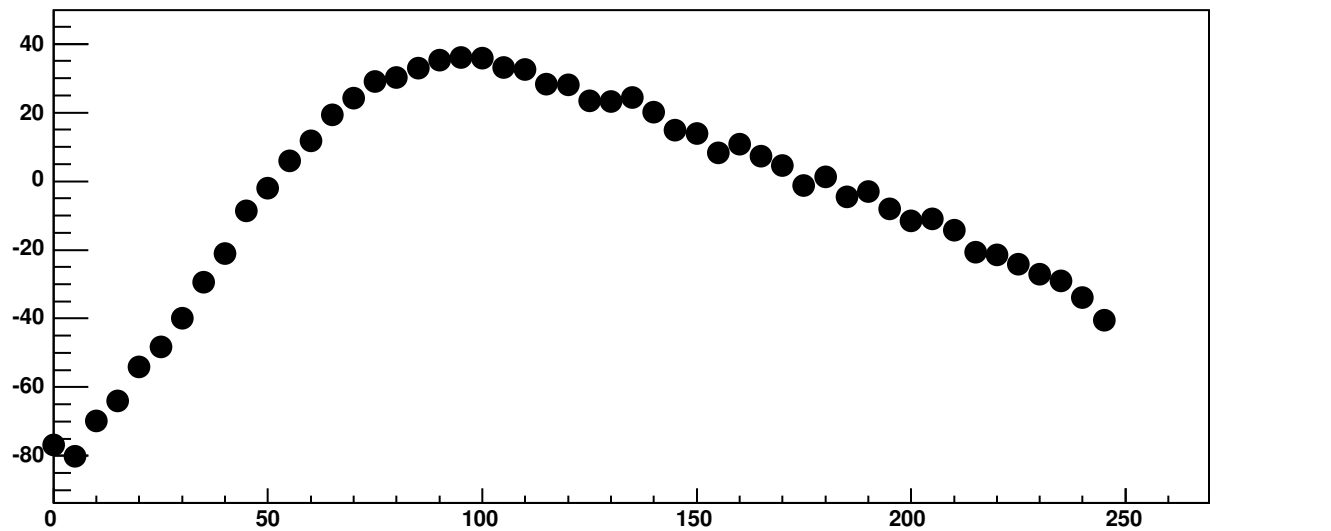
Chip 4, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold



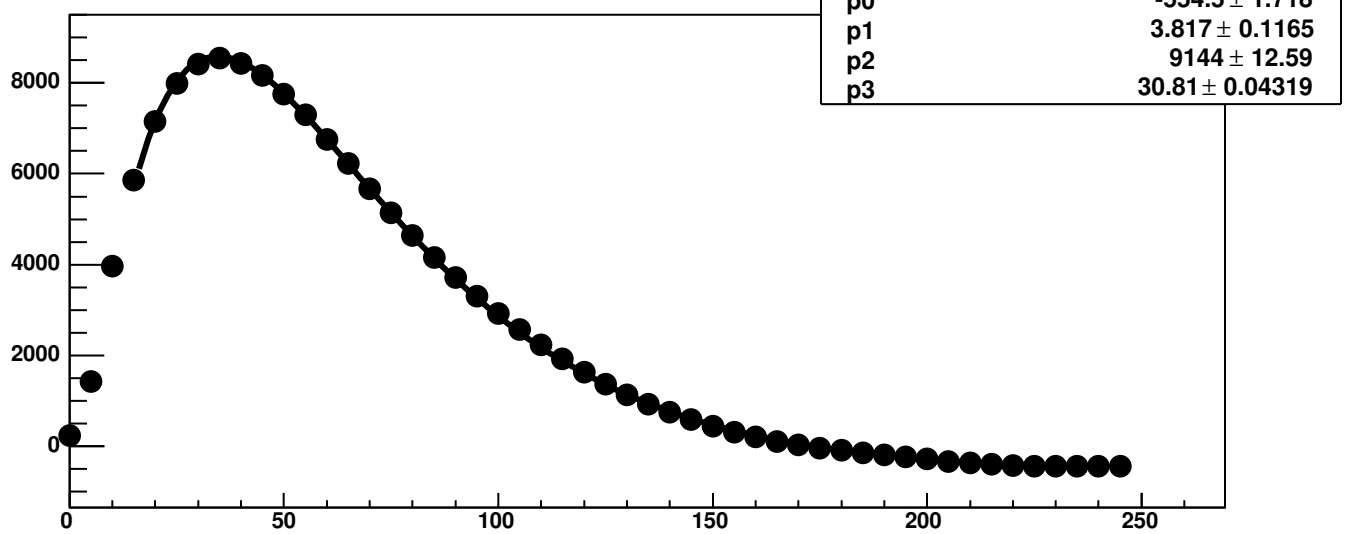
Chip 4, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



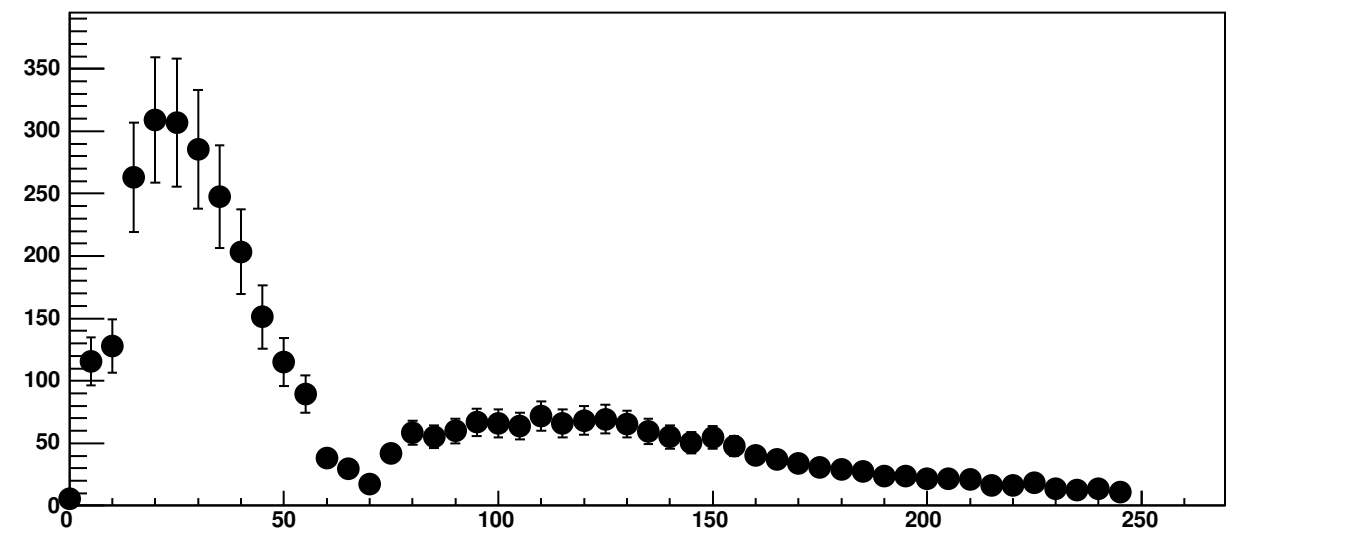
Chip 4, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold



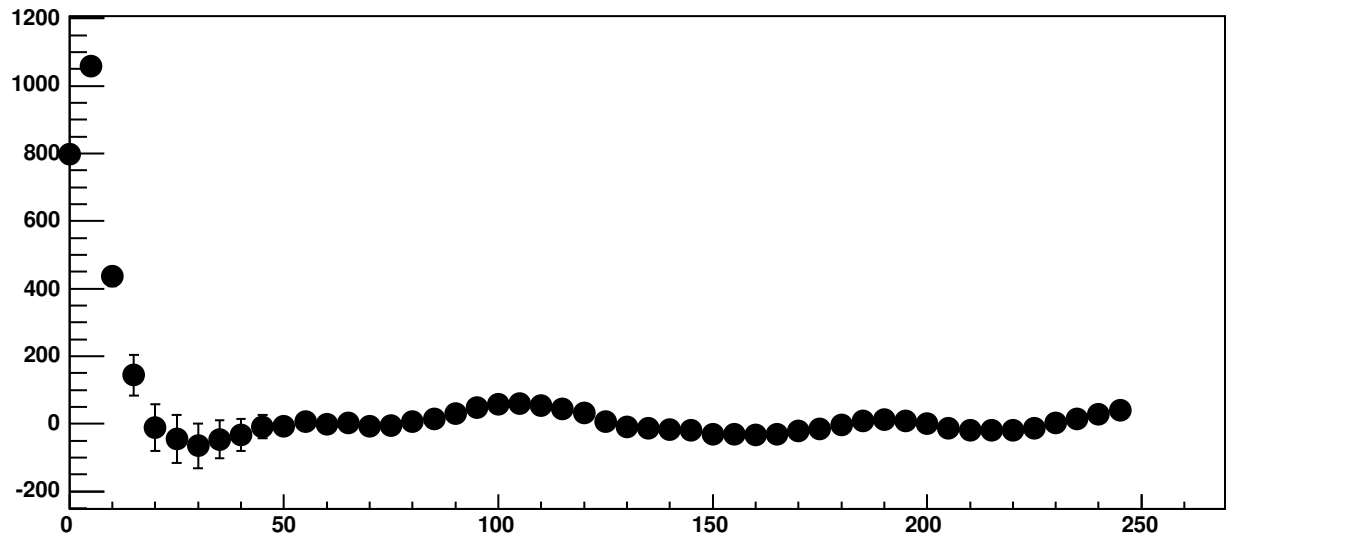
Chip 4, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold



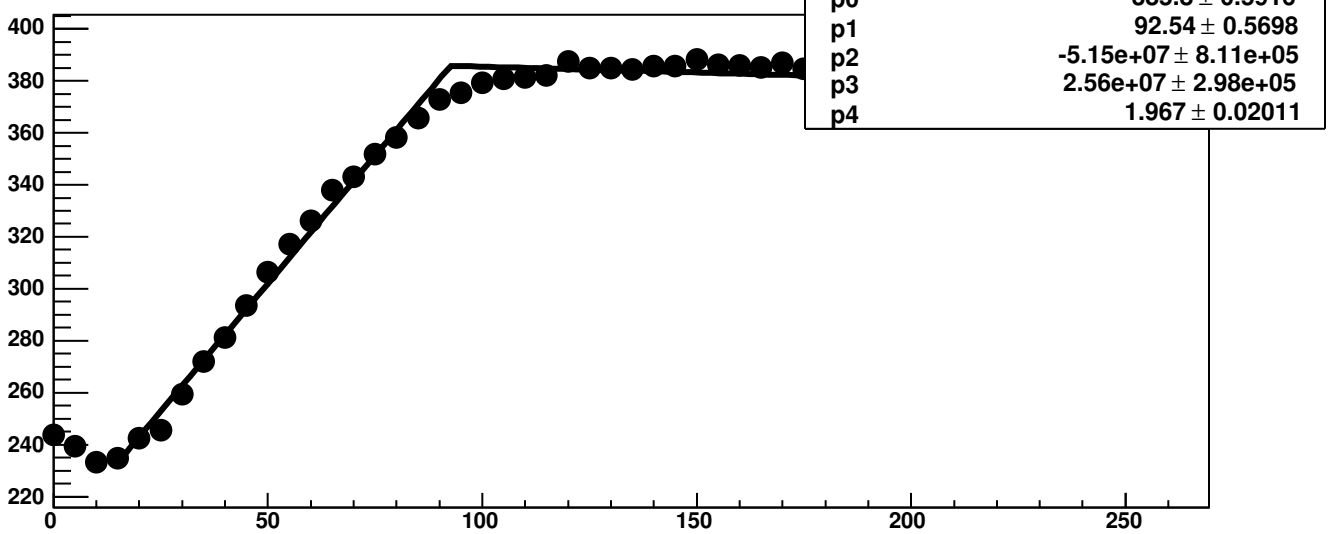
Chip 4, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold



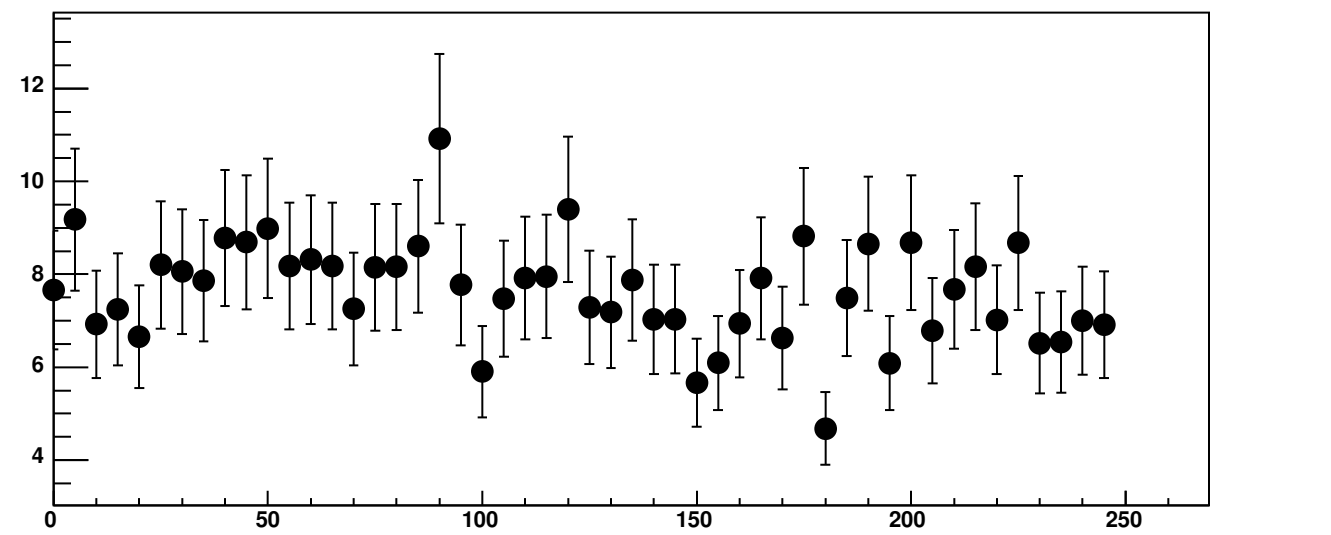
Chip 4, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold



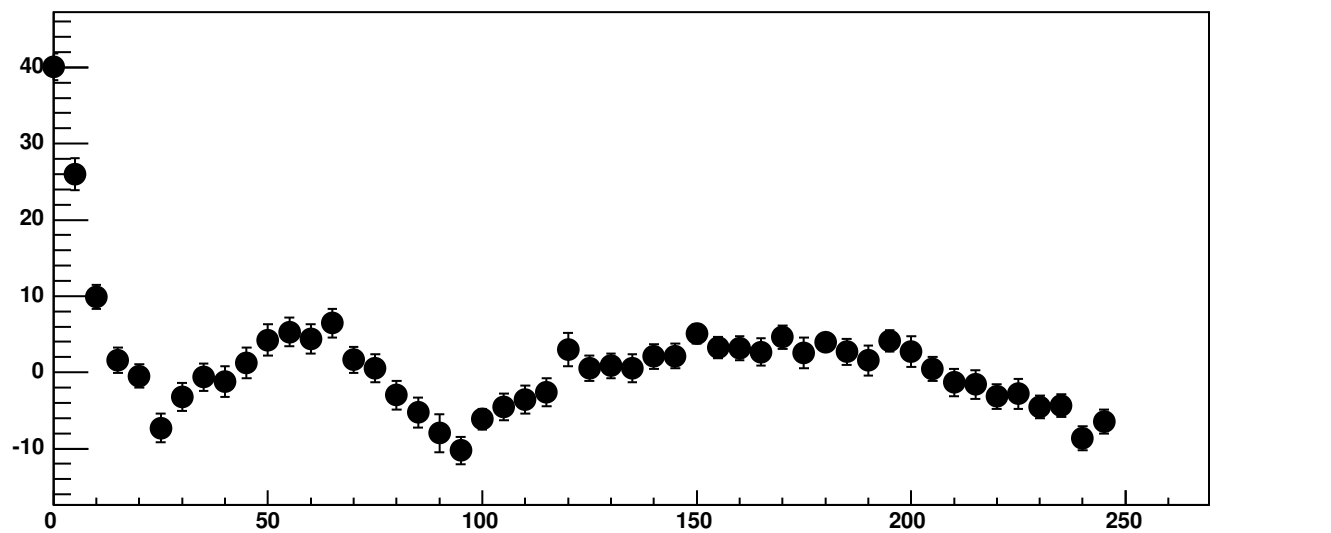
Chip 4, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



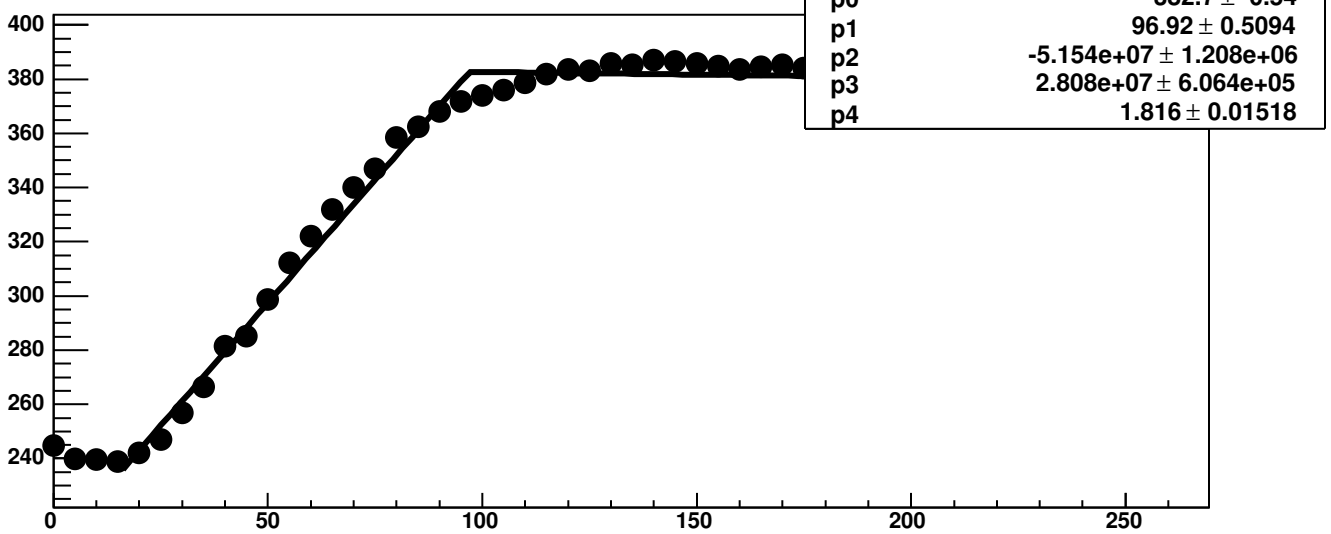
Chip 4, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



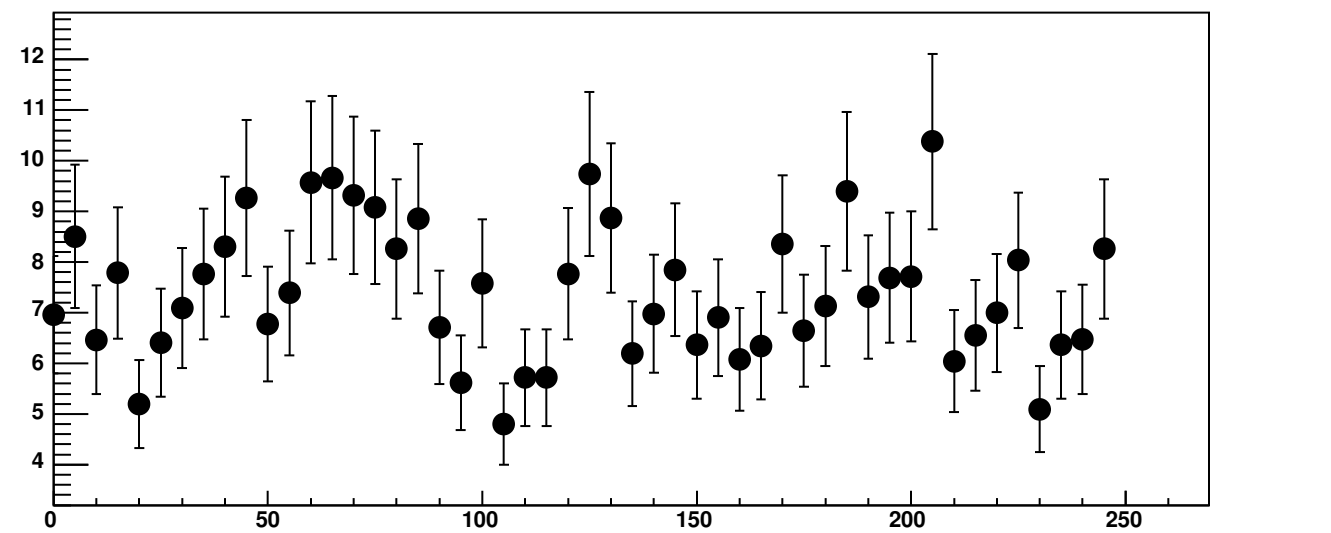
Chip 4, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold



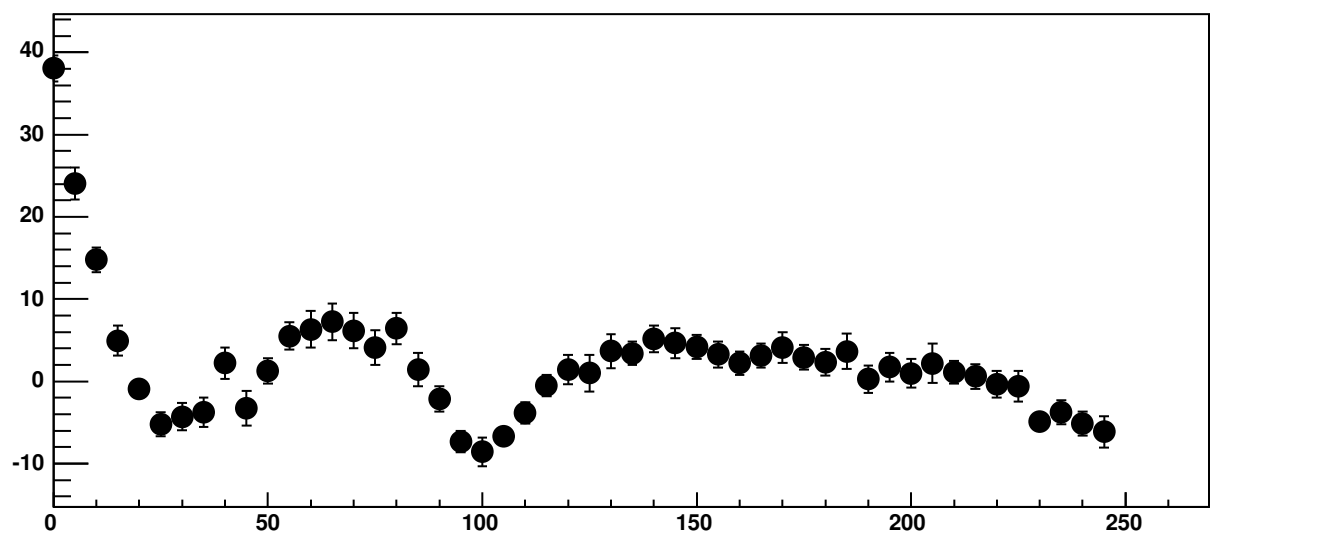
Chip 4, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



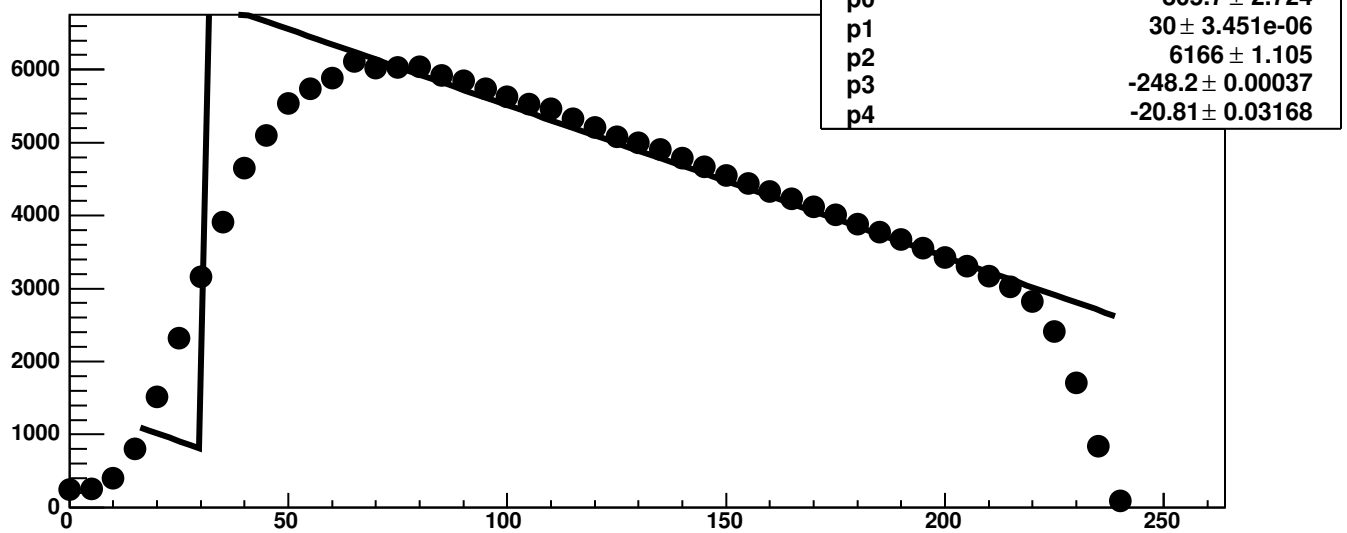
Chip 4, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



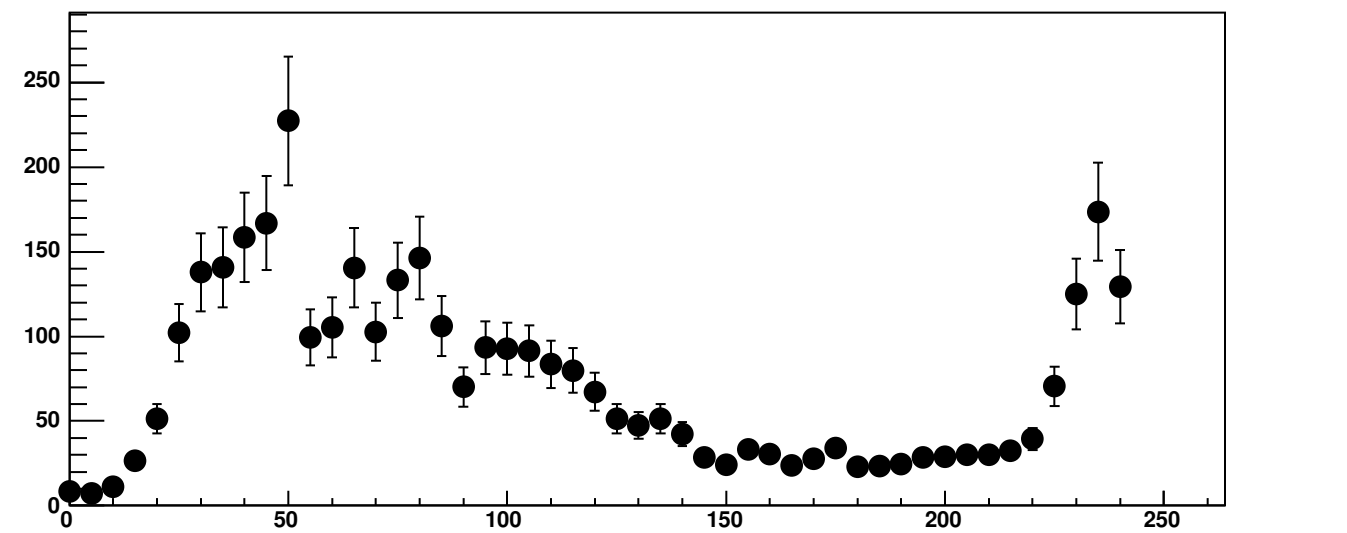
Chip 4, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold



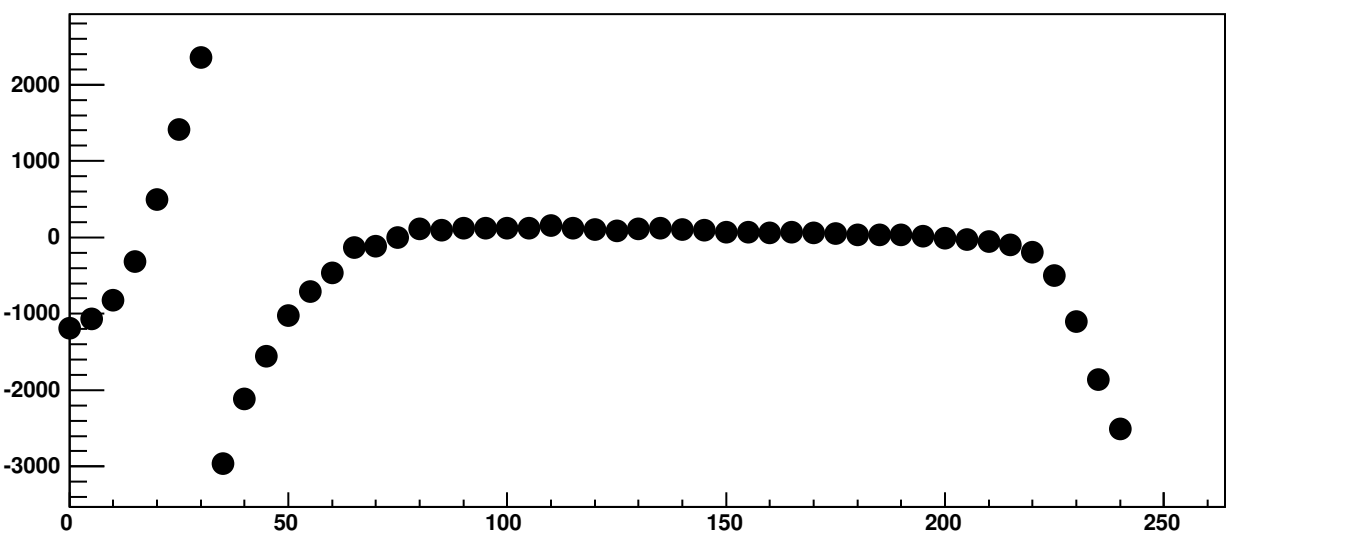
Chip 4, Channel 17, Enable 5, DAC=4000, ADC Mean vs Hold

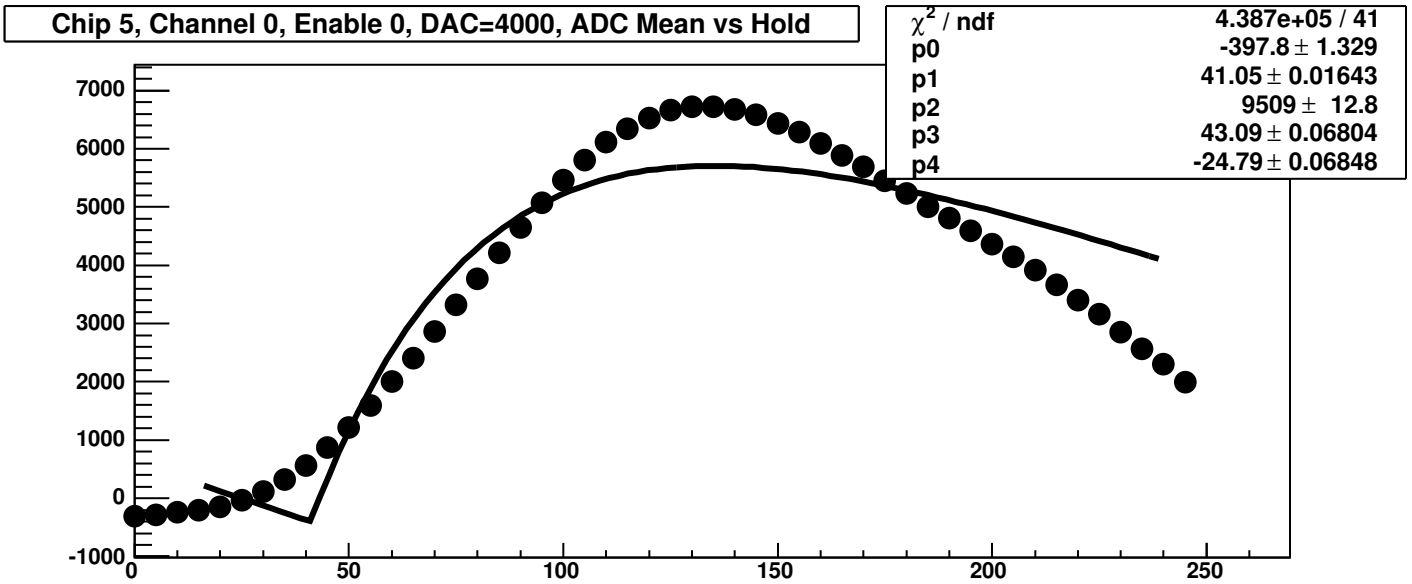


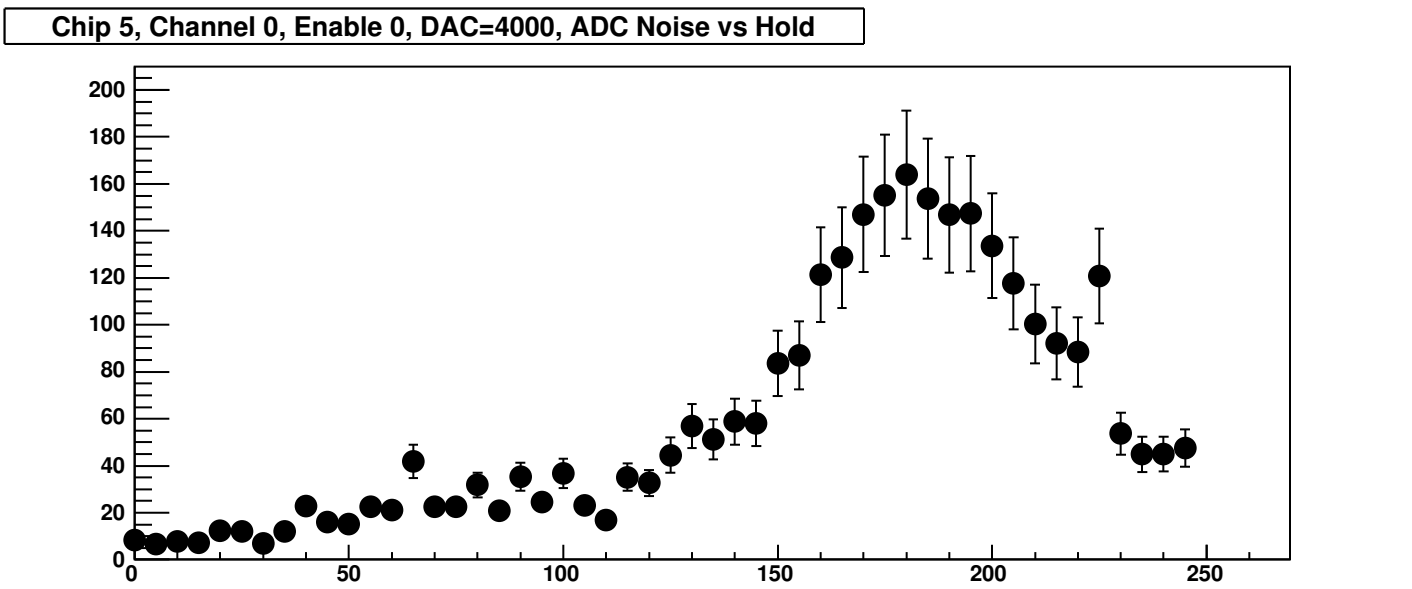
Chip 4, Channel 17, Enable 5, DAC=4000, ADC Noise vs Hold

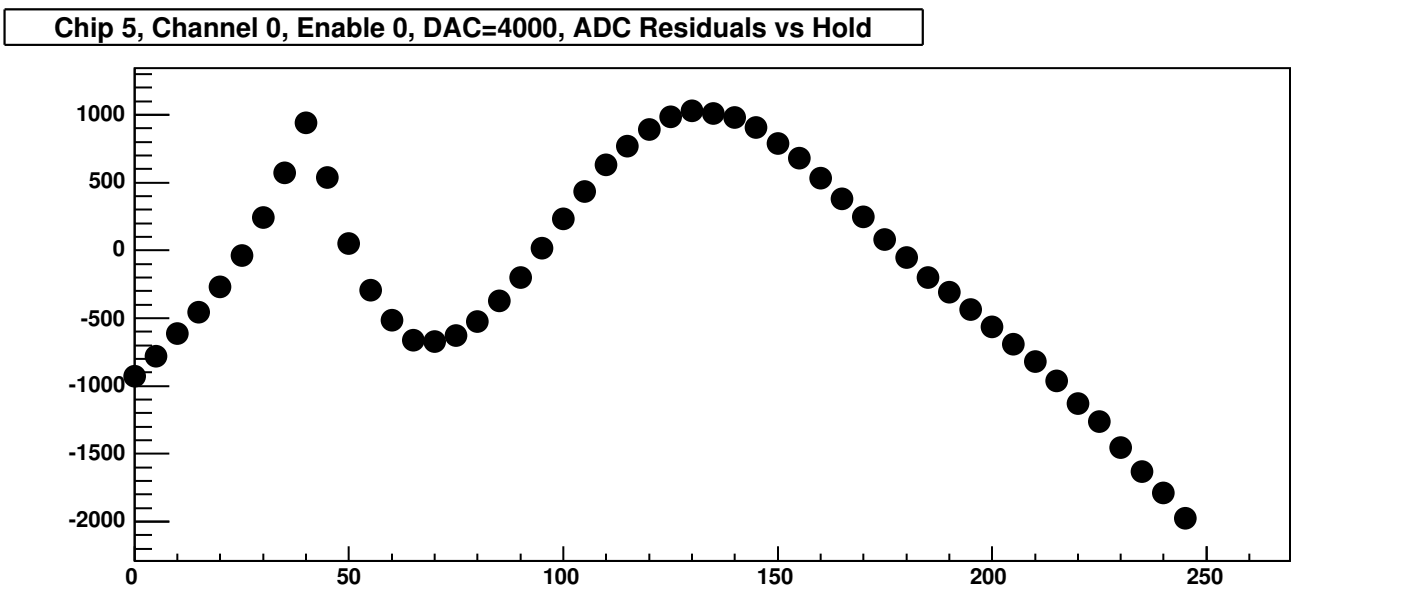


Chip 4, Channel 17, Enable 5, DAC=4000, ADC Residuals vs Hold

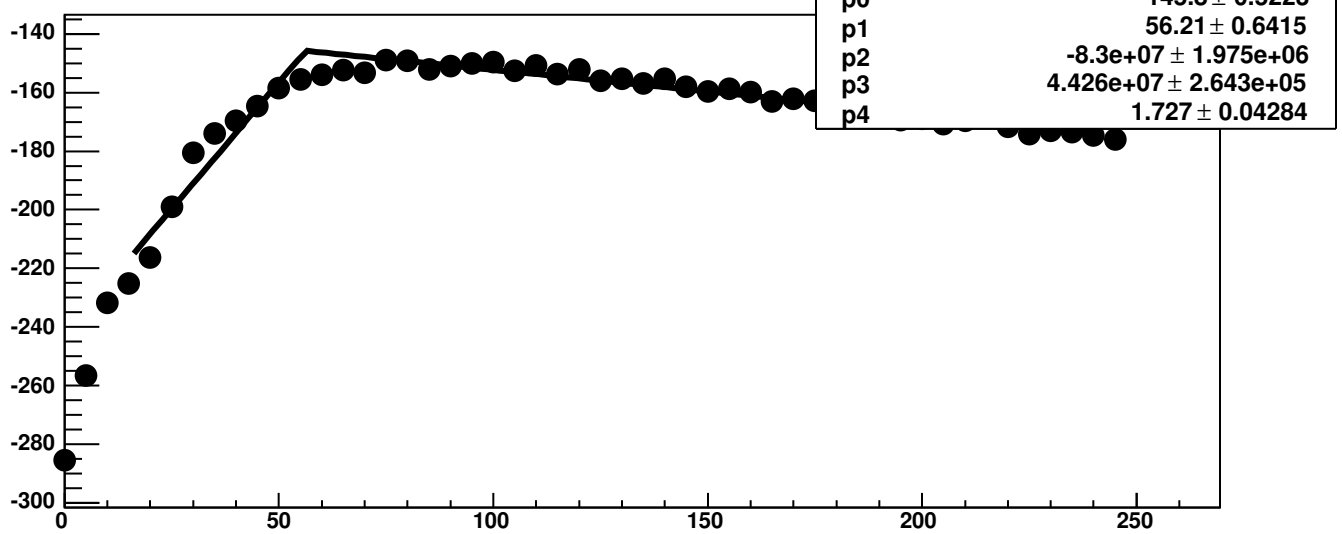



Chip 5, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.387e+05 / 41
p0 -397.8 ± 1.329
p1 41.05 ± 0.01643
p2 9509 ± 12.8
p3 43.09 ± 0.06804
p4 -24.79 ± 0.06848
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

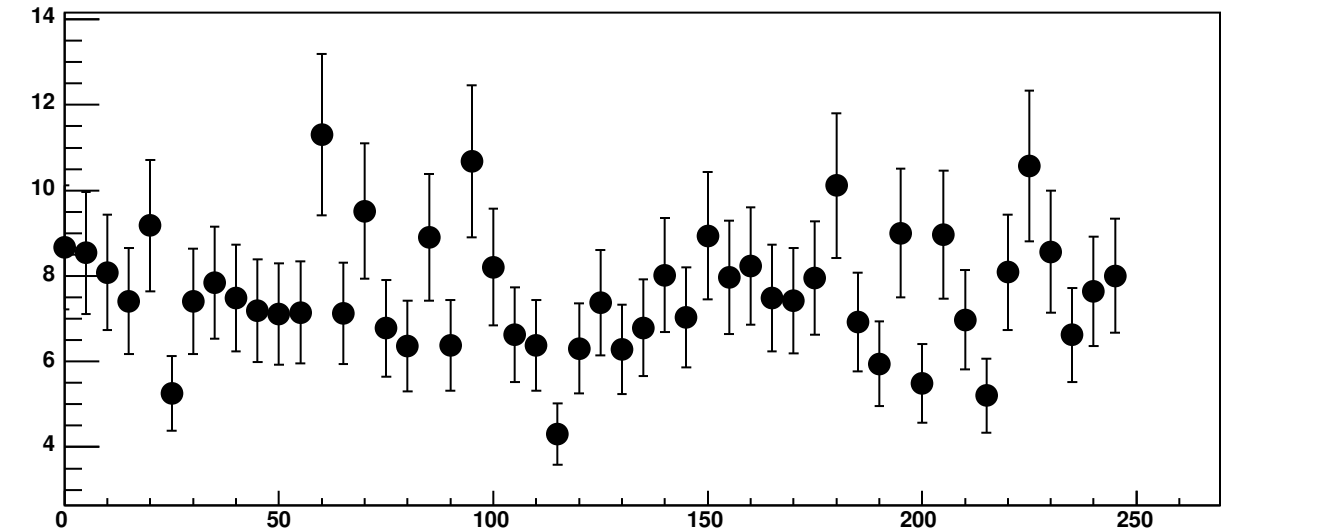

Chip 5, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 5, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

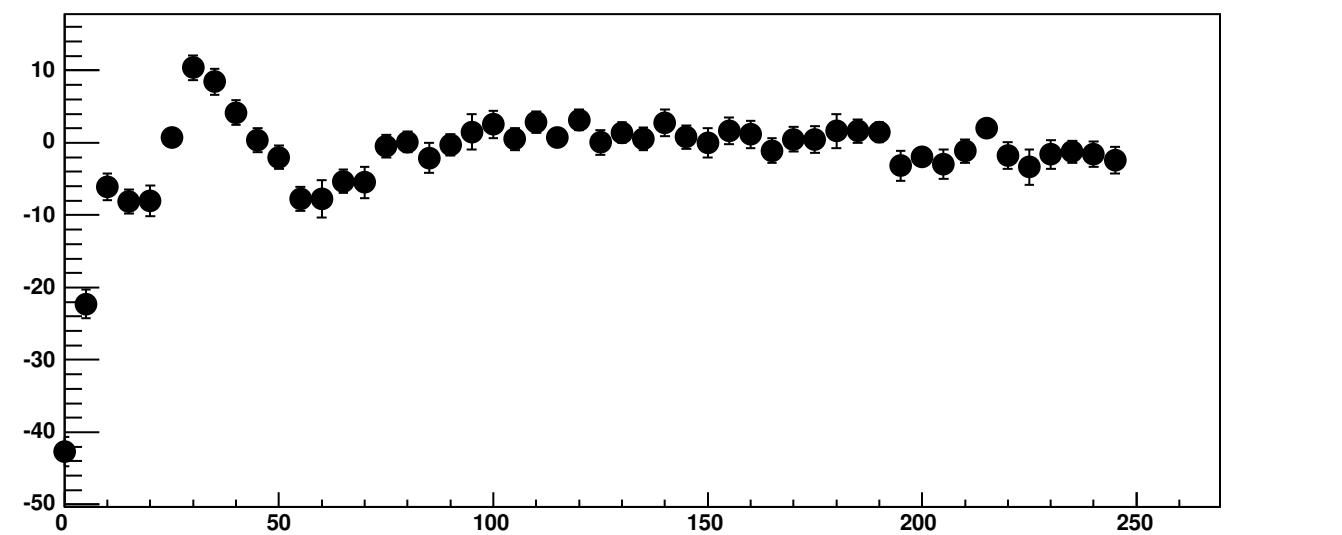
Chip 5, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold



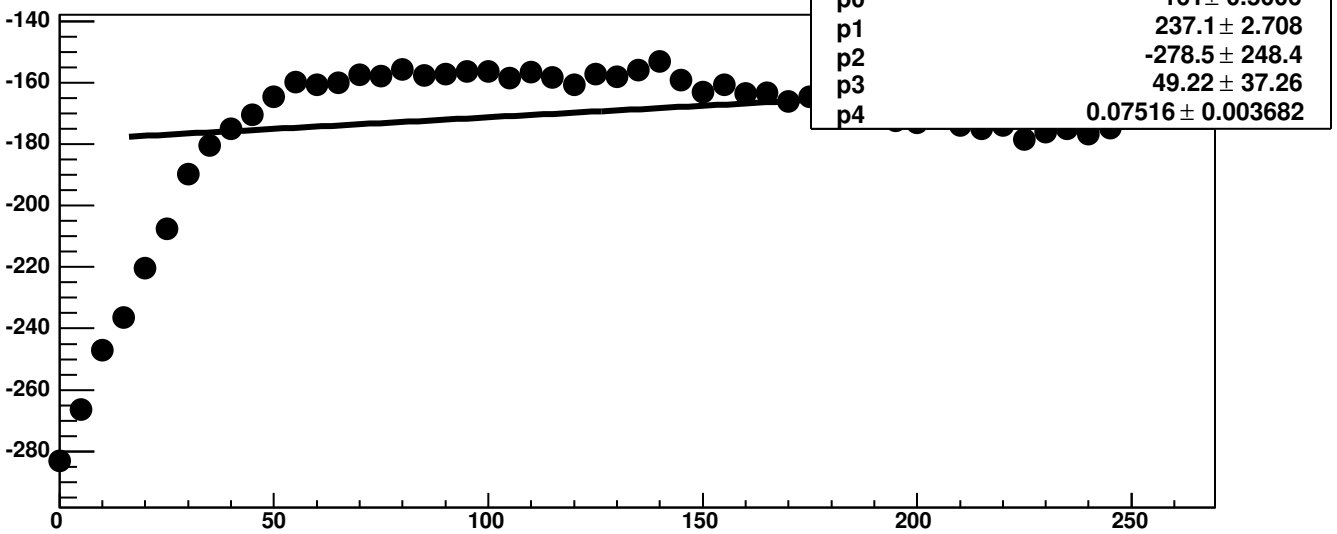
Chip 5, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



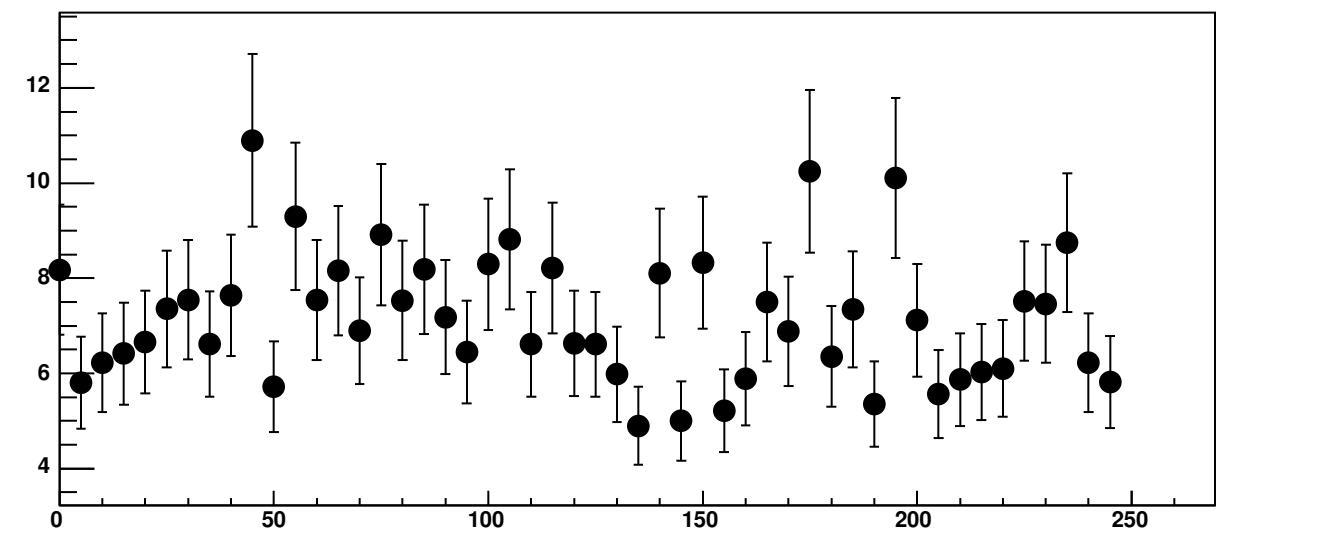
Chip 5, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold



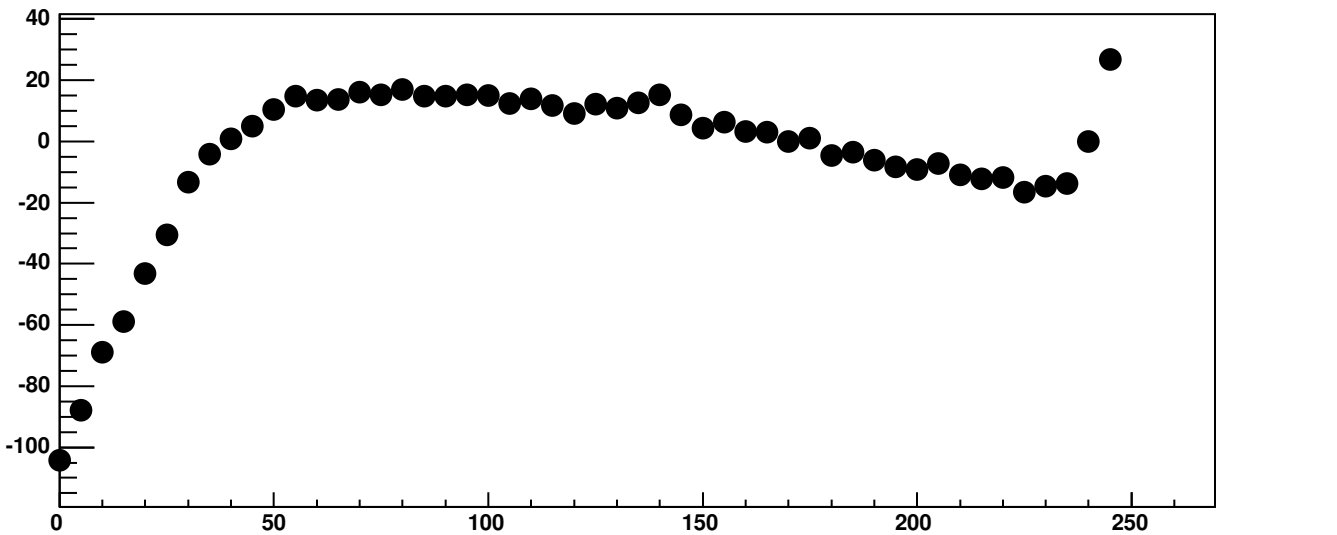
Chip 5, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold

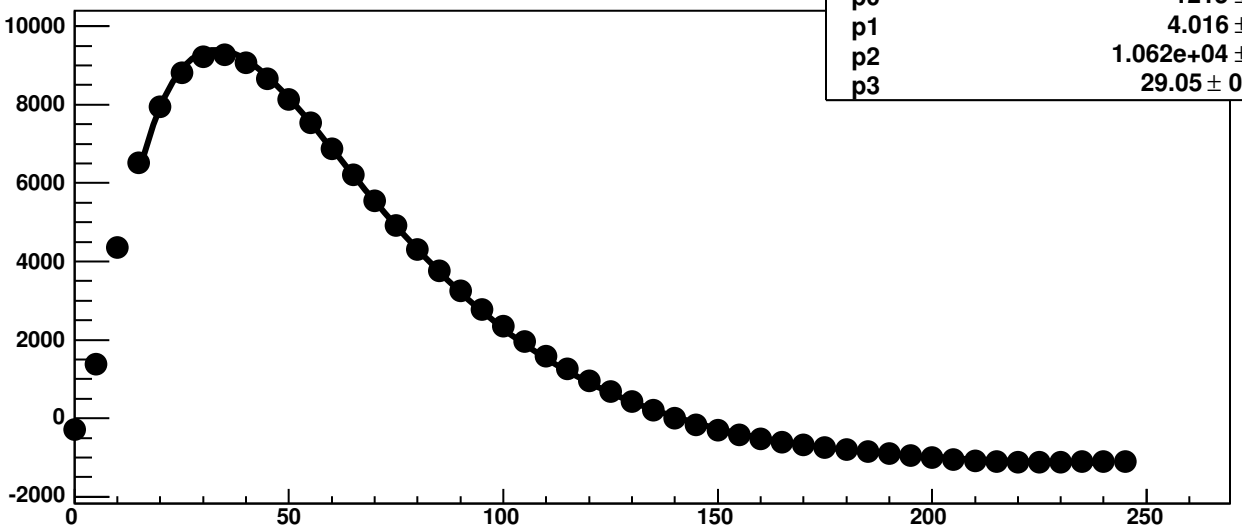


Chip 5, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold

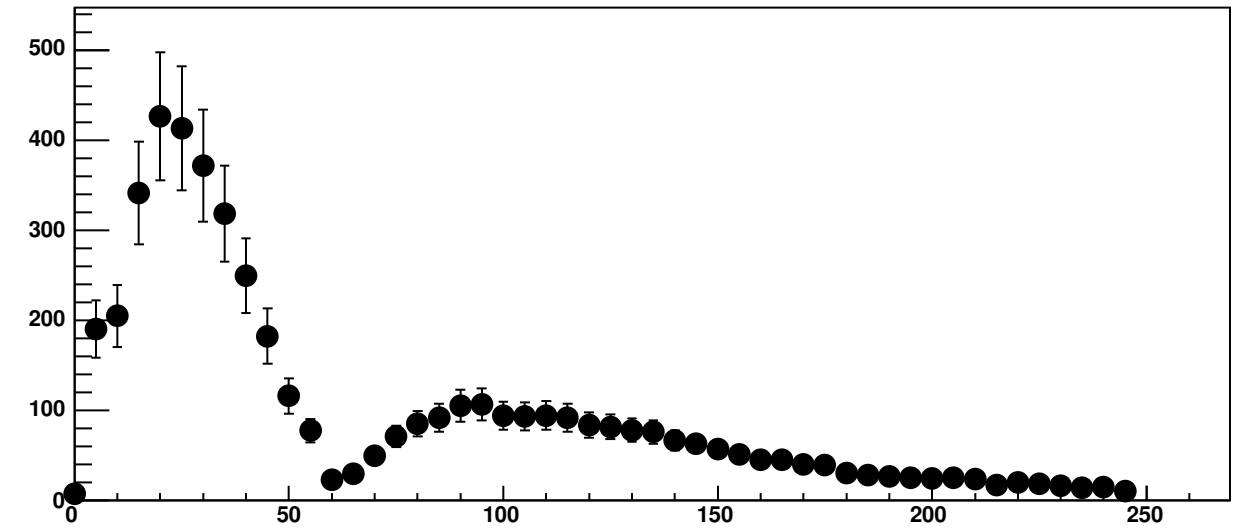


Chip 5, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold

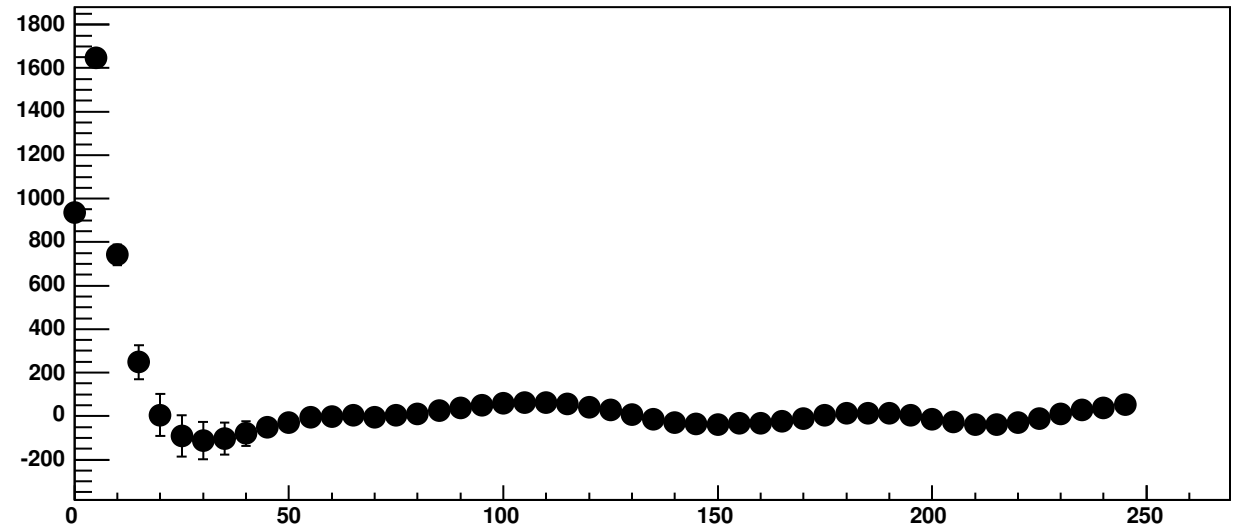
χ^2 / ndf	553.6 / 42
p0	-1215 ± 1.817
p1	4.016 ± 0.129
p2	1.062e+04 ± 17.07
p3	29.05 ± 0.04426



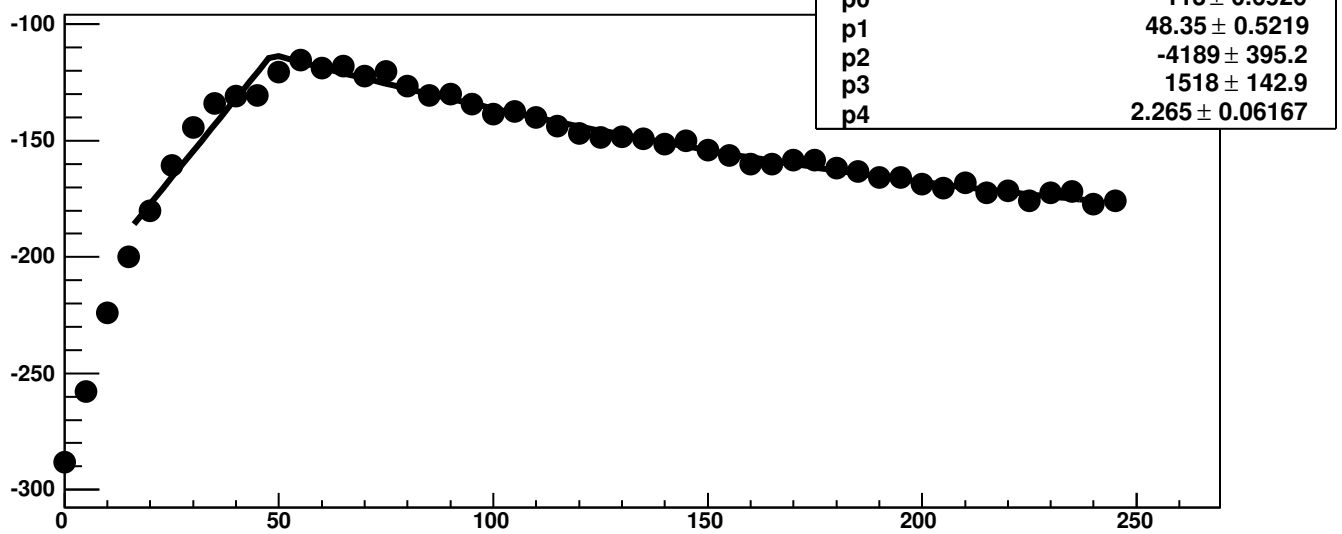
Chip 5, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold



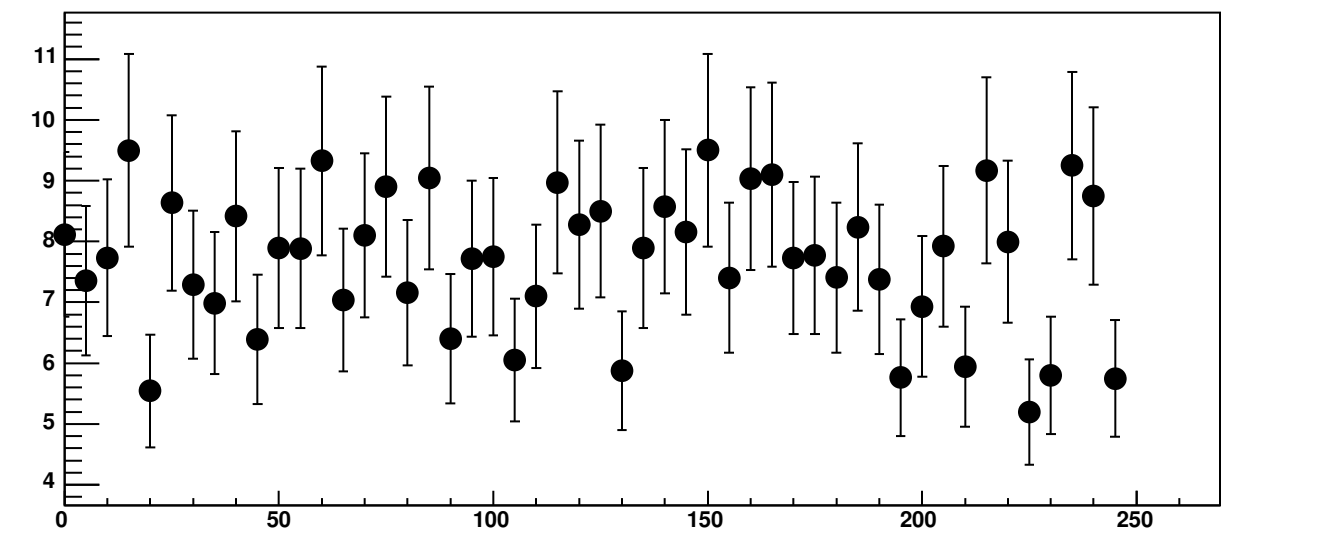
Chip 5, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold



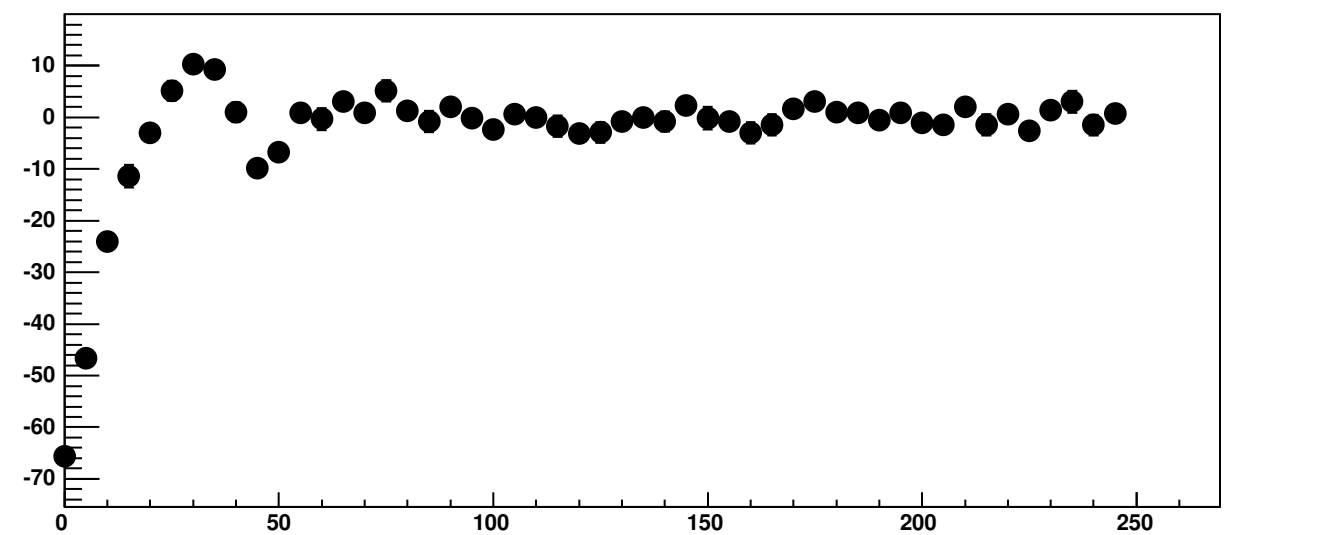
Chip 5, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

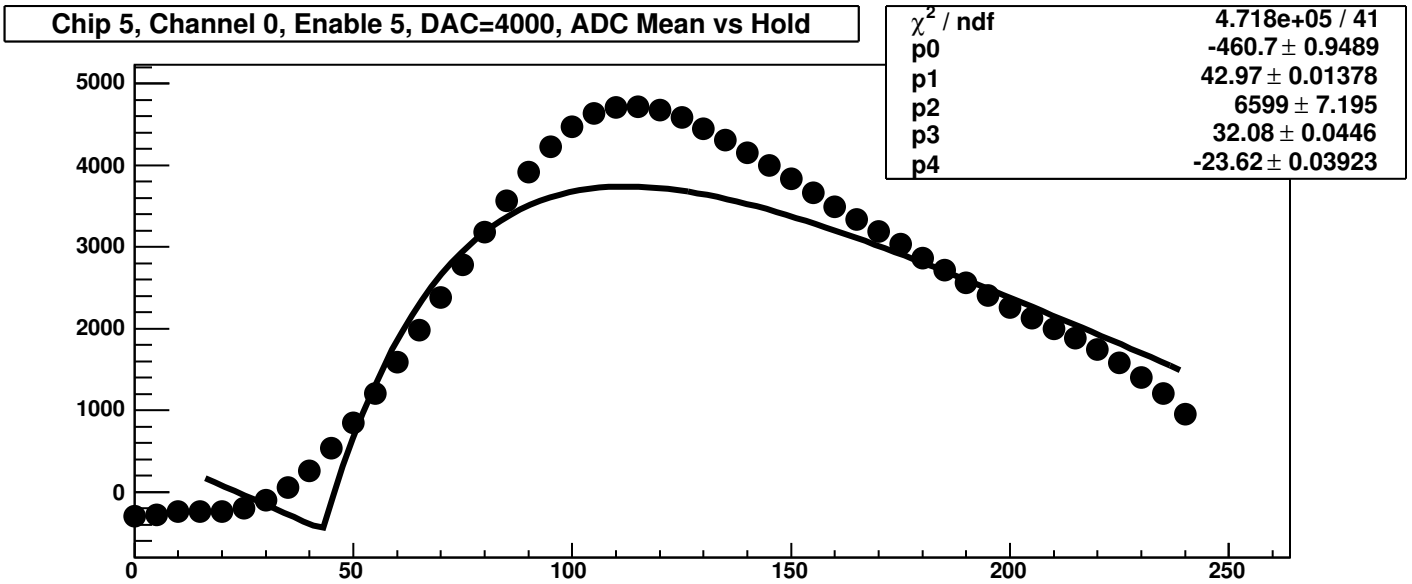


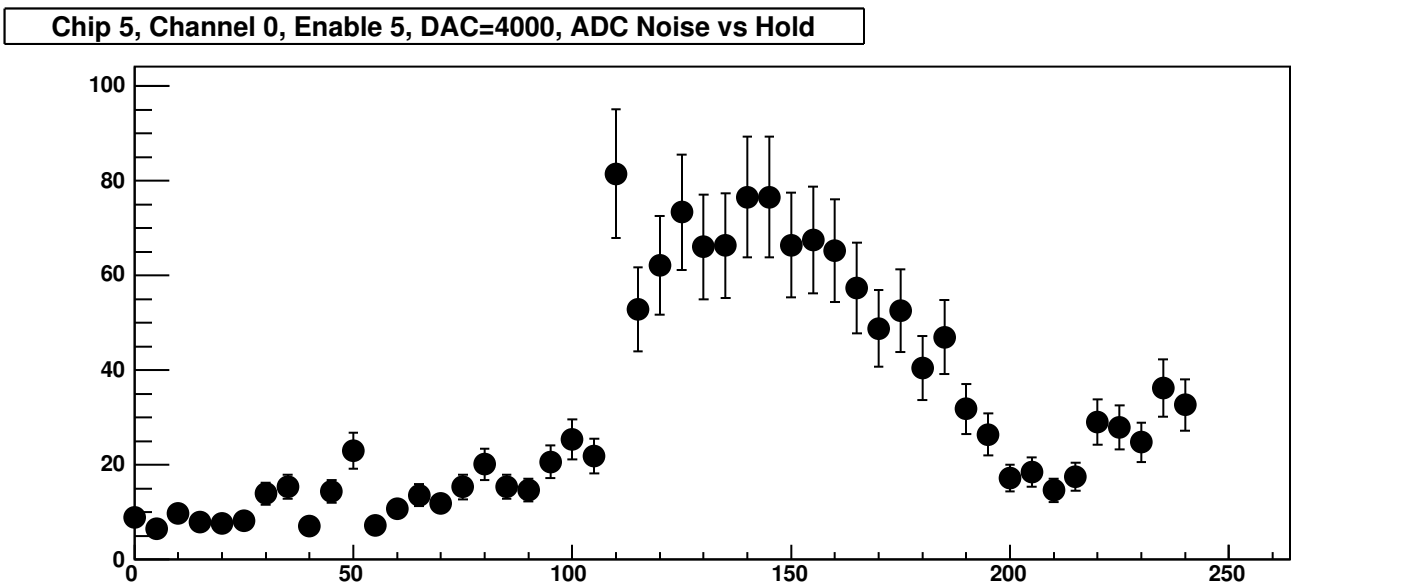
Chip 5, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold

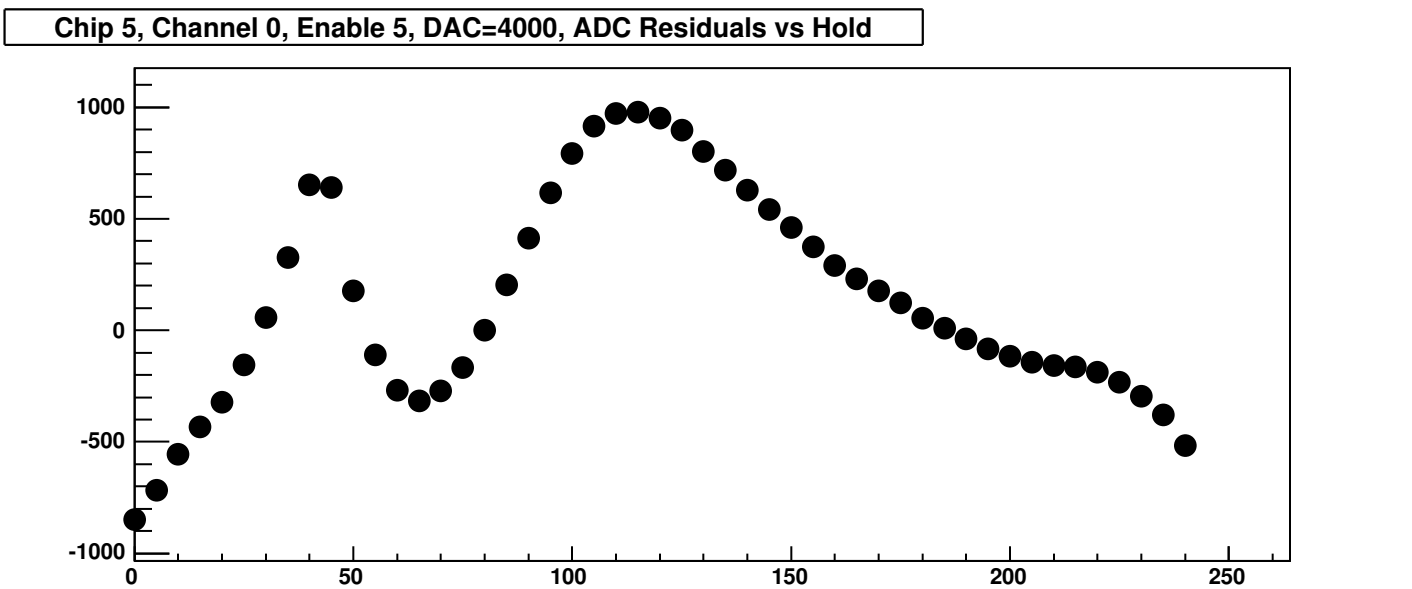


Chip 5, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold



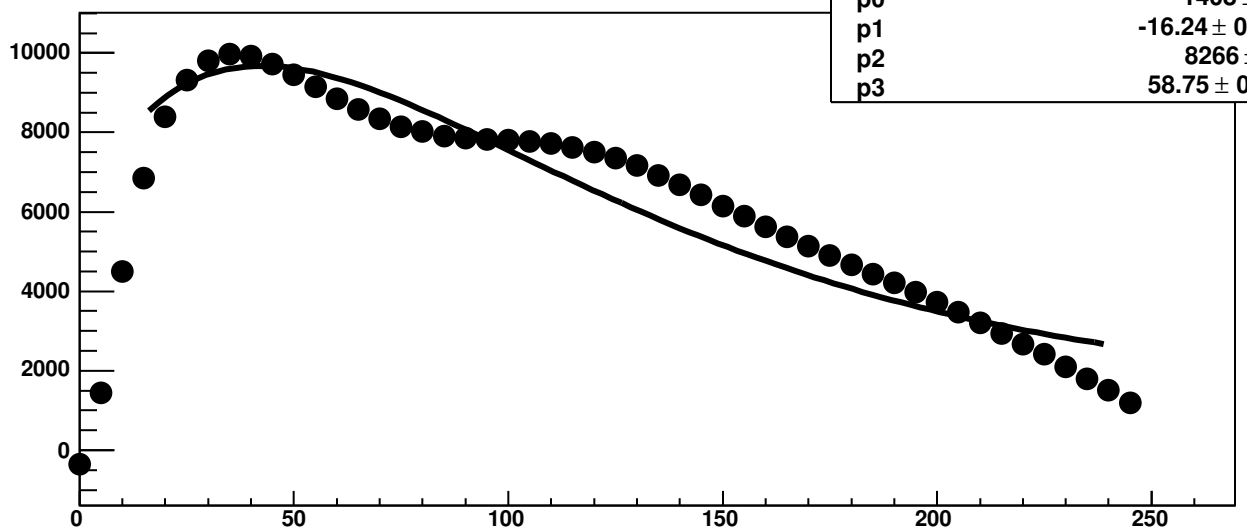

Chip 5, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.718e+05 / 41
p0 -460.7 ± 0.9489
p1 42.97 ± 0.01378
p2 6599 ± 7.195
p3 32.08 ± 0.0446
p4 -23.62 ± 0.03923
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 5, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250

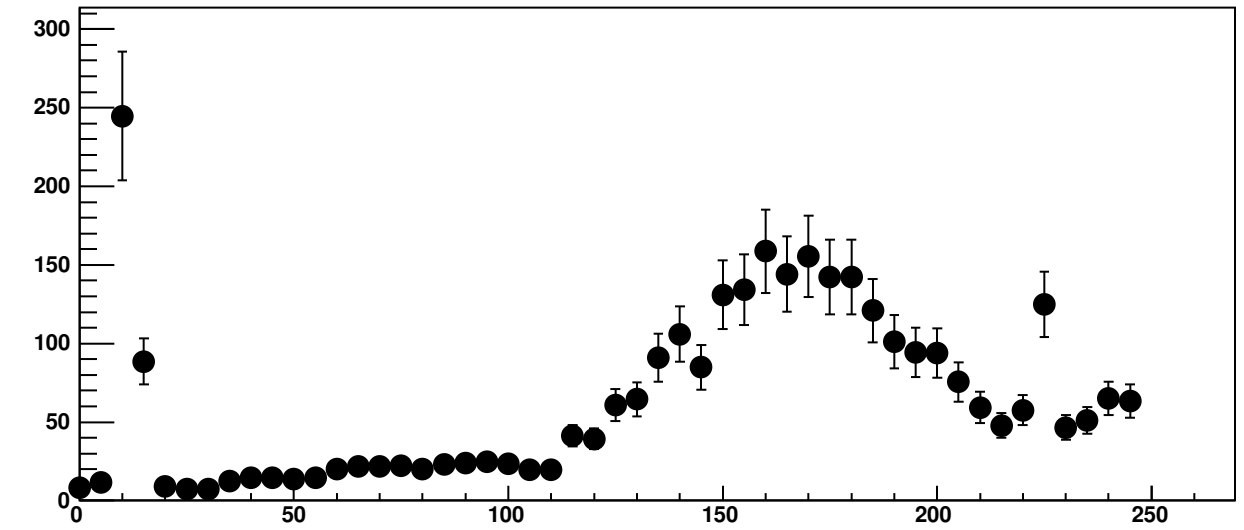

Chip 5, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
0
50
100
150
200
250

Chip 5, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold

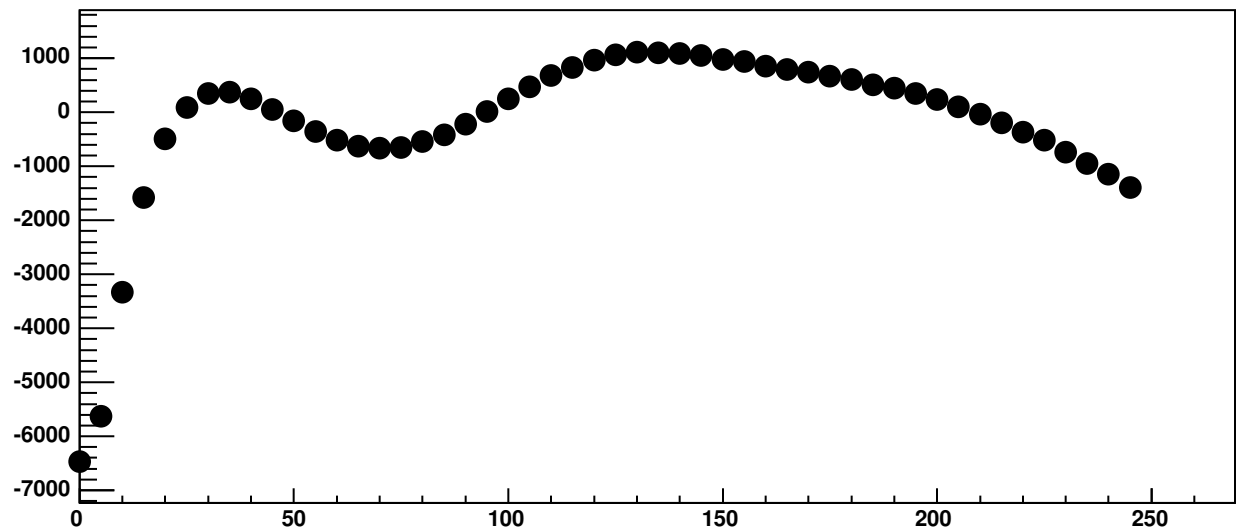
χ^2 / ndf	3.262e+05 / 42
p0	1408 ± 11.78
p1	-16.24 ± 0.09533
p2	8266 ± 11.21
p3	58.75 ± 0.09421

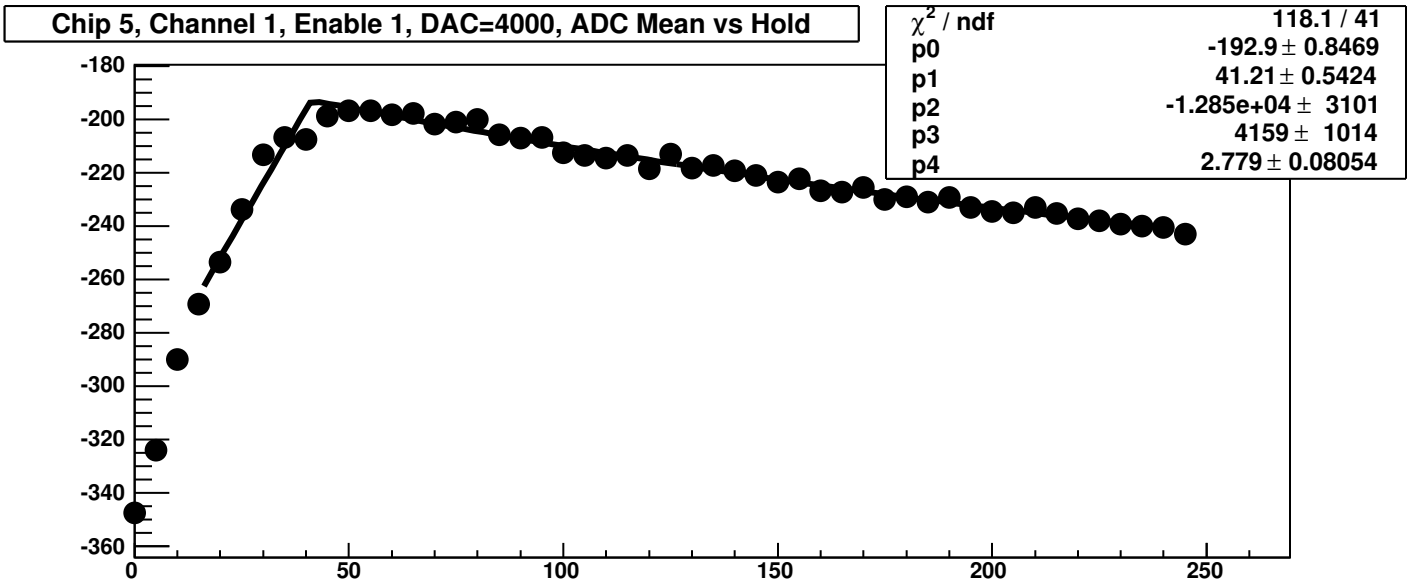


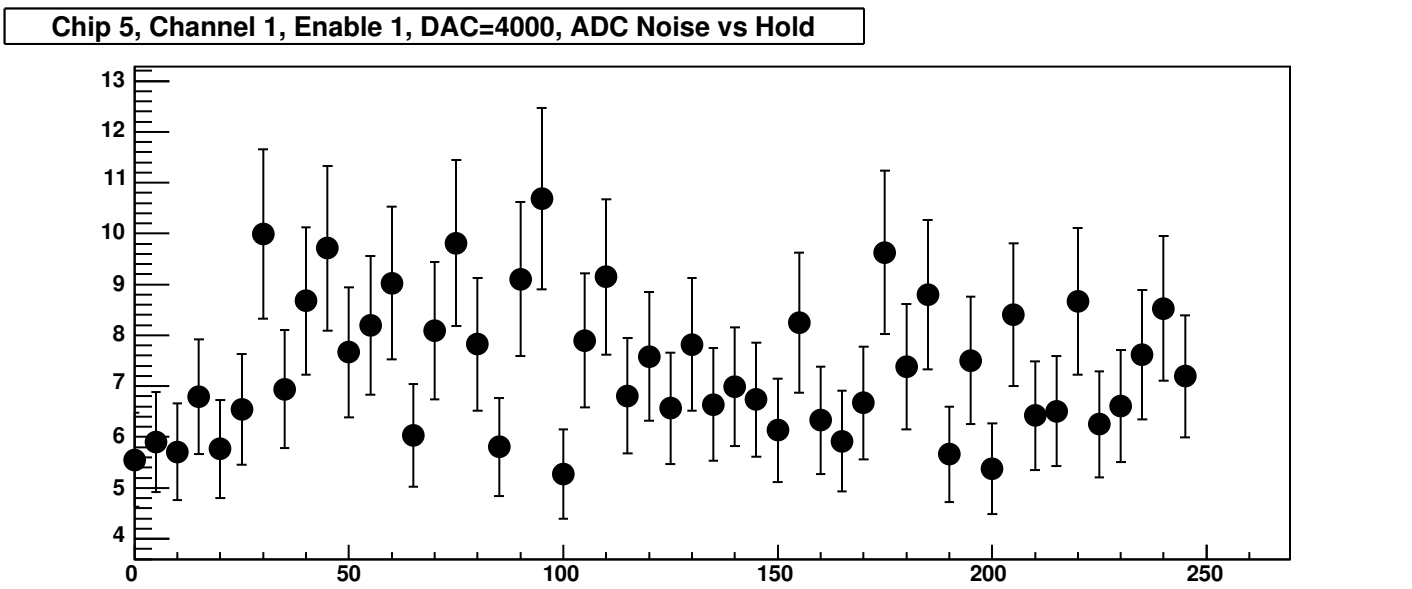
Chip 5, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold

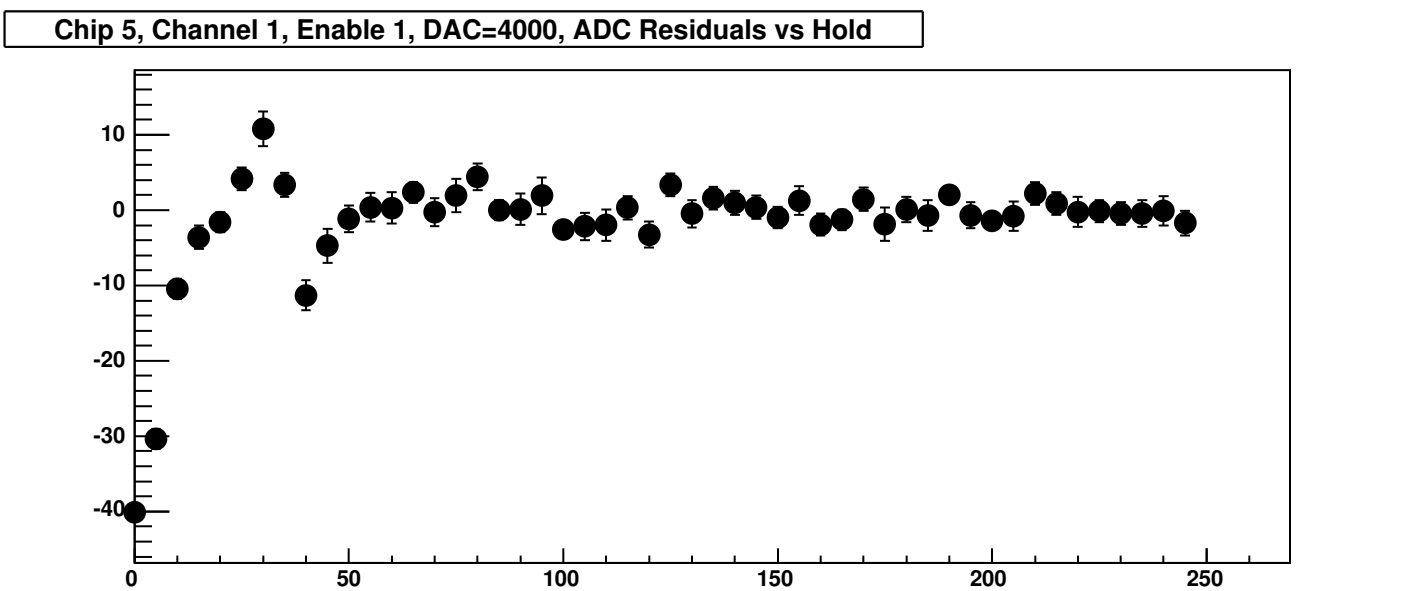


Chip 5, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold

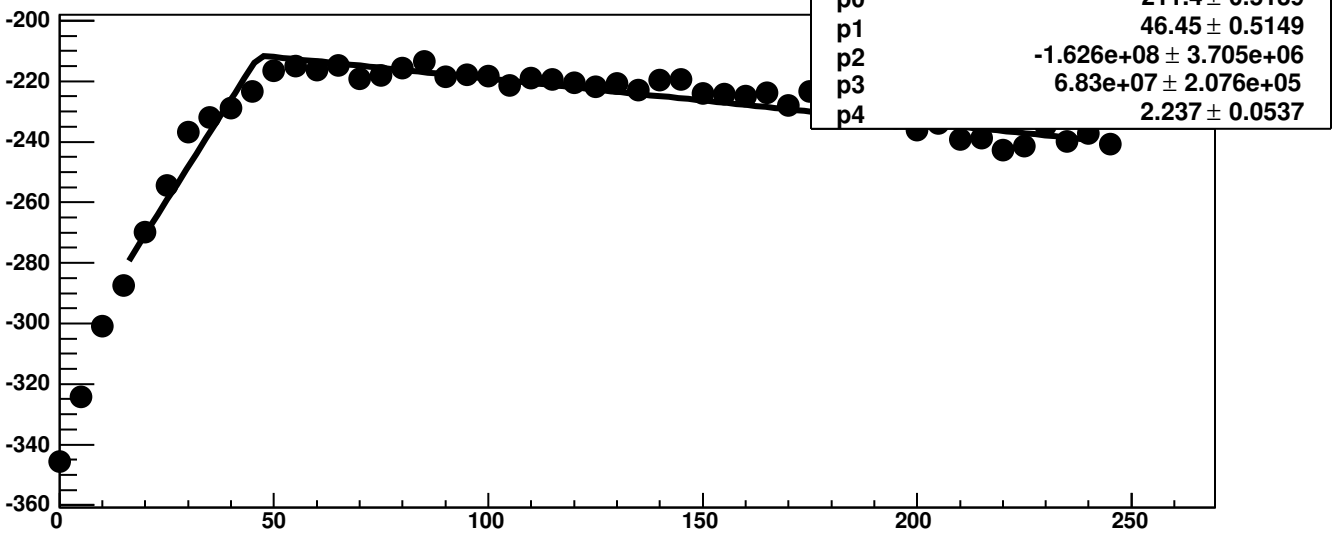



Chip 5, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 118.1 / 41
p0 -192.9 ± 0.8469
p1 41.21 ± 0.5424
p2 -1.285e+04 ± 3101
p3 4159 ± 1014
p4 2.779 ± 0.08054
-180
-200
-220
-240
-260
-280
-300
-320
-340
-360
0
50
100
150
200
250

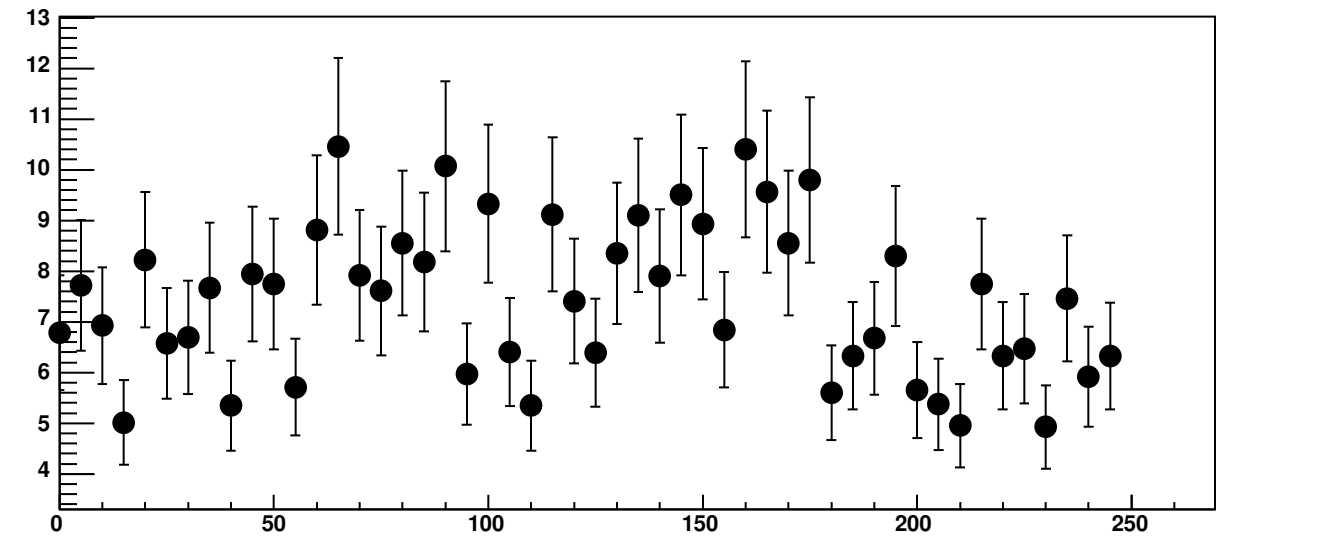

Chip 5, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 5, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold
10
0
-10
-20
-30
-40
0
50
100
150
200
250

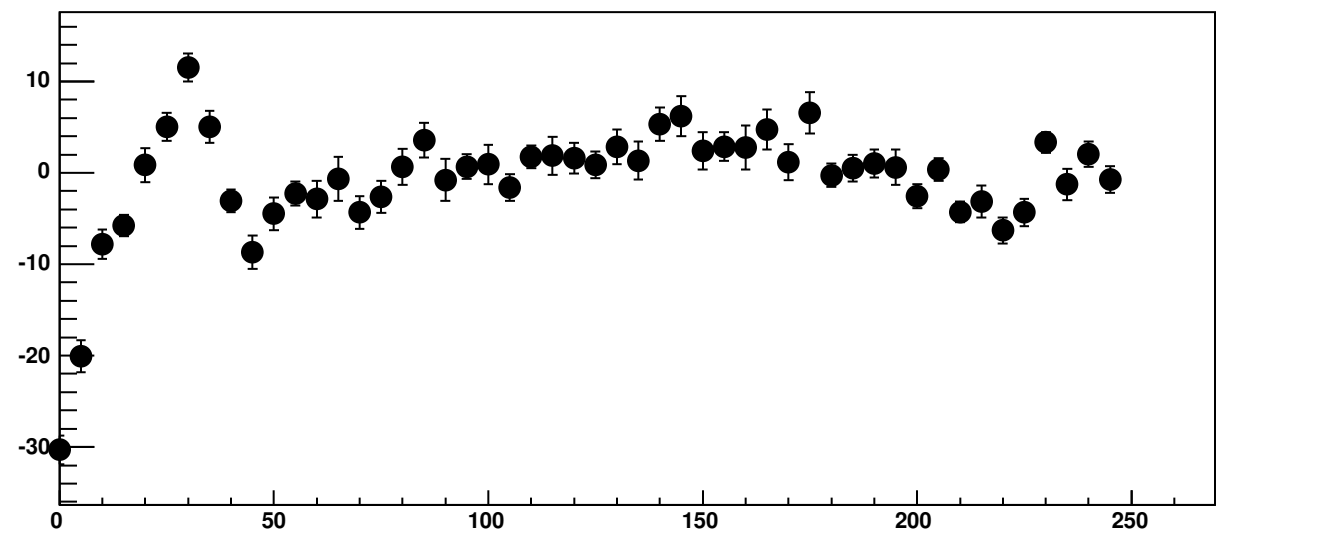
Chip 5, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

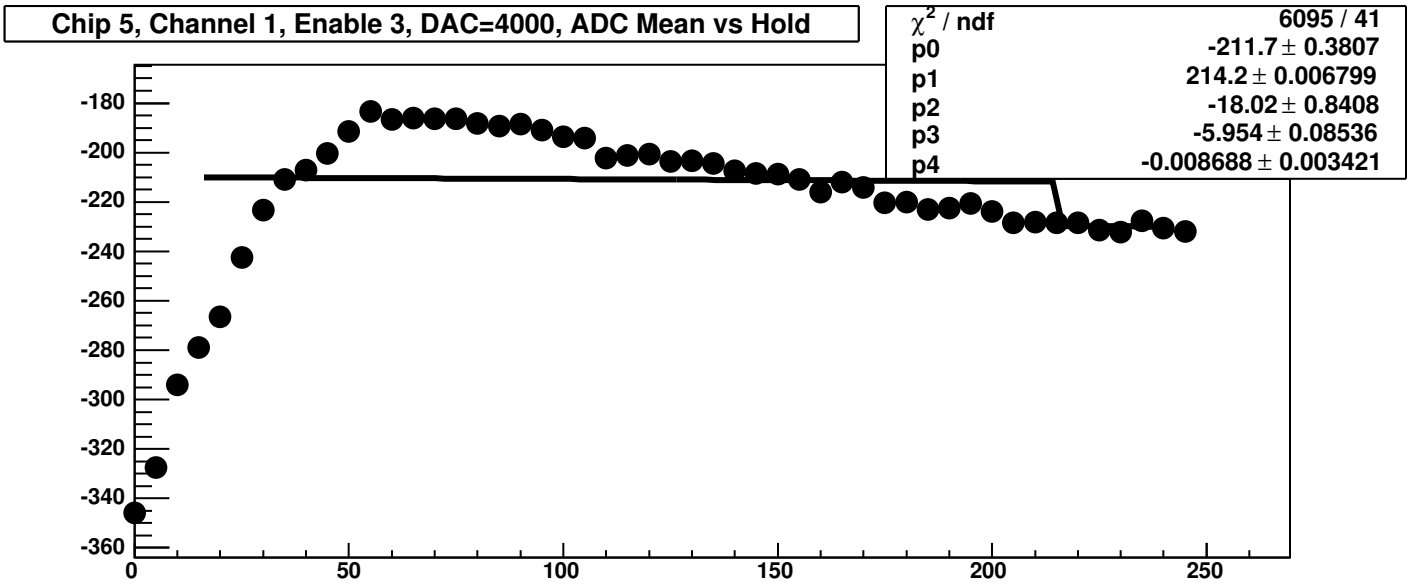


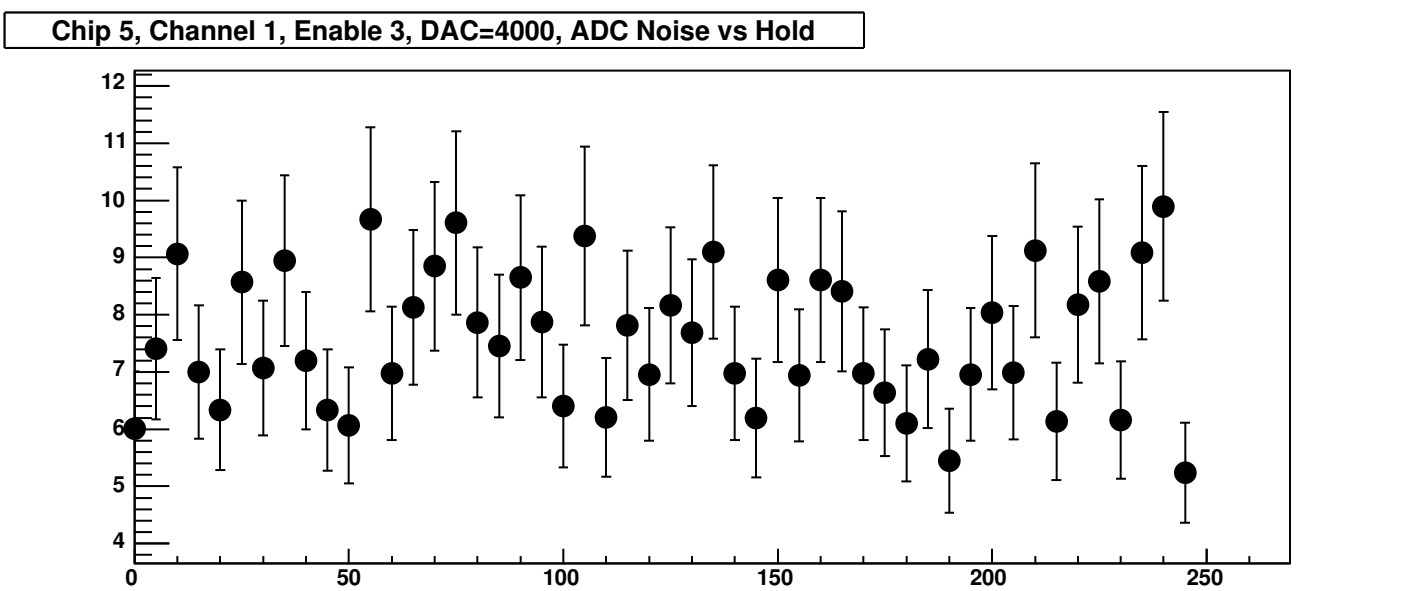
Chip 5, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold

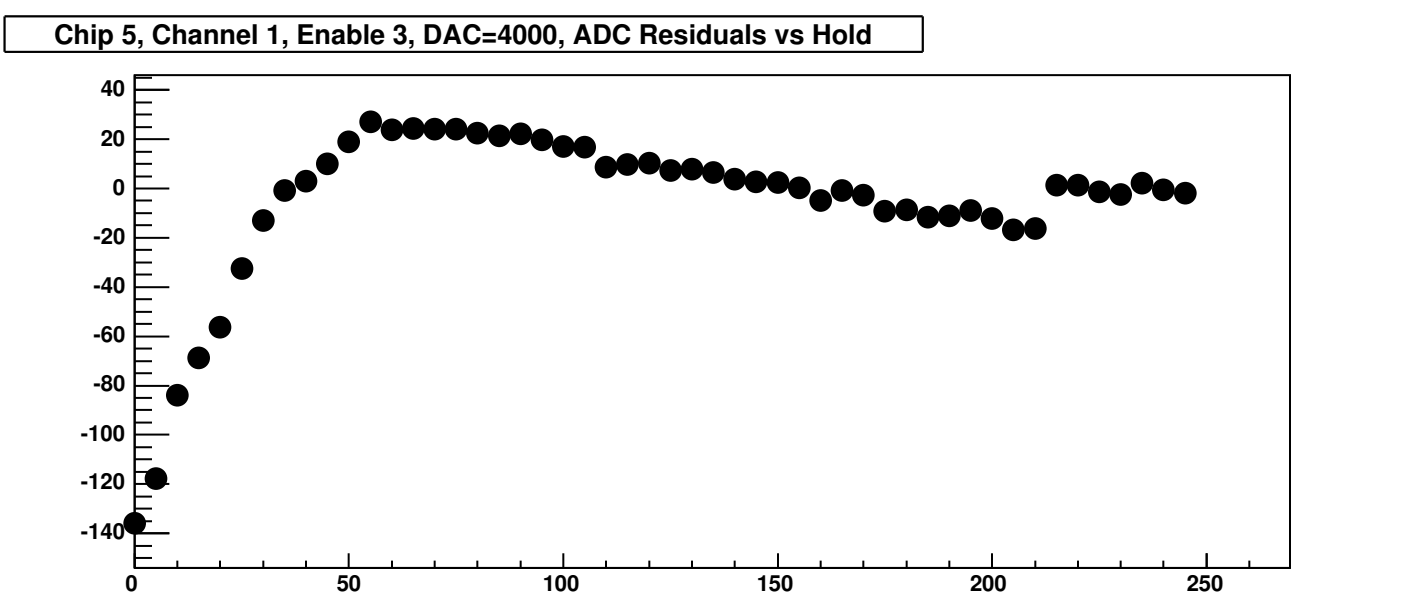


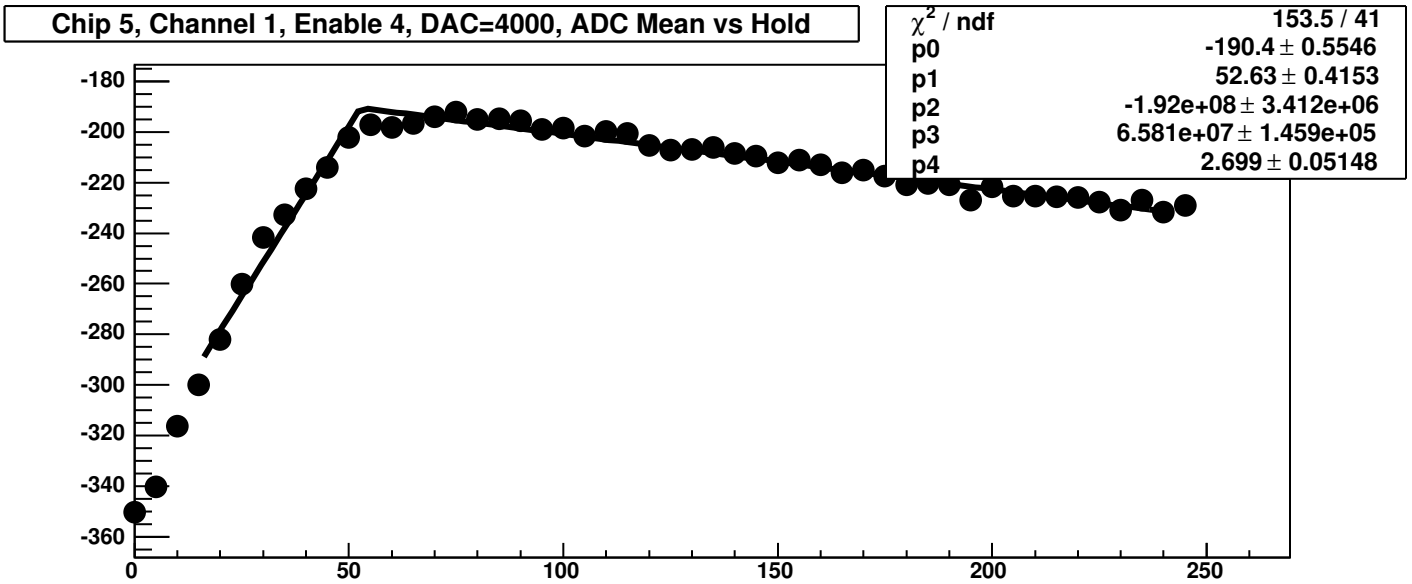
Chip 5, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

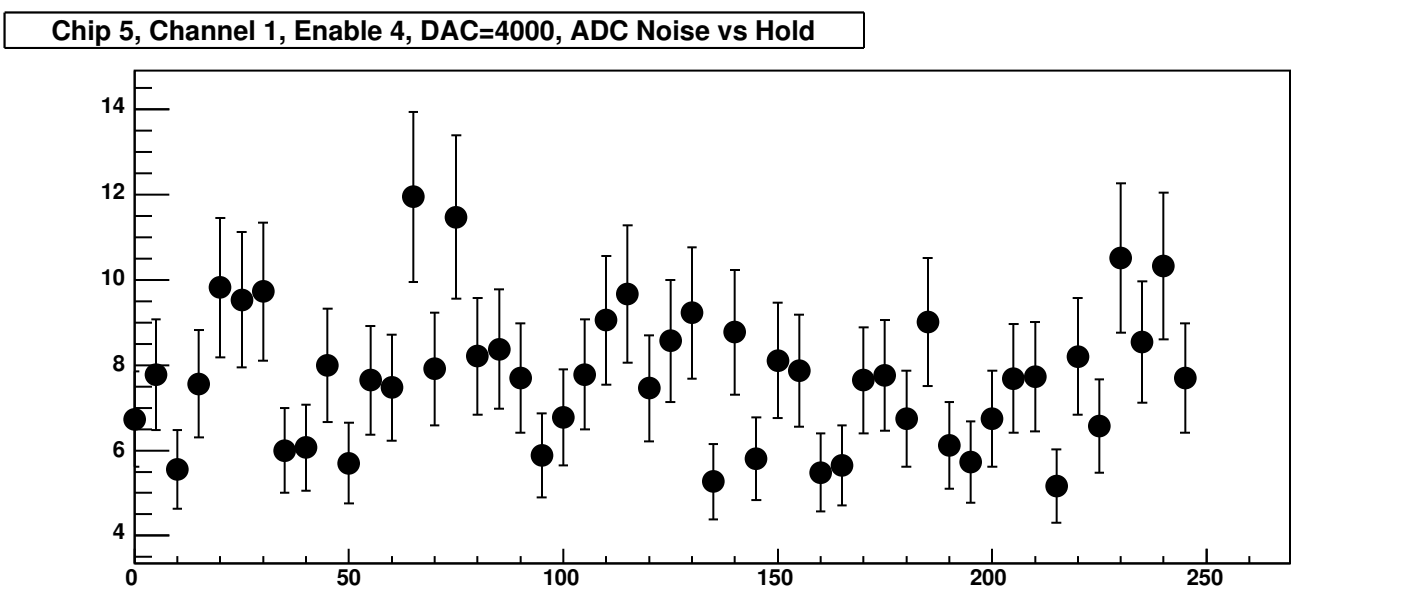


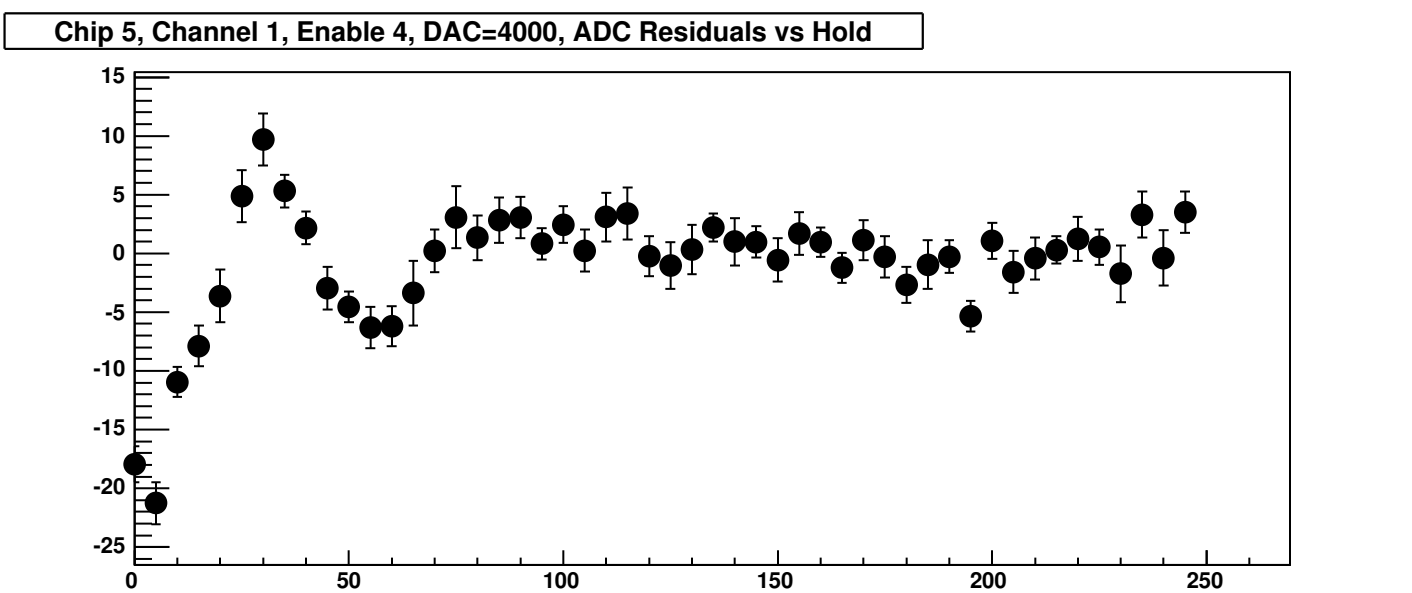

Chip 5, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 6095 / 41
p0 -211.7 ± 0.3807
p1 214.2 ± 0.006799
p2 -18.02 ± 0.8408
p3 -5.954 ± 0.08536
p4 -0.008688 ± 0.003421
-180
-200
-220
-240
-260
-280
-300
-320
-340
-360
0
50
100
150
200
250

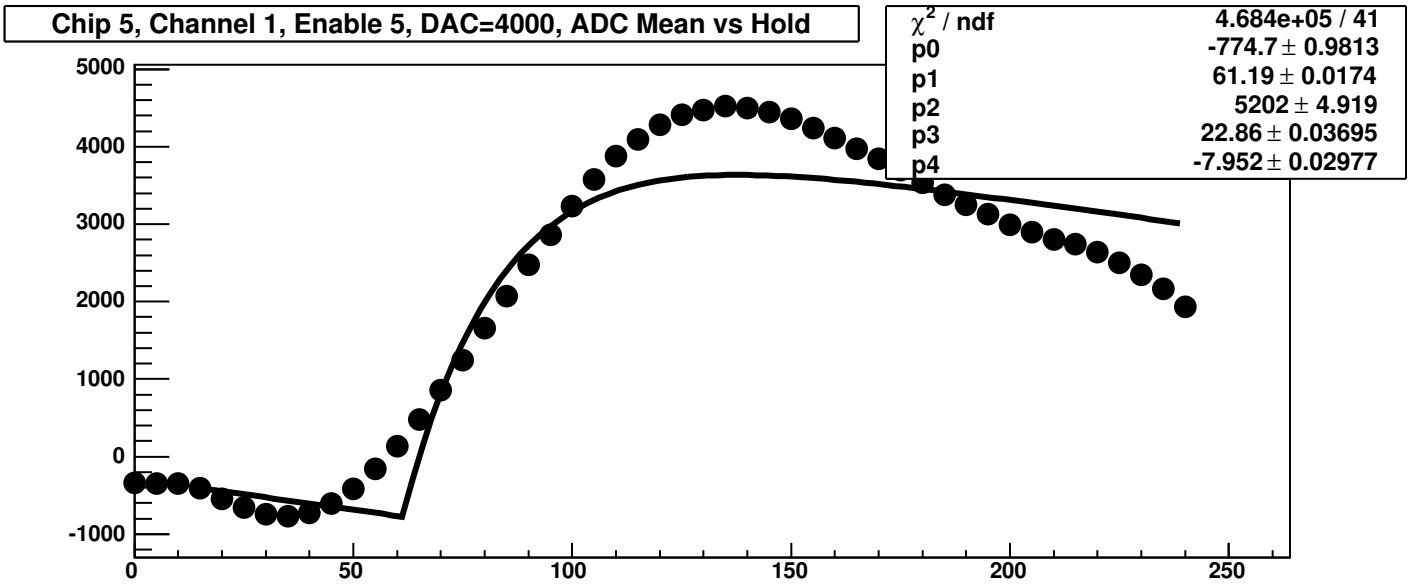

Chip 5, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

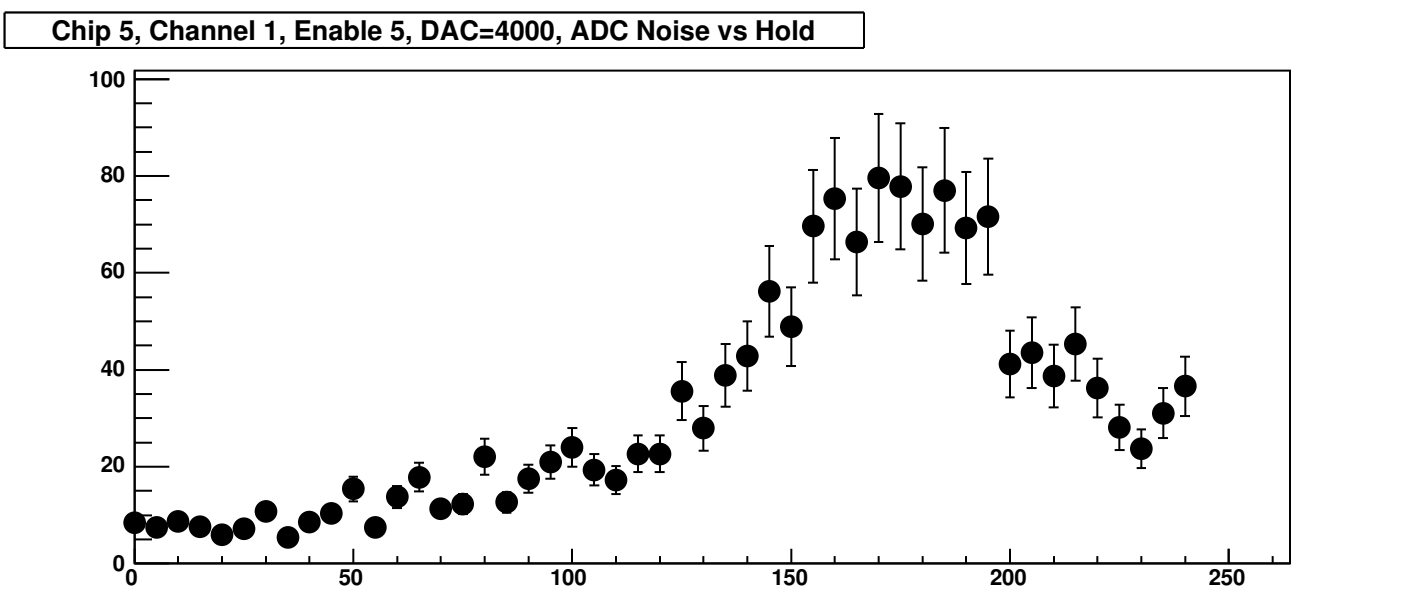

Chip 5, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
-100
-120
-140
0
50
100
150
200
250

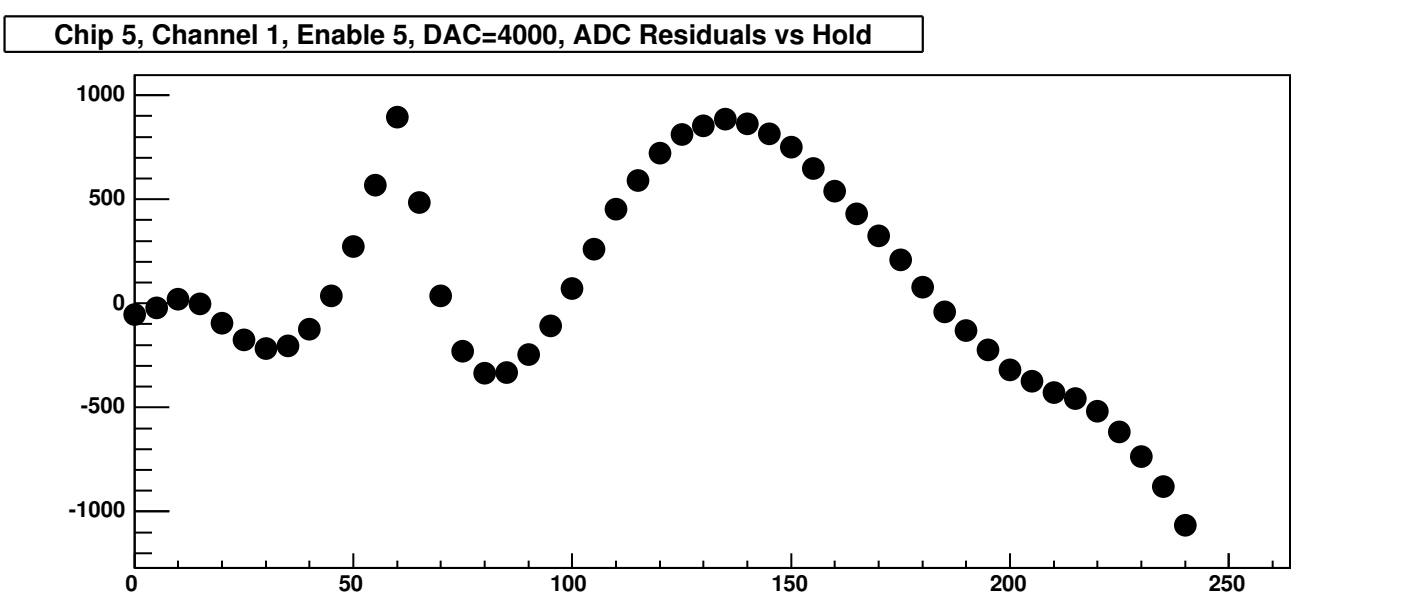

Chip 5, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 153.5 / 41
p0 -190.4 ± 0.5546
p1 52.63 ± 0.4153
p2 -1.92e+08 ± 3.412e+06
p3 6.581e+07 ± 1.459e+05
p4 2.699 ± 0.05148
-180
-200
-220
-240
-260
-280
-300
-320
-340
-360
0
50
100
150
200
250

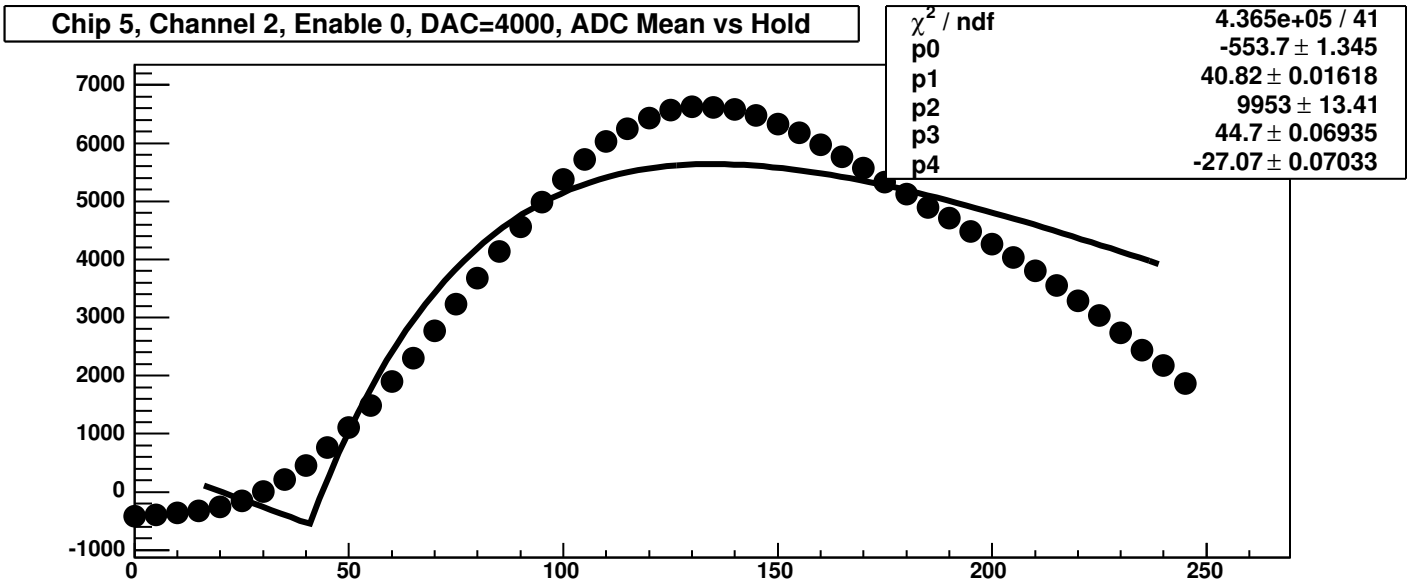

Chip 5, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

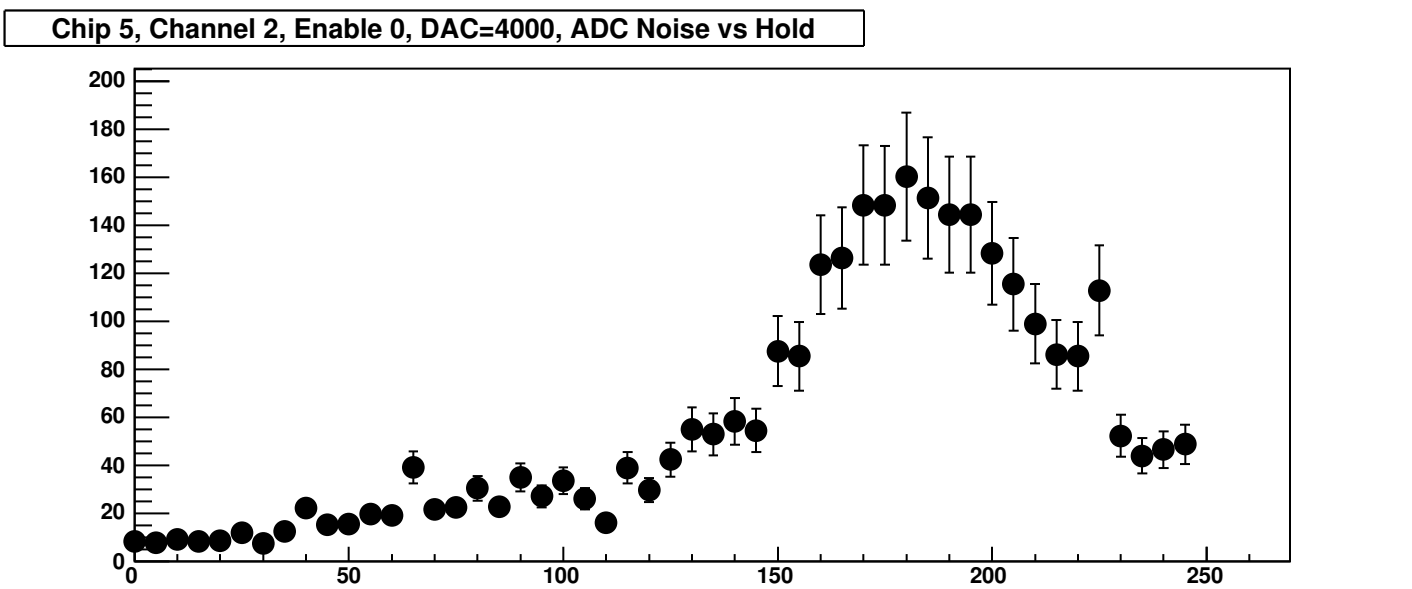

Chip 5, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
-15
-20
-25
0
50
100
150
200
250

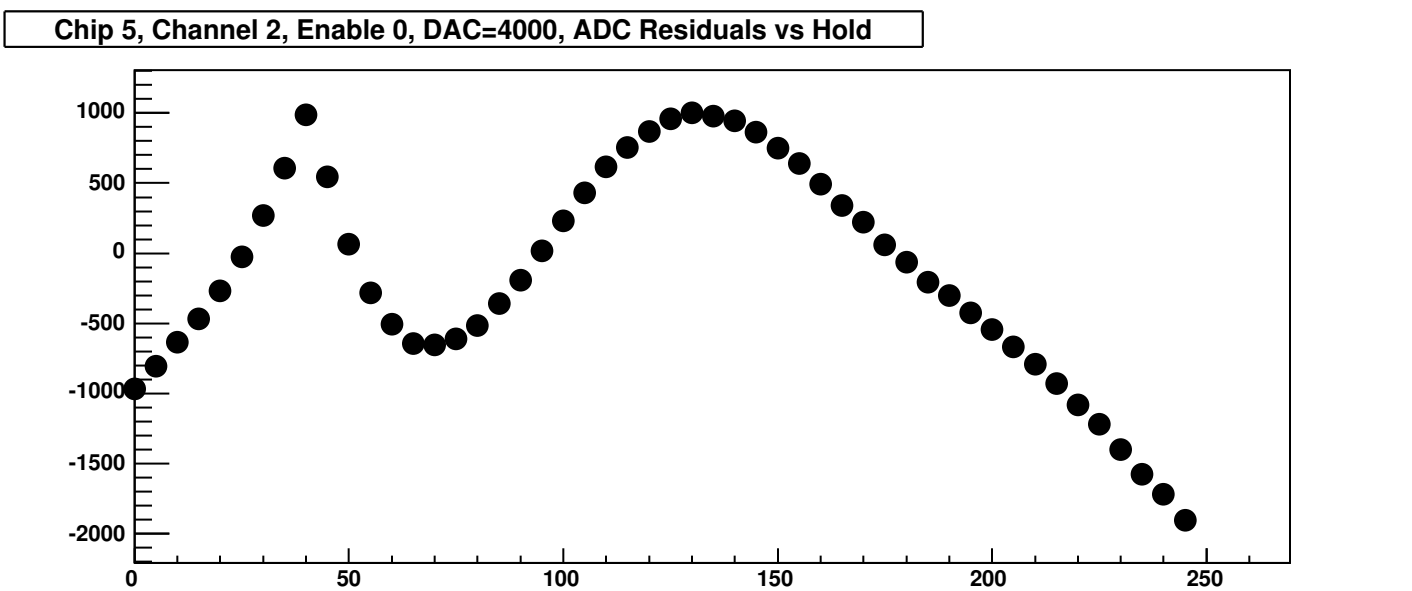

Chip 5, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.684e+05 / 41
p0 -774.7 ± 0.9813
p1 61.19 ± 0.0174
p2 5202 ± 4.919
p3 22.86 ± 0.03695
p4 -7.952 ± 0.02977
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 5, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250

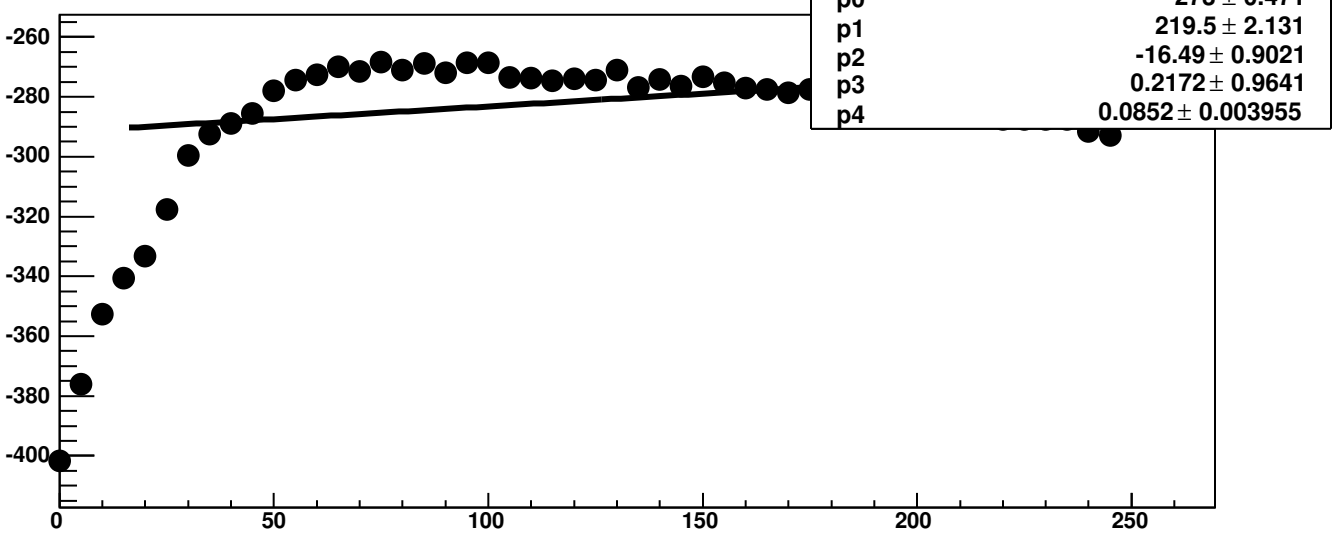

Chip 5, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
0
50
100
150
200
250


Chip 5, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.365e+05 / 41
p0 -553.7 ± 1.345
p1 40.82 ± 0.01618
p2 9953 ± 13.41
p3 44.7 ± 0.06935
p4 -27.07 ± 0.07033
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

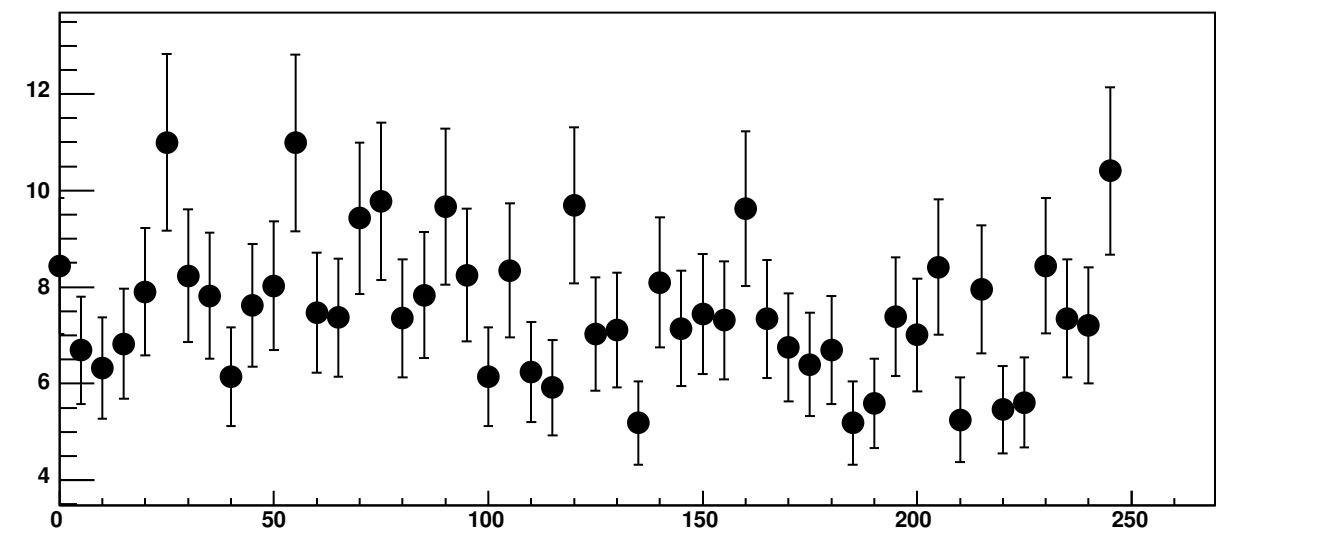

Chip 5, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 5, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

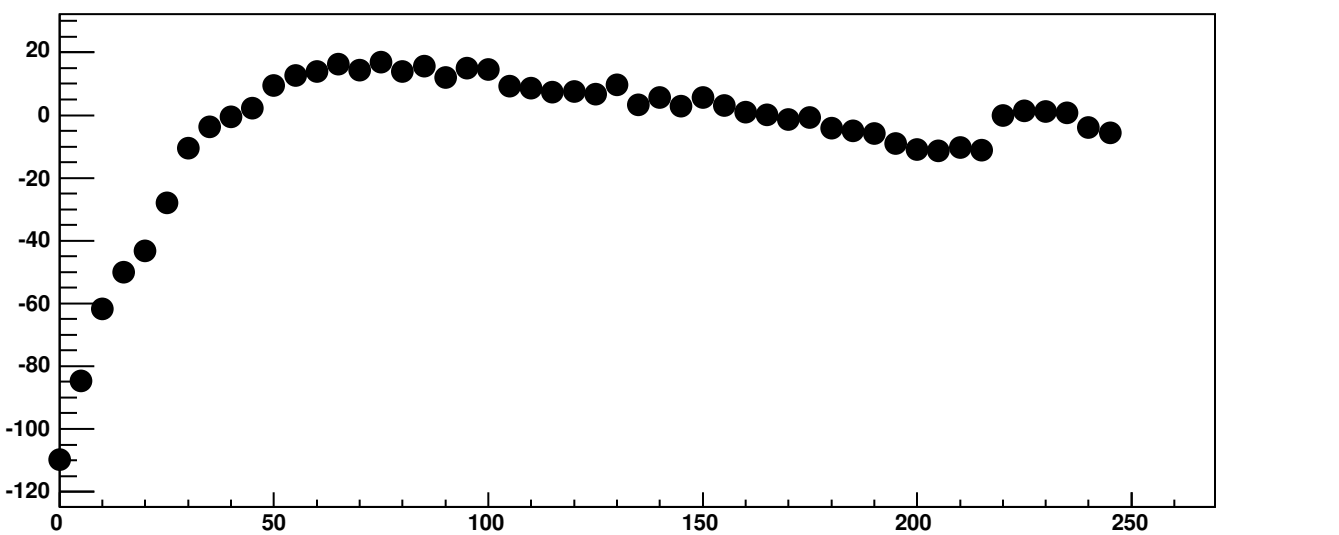
Chip 5, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold



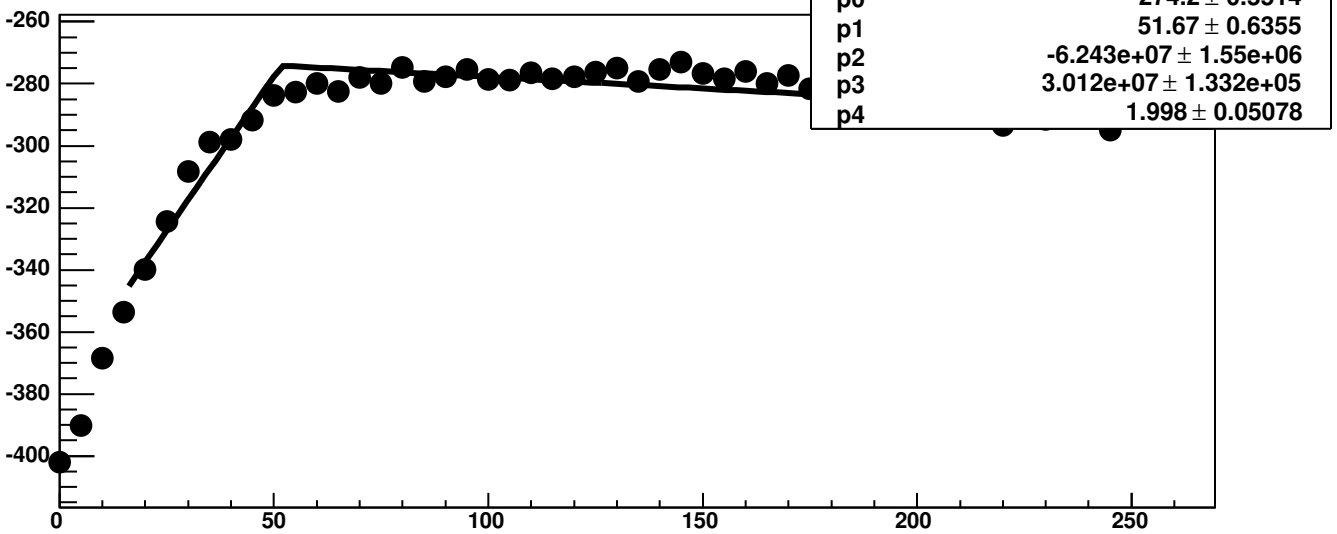
Chip 5, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



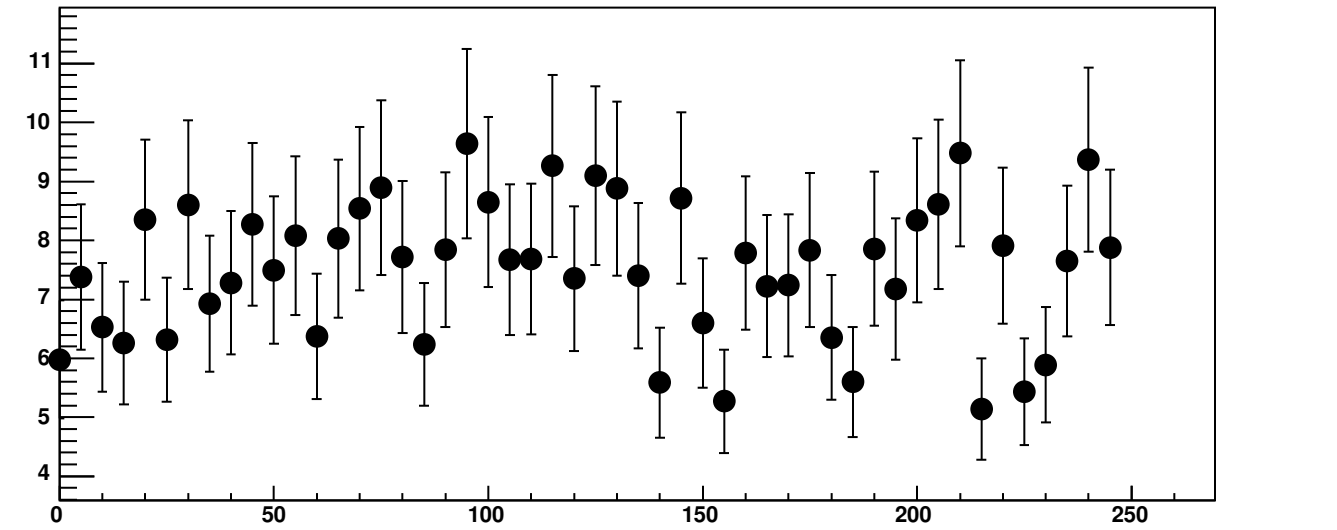
Chip 5, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold



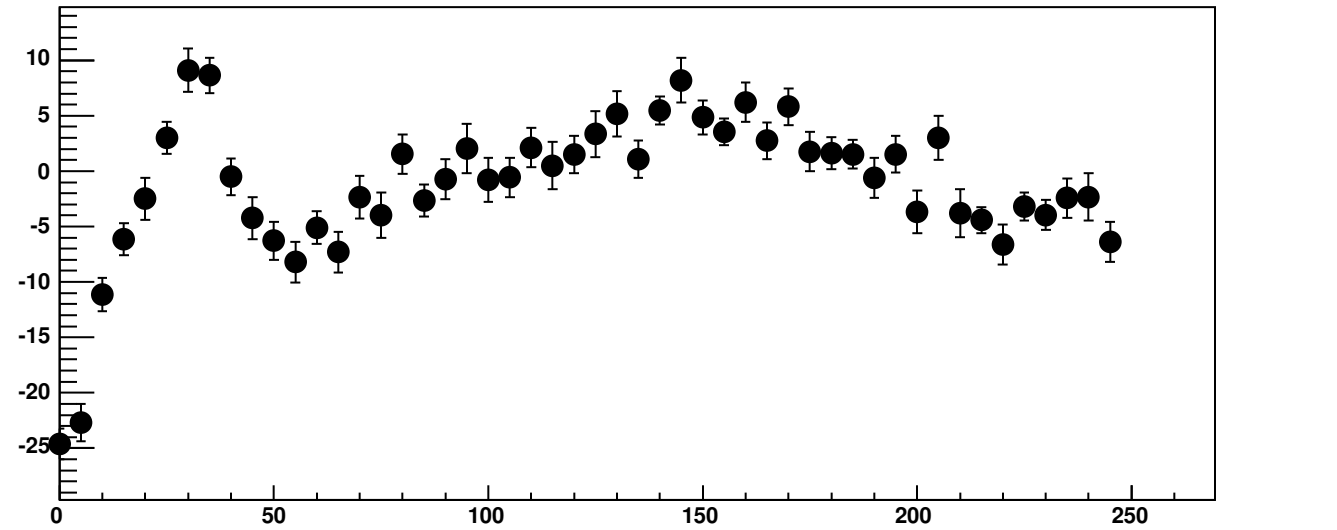
Chip 5, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold

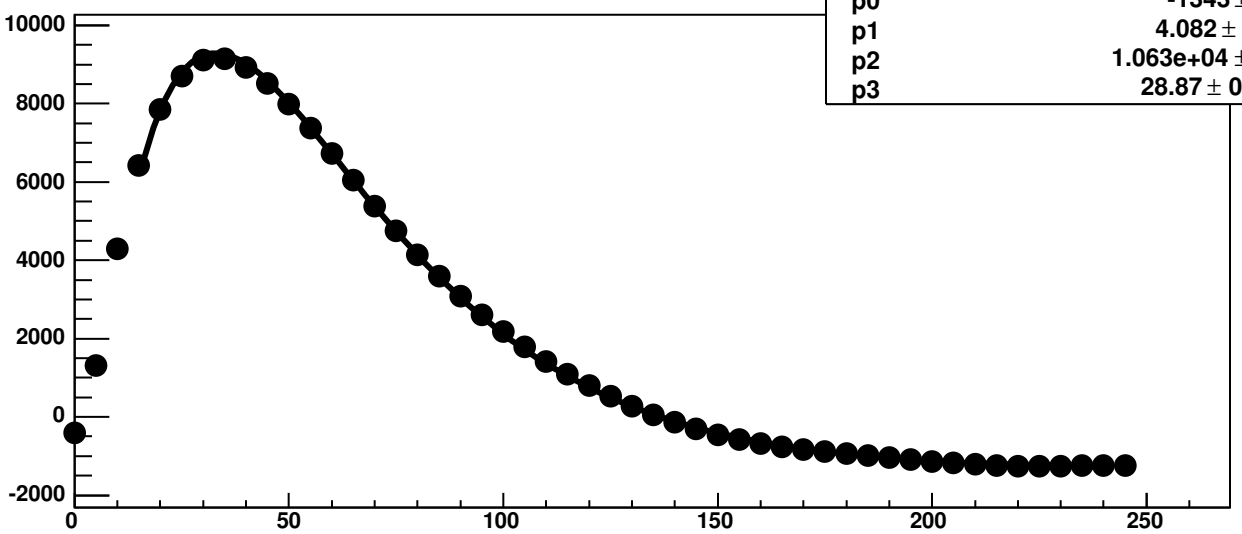


Chip 5, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

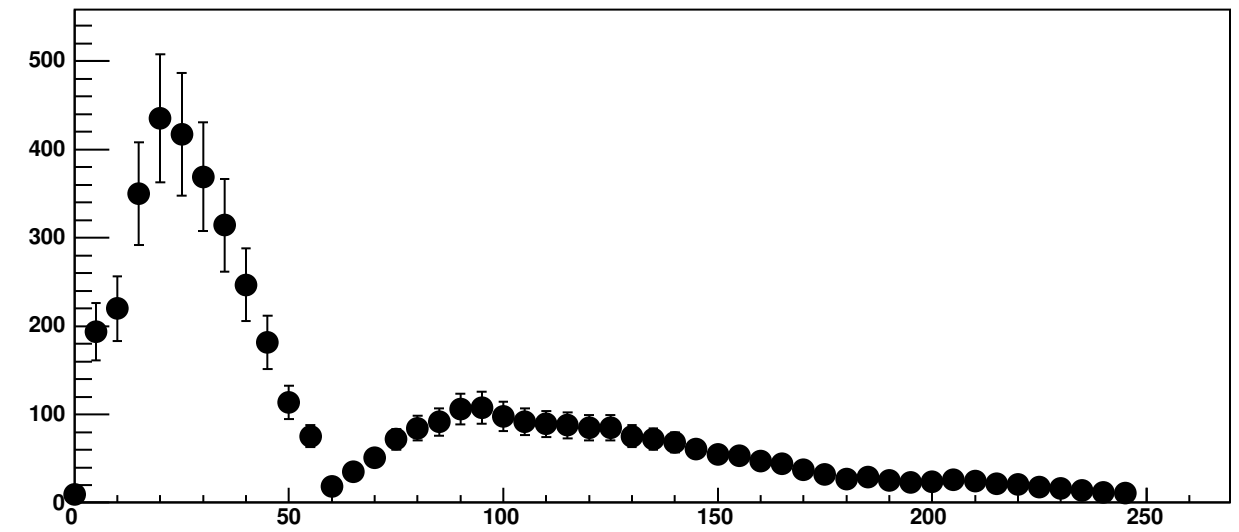


Chip 5, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold

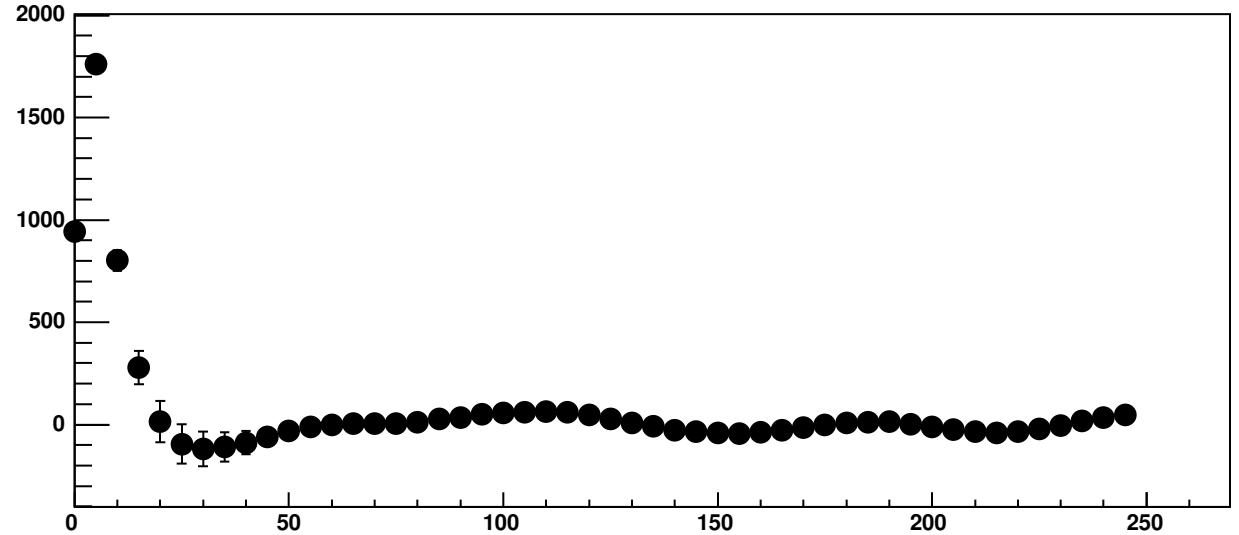
χ^2 / ndf	536.8 / 42
p0	-1343 ± 1.708
p1	4.082 ± 0.1292
p2	1.063e+04 ± 17.18
p3	28.87 ± 0.04266



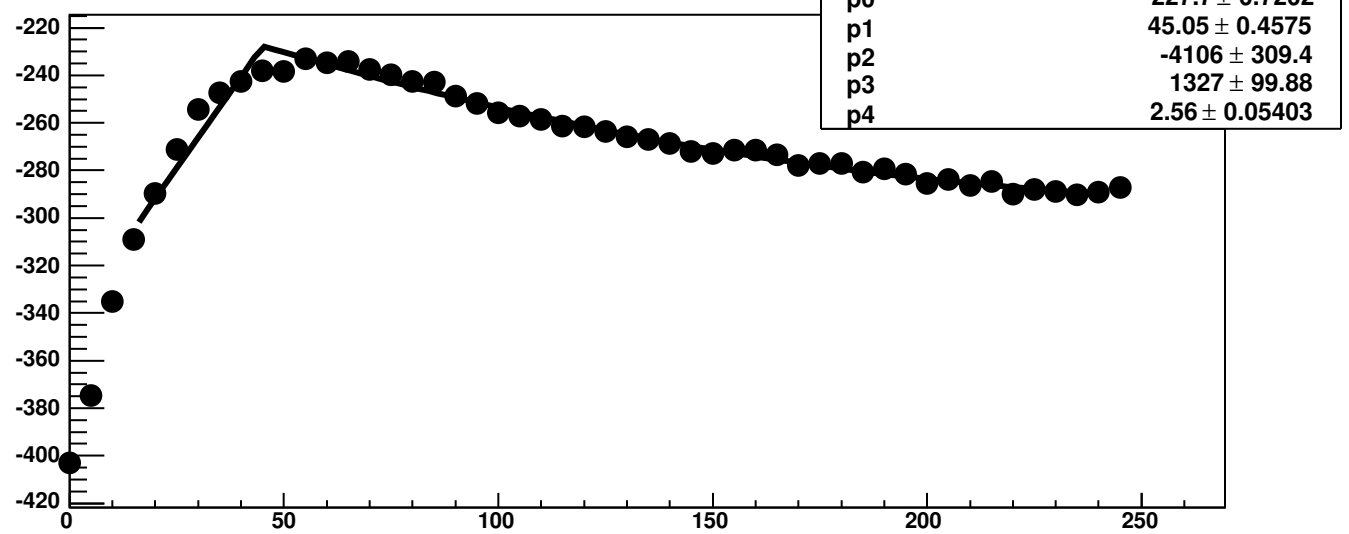
Chip 5, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold



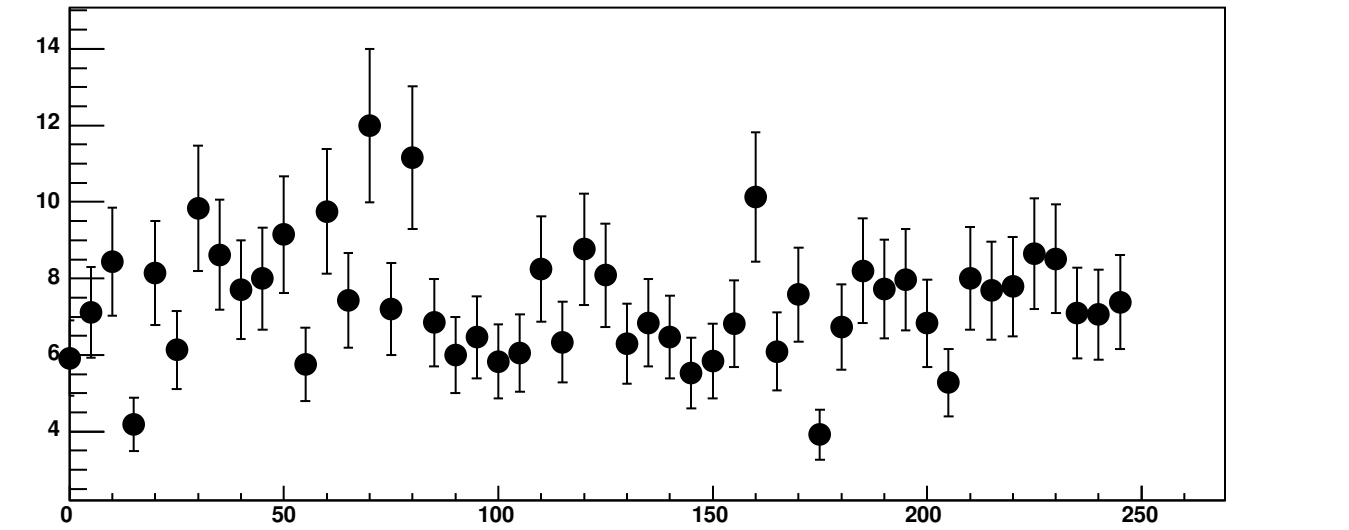
Chip 5, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold



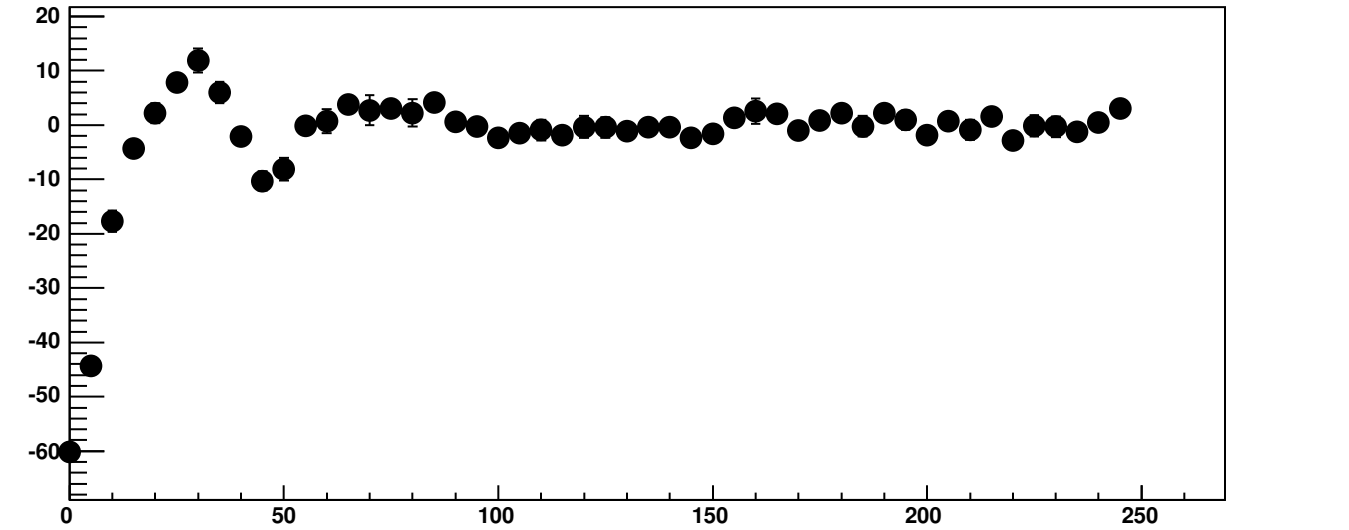
Chip 5, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold



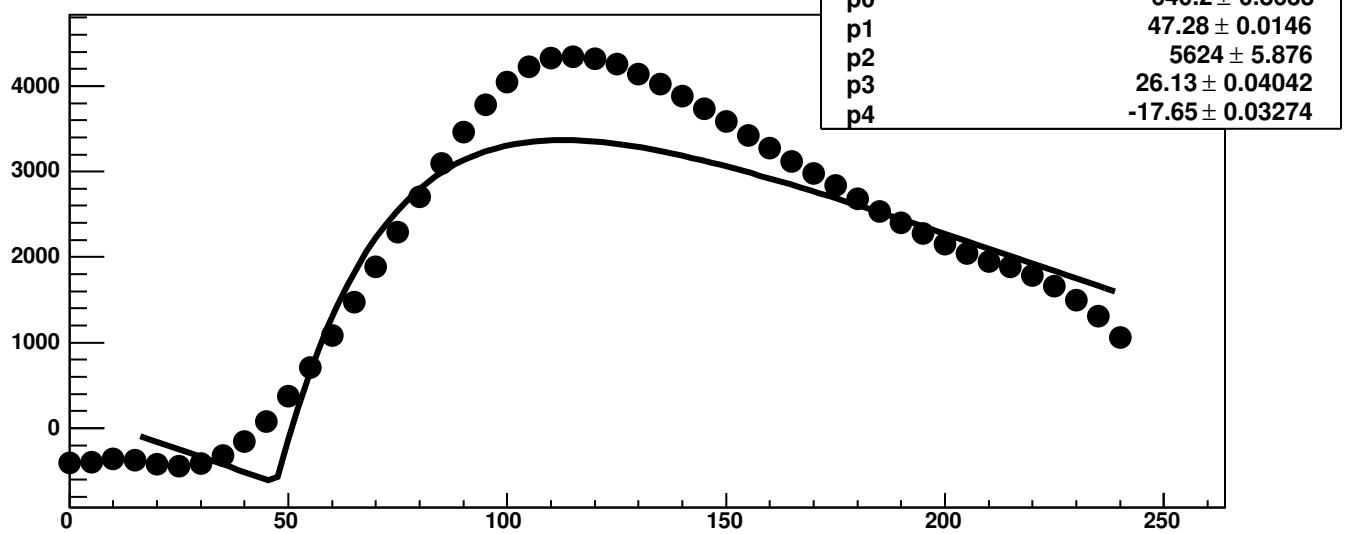
Chip 5, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



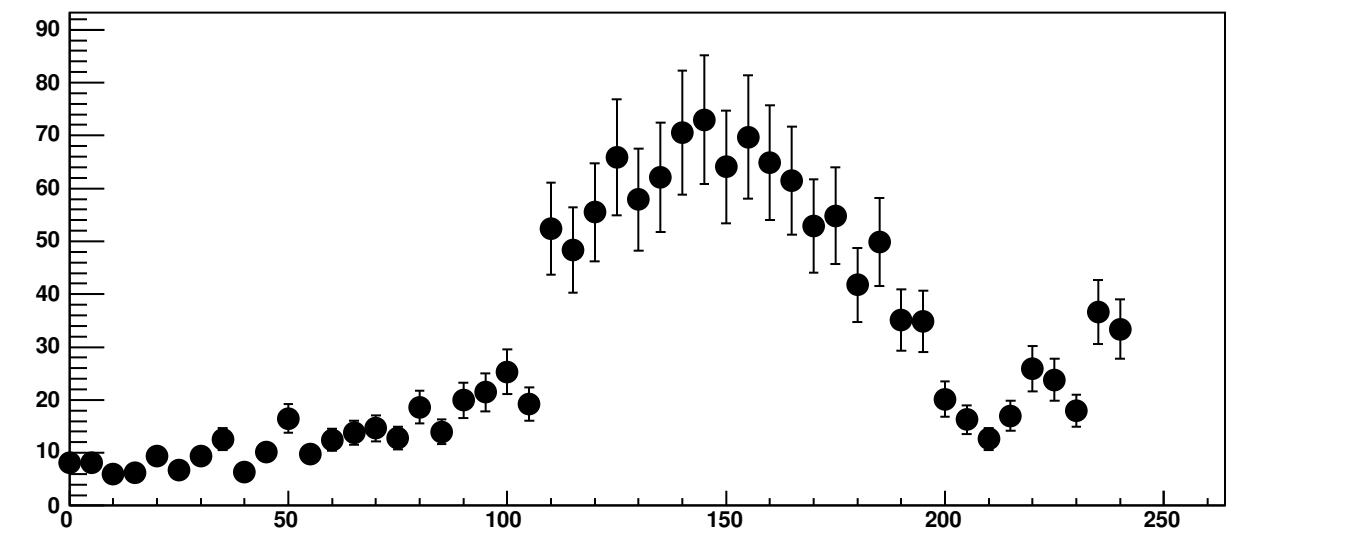
Chip 5, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold



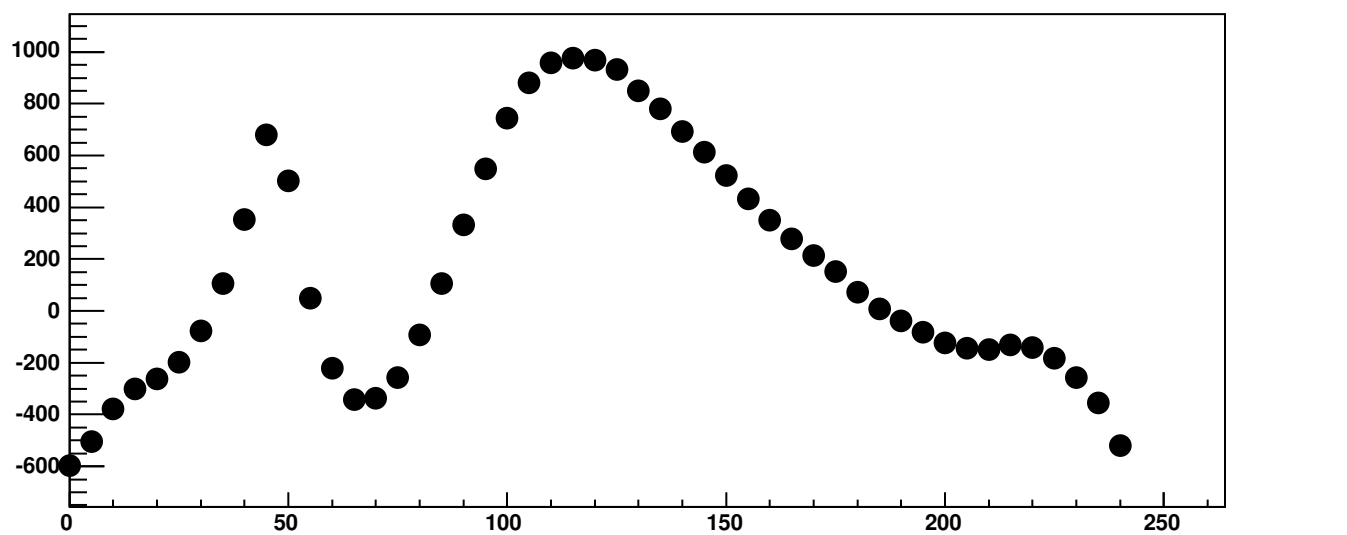
Chip 5, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

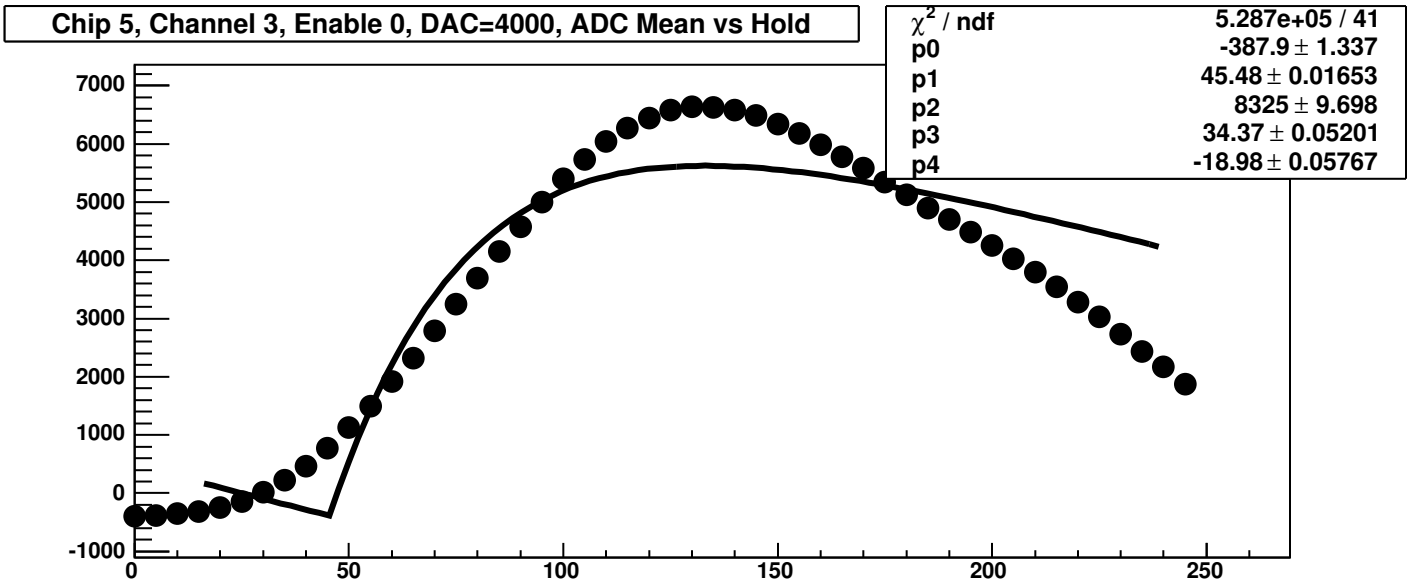


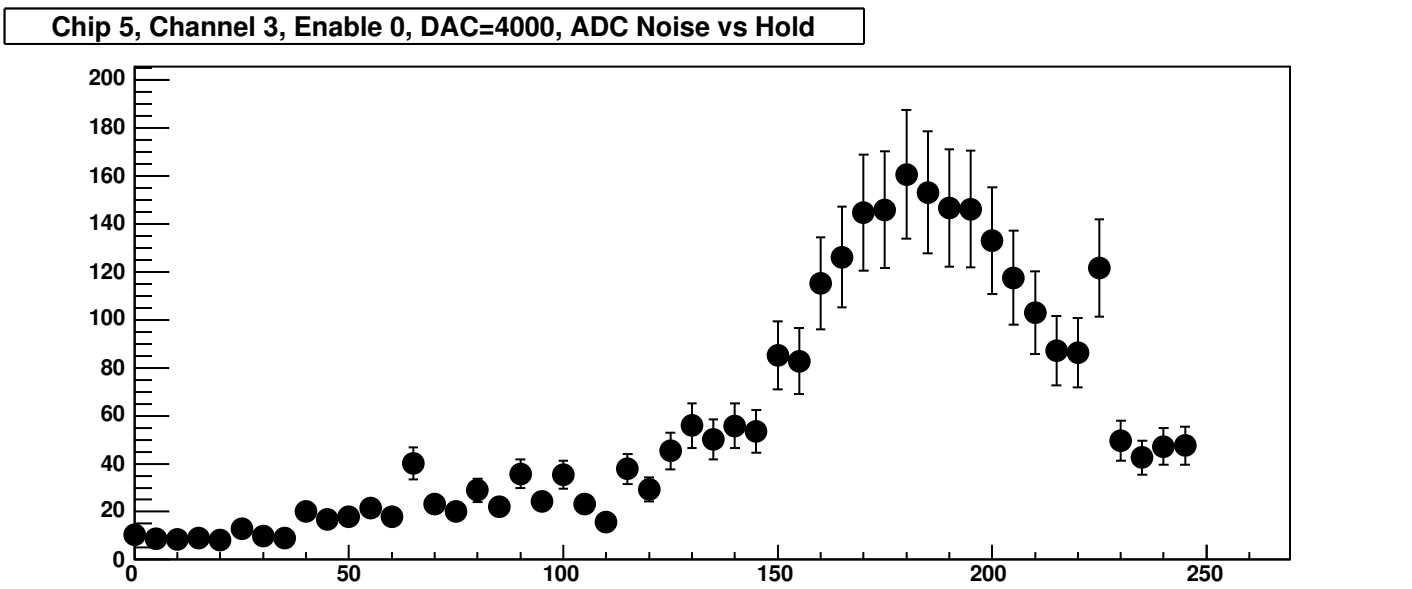
Chip 5, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold

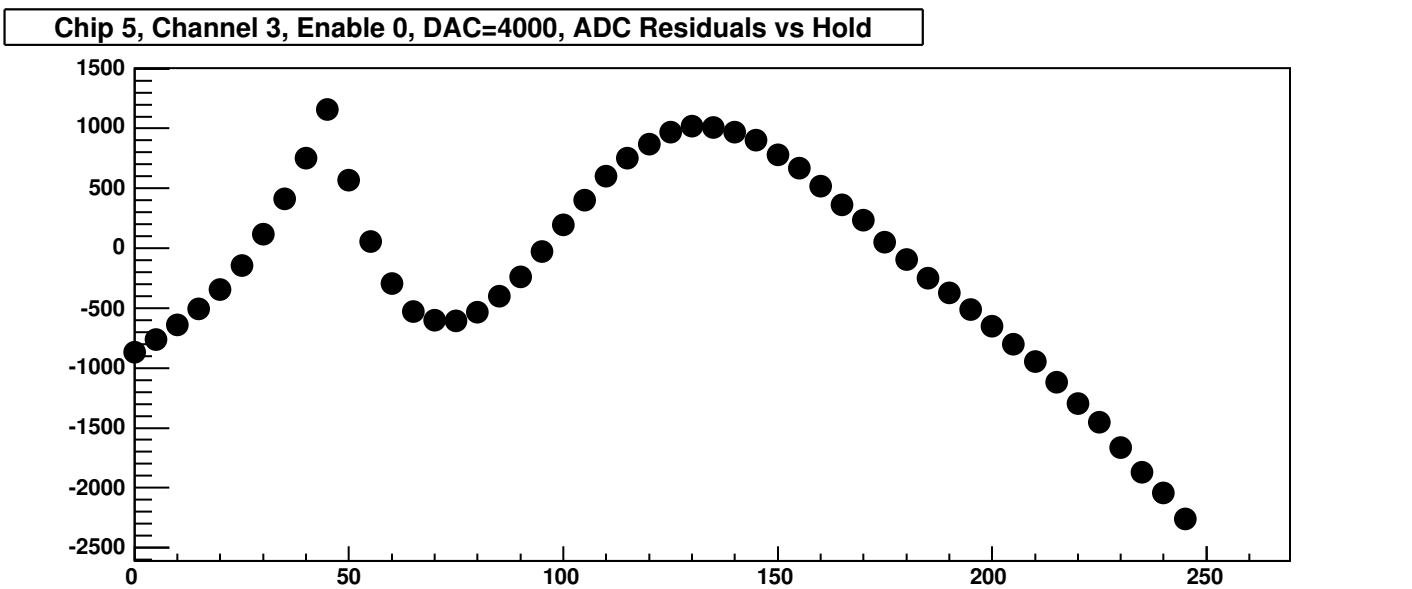


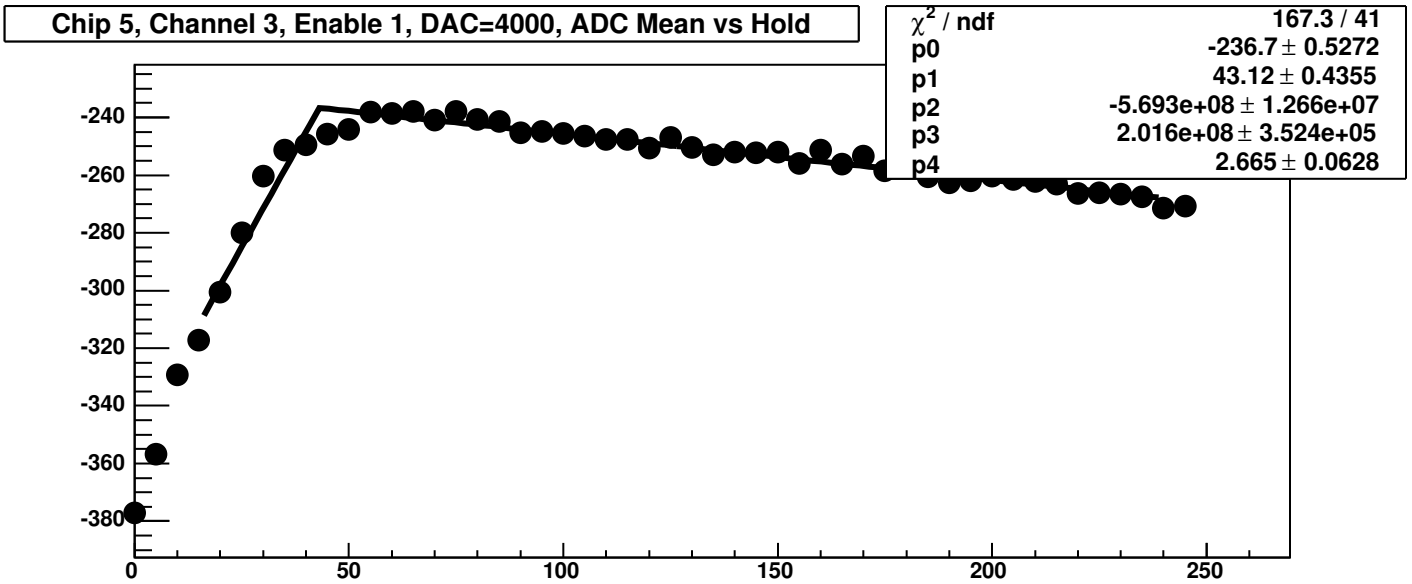
Chip 5, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold

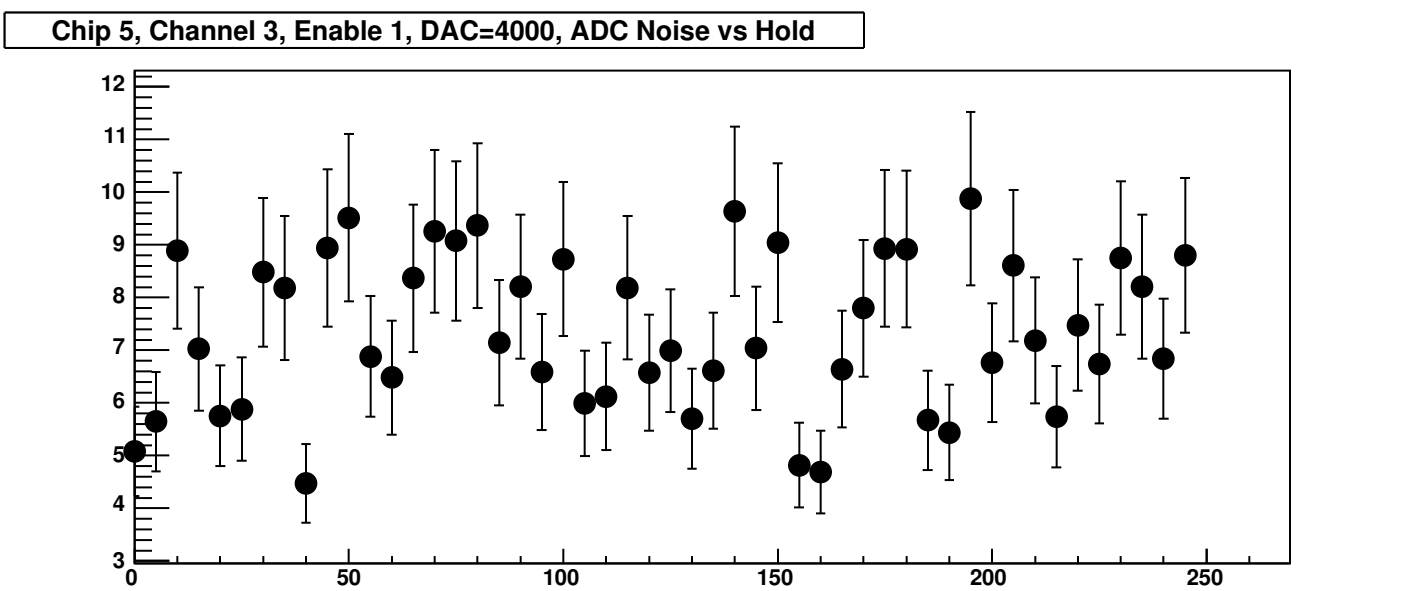


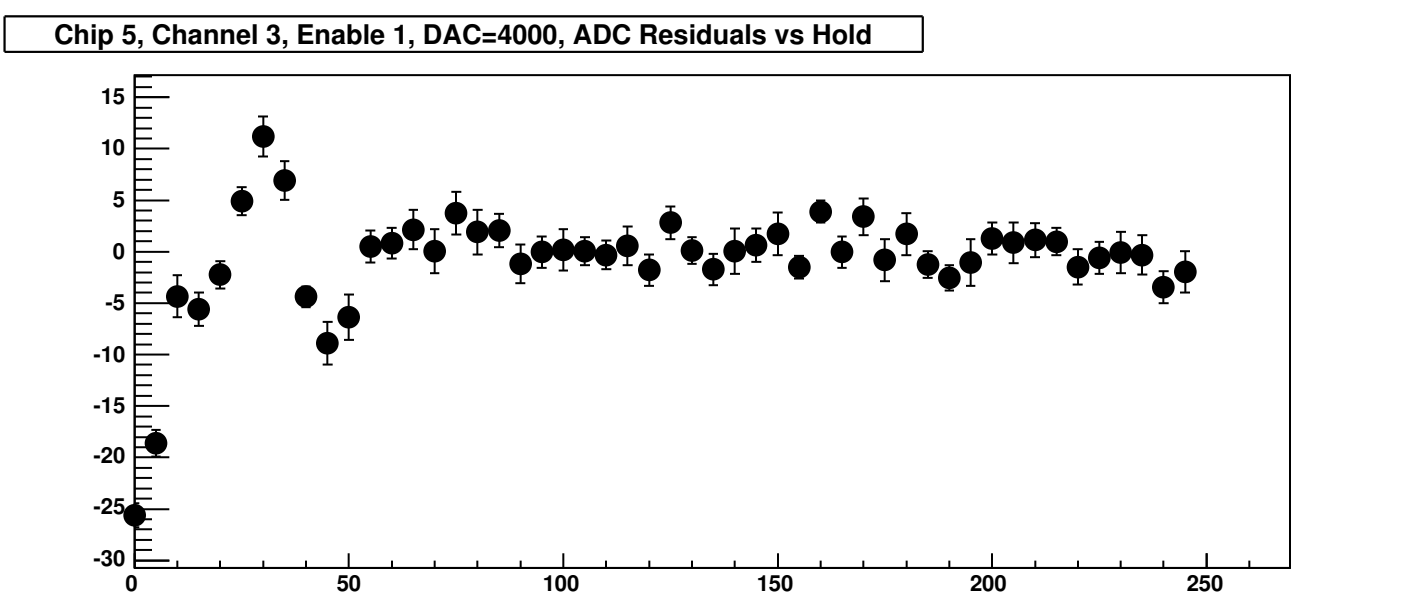

Chip 5, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.287e+05 / 41
p0 -387.9 ± 1.337
p1 45.48 ± 0.01653
p2 8325 ± 9.698
p3 34.37 ± 0.05201
p4 -18.98 ± 0.05767
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 5, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

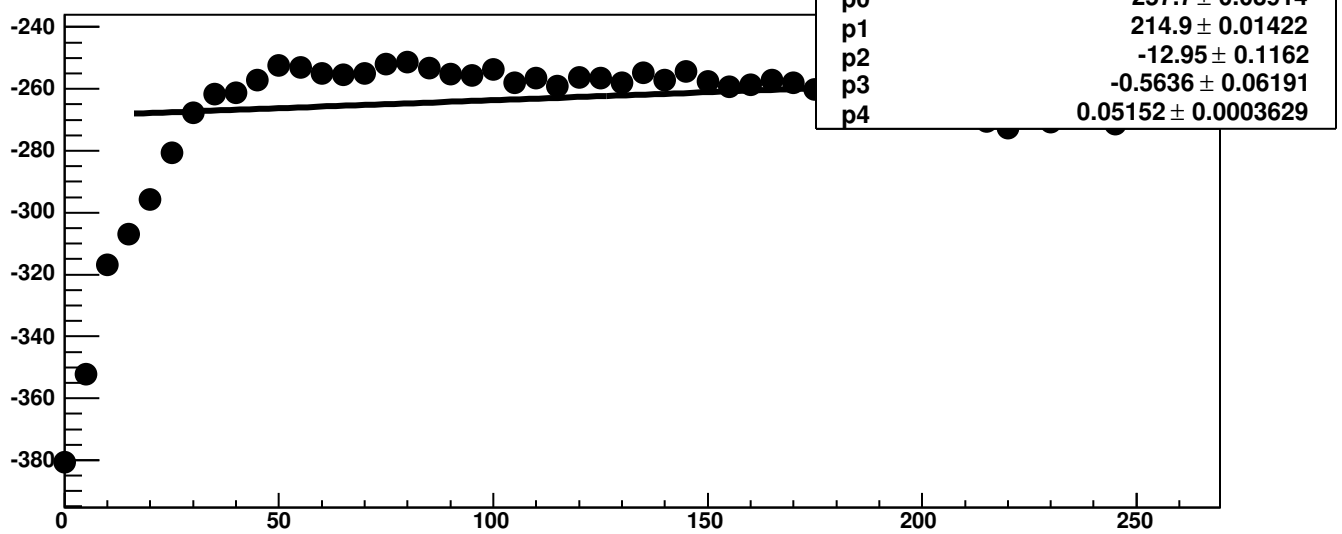

Chip 5, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
50
100
150
200
250


Chip 5, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 167.3 / 41
p0 -236.7 ± 0.5272
p1 43.12 ± 0.4355
p2 -5.693e+08 ± 1.266e+07
p3 2.016e+08 ± 3.524e+05
p4 2.665 ± 0.0628
-240
-260
-280
-300
-320
-340
-360
-380
0
50
100
150
200
250

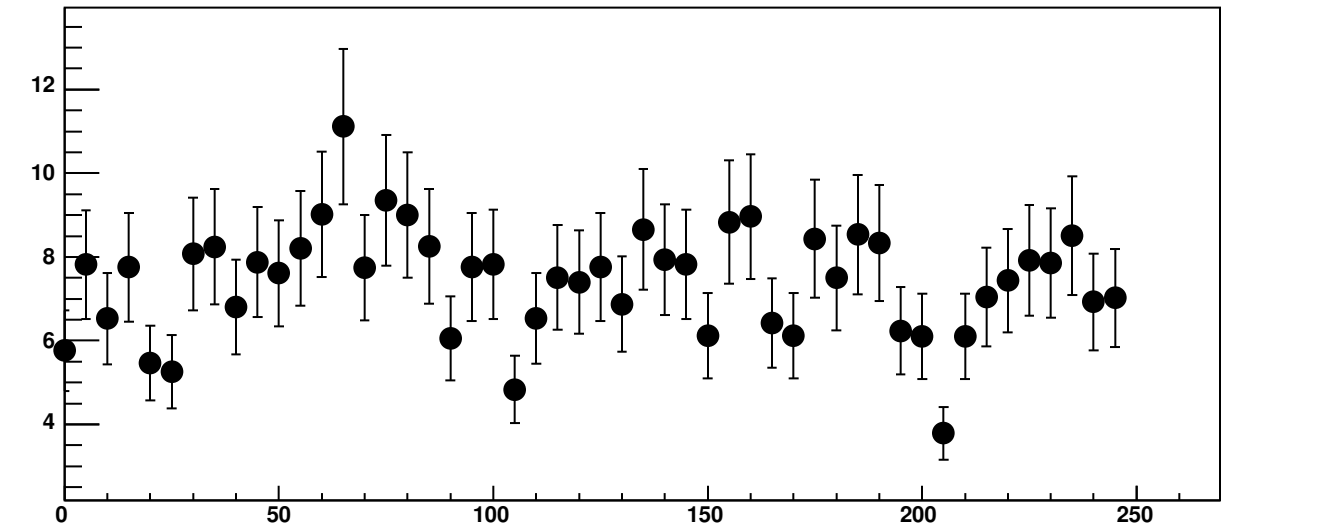

Chip 5, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 5, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
-15
-20
-25
-30
0
50
100
150
200
250

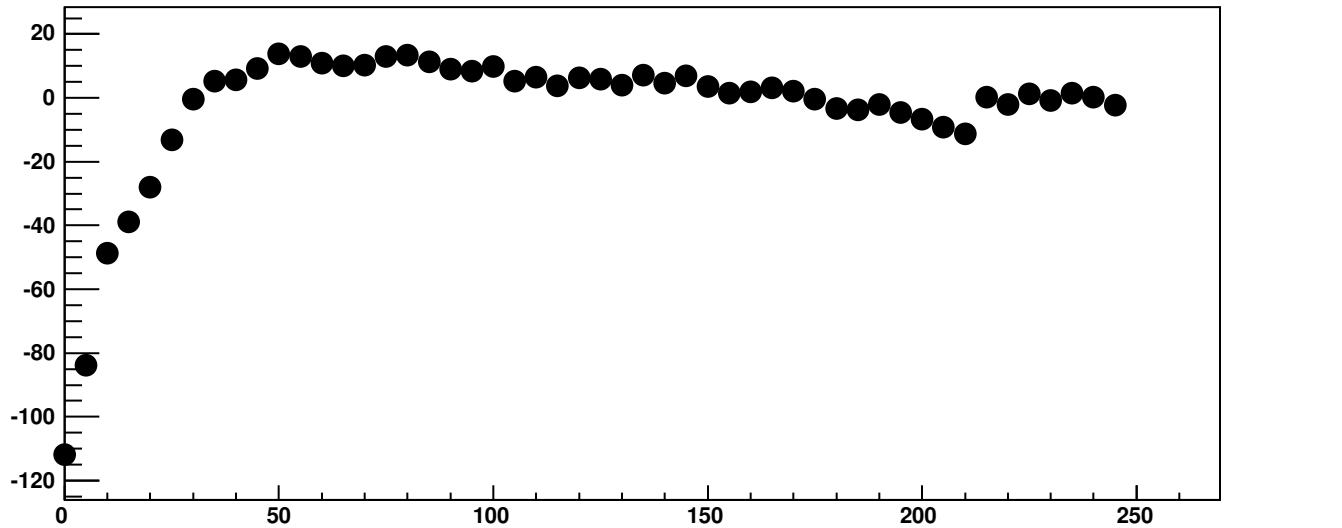
Chip 5, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold



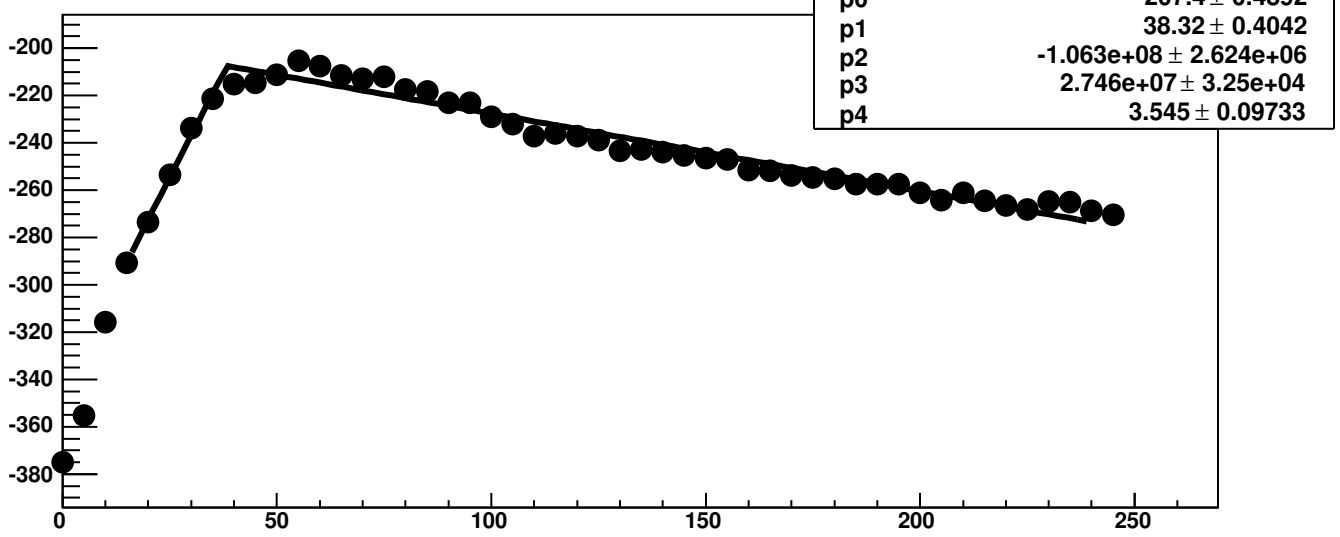
Chip 5, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



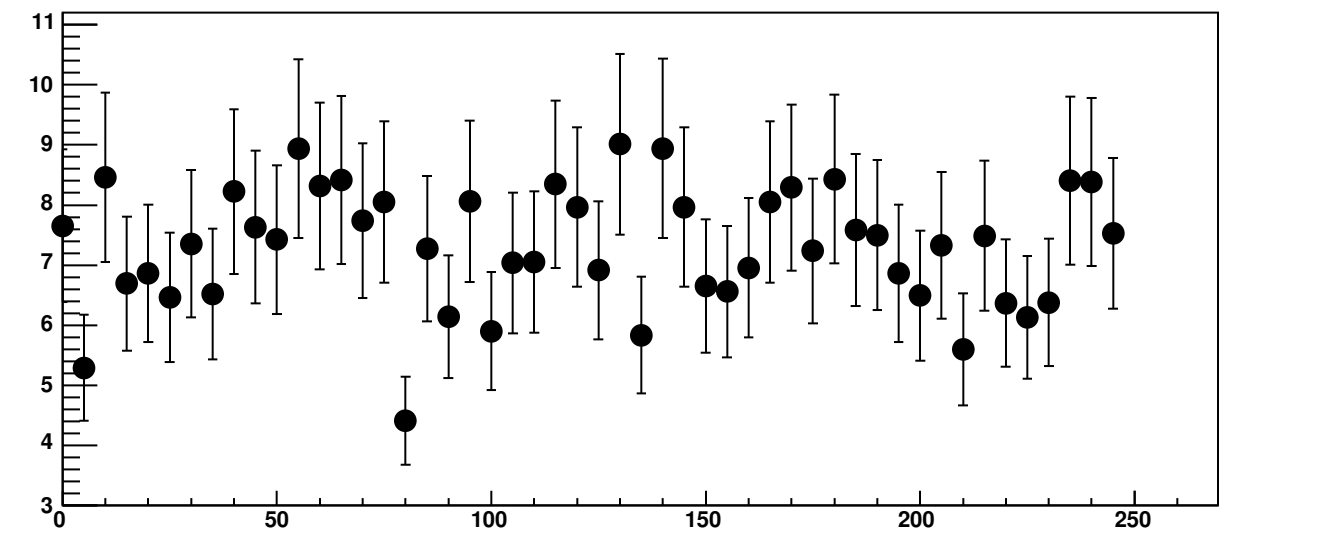
Chip 5, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold



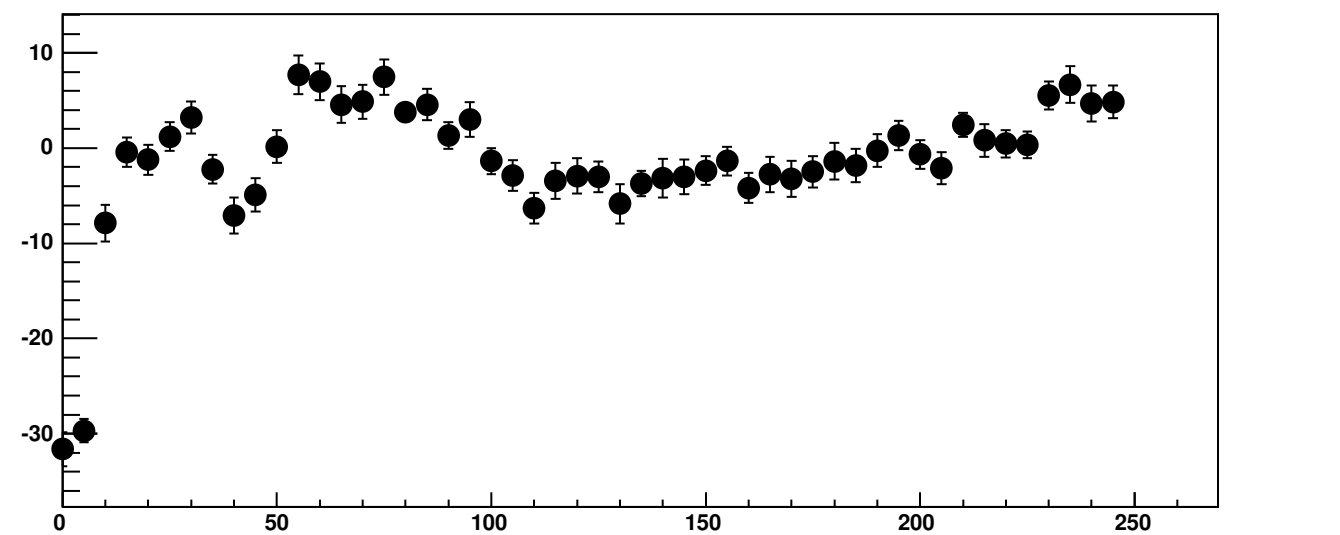
Chip 5, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold

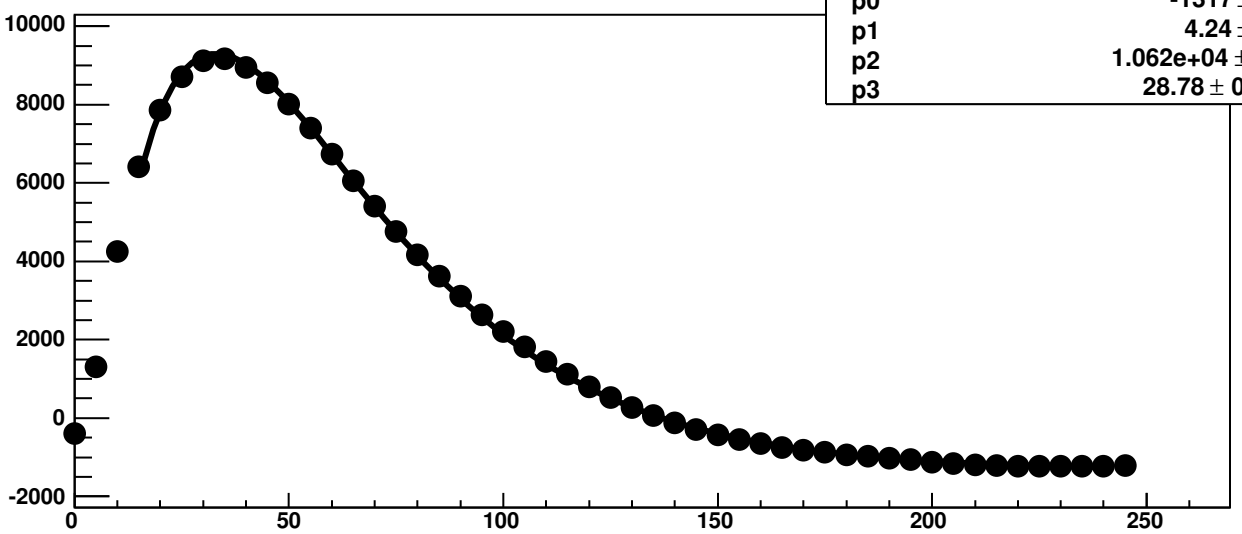


Chip 5, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

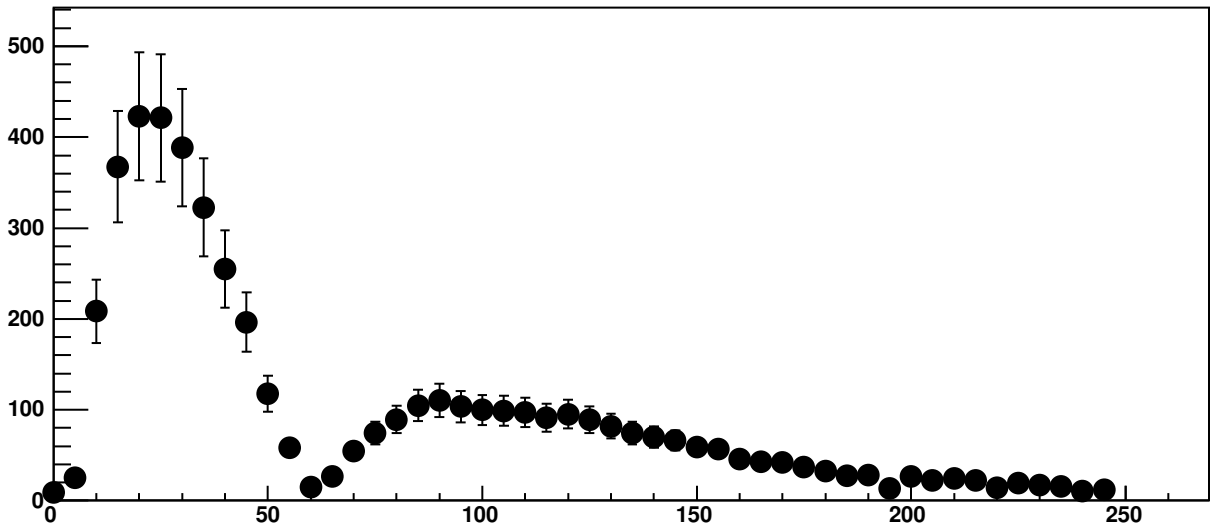


Chip 5, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold

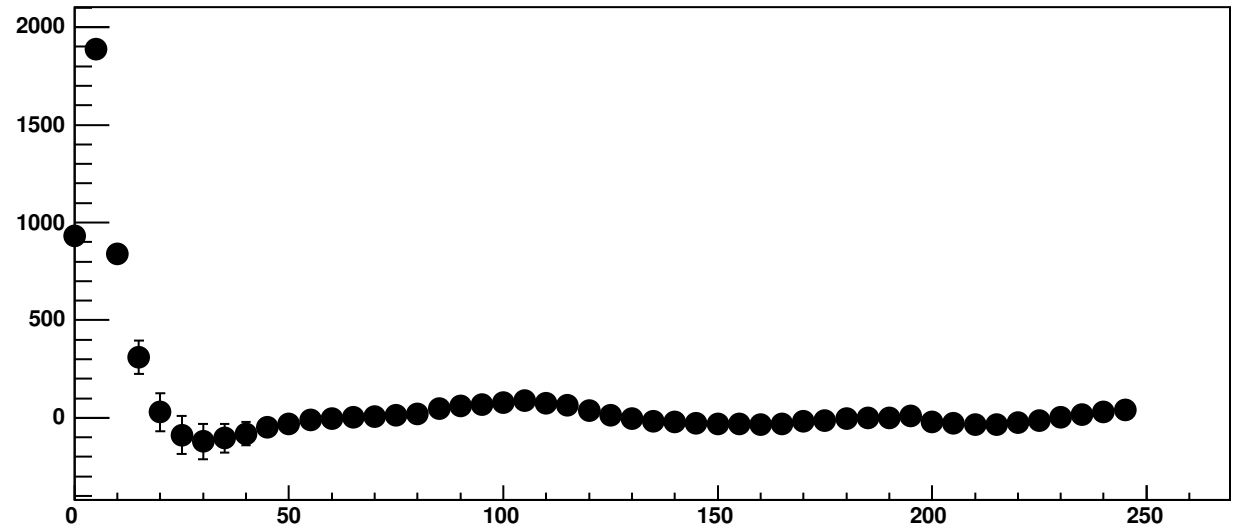
χ^2 / ndf	532.4 / 42
p0	-1317 ± 1.651
p1	4.24 ± 0.131
p2	1.062e+04 ± 17.33
p3	28.78 ± 0.04341



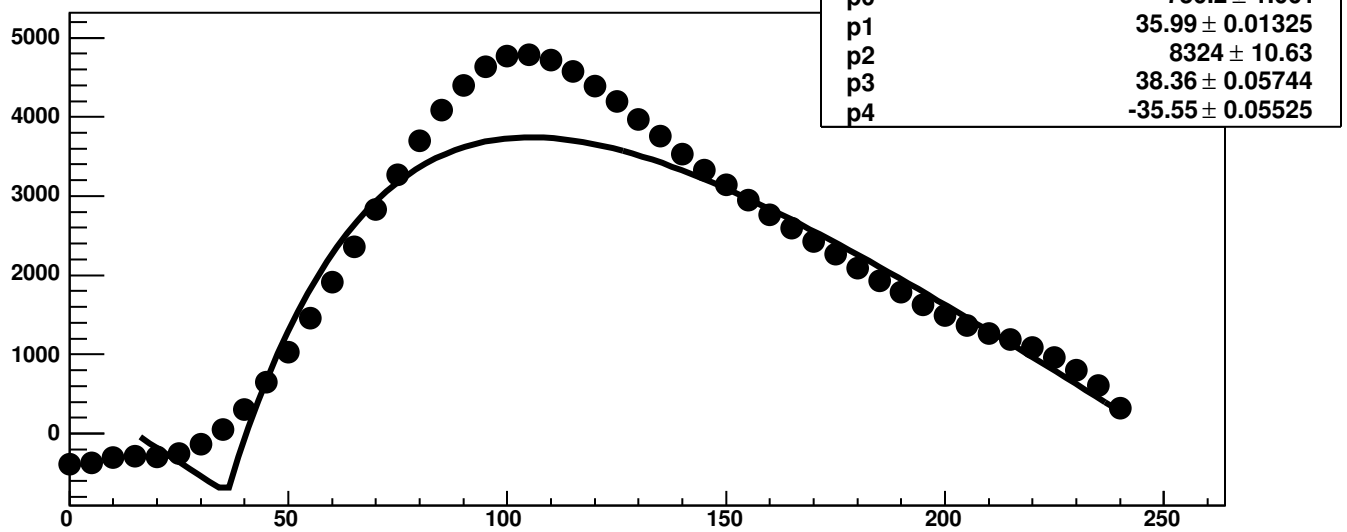
Chip 5, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold



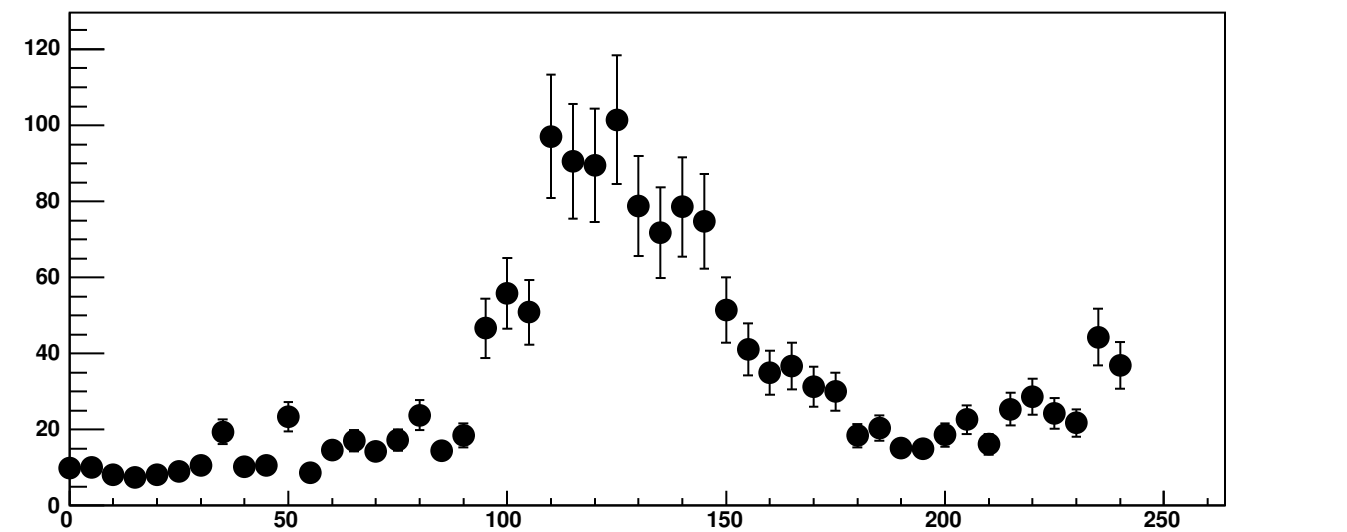
Chip 5, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold



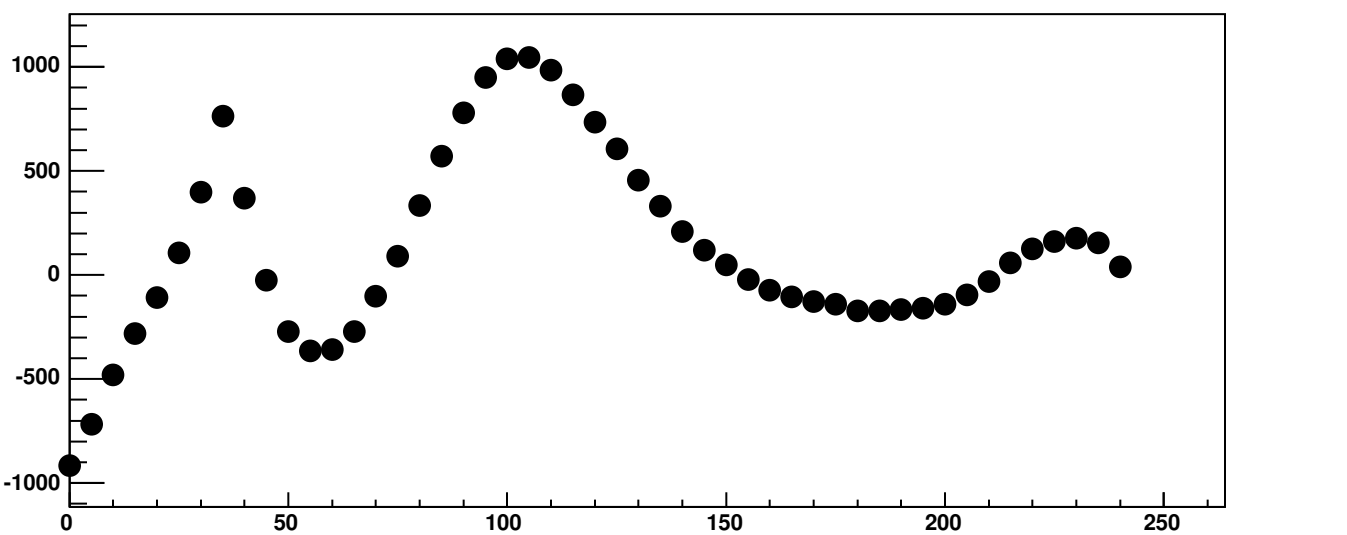
Chip 5, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold

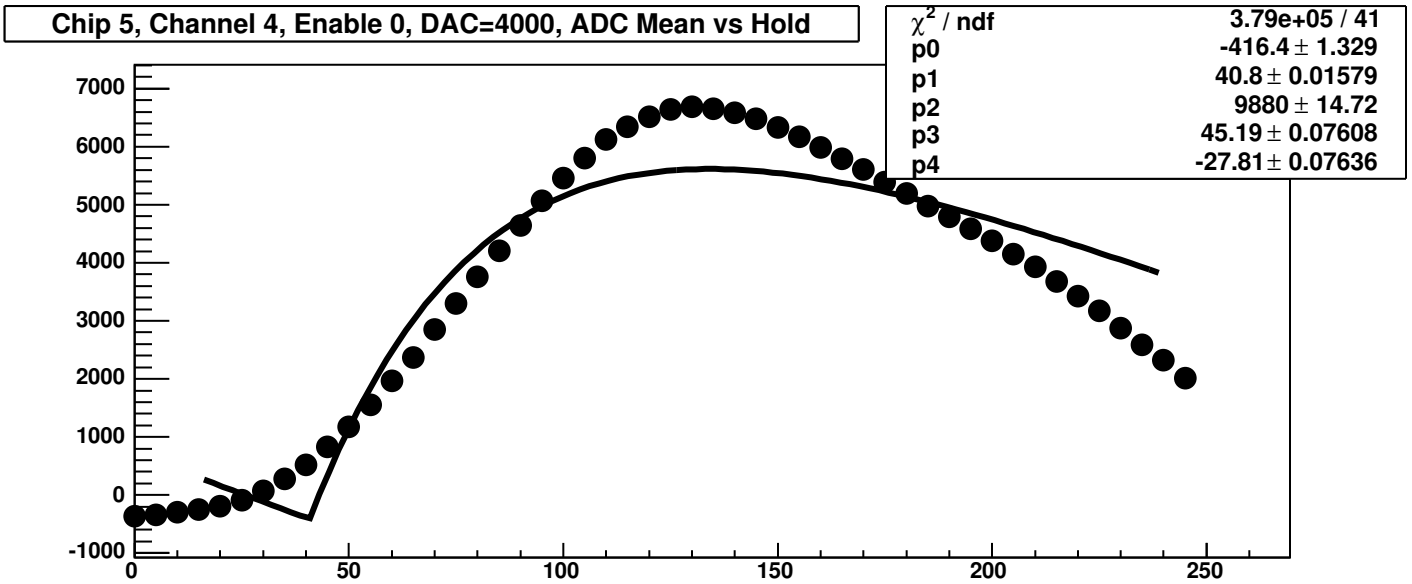


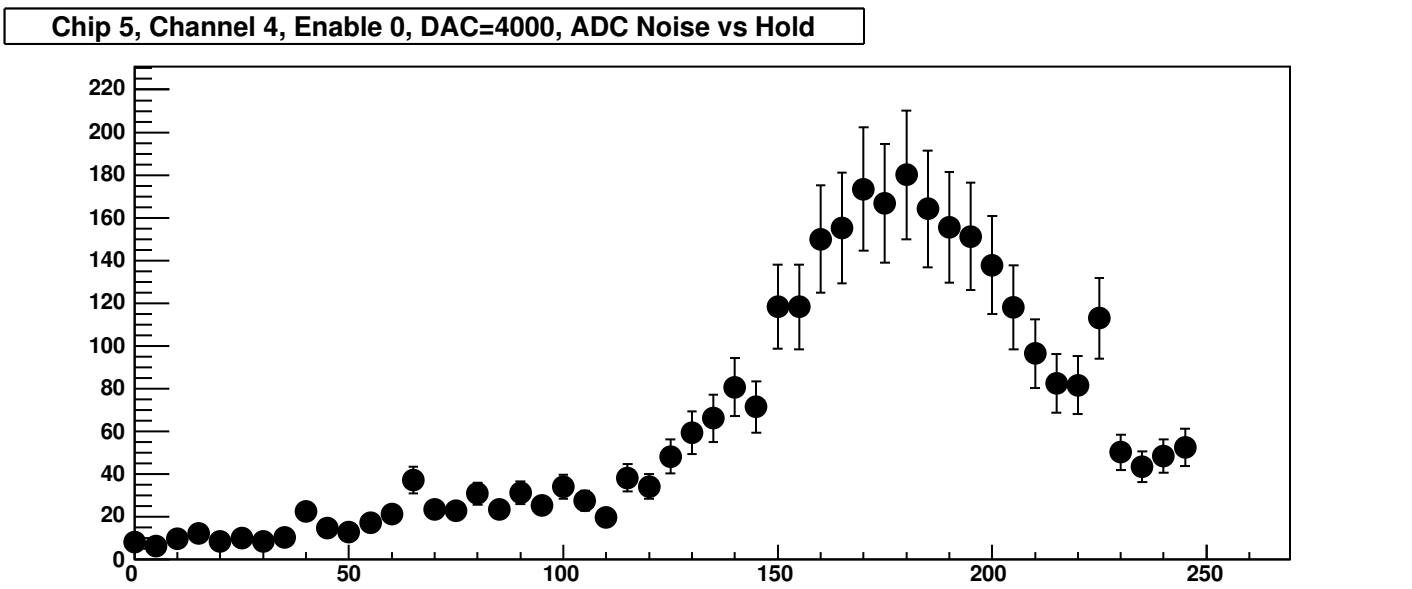
Chip 5, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold

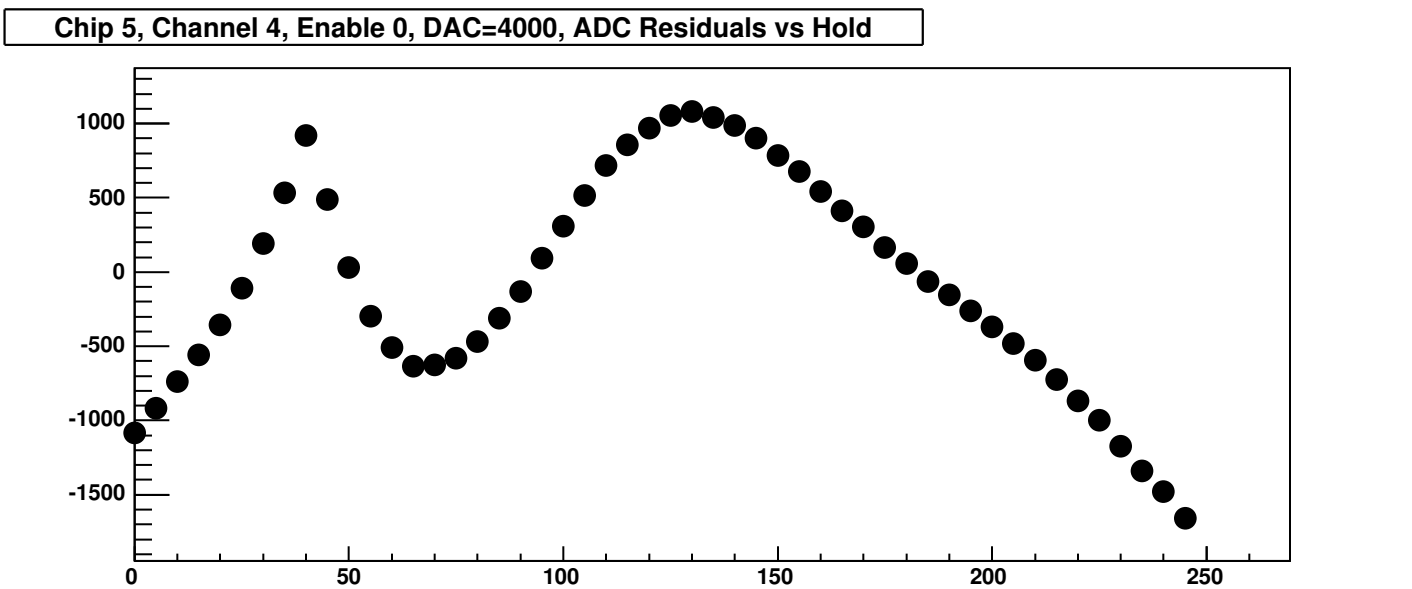


Chip 5, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold



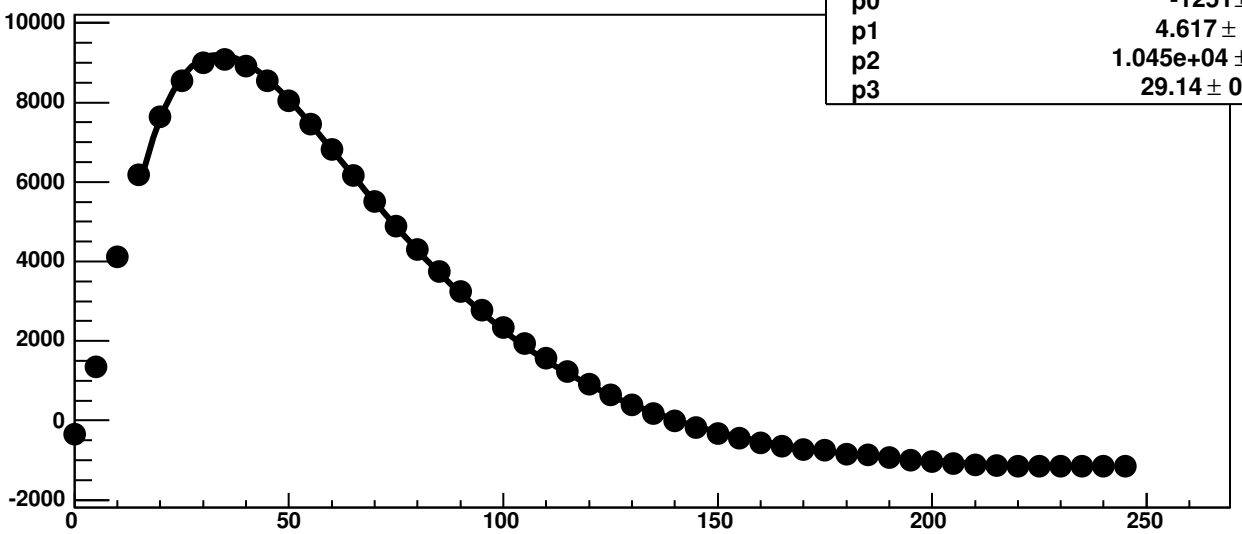

Chip 5, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.79e+05 / 41
p0 -416.4 ± 1.329
p1 40.8 ± 0.01579
p2 9880 ± 14.72
p3 45.19 ± 0.07608
p4 -27.81 ± 0.07636
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 5, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

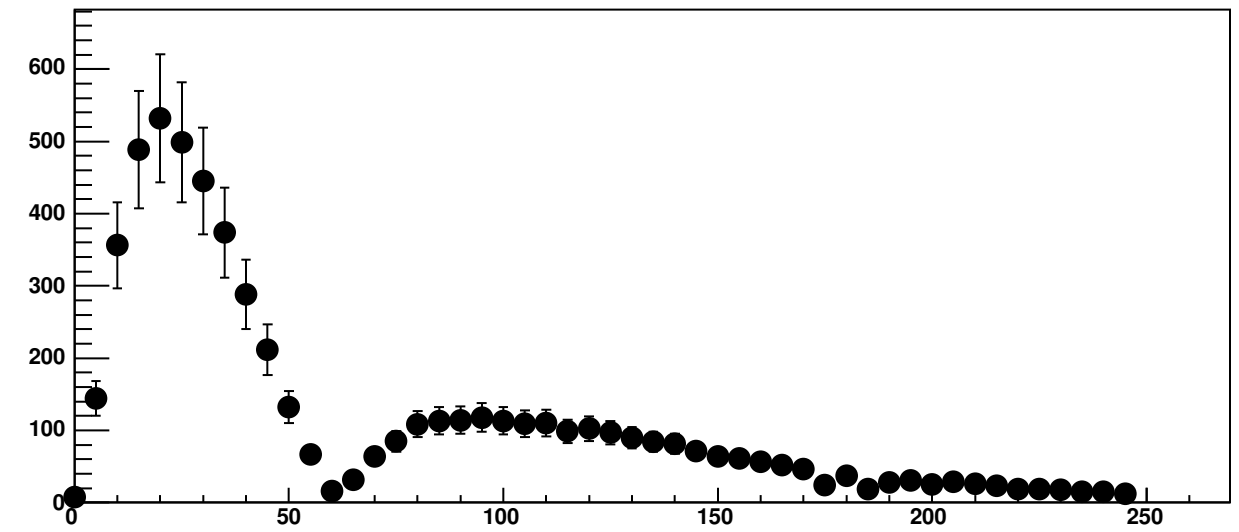

Chip 5, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

Chip 5, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold

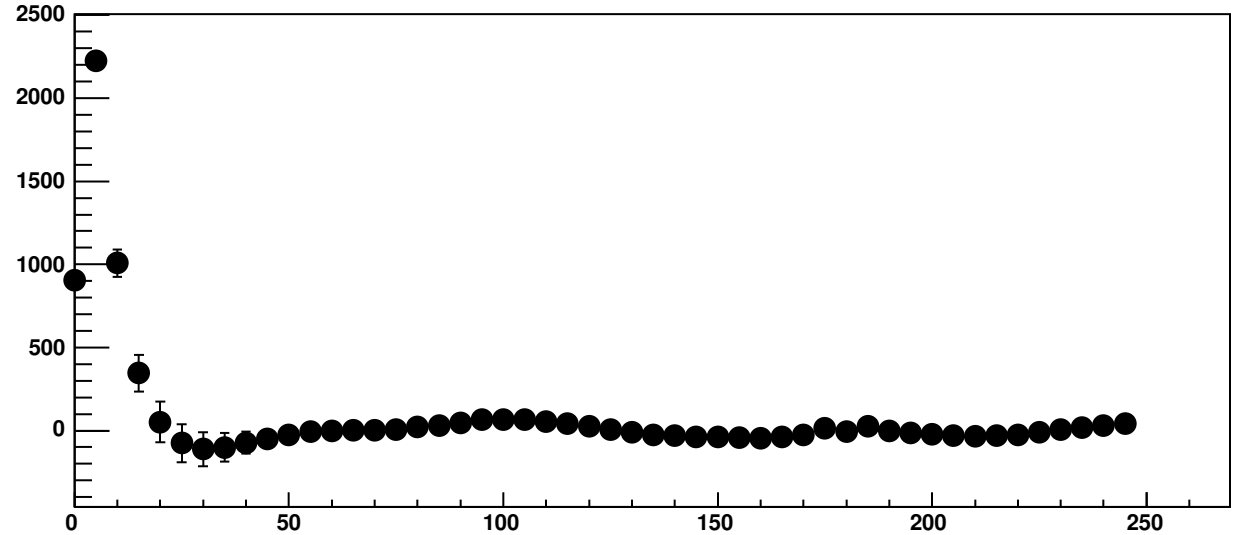
χ^2 / ndf	404.6 / 42
p0	-1251 ± 1.984
p1	4.617 ± 0.1622
p2	1.045e+04 ± 20.34
p3	29.14 ± 0.04968



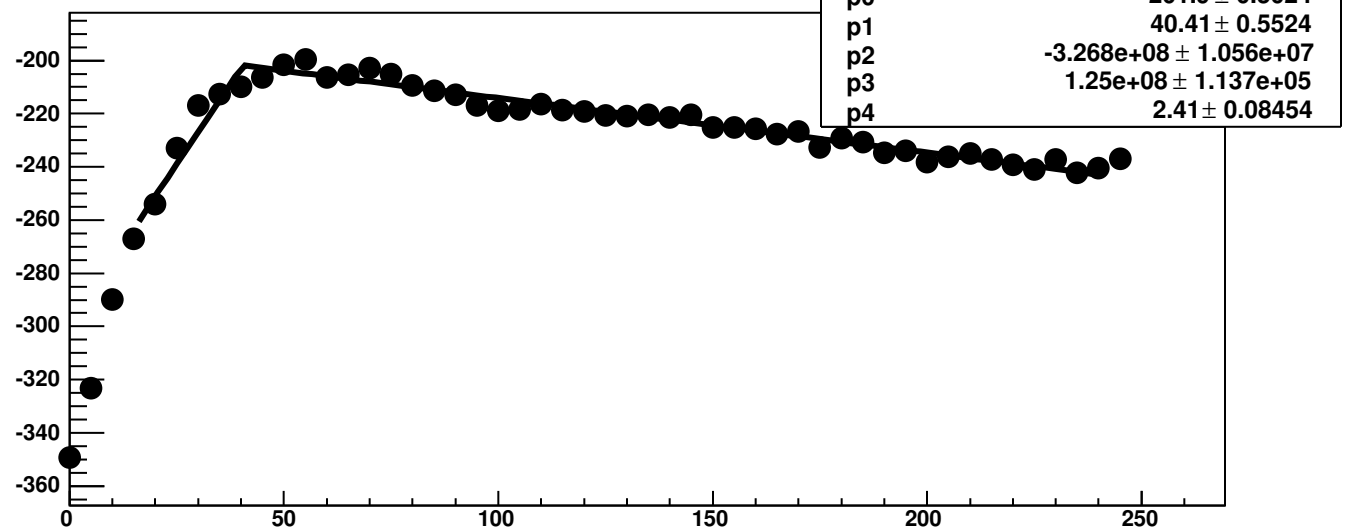
Chip 5, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold



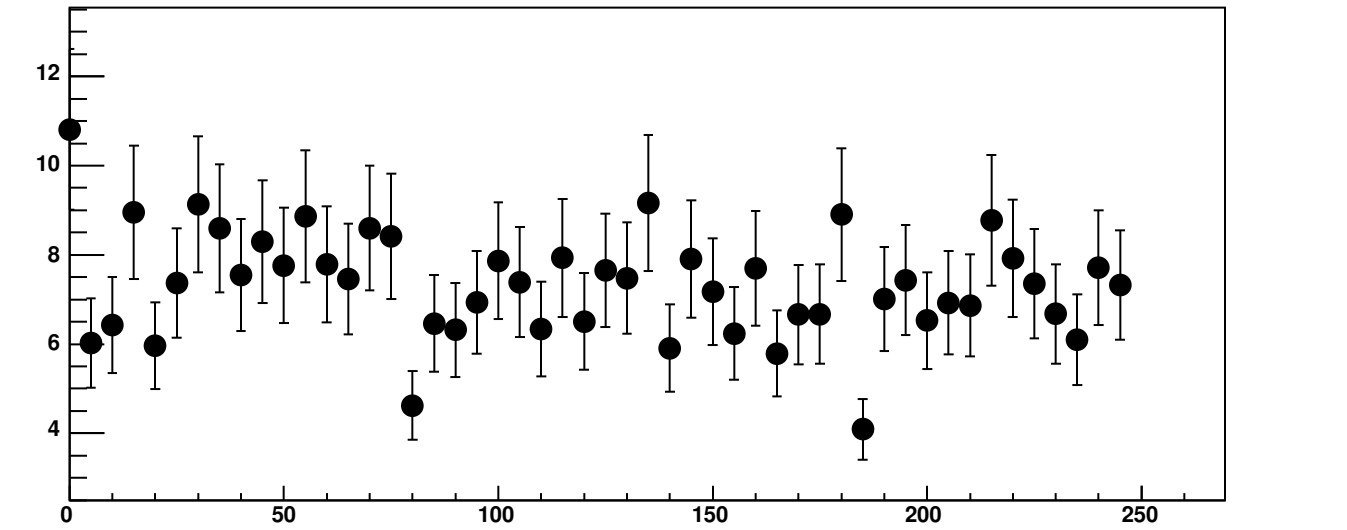
Chip 5, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold



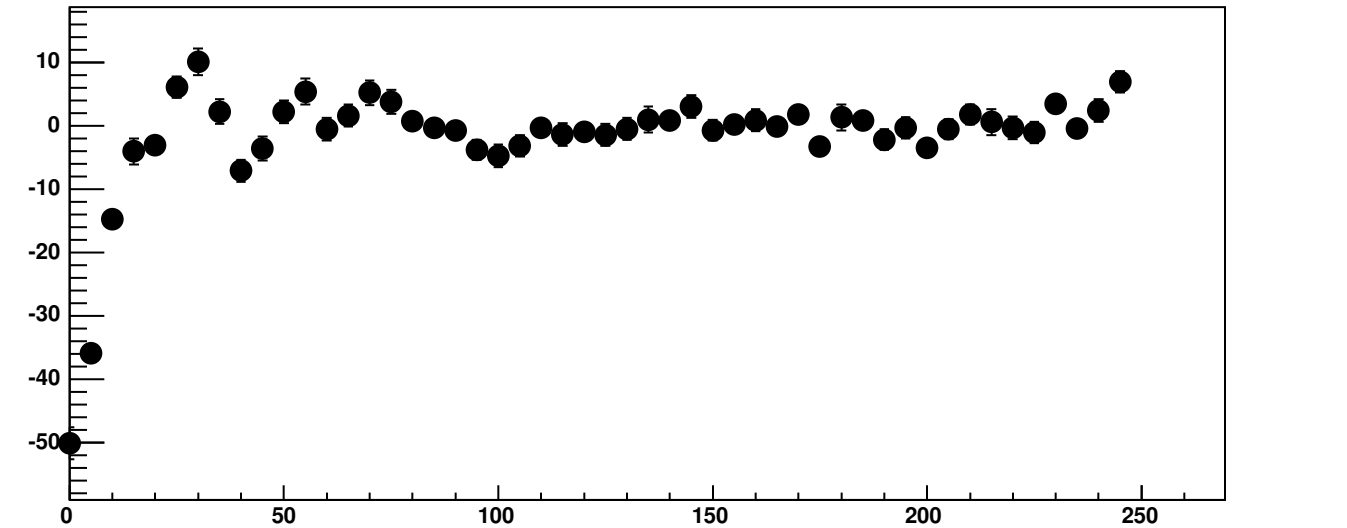
Chip 5, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold



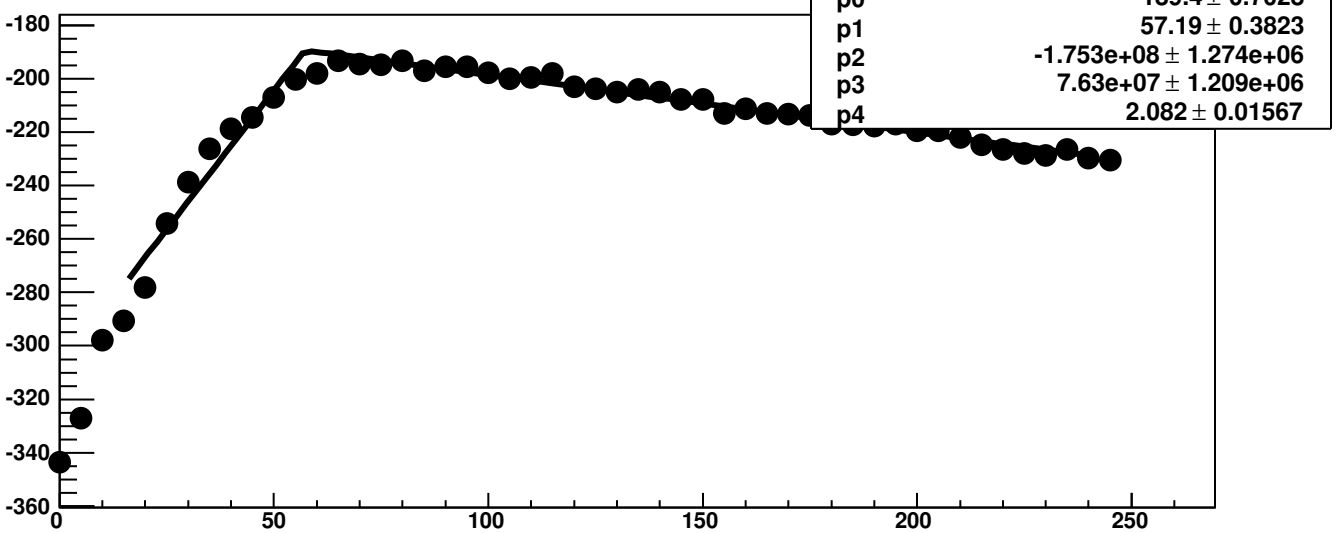
Chip 5, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



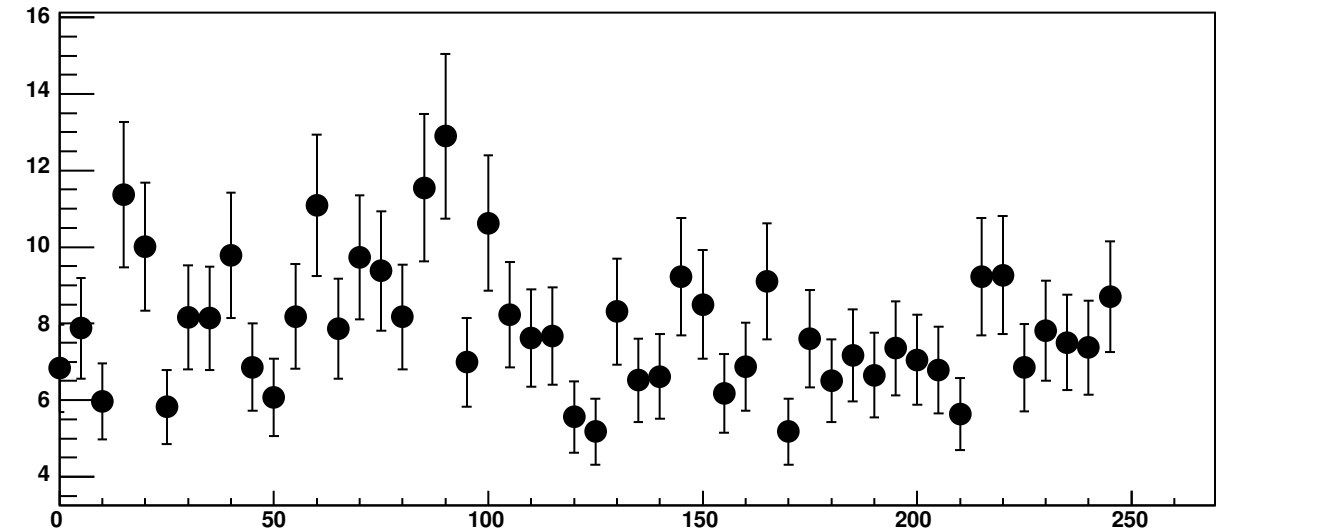
Chip 5, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold



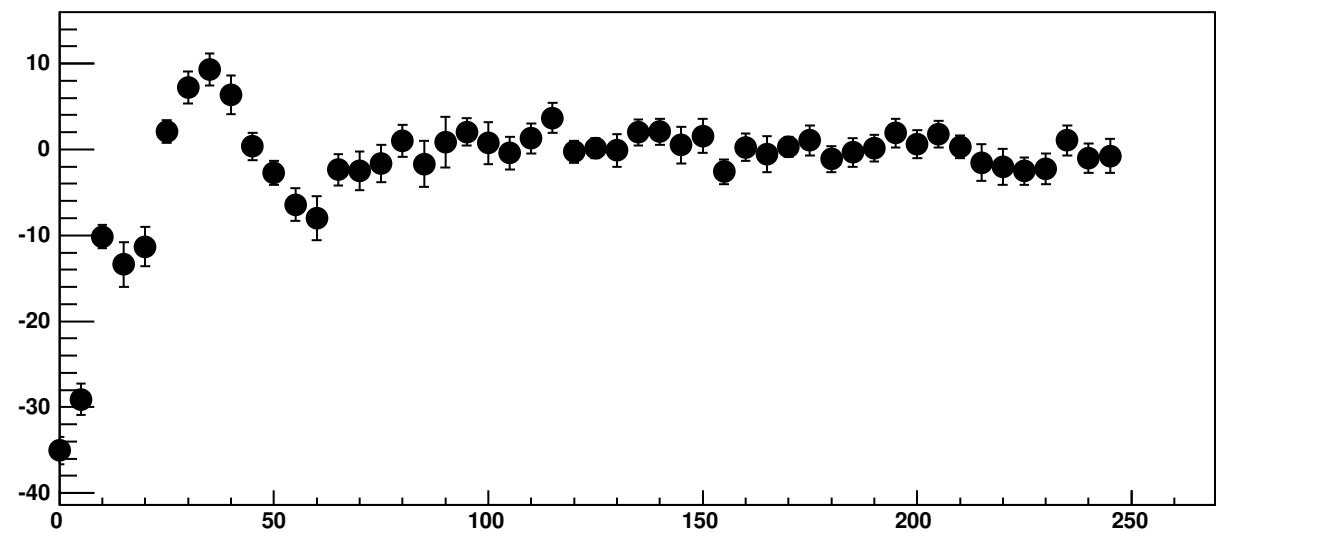
Chip 5, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold



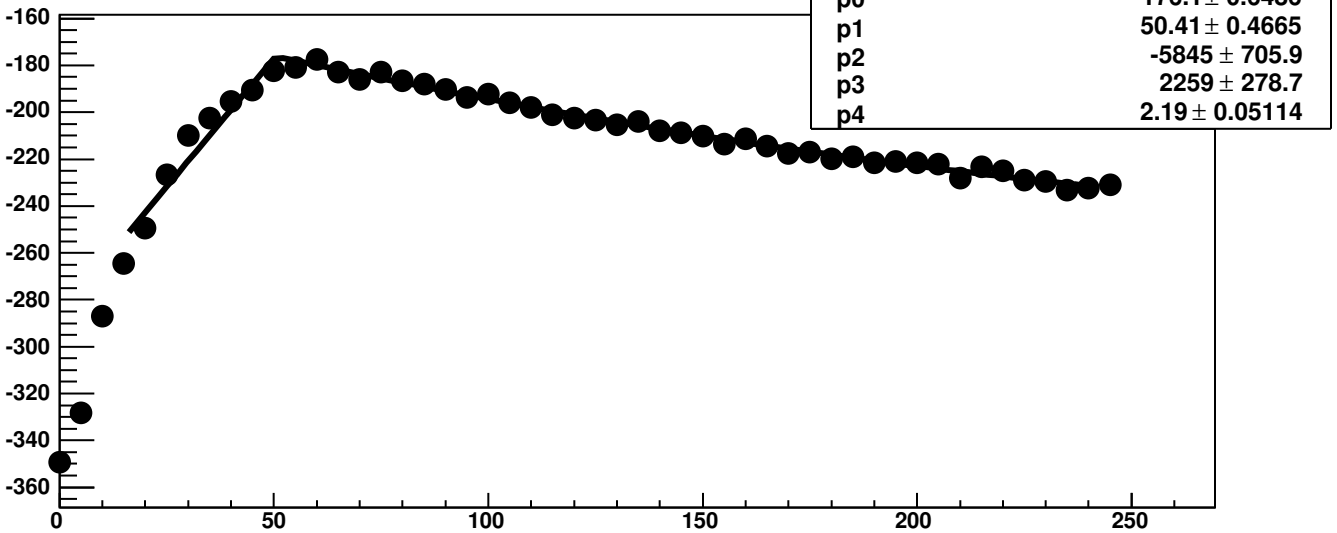
Chip 5, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



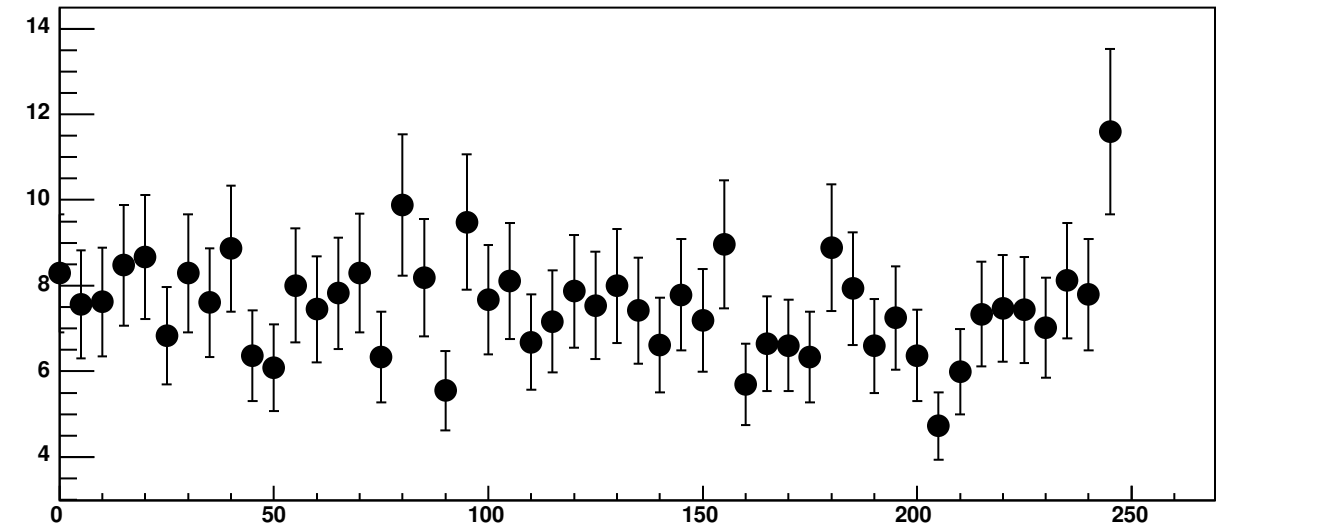
Chip 5, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold



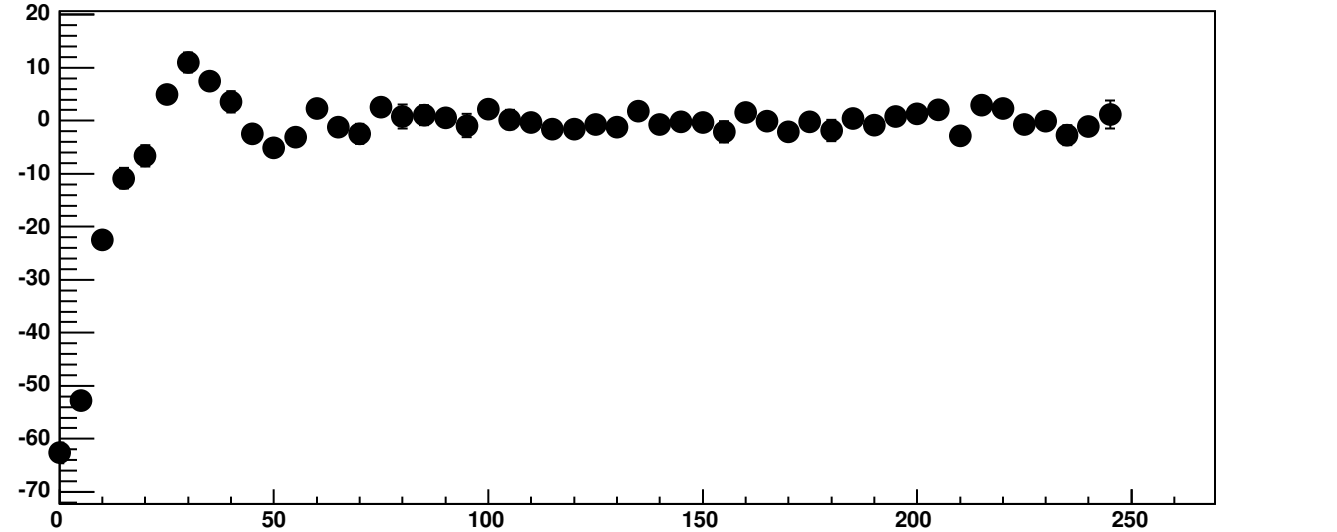
Chip 5, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold



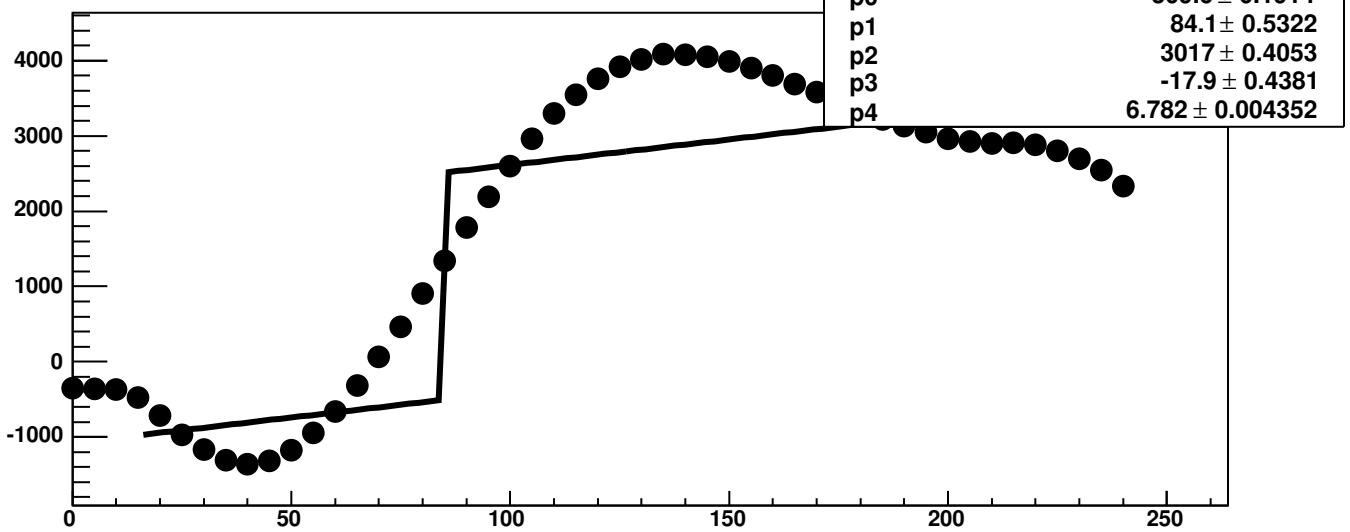
Chip 5, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold



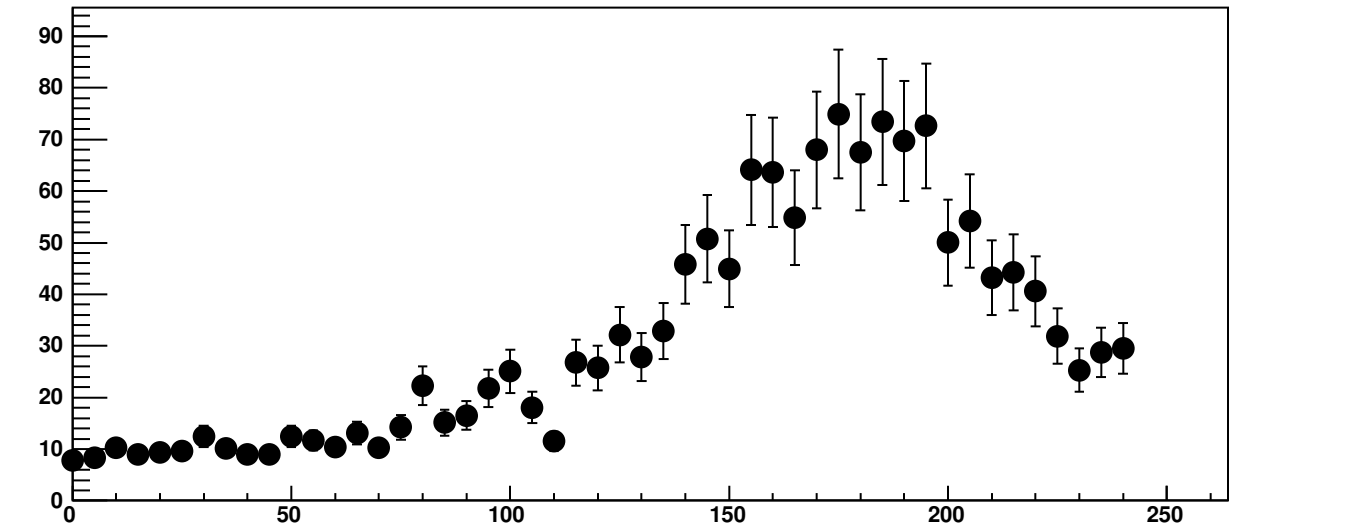
Chip 5, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold



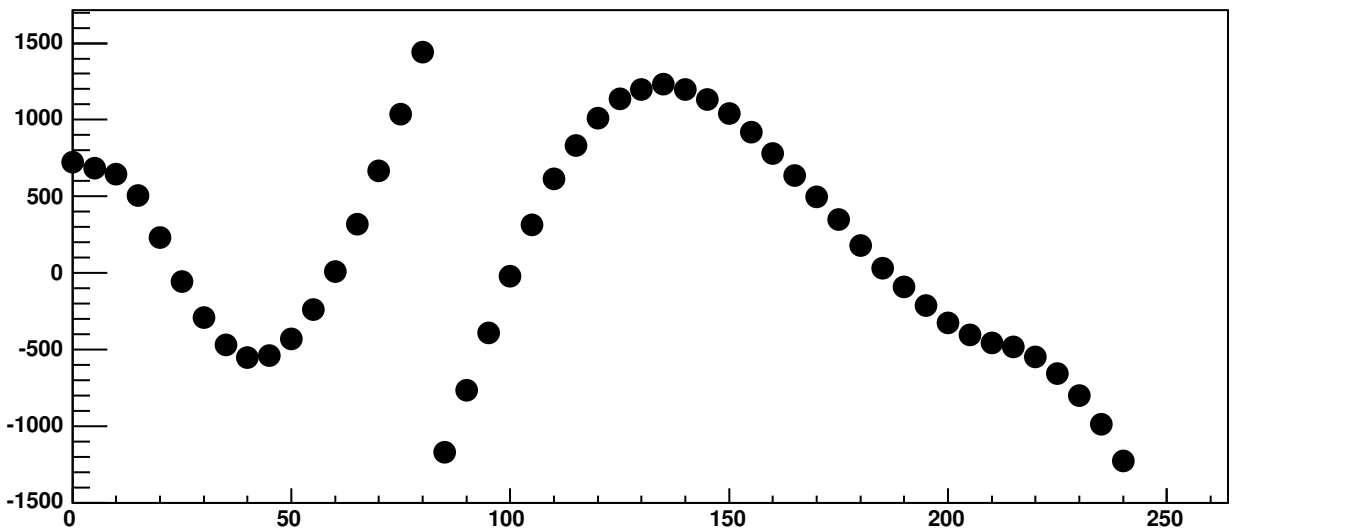
Chip 5, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold

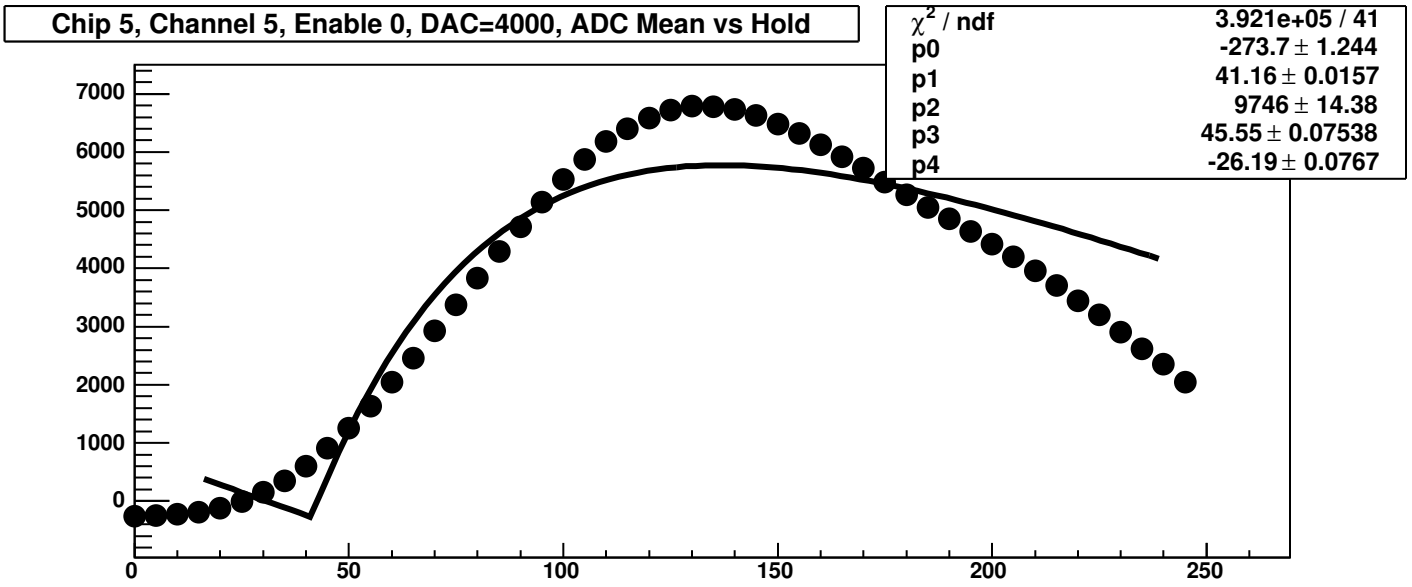


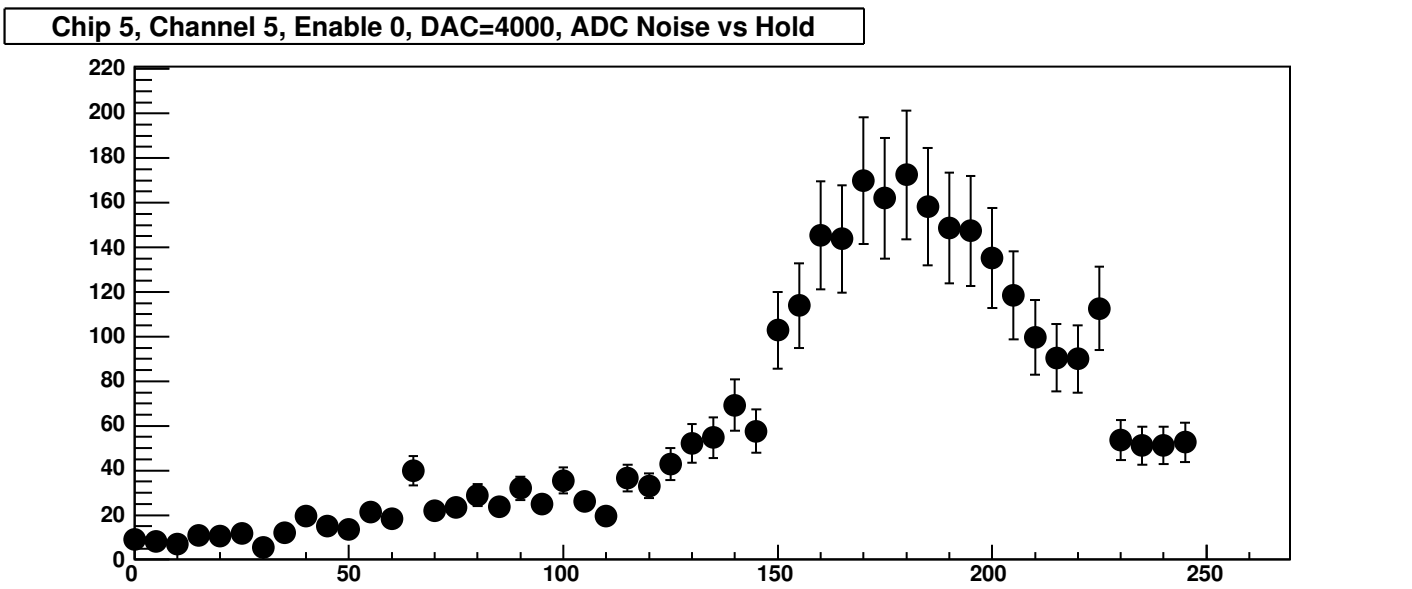
Chip 5, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold

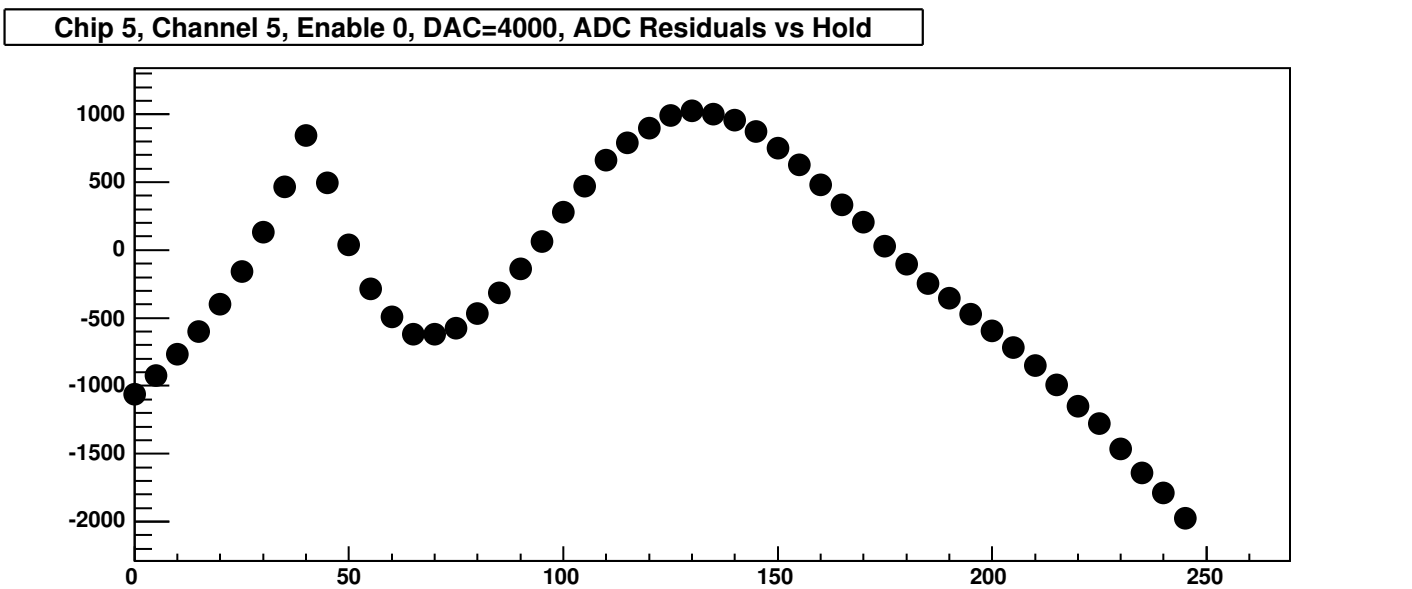


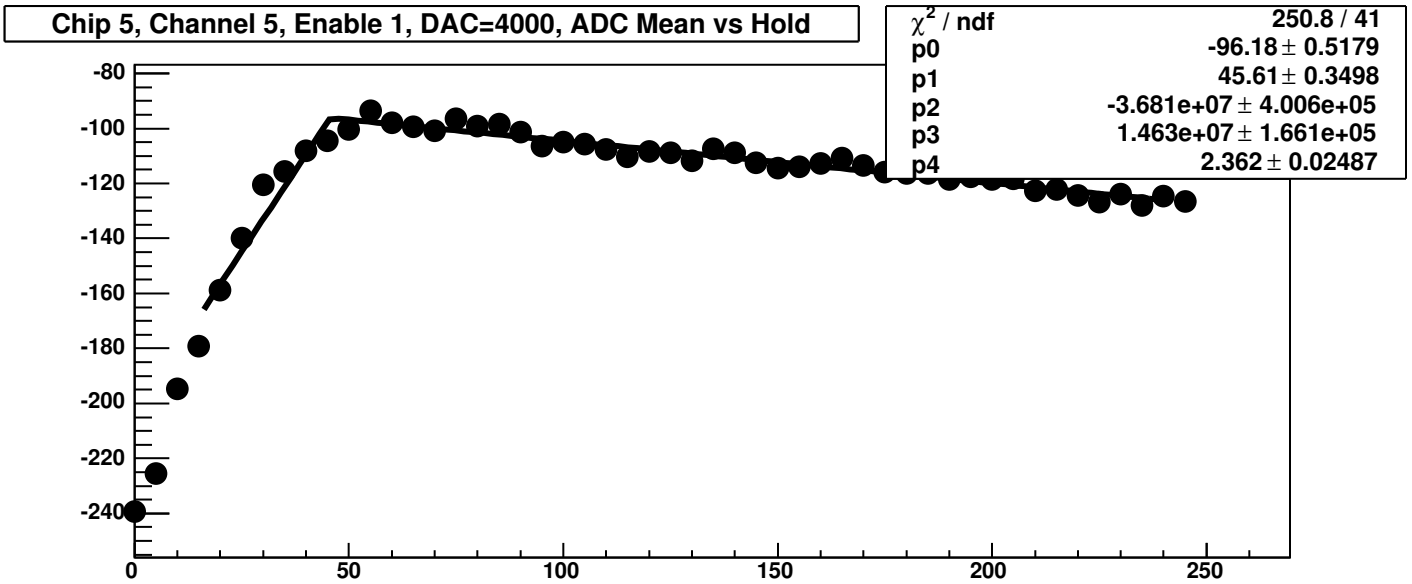
Chip 5, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold

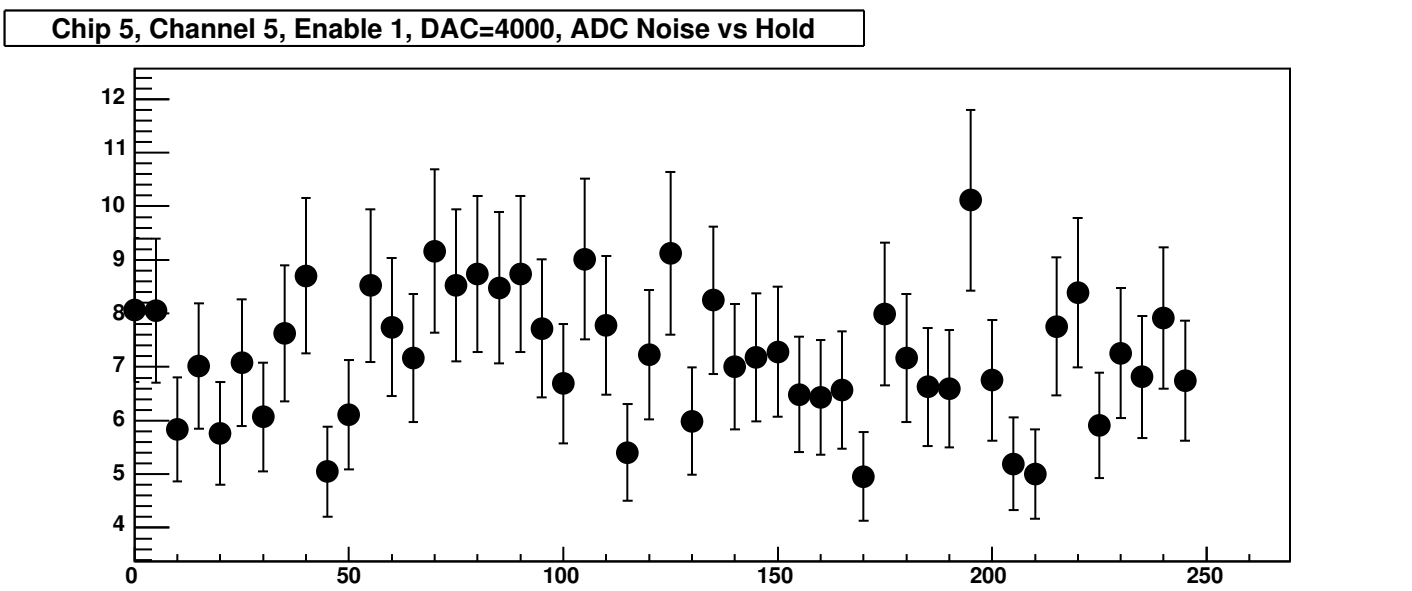


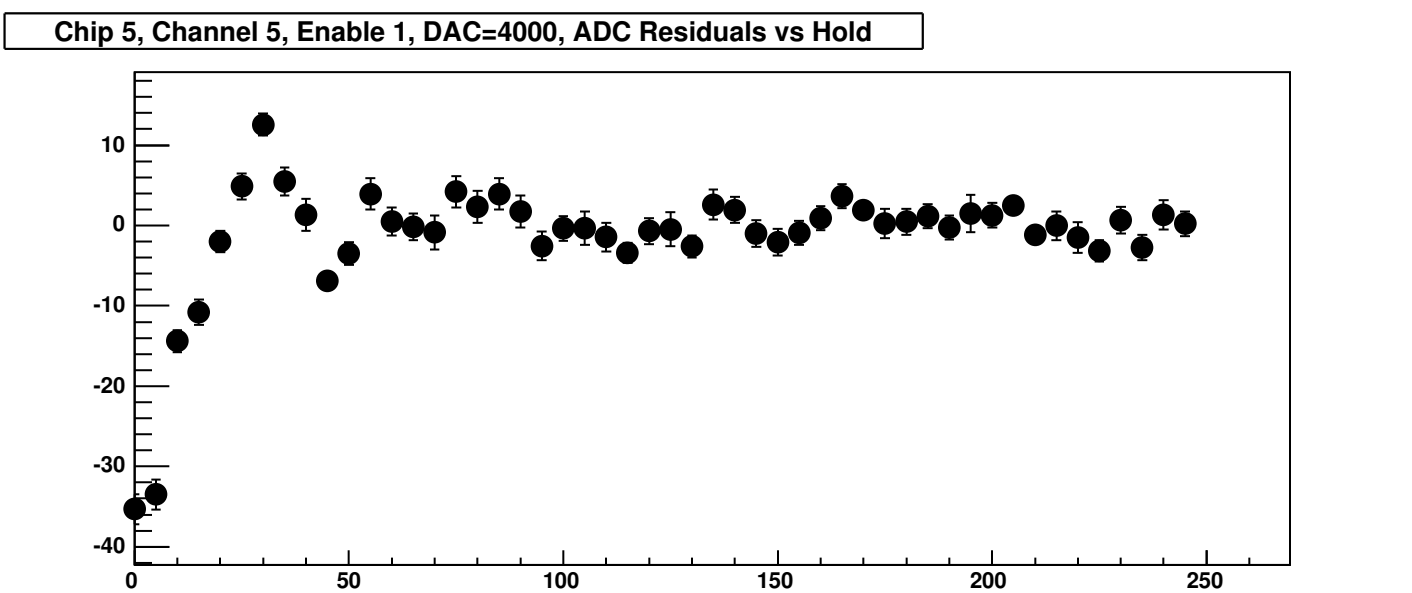

Chip 5, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.921e+05 / 41
p0 -273.7 ± 1.244
p1 41.16 ± 0.0157
p2 9746 ± 14.38
p3 45.55 ± 0.07538
p4 -26.19 ± 0.0767
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 5, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

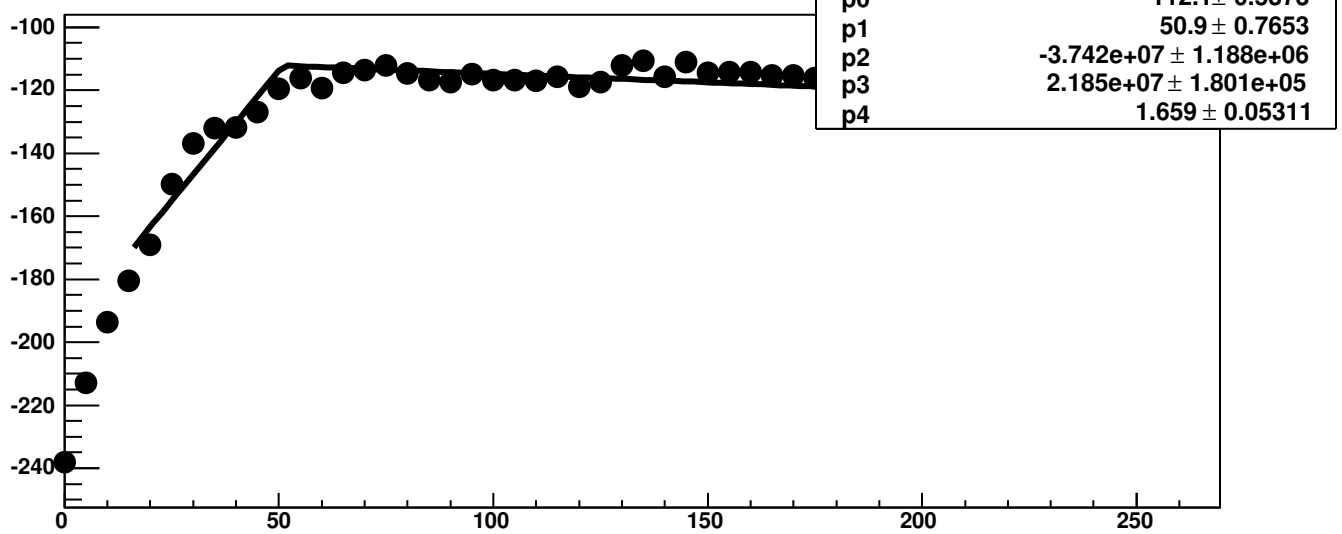

Chip 5, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250


Chip 5, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 250.8 / 41
p0 -96.18 ± 0.5179
p1 45.61 ± 0.3498
p2 -3.681e+07 ± 4.006e+05
p3 1.463e+07 ± 1.661e+05
p4 2.362 ± 0.02487
-80
-100
-120
-140
-160
-180
-200
-220
-240
0
50
100
150
200
250

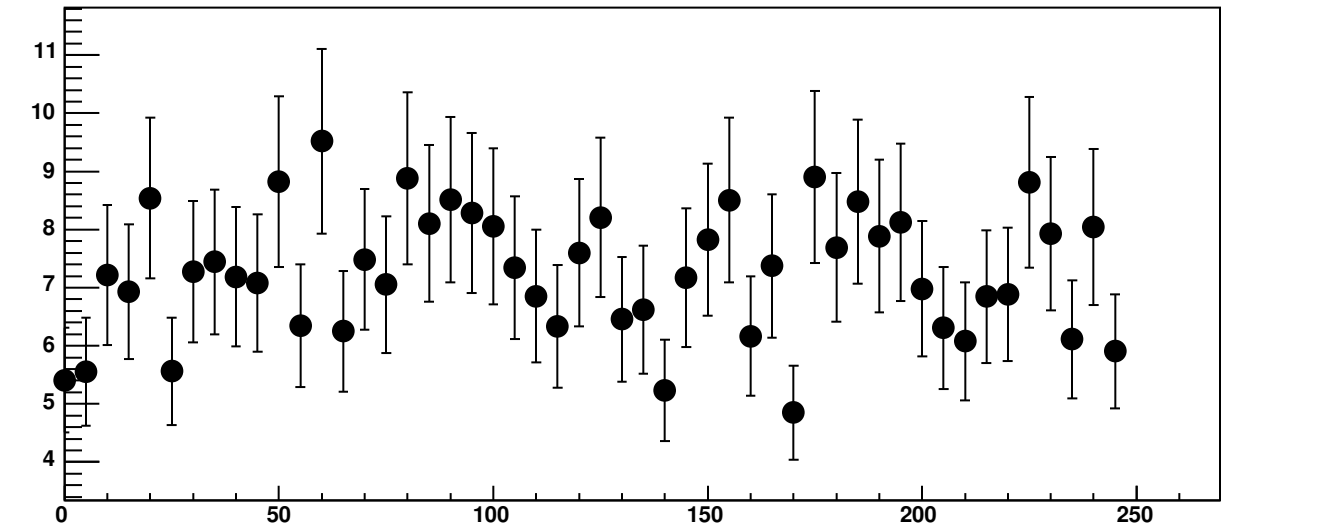

Chip 5, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 5, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold
10
0
-10
-20
-30
-40
0
50
100
150
200
250

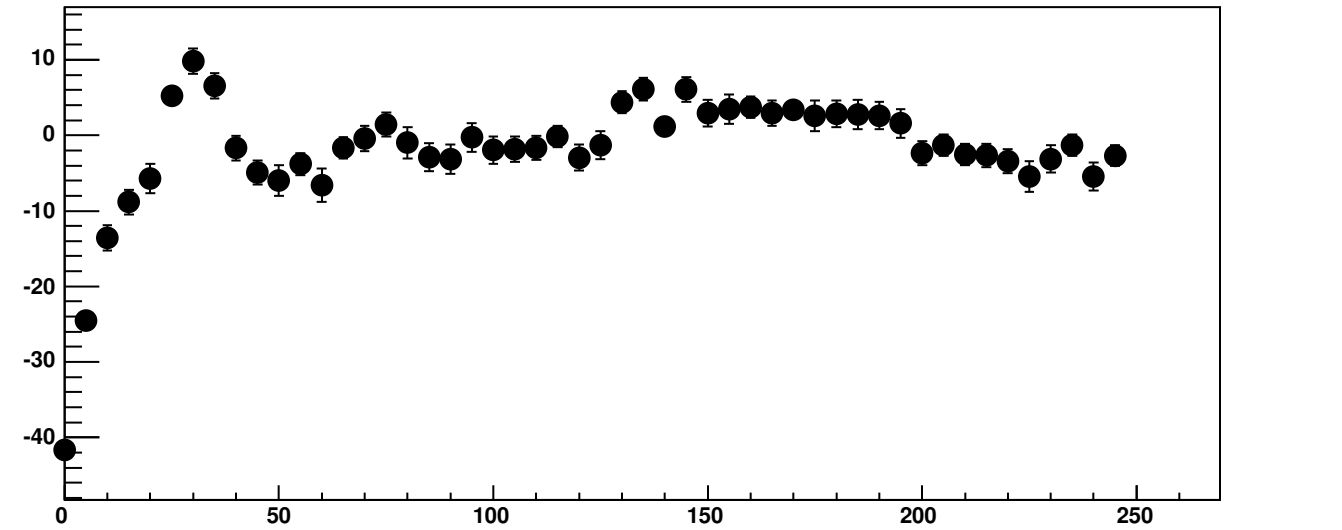
Chip 5, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold



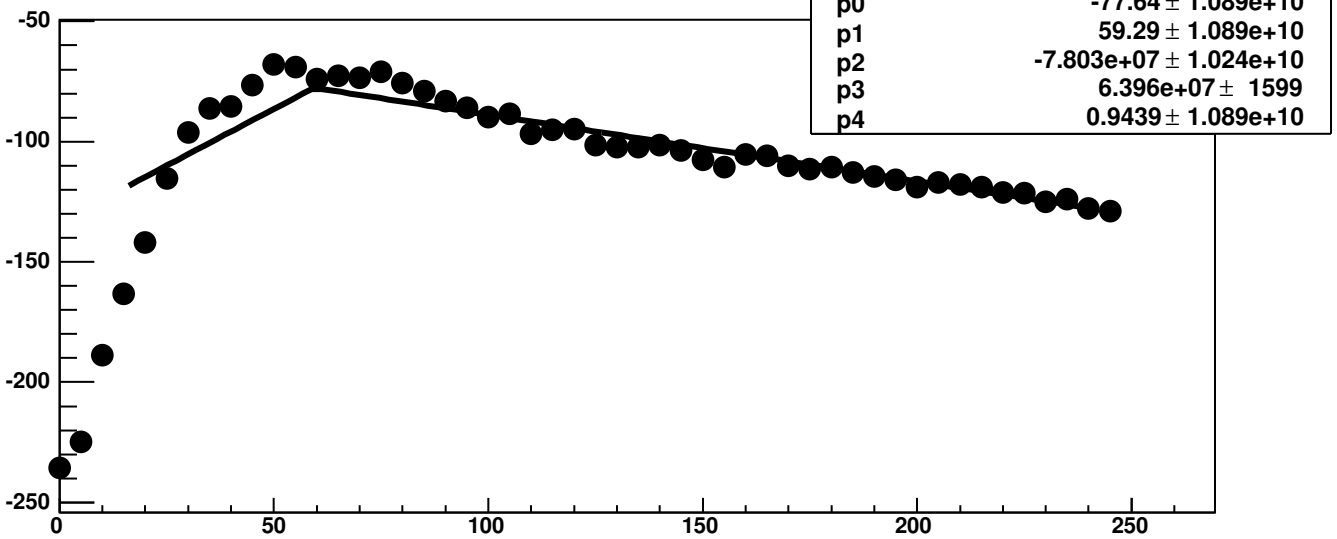
Chip 5, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



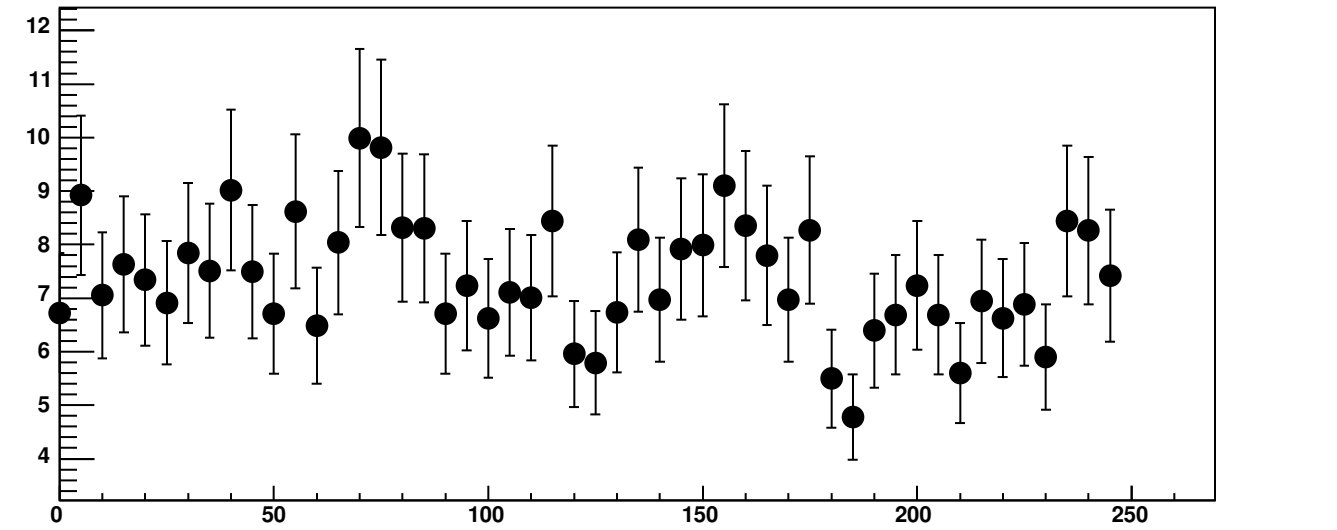
Chip 5, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold



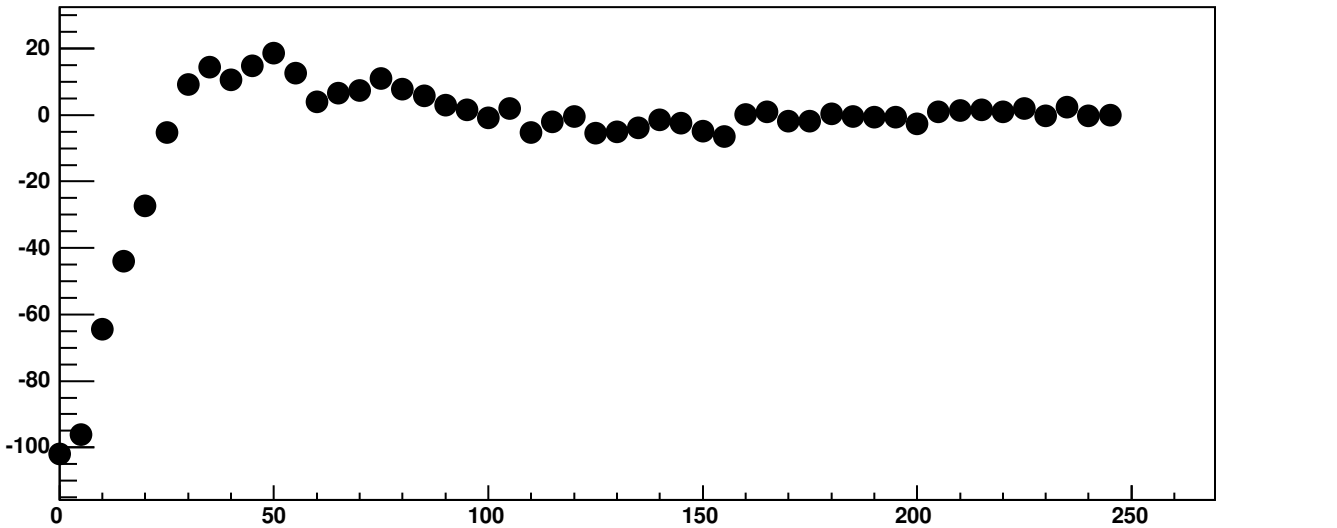
Chip 5, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold



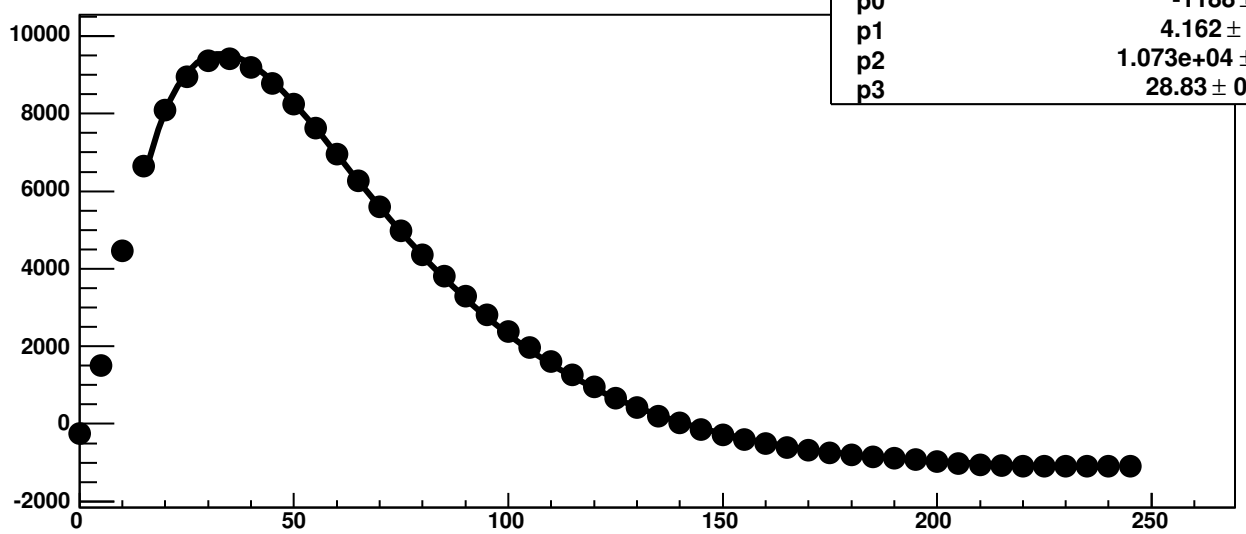
Chip 5, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



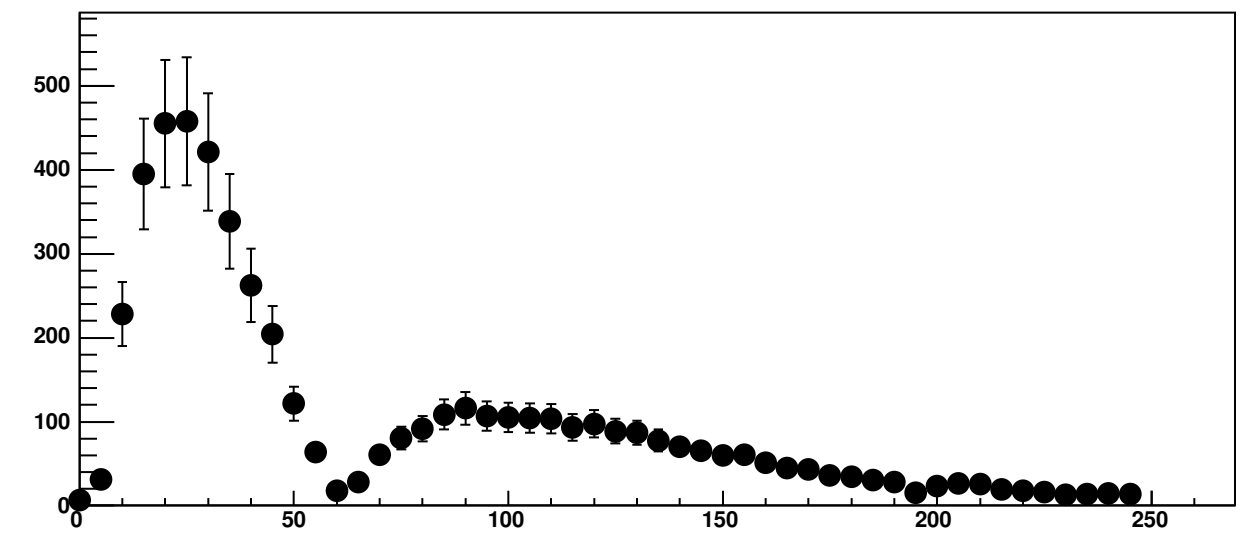
Chip 5, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold



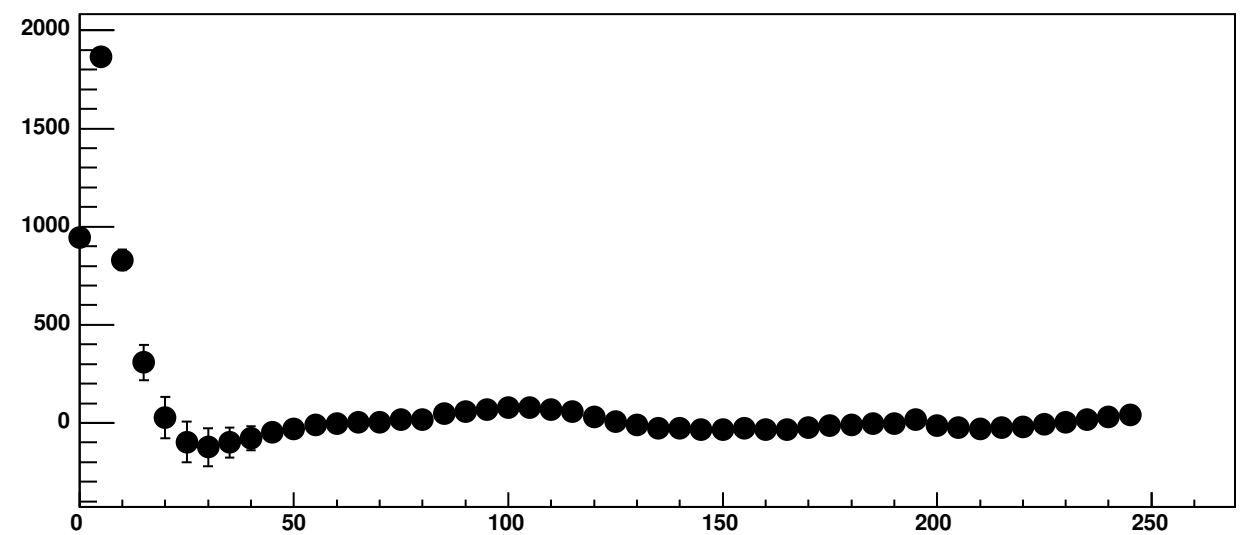
Chip 5, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold



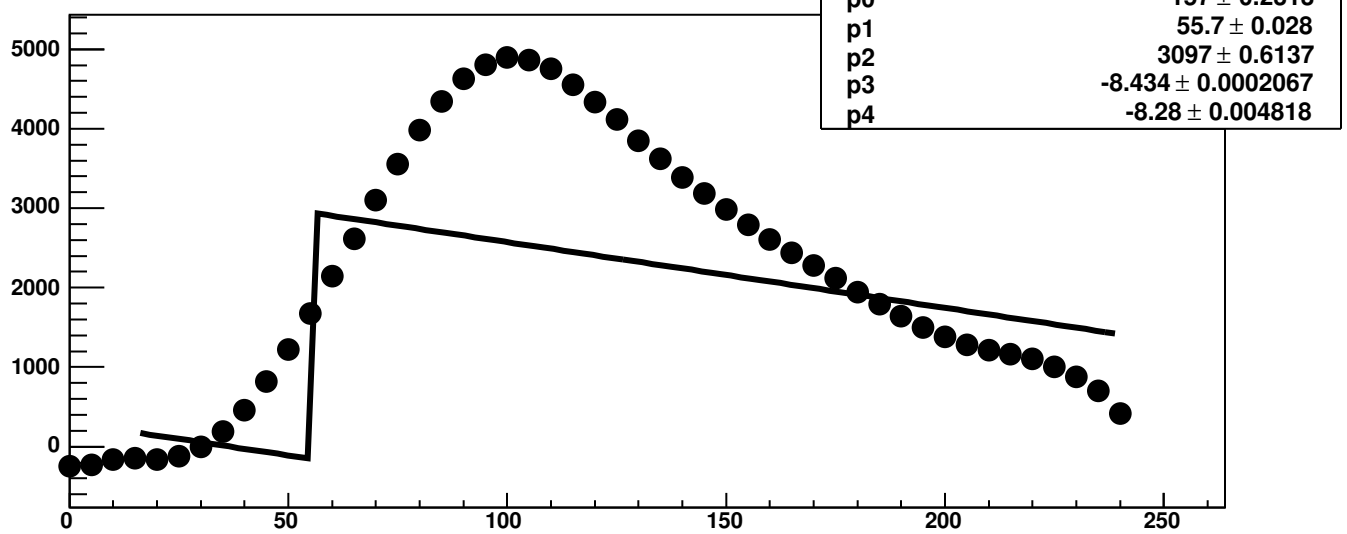
Chip 5, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold



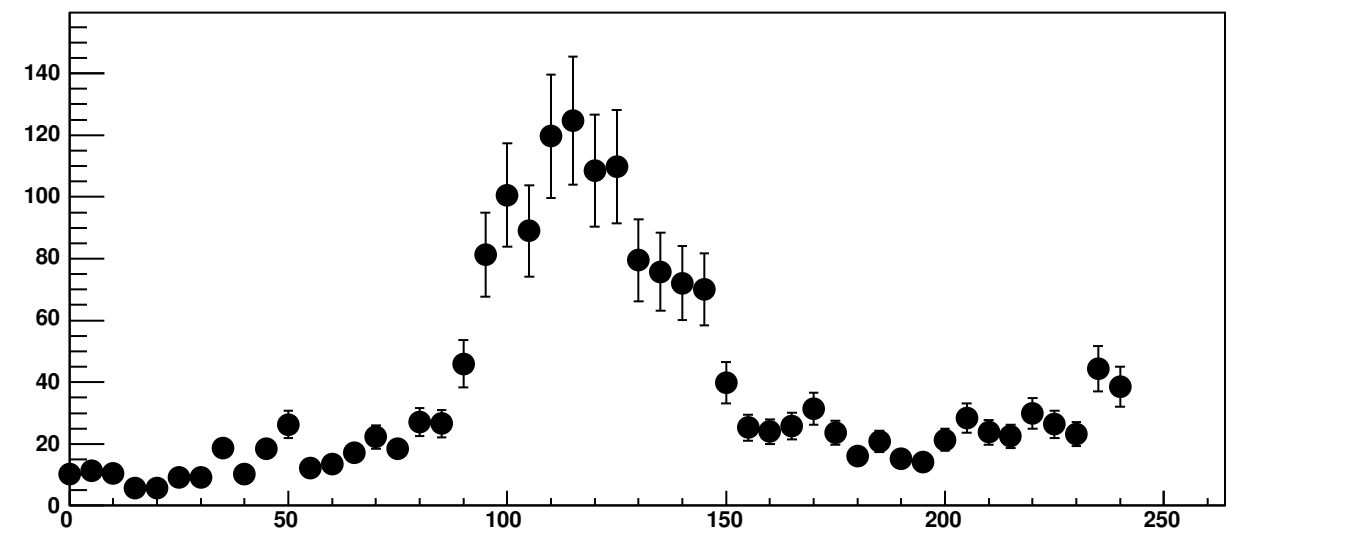
Chip 5, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold



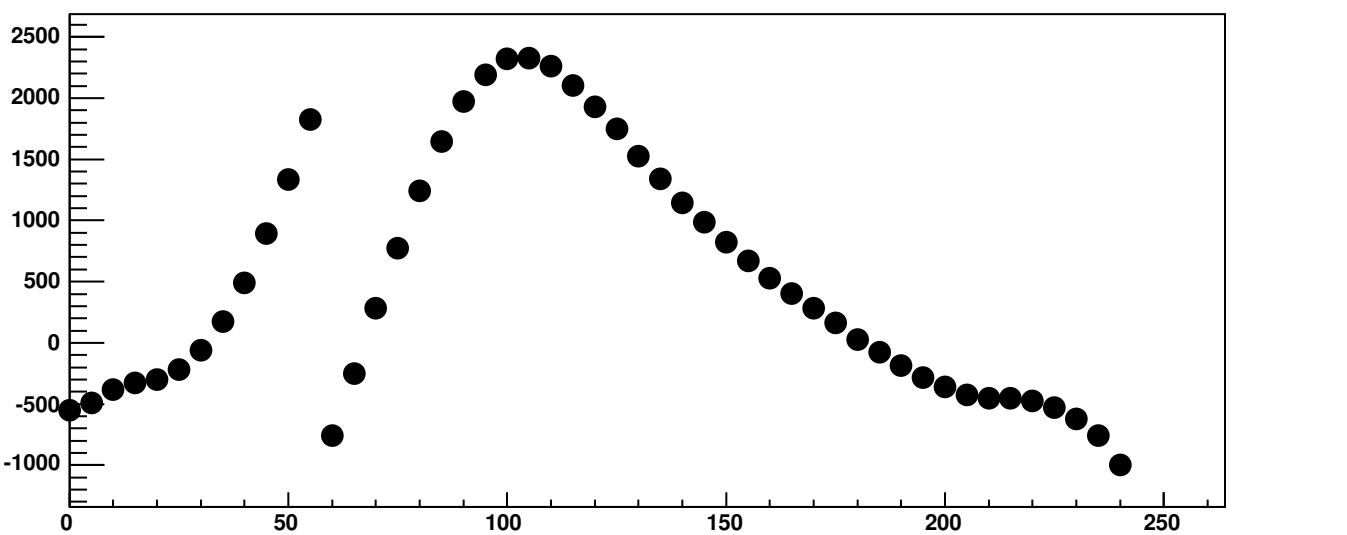
Chip 5, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

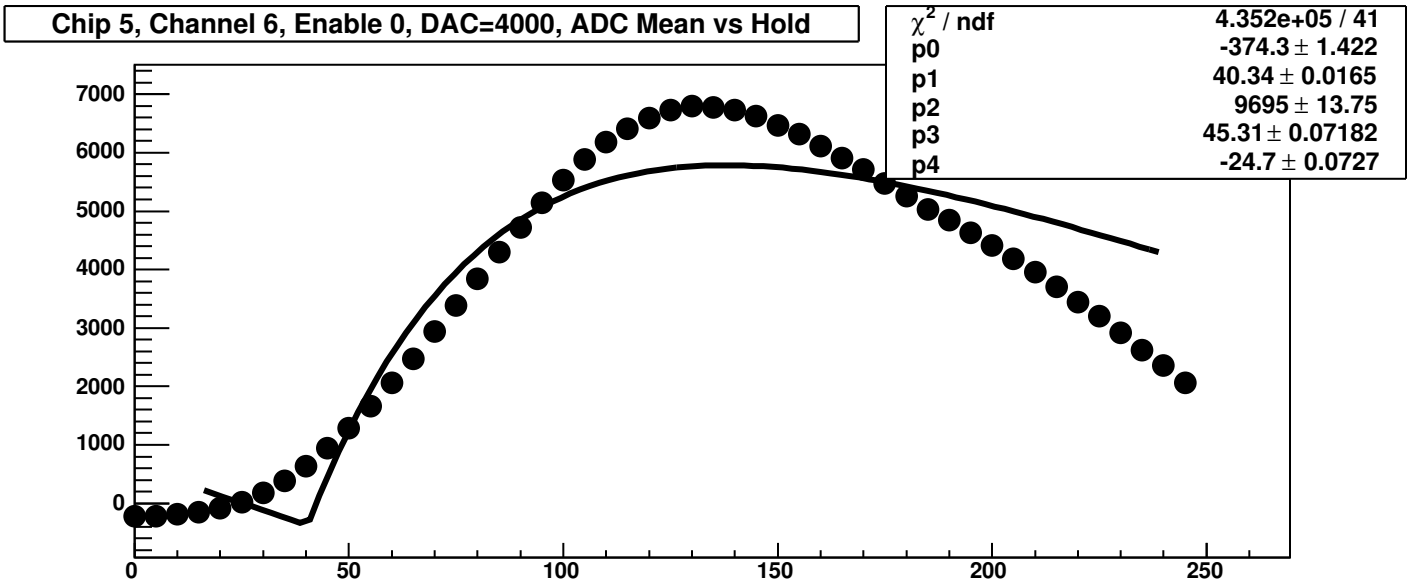


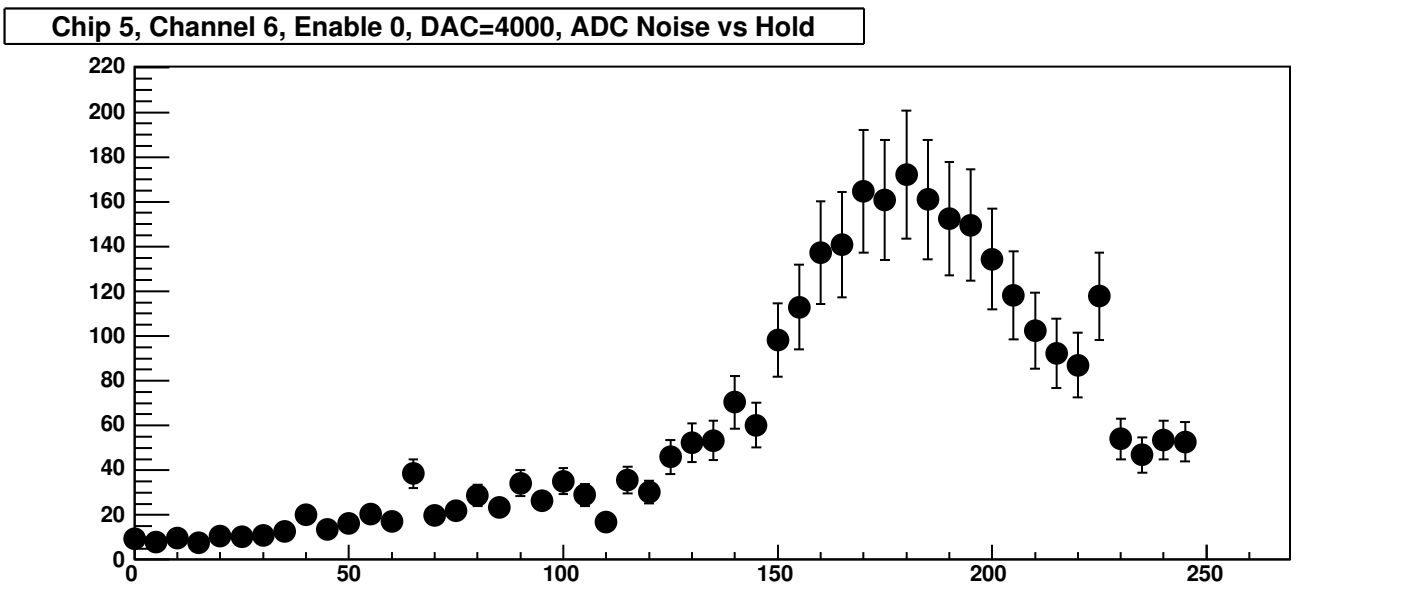
Chip 5, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold

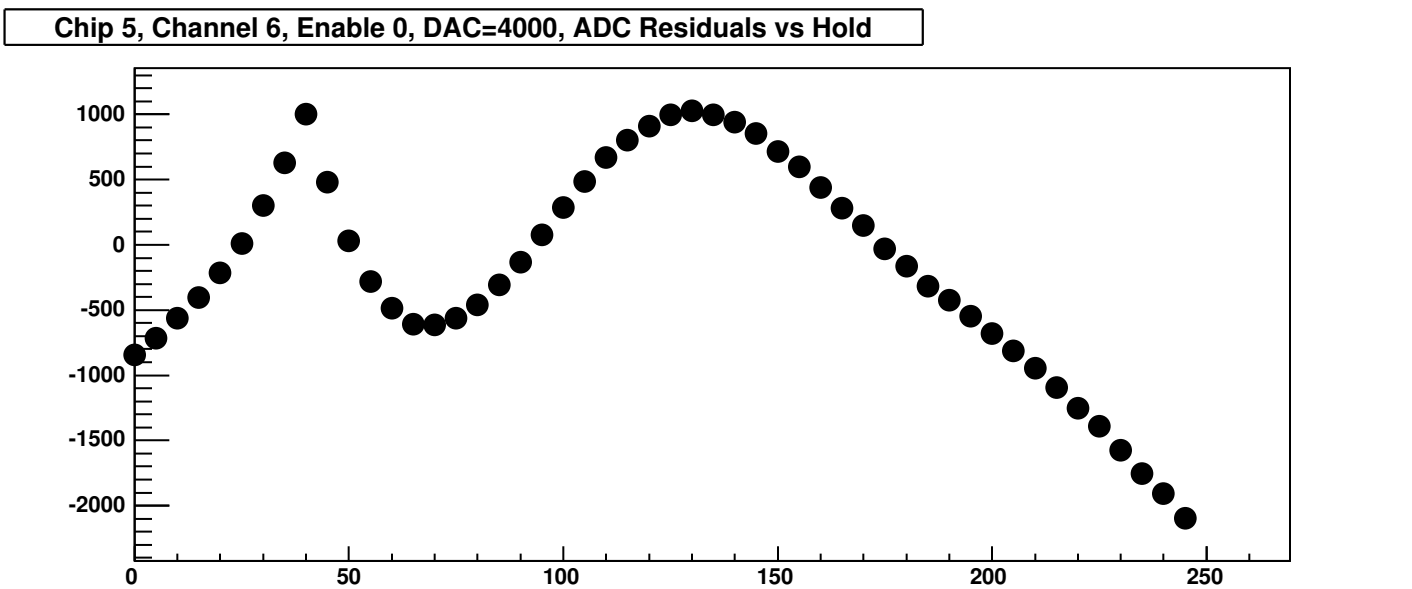


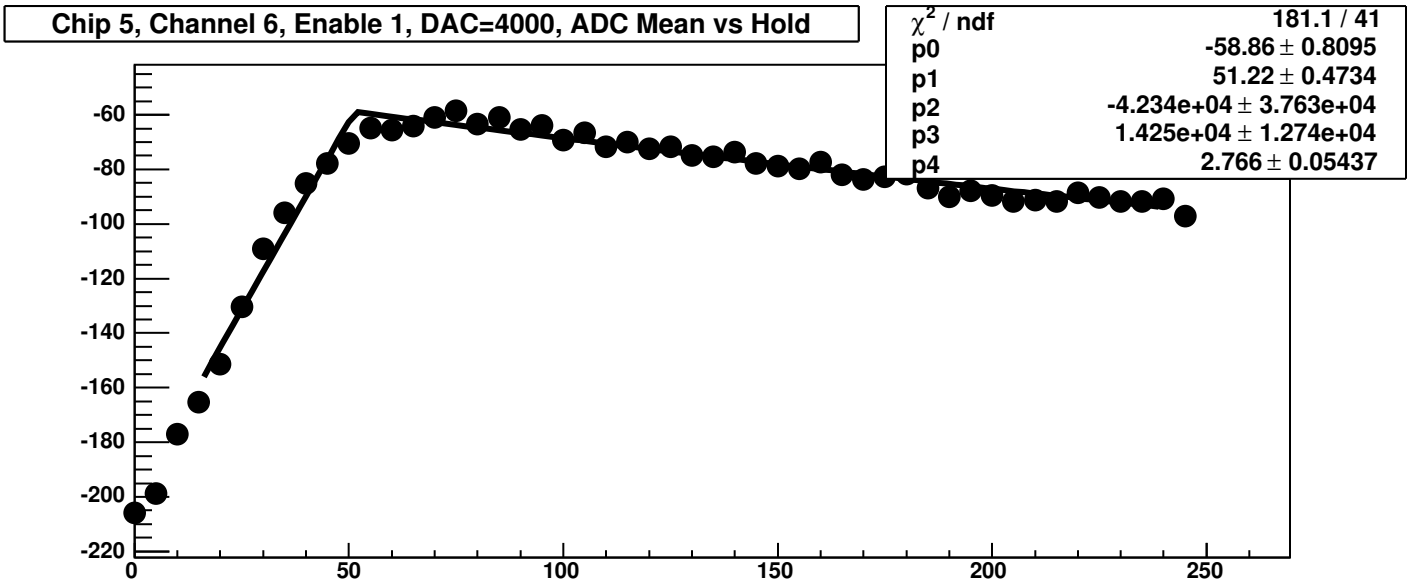
Chip 5, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

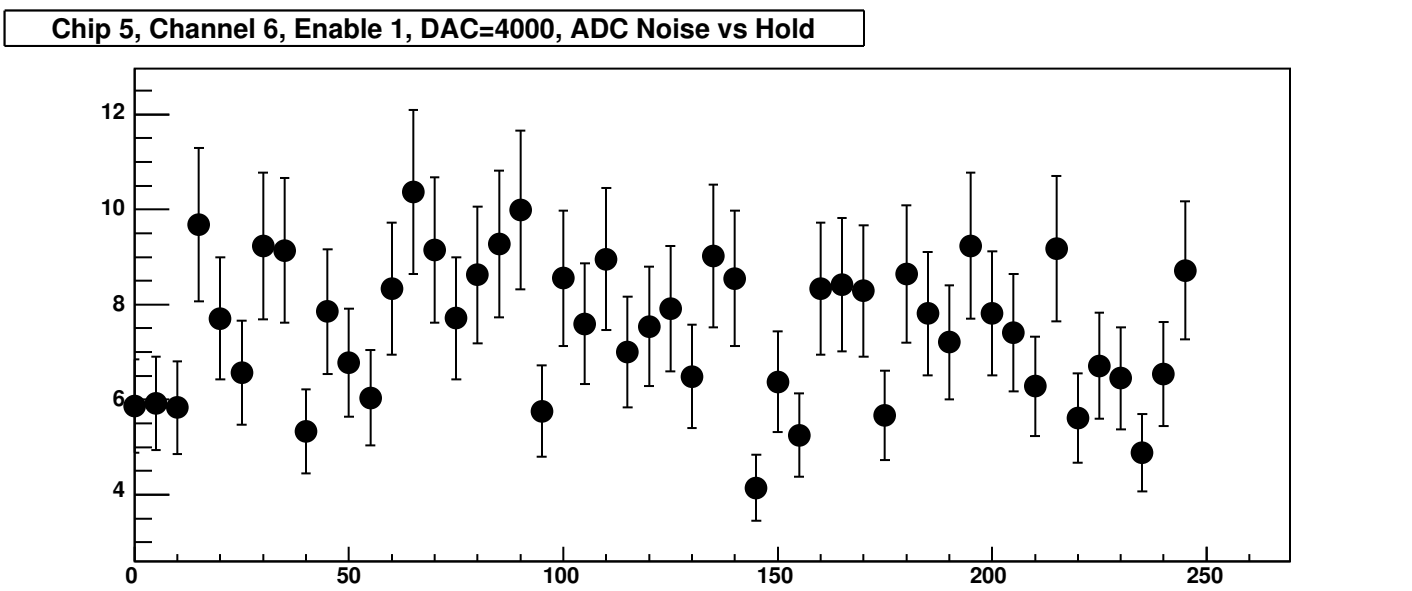


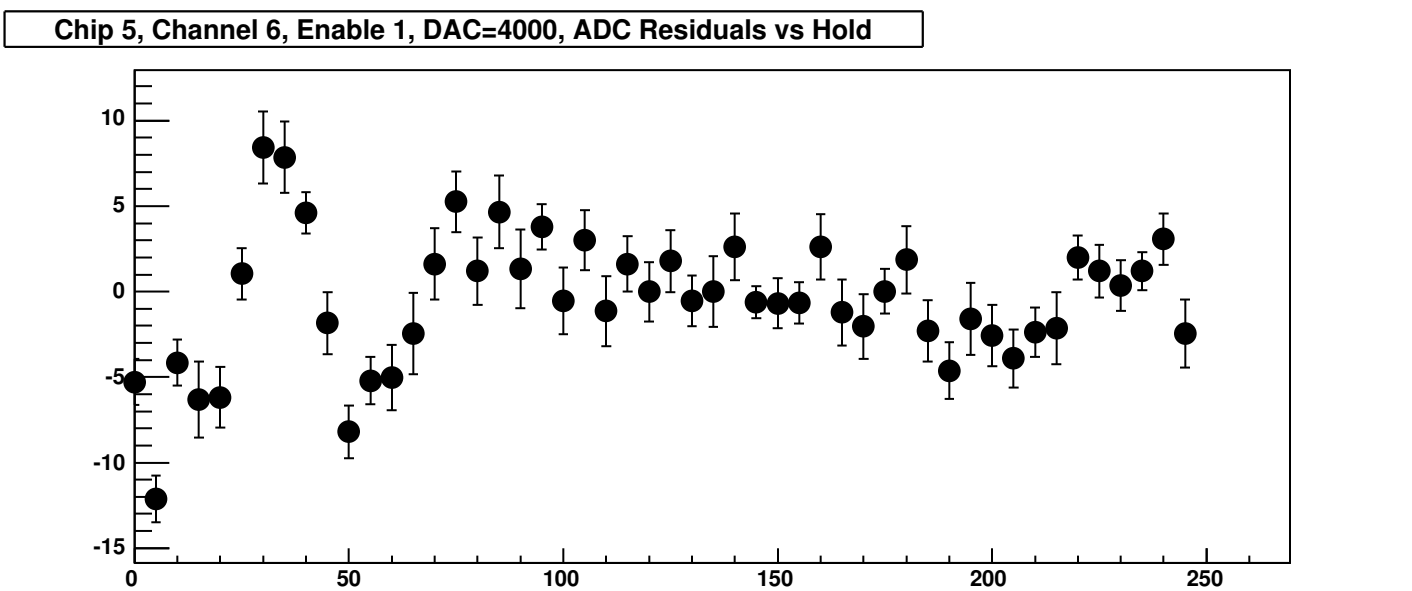

Chip 5, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.352e+05 / 41
p0 -374.3 ± 1.422
p1 40.34 ± 0.0165
p2 9695 ± 13.75
p3 45.31 ± 0.07182
p4 -24.7 ± 0.0727
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 5, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

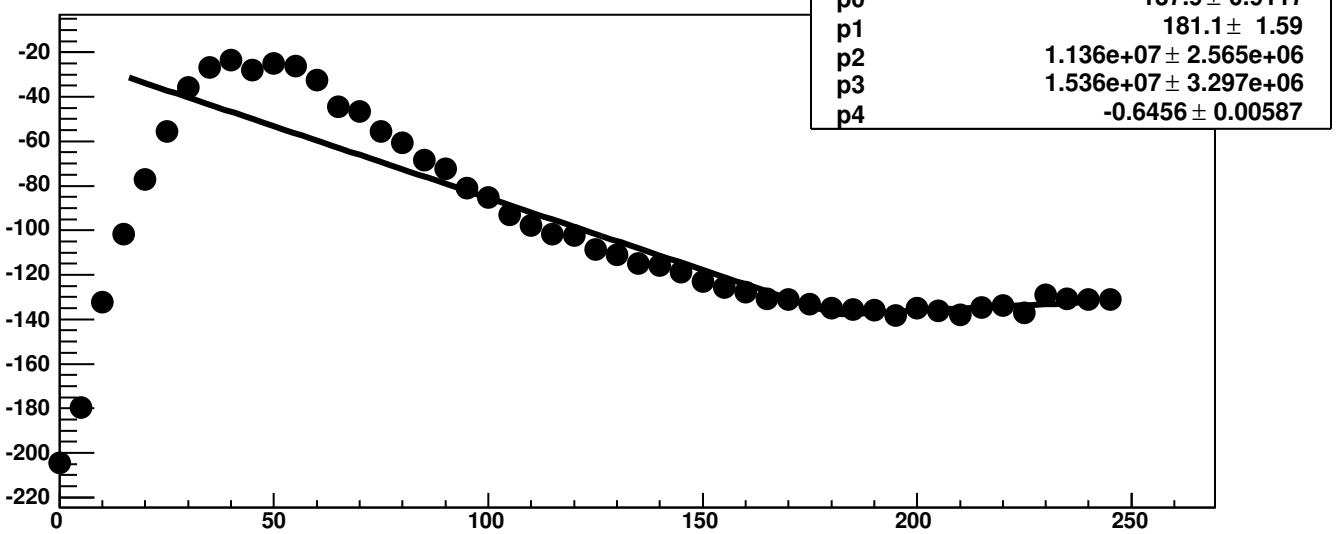

Chip 5, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250


Chip 5, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 181.1 / 41
p0 -58.86 ± 0.8095
p1 51.22 ± 0.4734
p2 -4.234e+04 ± 3.763e+04
p3 1.425e+04 ± 1.274e+04
p4 2.766 ± 0.05437
-60
-80
-100
-120
-140
-160
-180
-200
-220
0
50
100
150
200
250

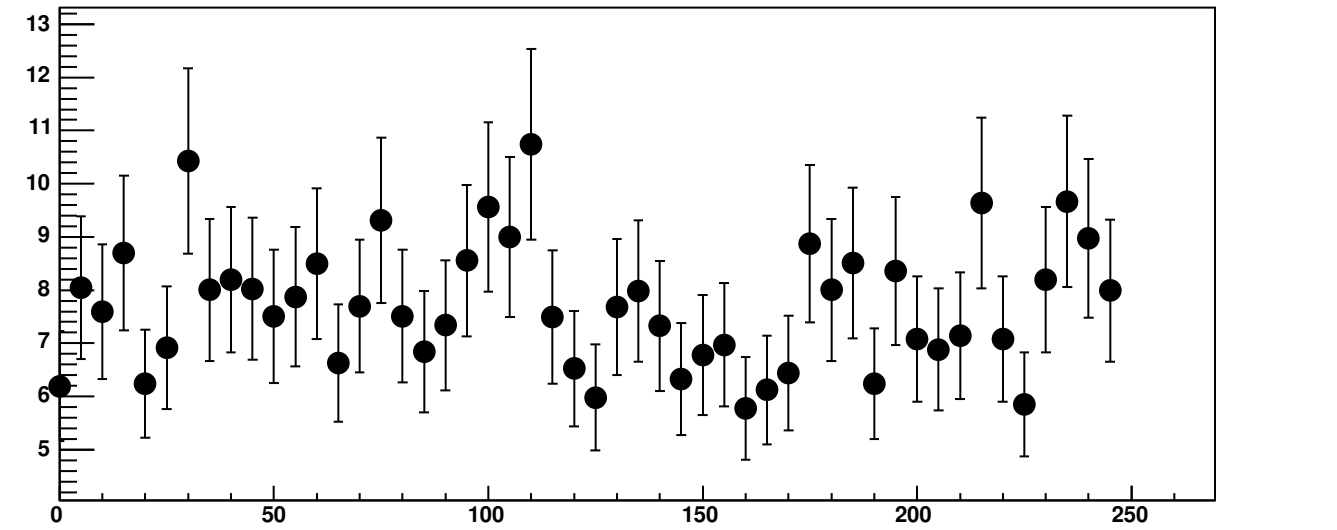

Chip 5, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 5, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
0
50
100
150
200
250

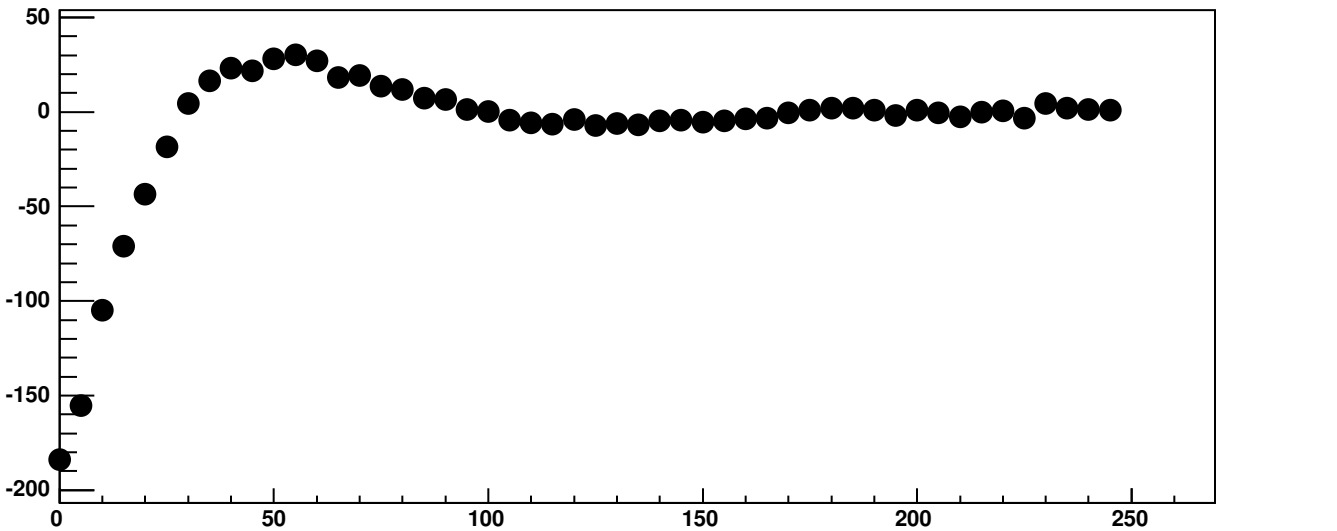
Chip 5, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold



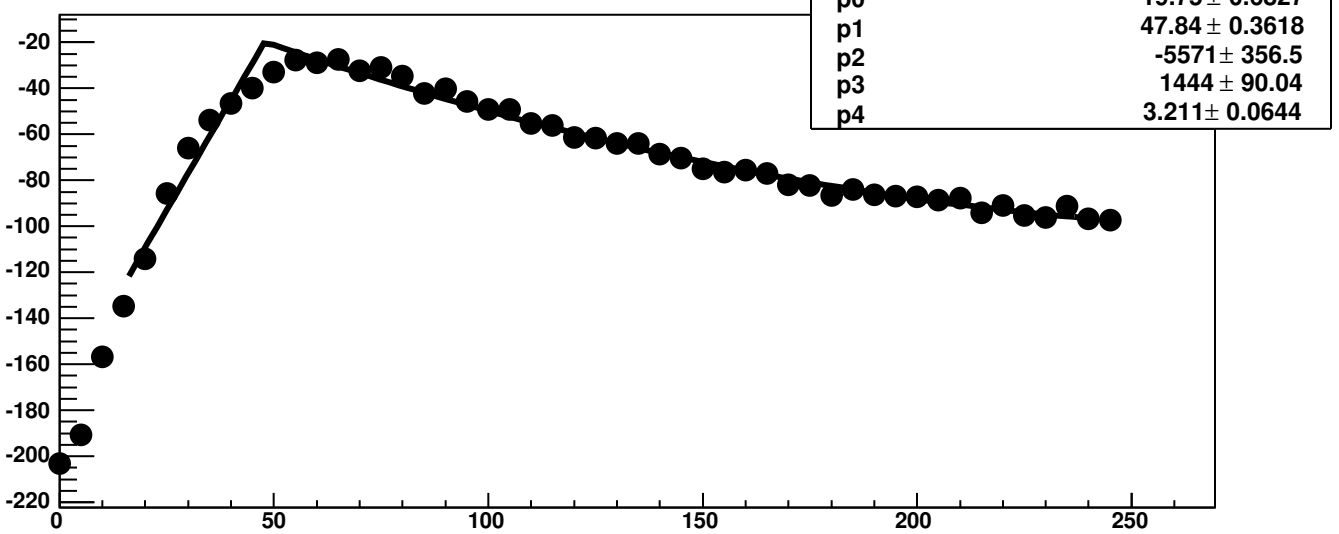
Chip 5, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold



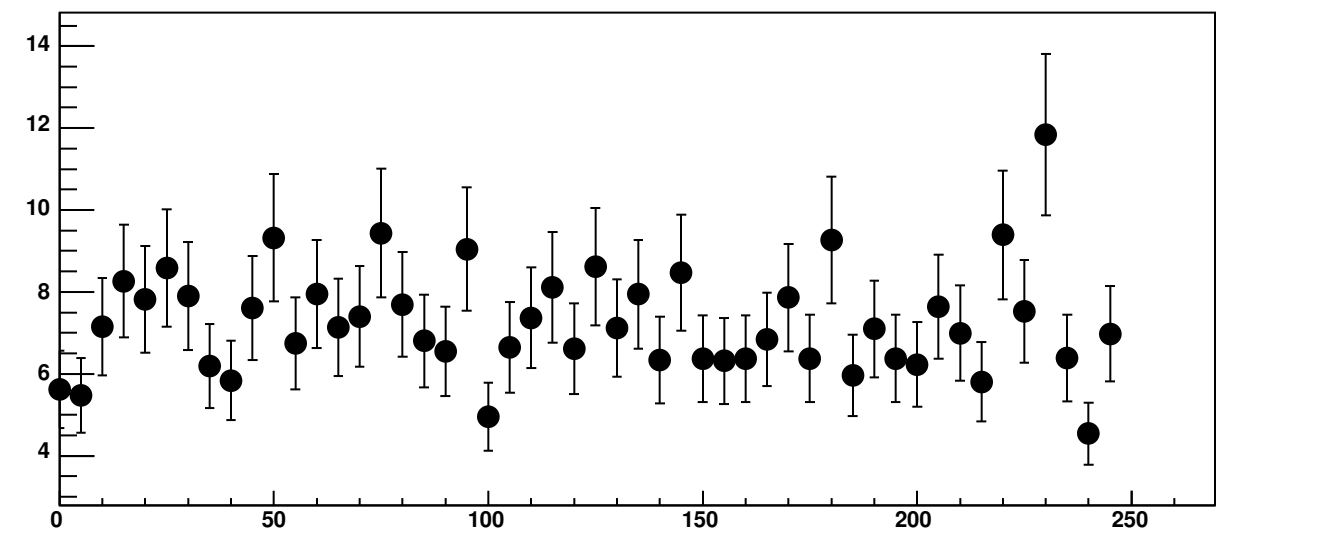
Chip 5, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold



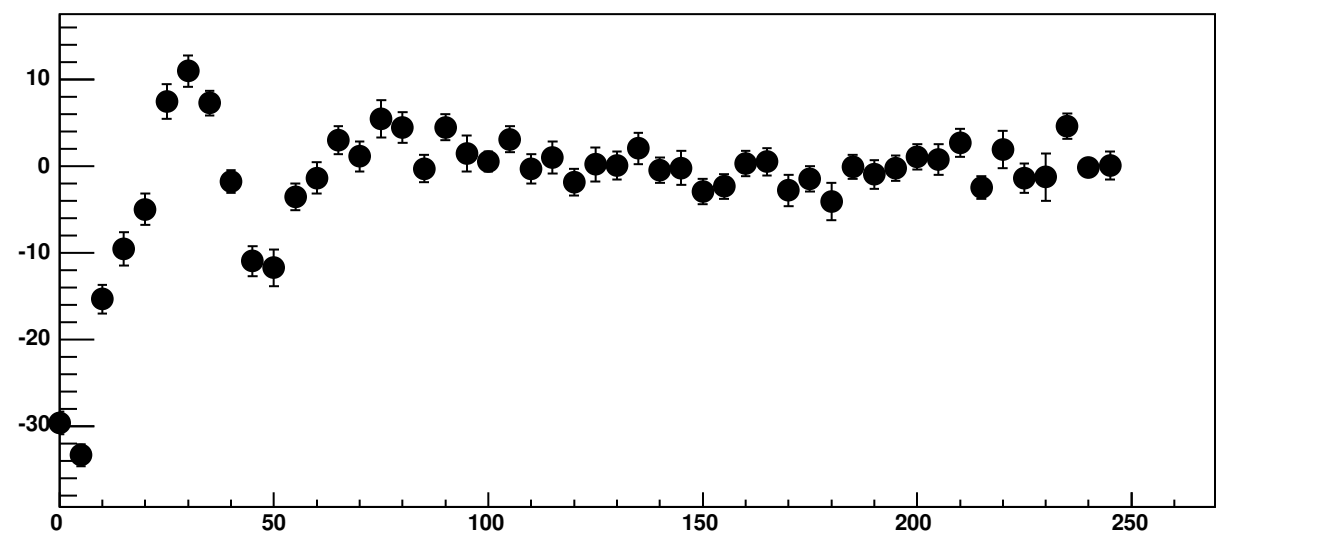
Chip 5, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold



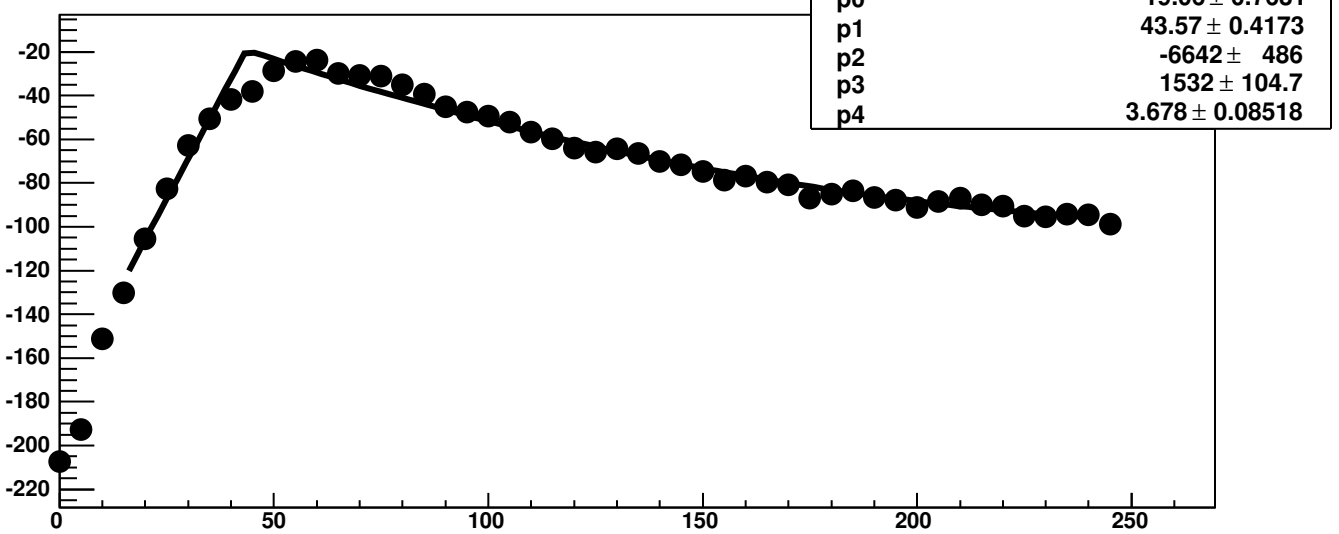
Chip 5, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



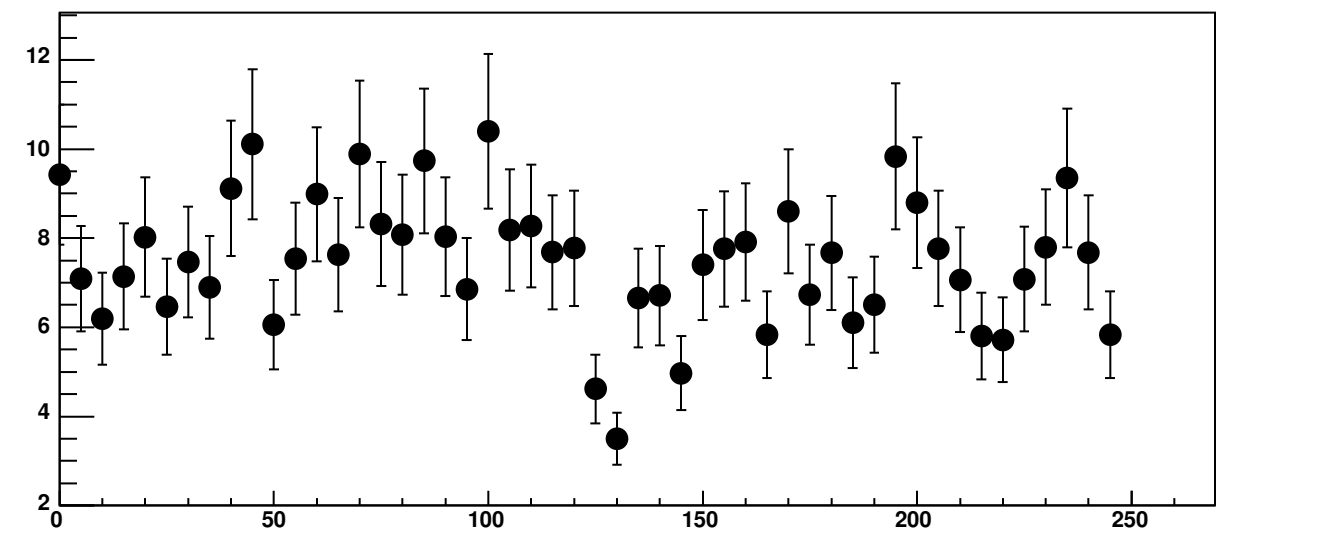
Chip 5, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold



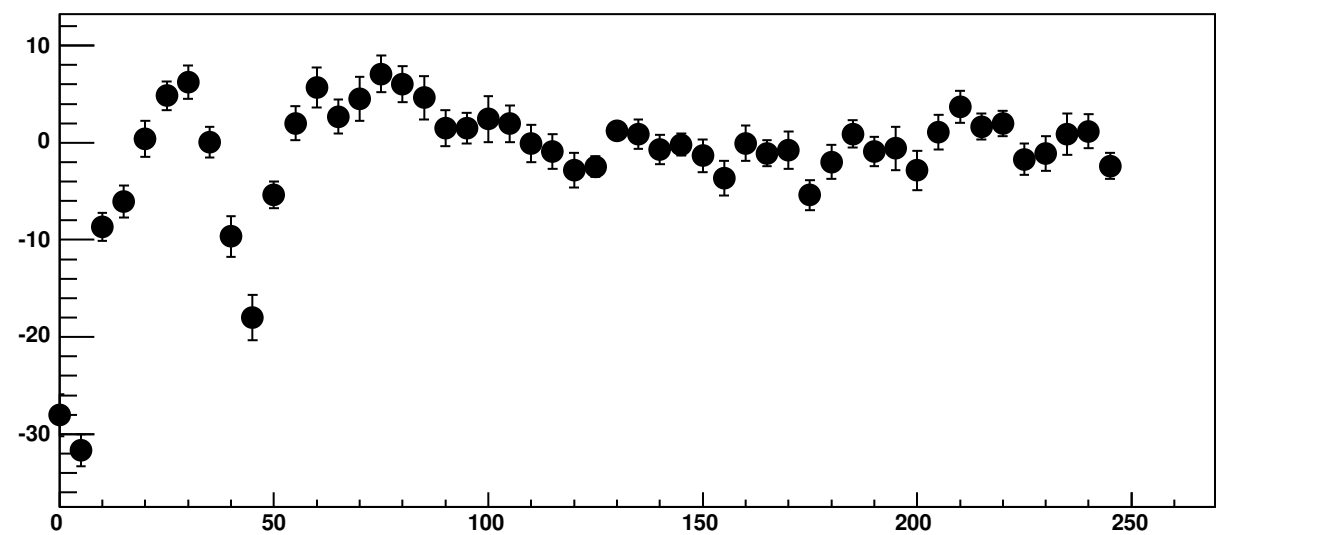
Chip 5, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold



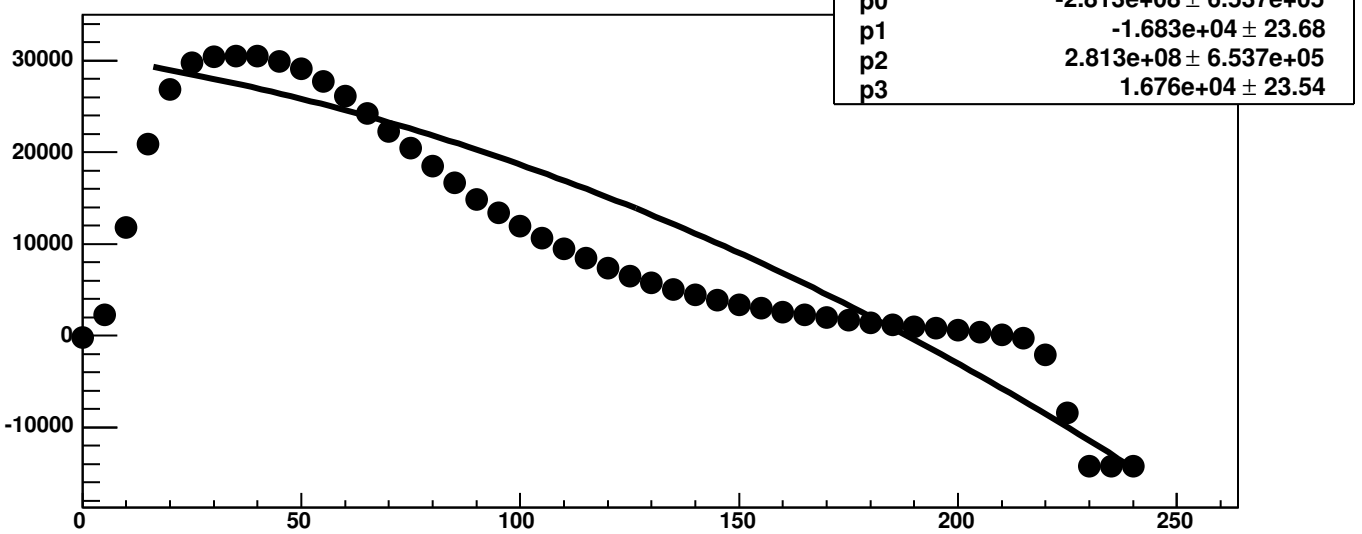
Chip 5, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



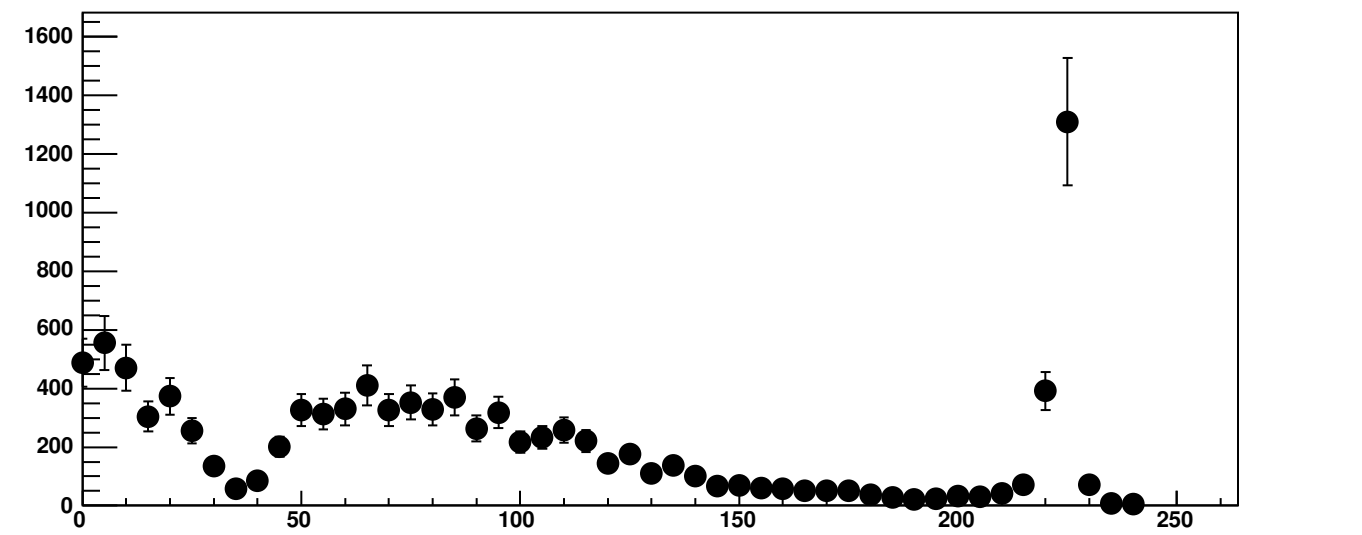
Chip 5, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold



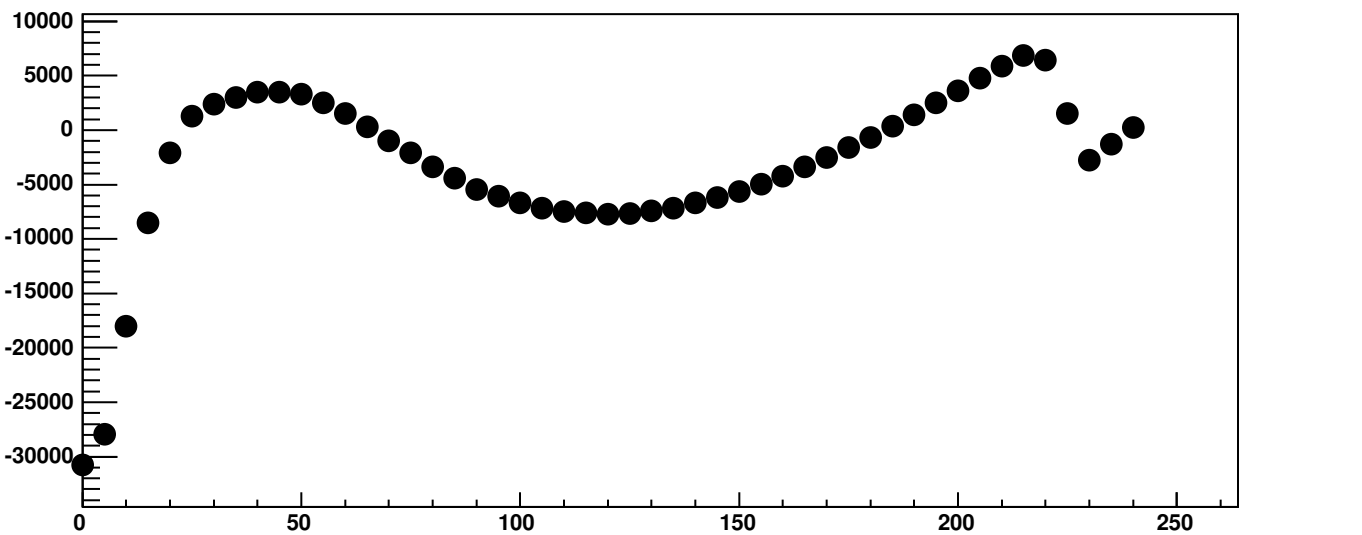
Chip 5, Channel 6, Enable 5!, DAC=4000, ADC Mean vs Hold

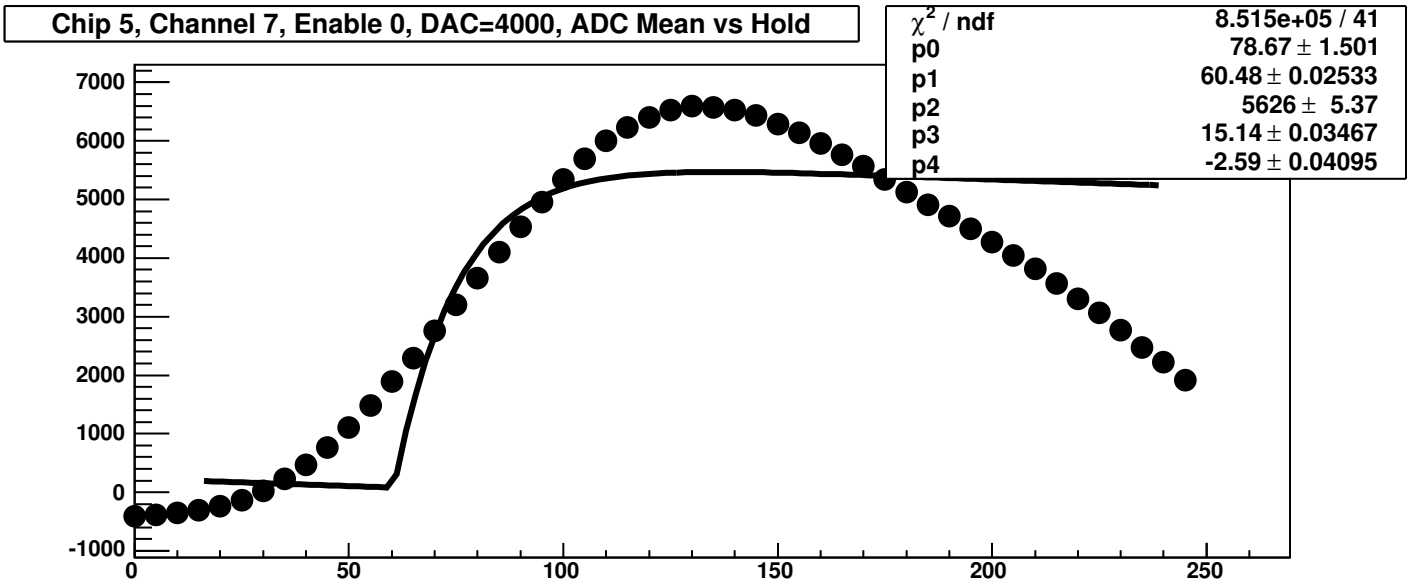


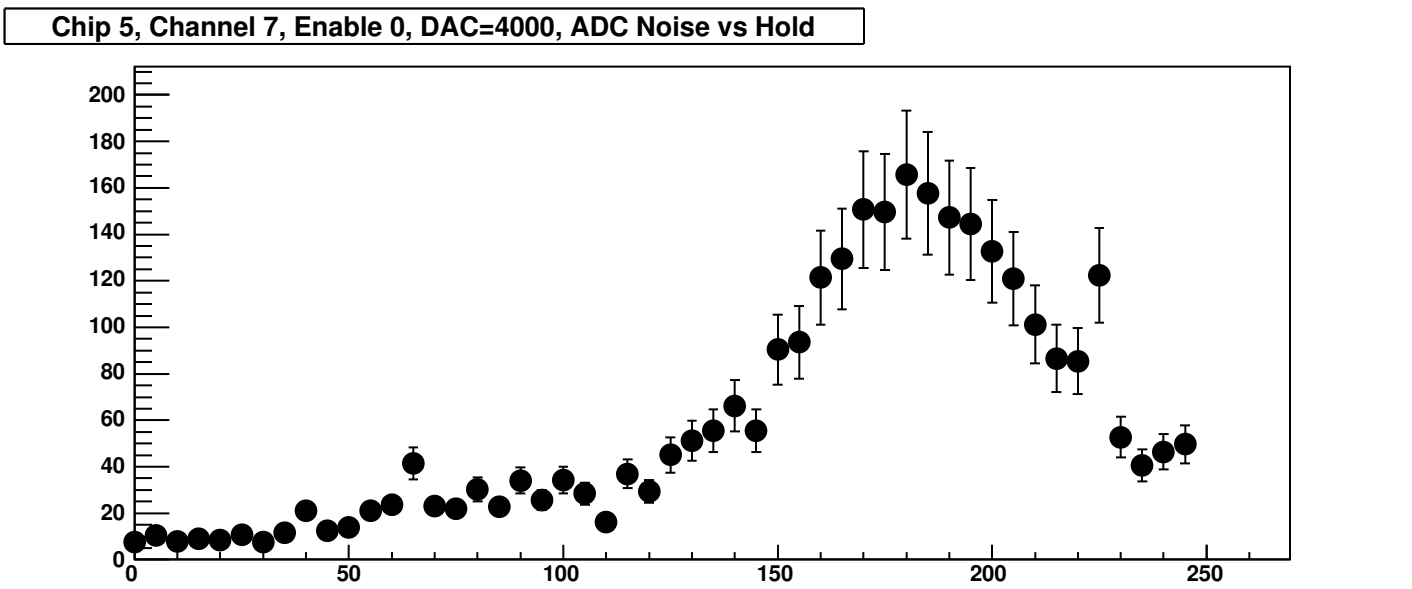
Chip 5, Channel 6, Enable 5!, DAC=4000, ADC Noise vs Hold

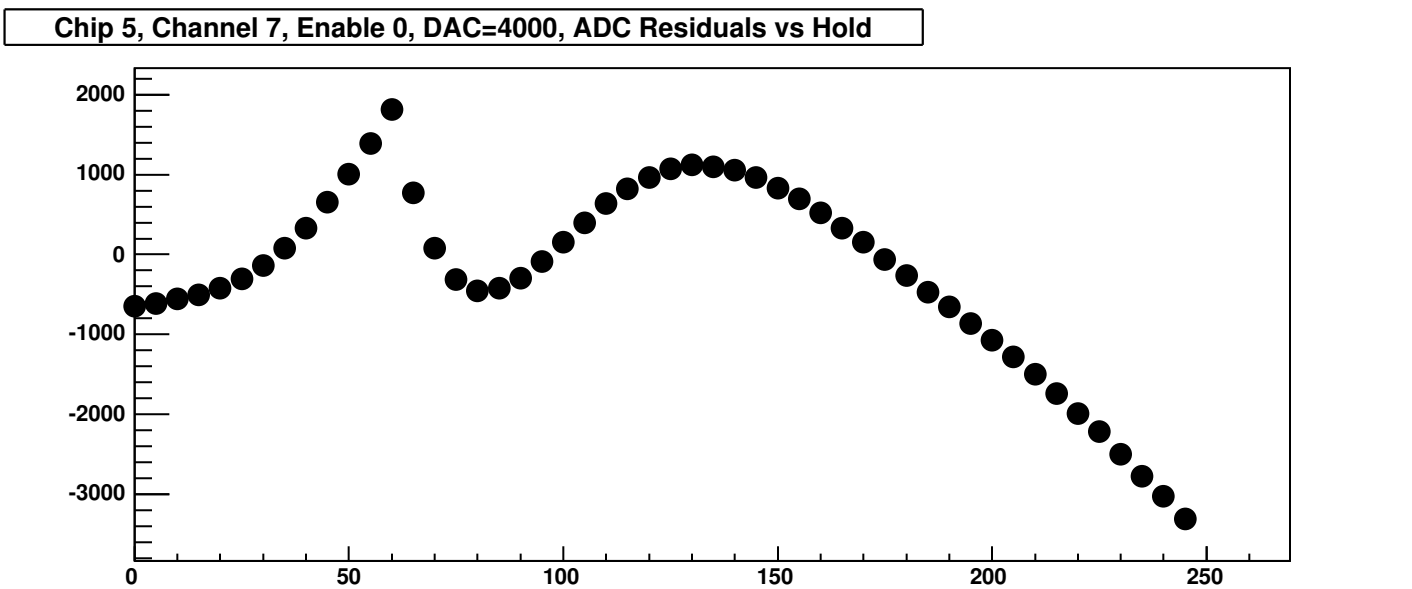


Chip 5, Channel 6, Enable 5!, DAC=4000, ADC Residuals vs Hold

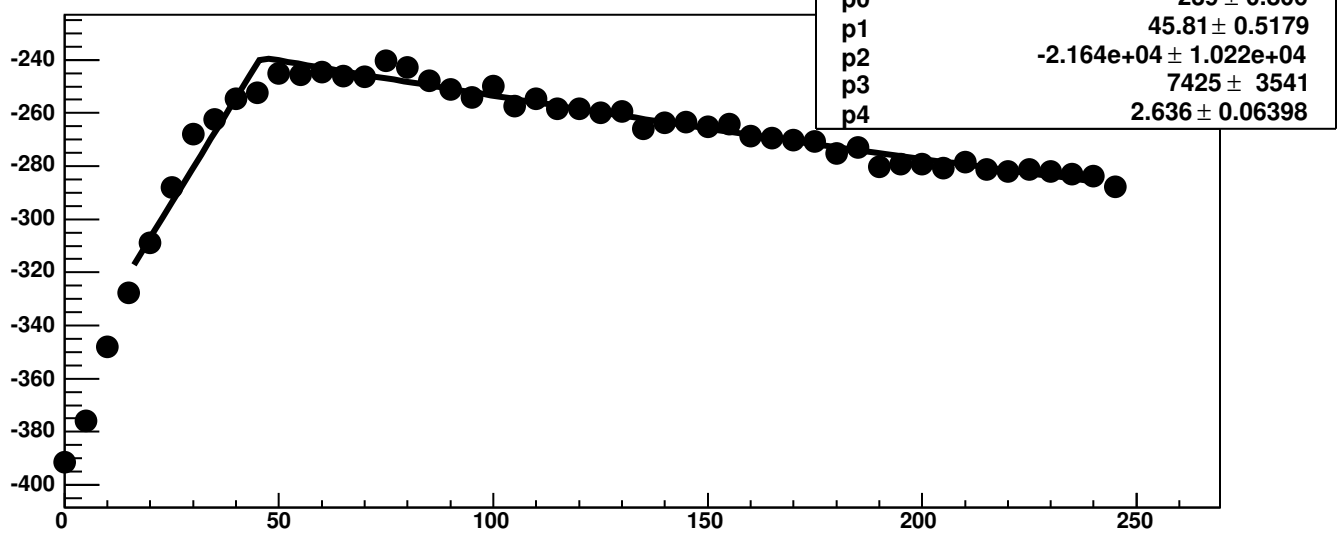



Chip 5, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 8.515e+05 / 41
p0 78.67 ± 1.501
p1 60.48 ± 0.02533
p2 5626 ± 5.37
p3 15.14 ± 0.03467
p4 -2.59 ± 0.04095
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

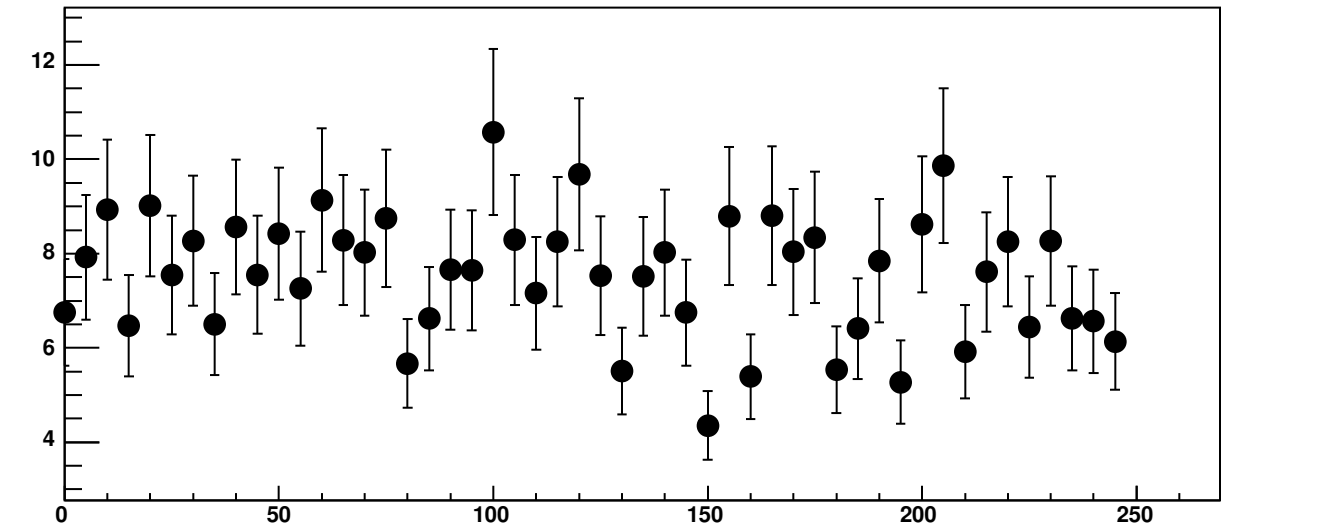

Chip 5, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 5, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

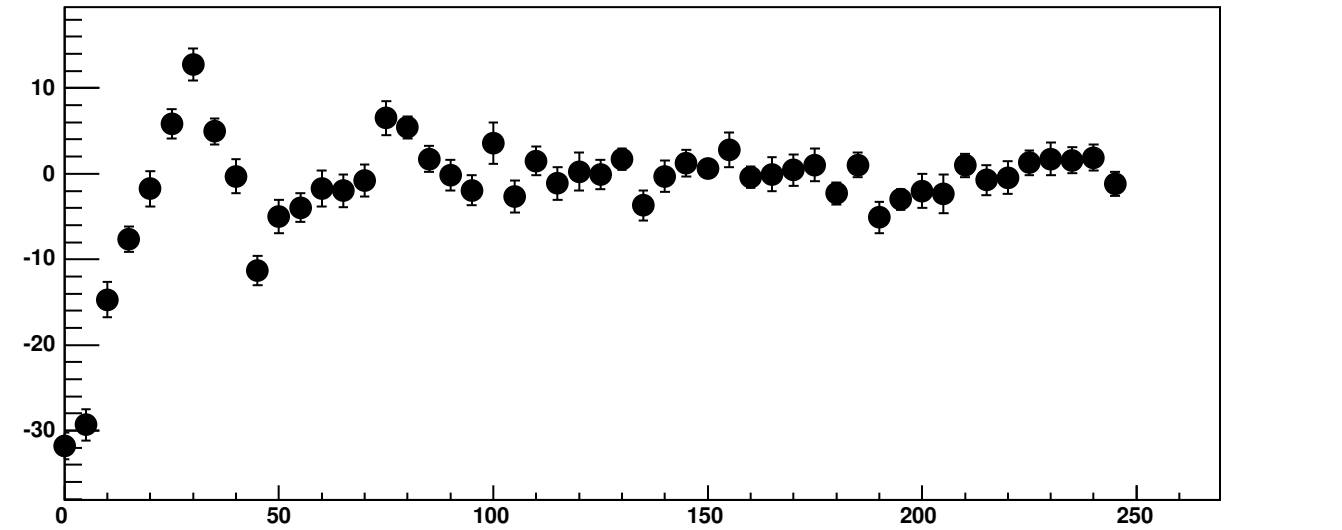
Chip 5, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold

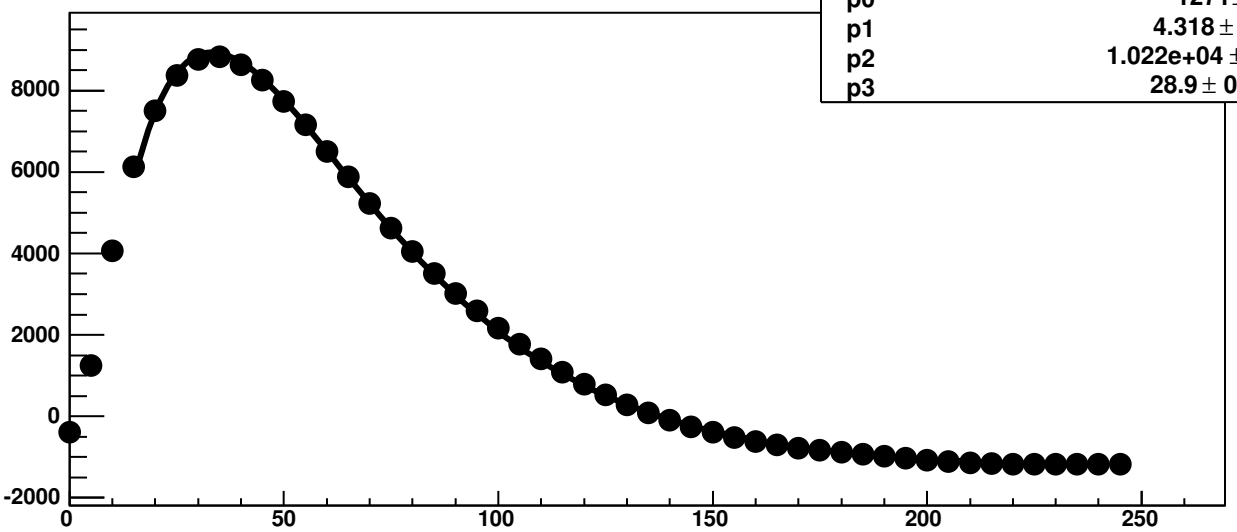


Chip 5, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

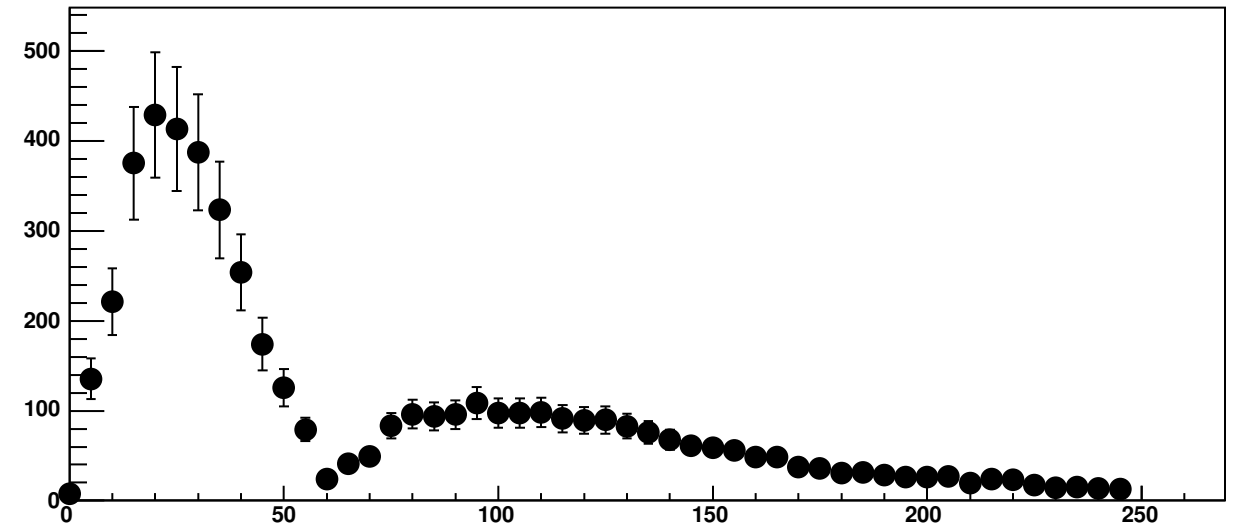


Chip 5, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold

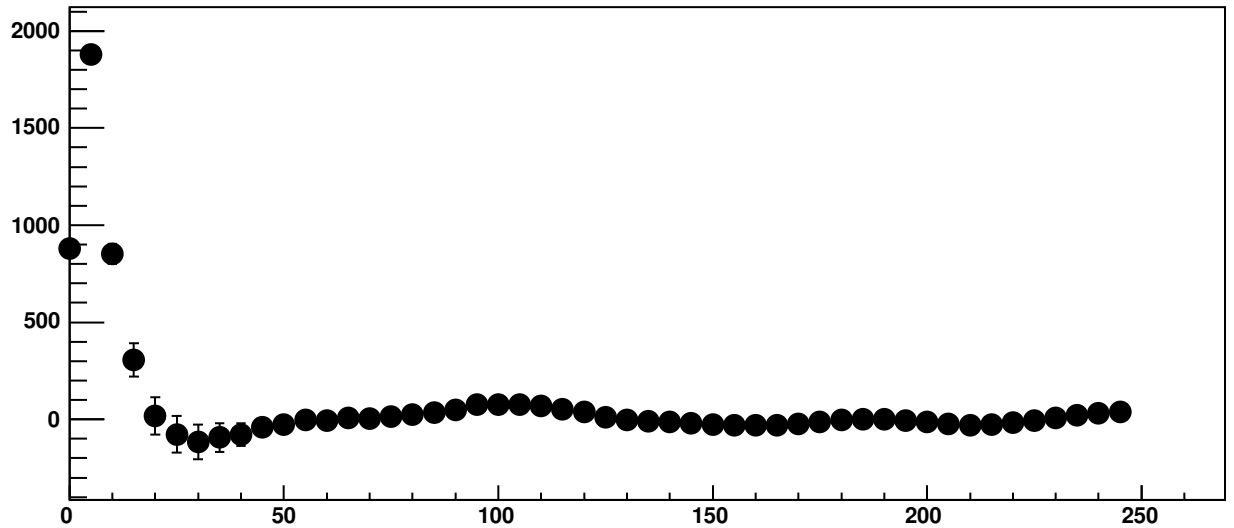
χ^2 / ndf	361.4 / 42
p0	-1271 ± 1.791
p1	4.318 ± 0.1381
p2	1.022e+04 ± 17.63
p3	28.9 ± 0.04662

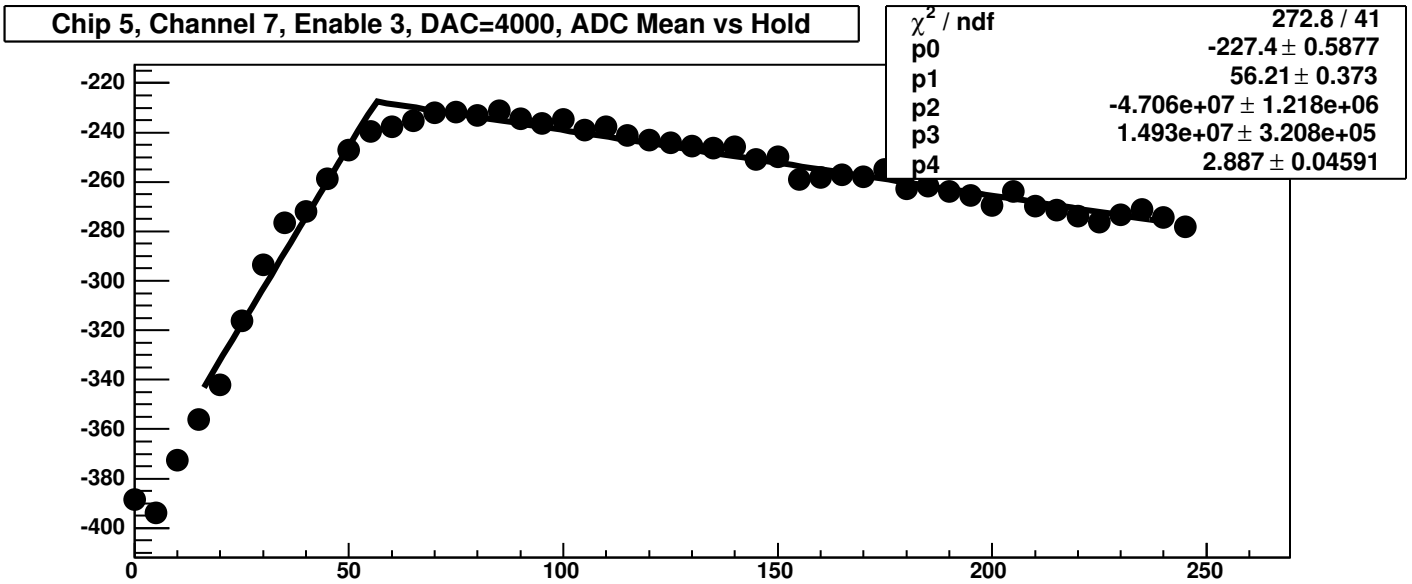


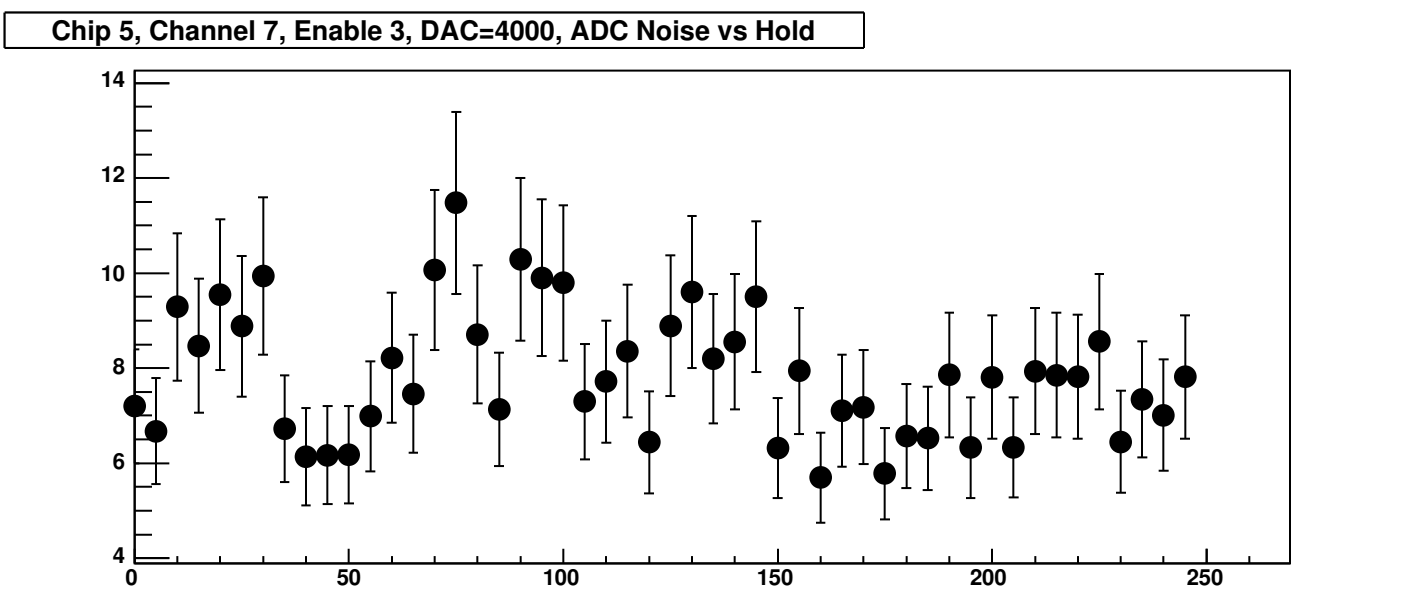
Chip 5, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold

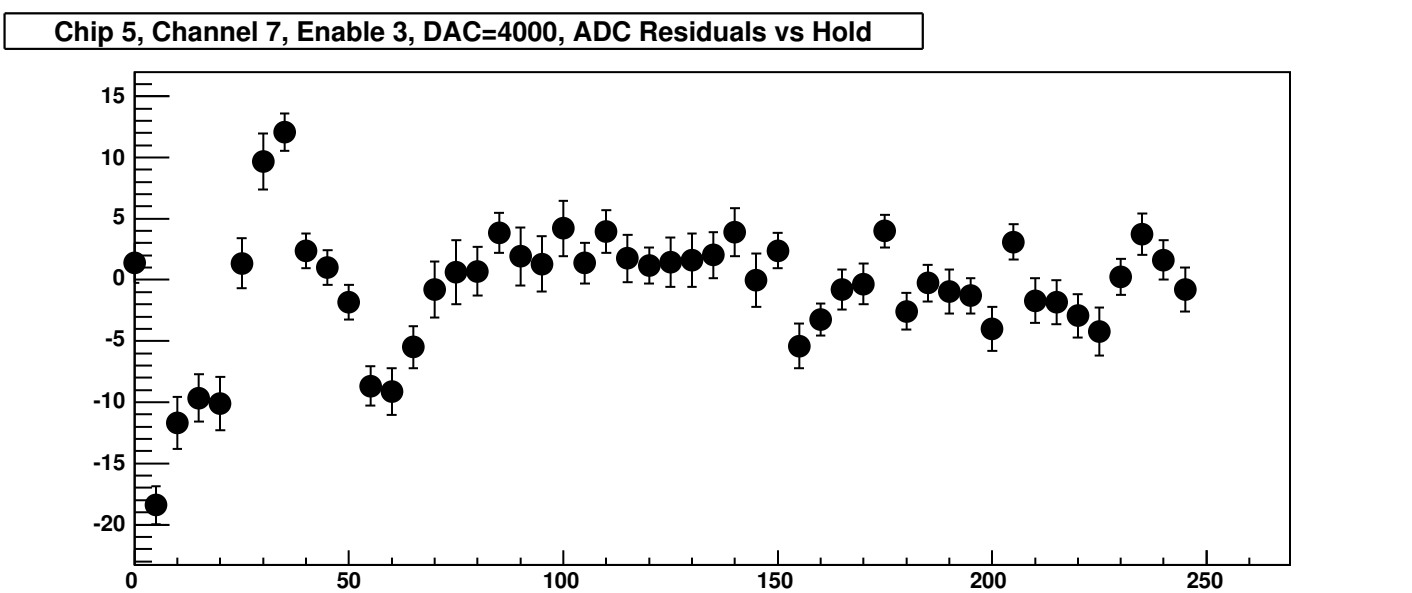


Chip 5, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold

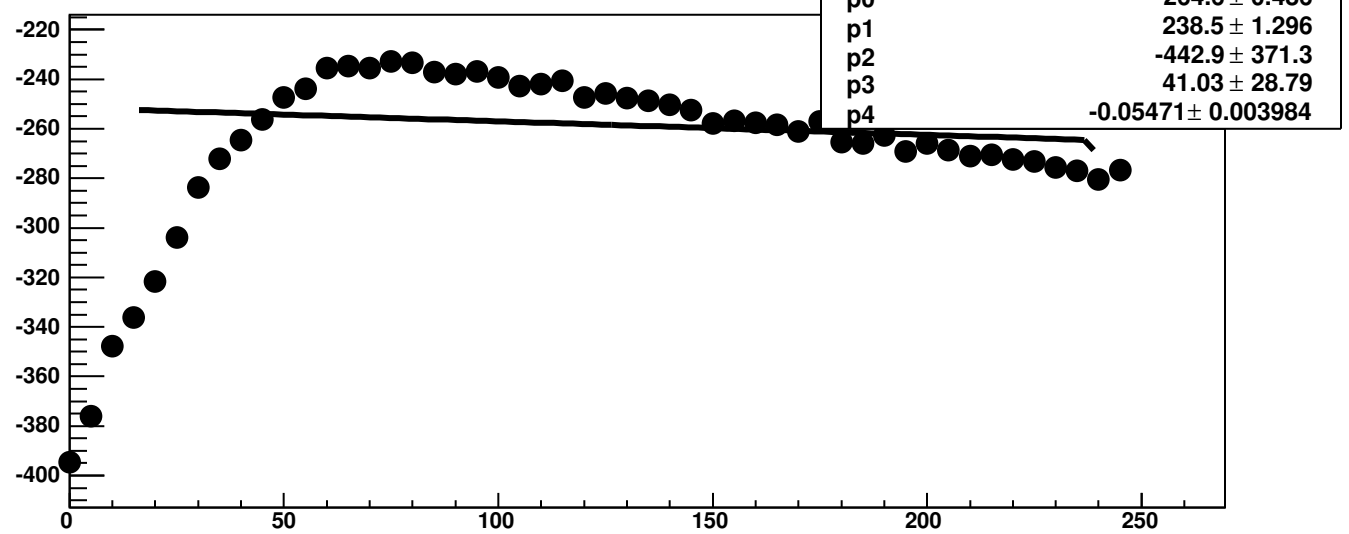



Chip 5, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 272.8 / 41
p0 -227.4 ± 0.5877
p1 56.21 ± 0.373
p2 -4.706e+07 ± 1.218e+06
p3 1.493e+07 ± 3.208e+05
p4 2.887 ± 0.04591
-220
-240
-260
-280
-300
-320
-340
-360
-380
-400
0
50
100
150
200
250

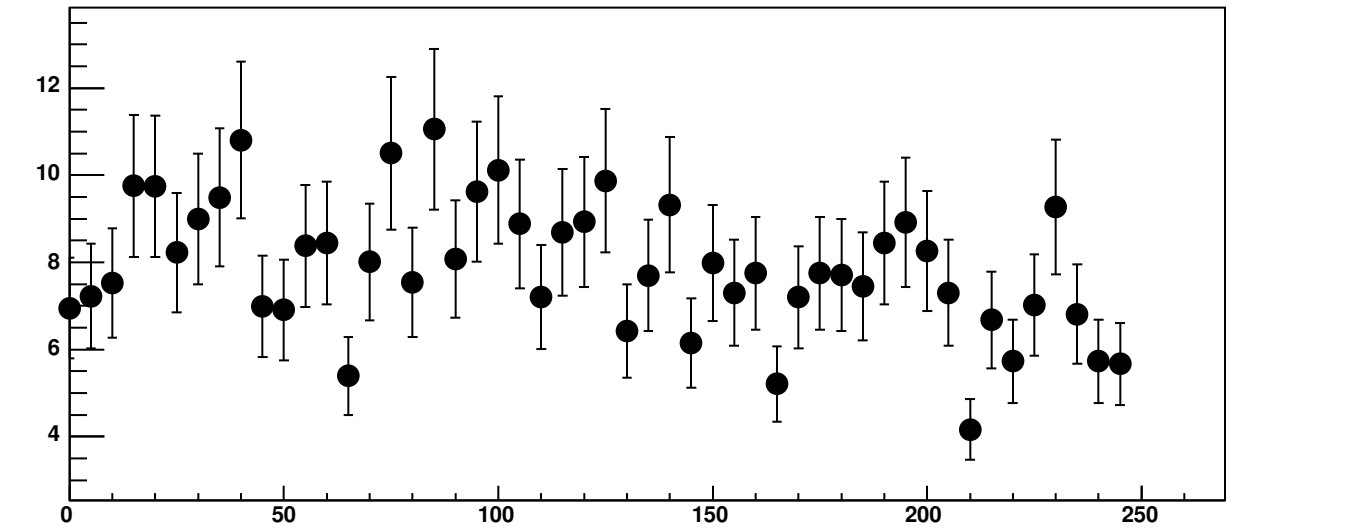

Chip 5, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 5, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
-15
-20
0
50
100
150
200
250

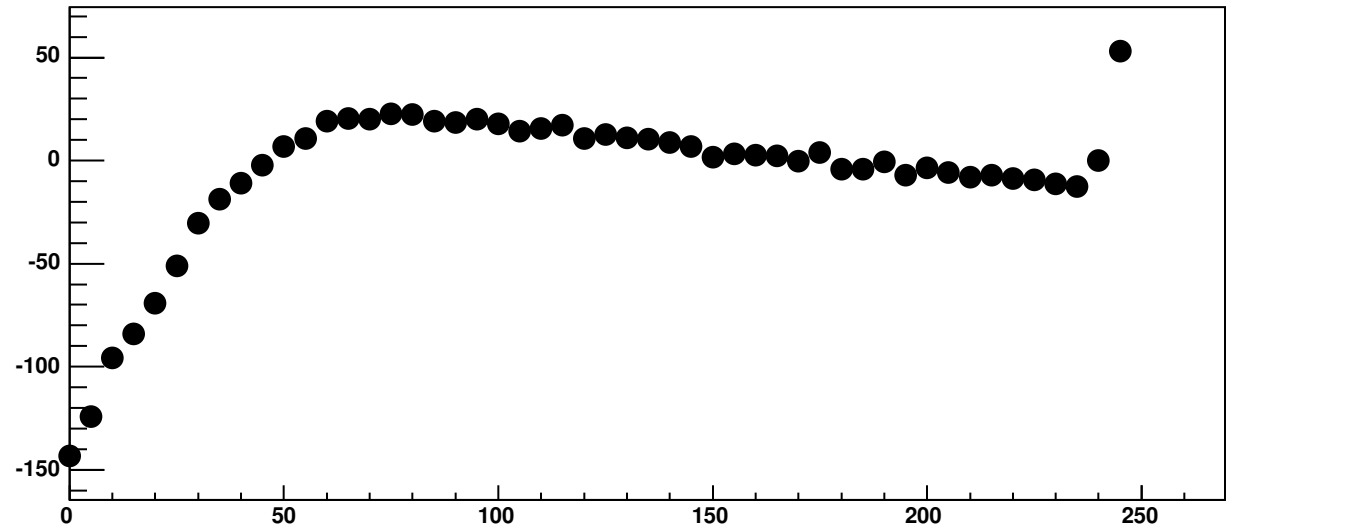
Chip 5, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

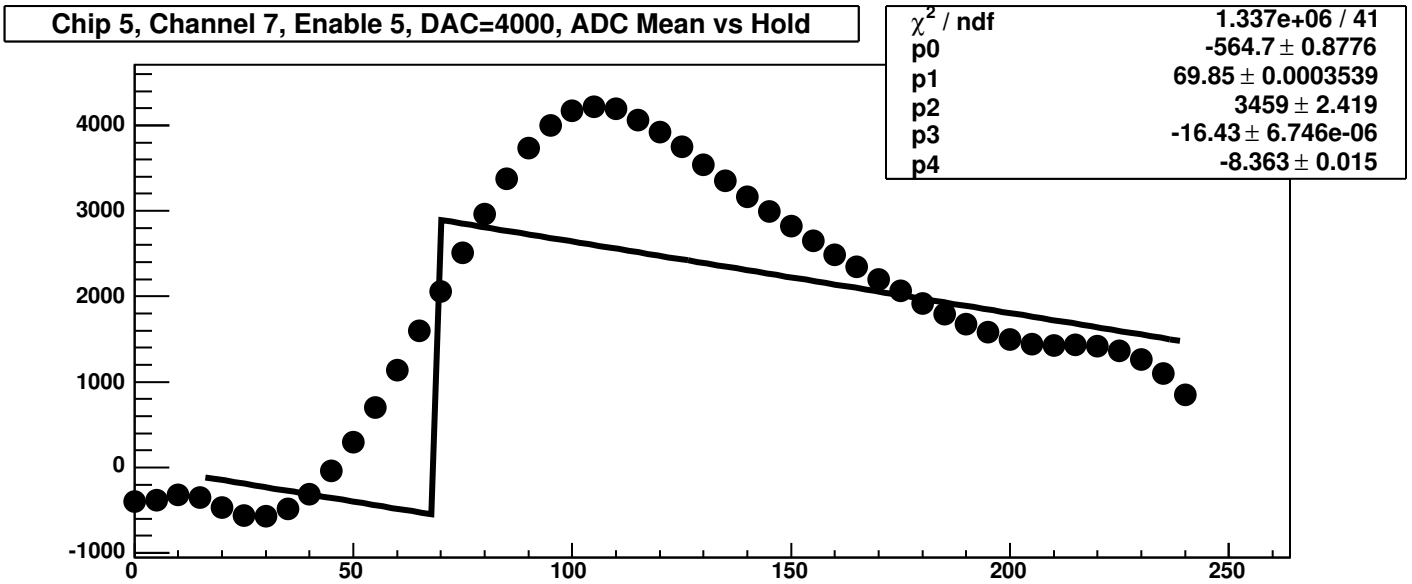


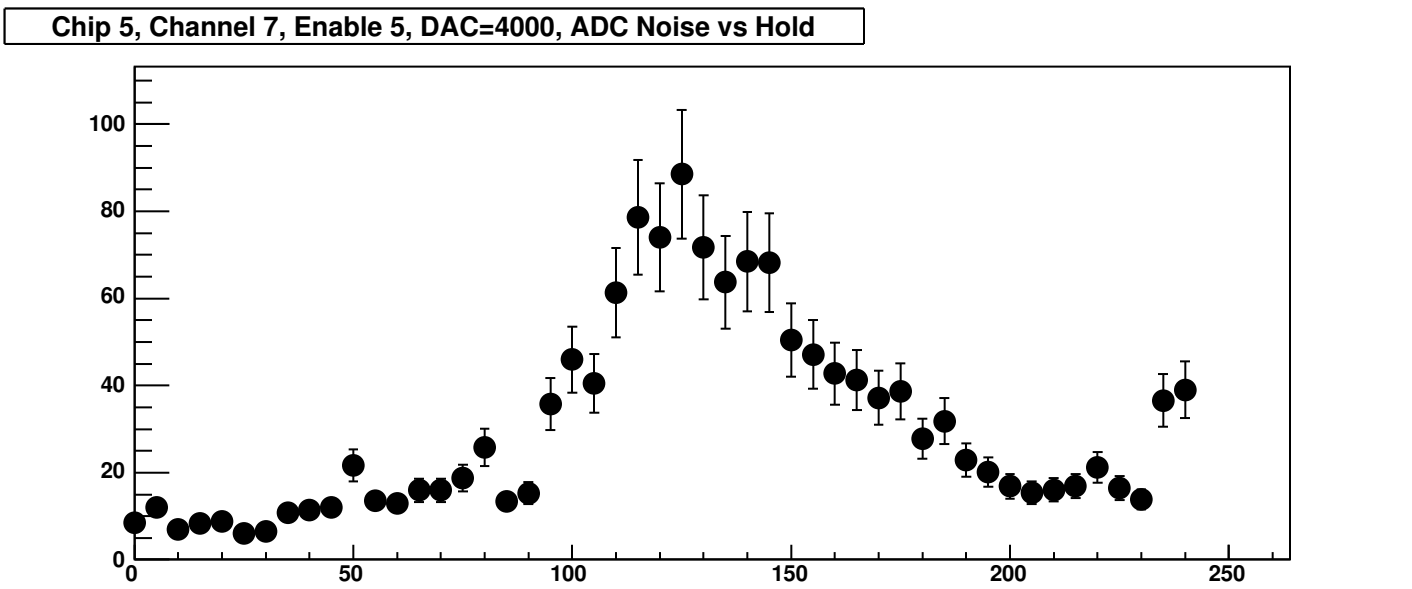
Chip 5, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold

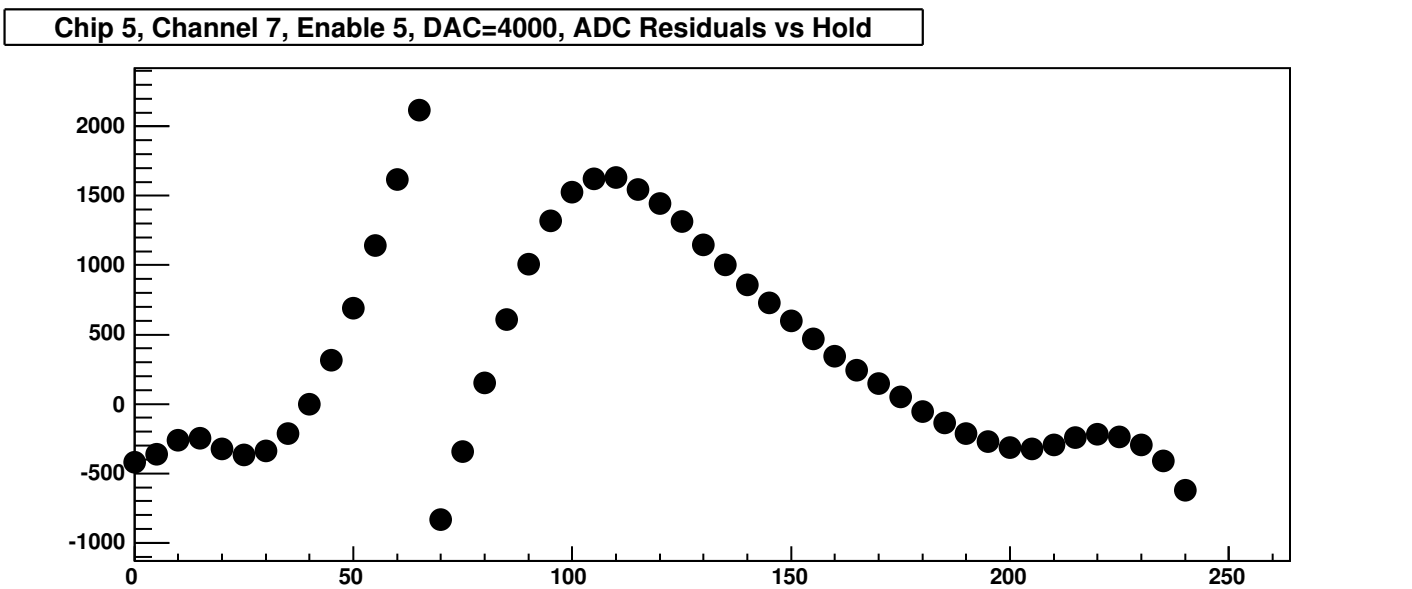


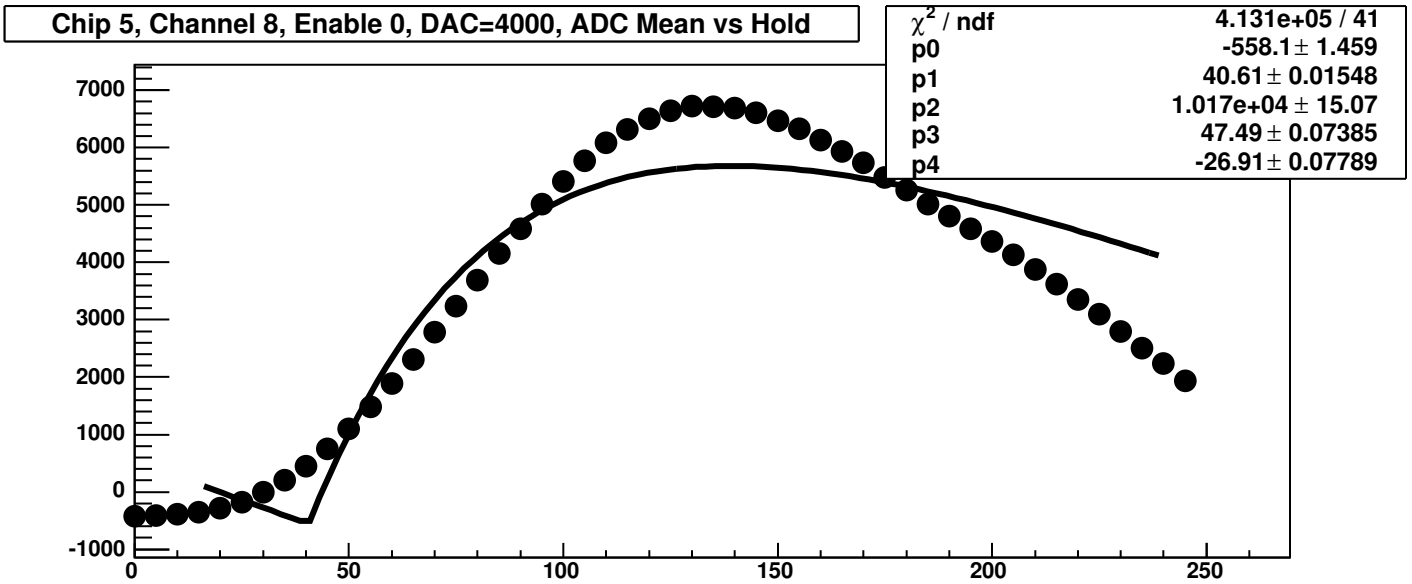
Chip 5, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

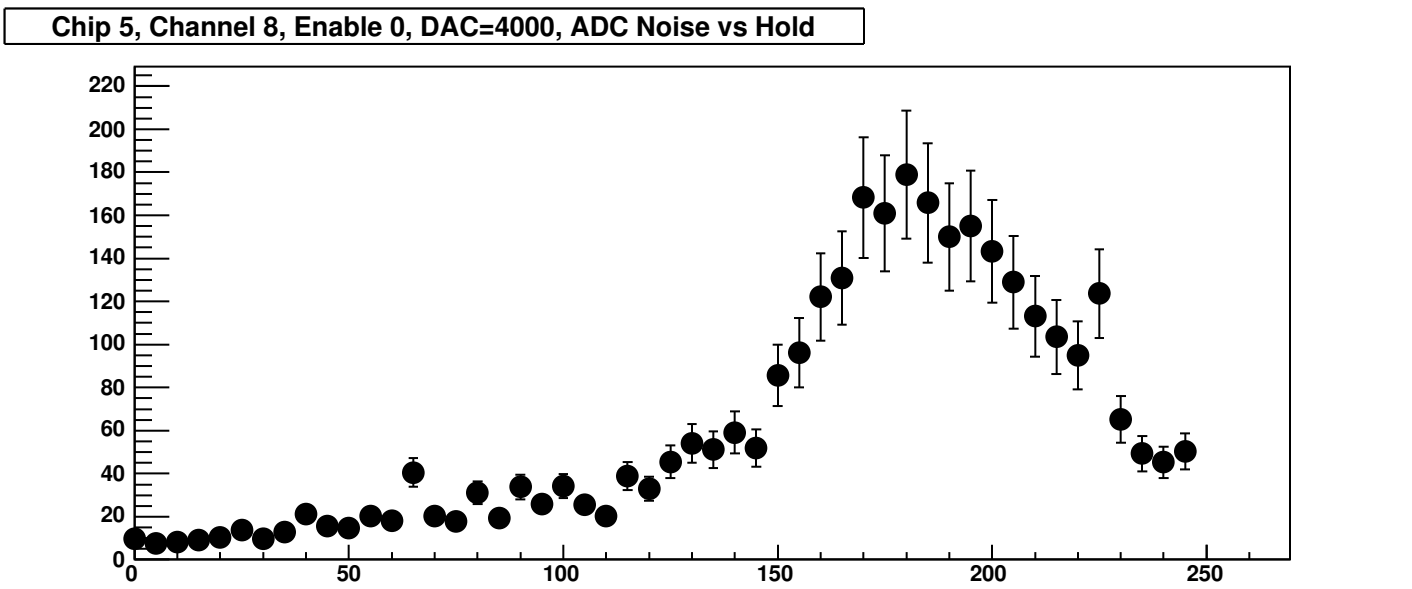


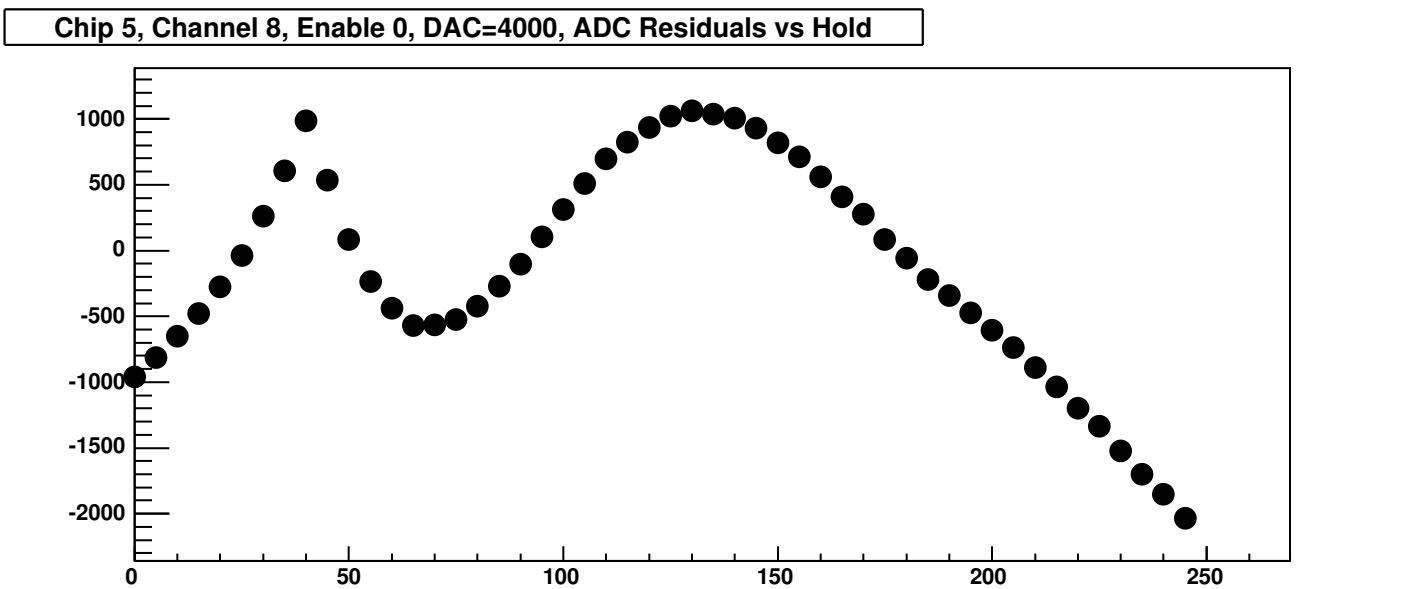

Chip 5, Channel 7, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.337e+06 / 41
p0 -564.7 ± 0.8776
p1 69.85 ± 0.0003539
p2 3459 ± 2.419
p3 -16.43 ± 6.746e-06
p4 -8.363 ± 0.015
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

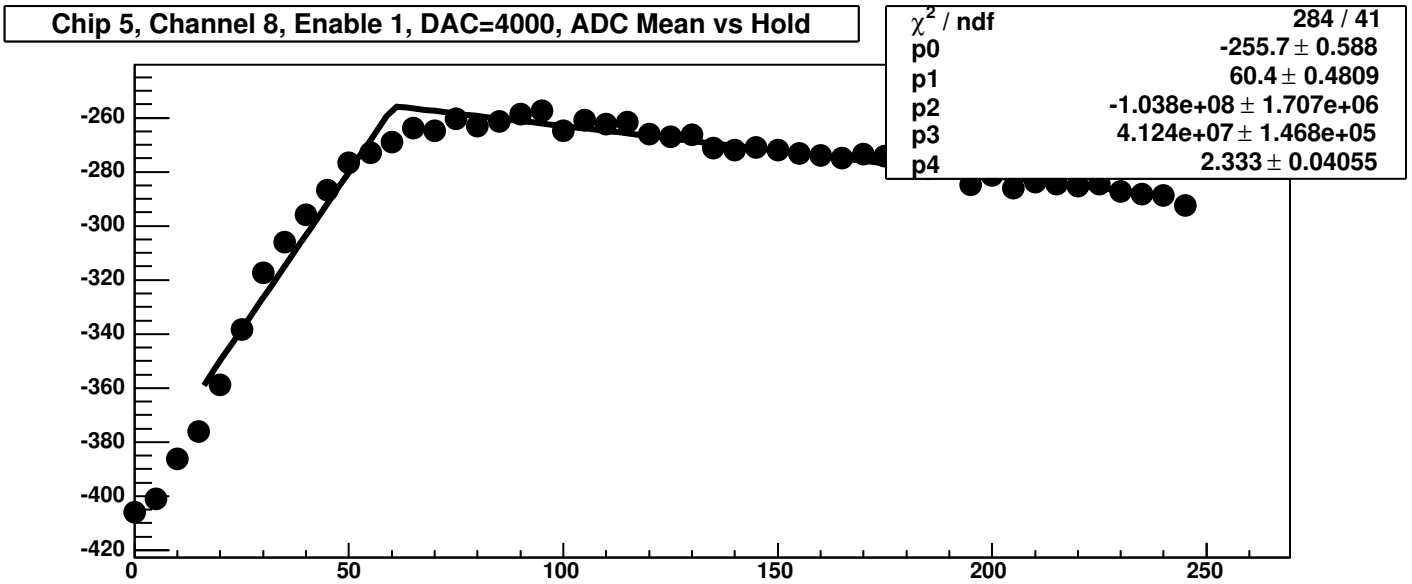

Chip 5, Channel 7, Enable 5, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250

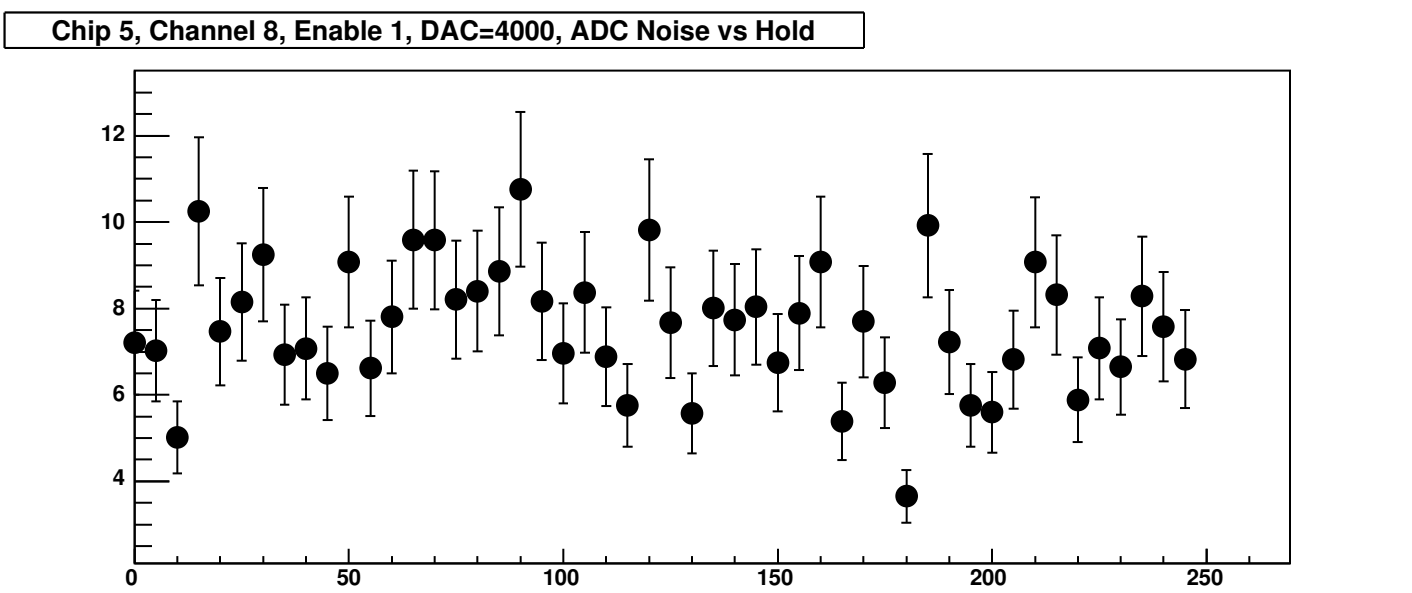

Chip 5, Channel 7, Enable 5, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
-500
-1000
0
50
100
150
200
250

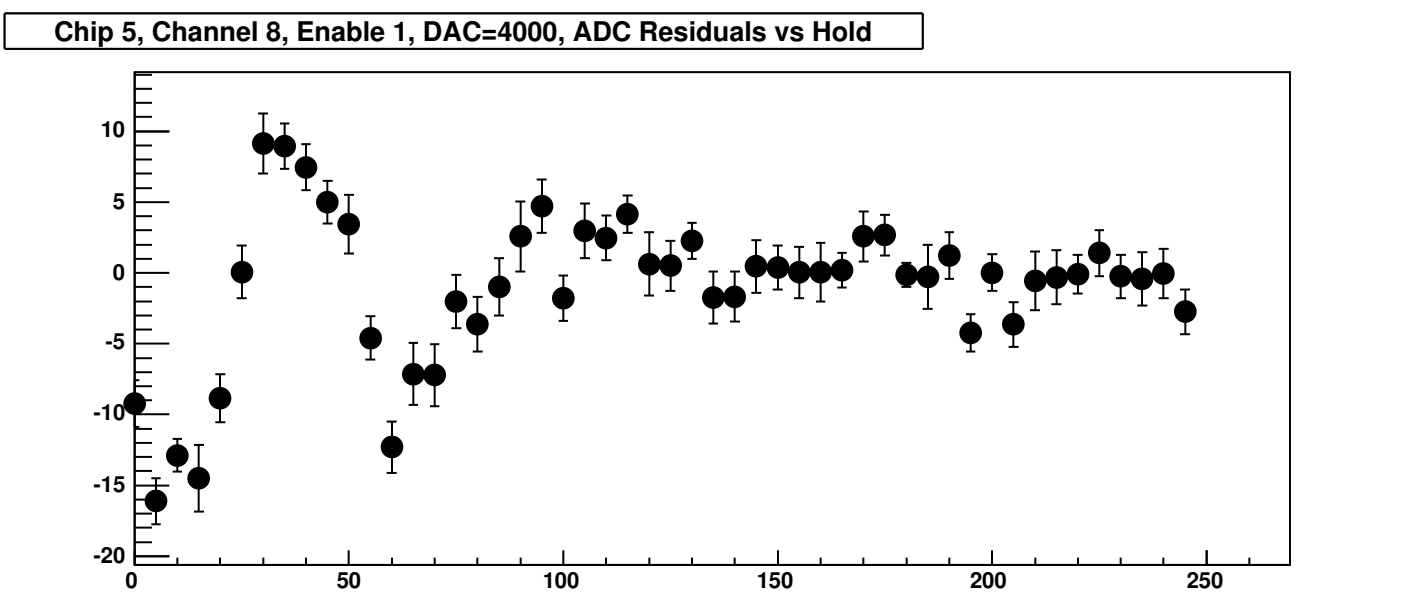

Chip 5, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.131e+05 / 41
p0 -558.1 ± 1.459
p1 40.61 ± 0.01548
p2 1.017e+04 ± 15.07
p3 47.49 ± 0.07385
p4 -26.91 ± 0.07789
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 5, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

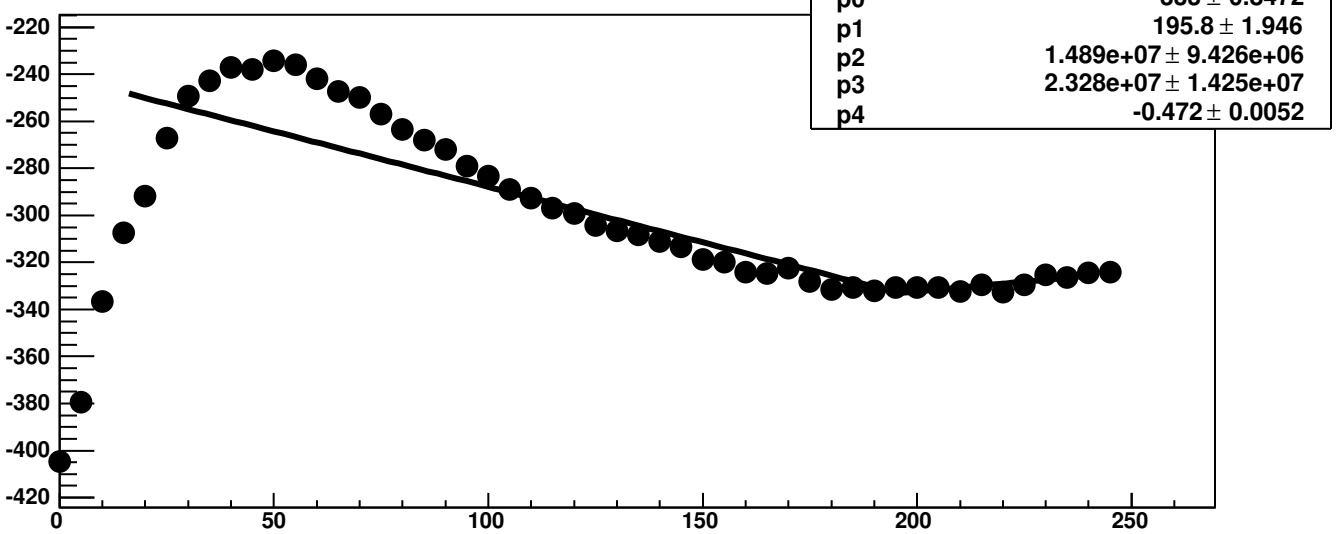

Chip 5, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250


Chip 5, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 284 / 41
p0 -255.7 ± 0.588
p1 60.4 ± 0.4809
p2 -1.038e+08 ± 1.707e+06
p3 4.124e+07 ± 1.468e+05
p4 2.333 ± 0.04055
-260
-280
-300
-320
-340
-360
-380
-400
-420
0
50
100
150
200
250

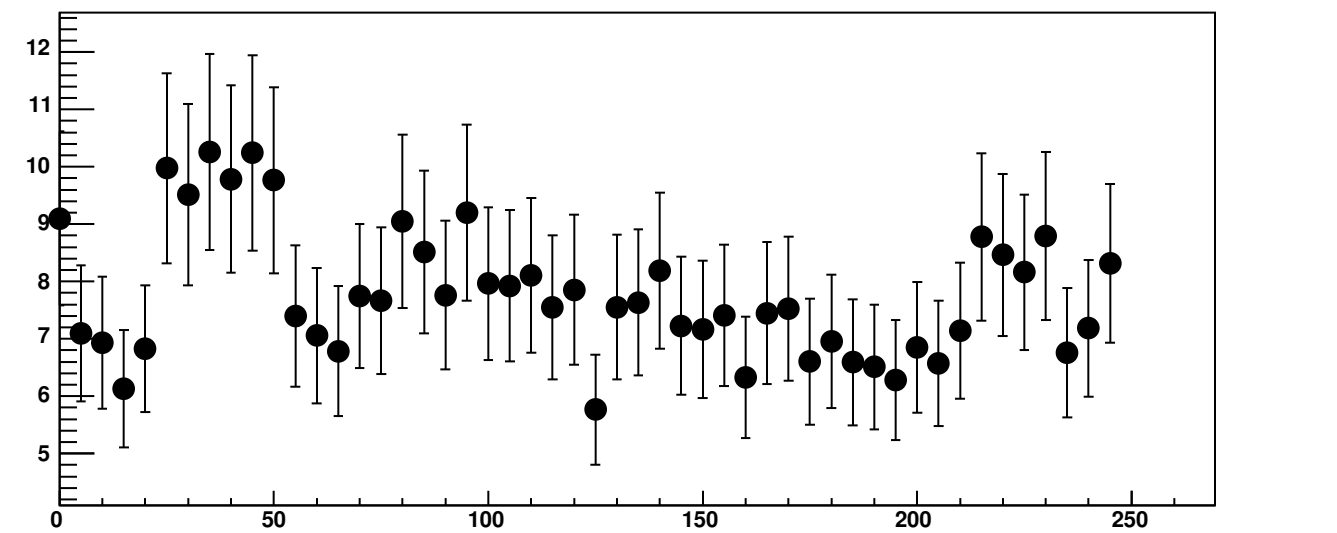

Chip 5, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 5, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
-20
0
50
100
150
200
250

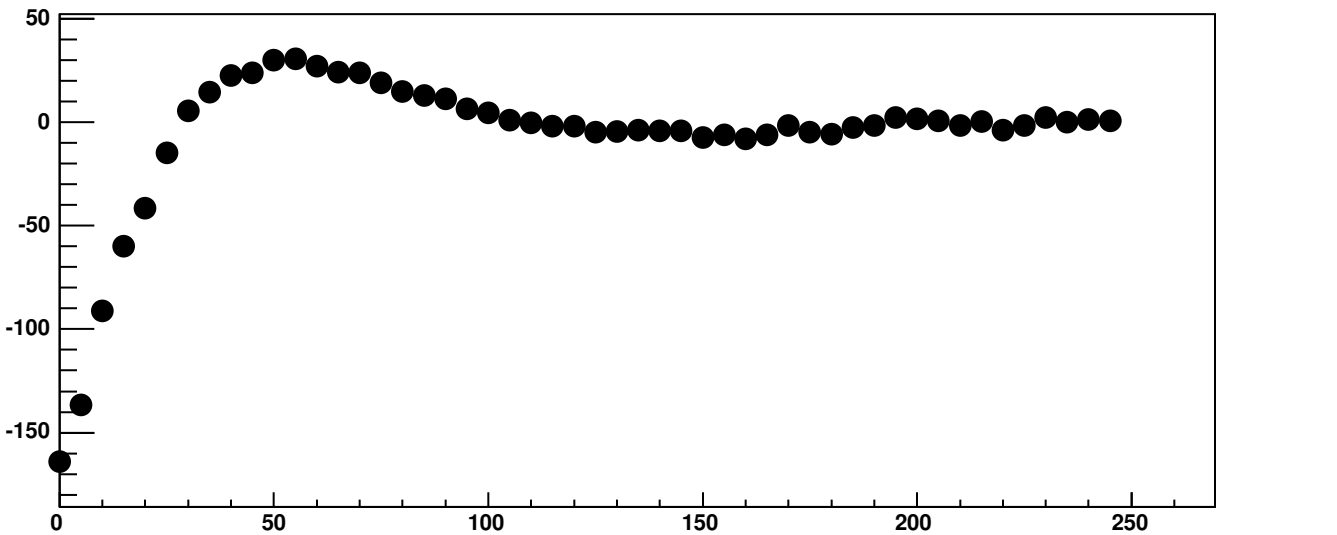
Chip 5, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold



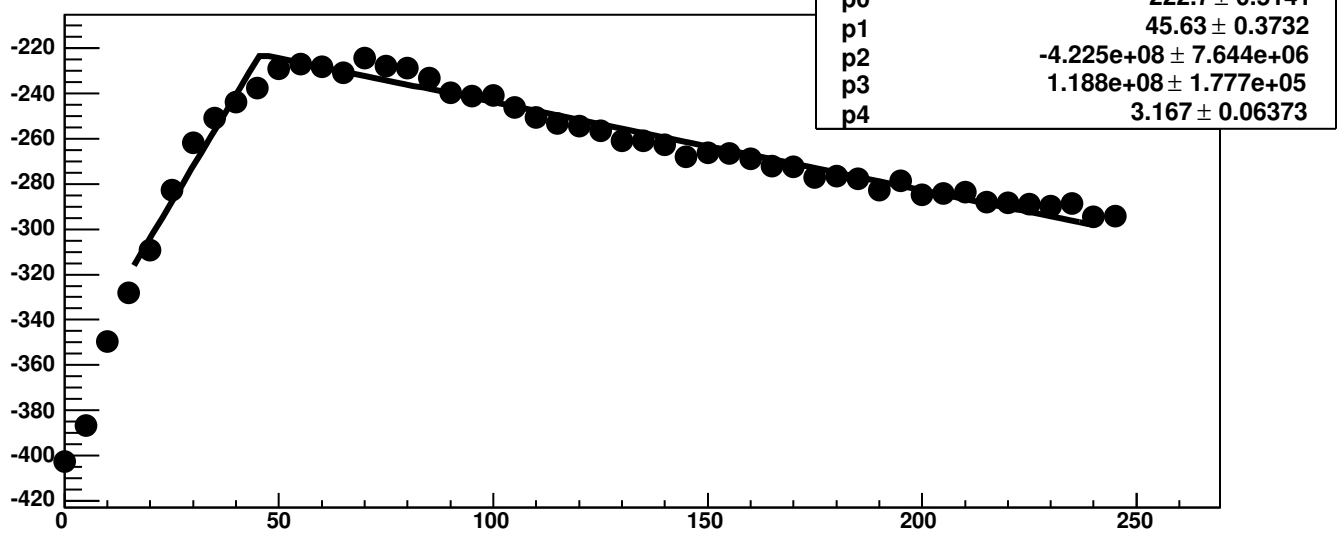
Chip 5, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold



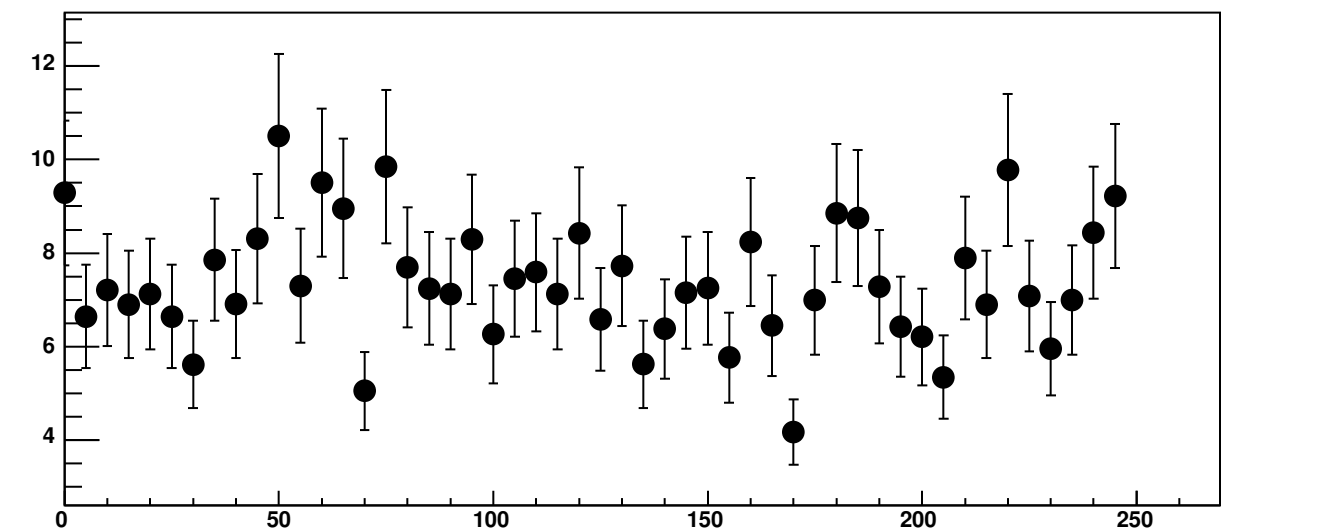
Chip 5, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold



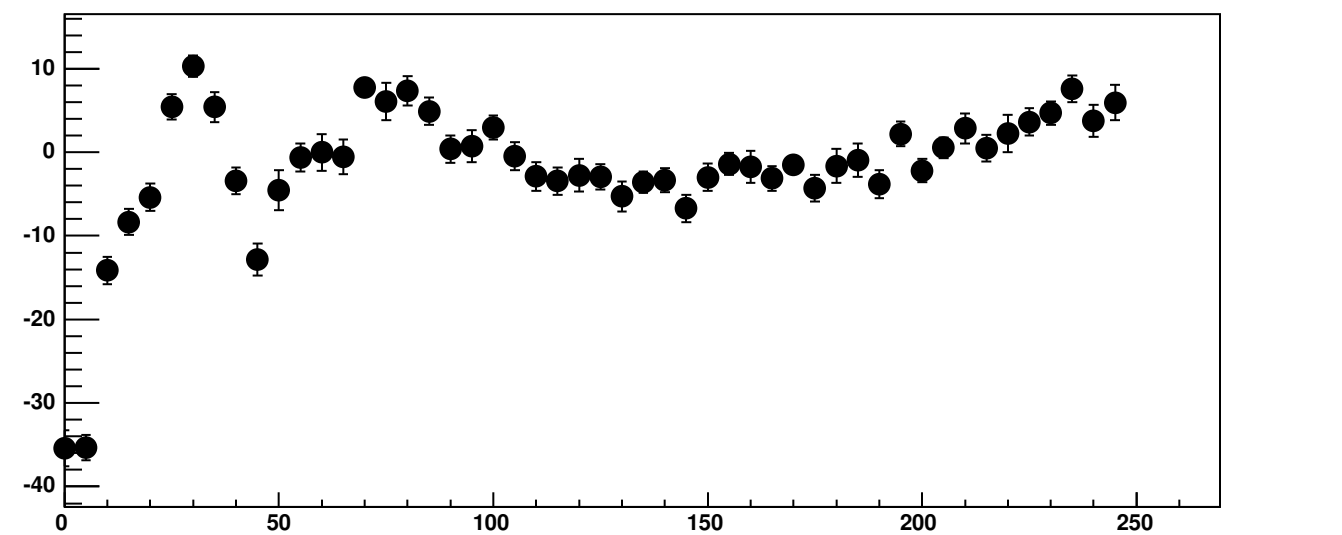
Chip 5, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold



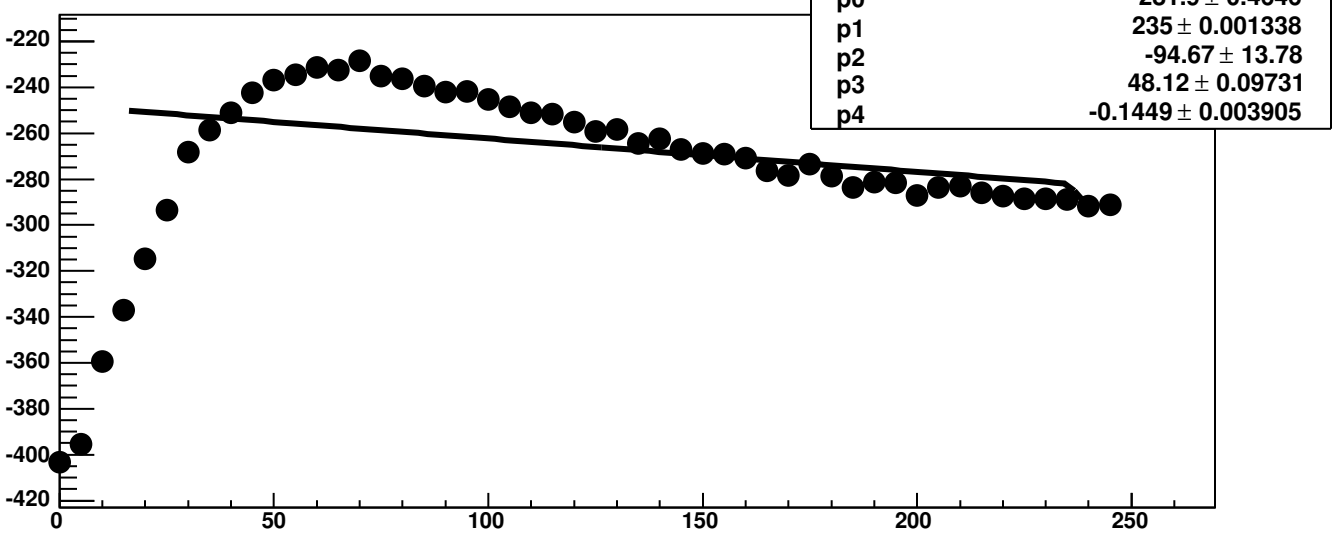
Chip 5, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



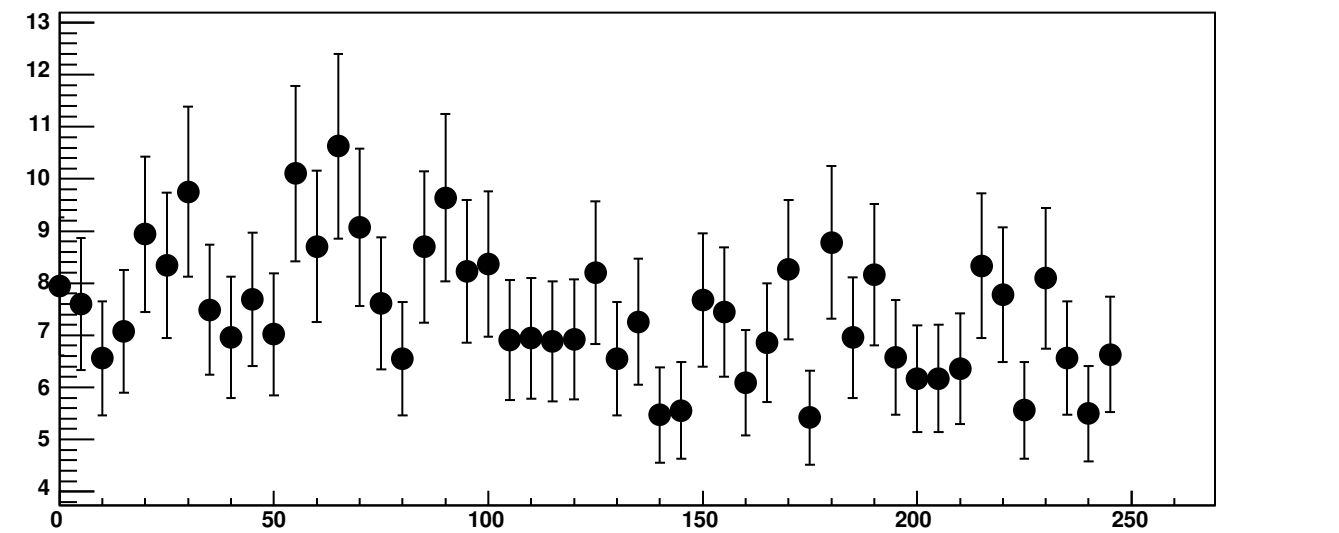
Chip 5, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold



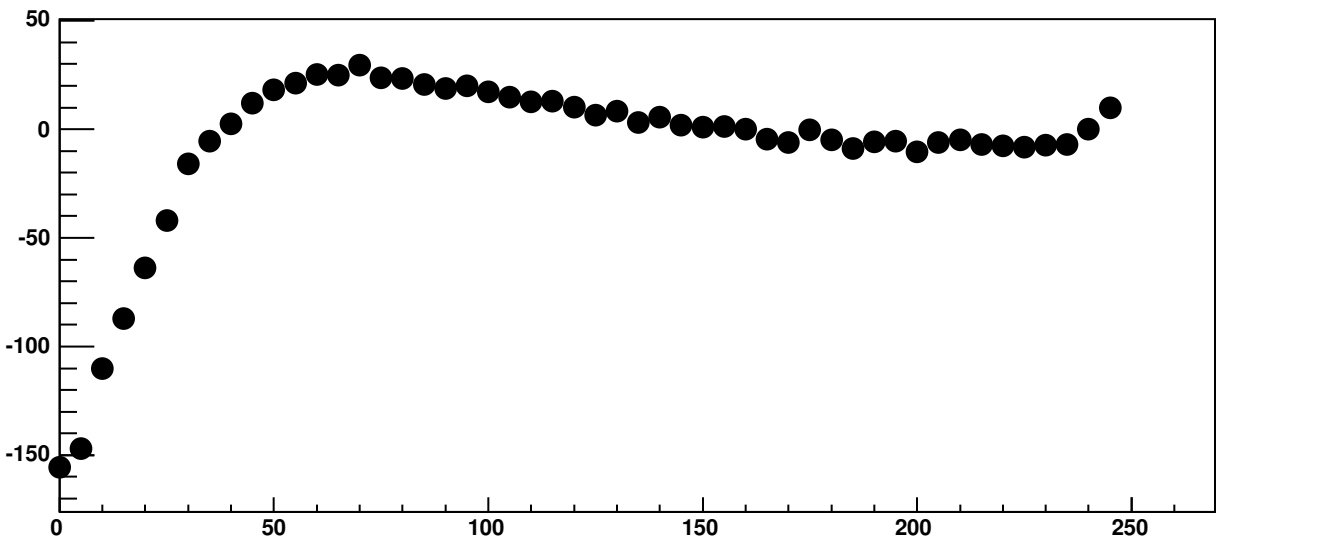
Chip 5, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold



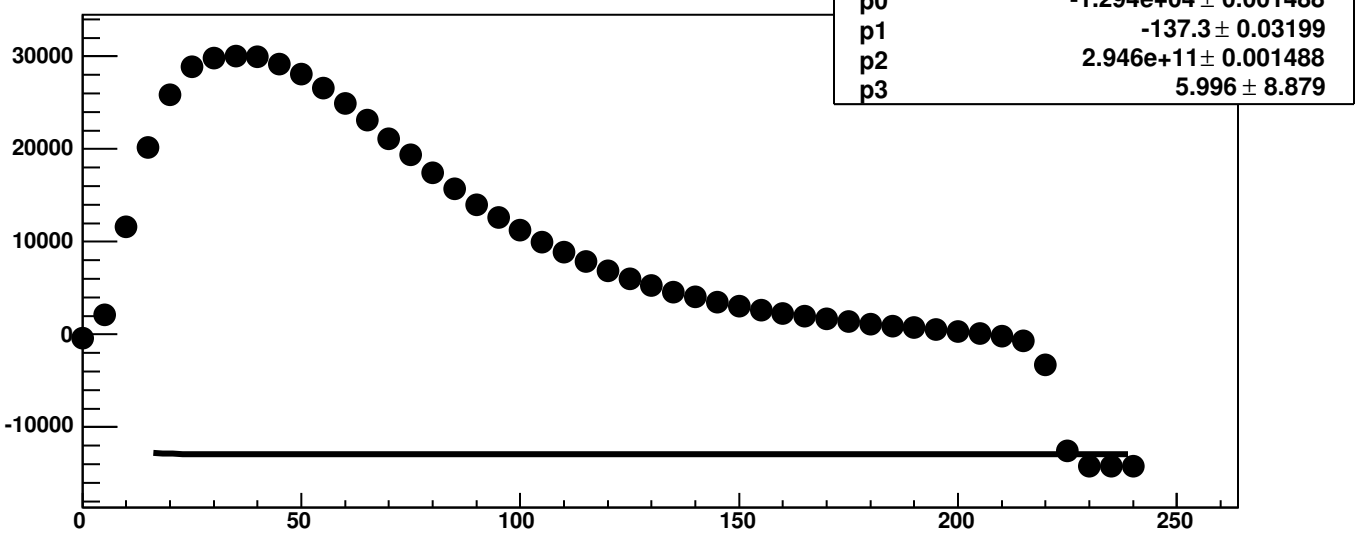
Chip 5, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



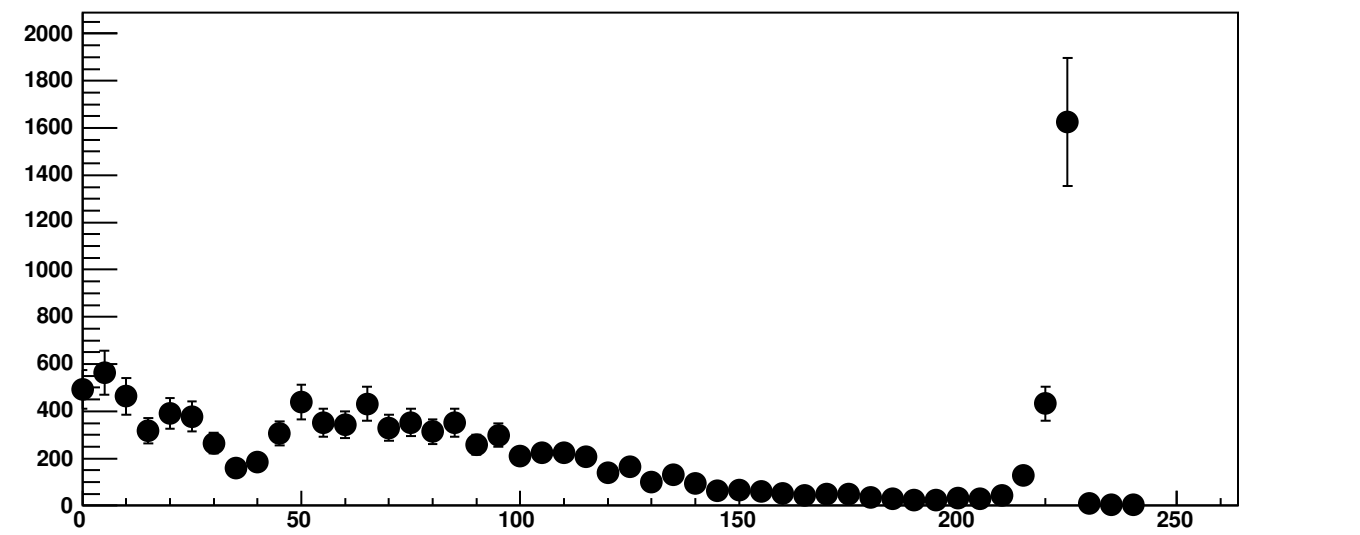
Chip 5, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold



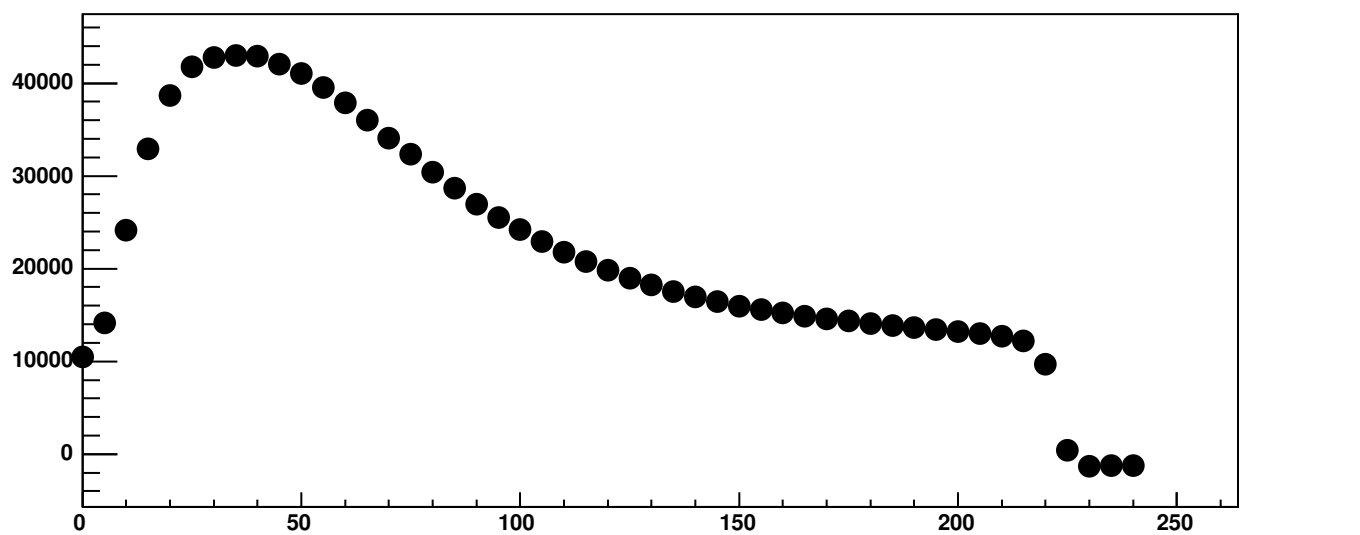
Chip 5, Channel 8, Enable 5!, DAC=4000, ADC Mean vs Hold

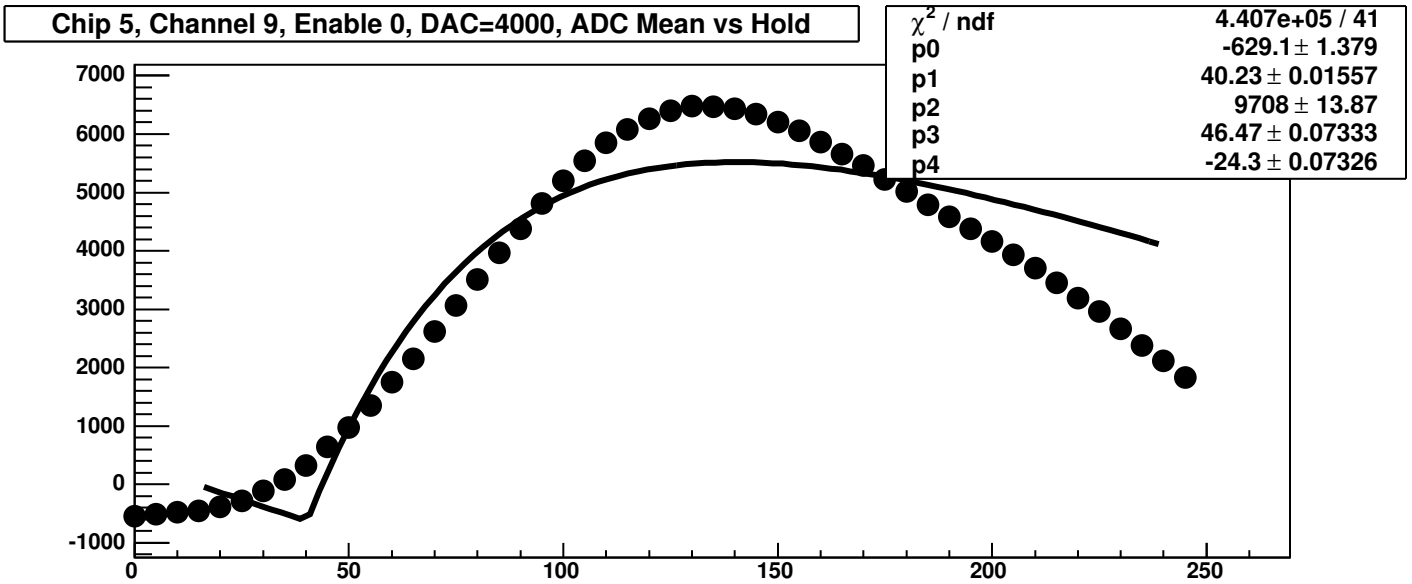


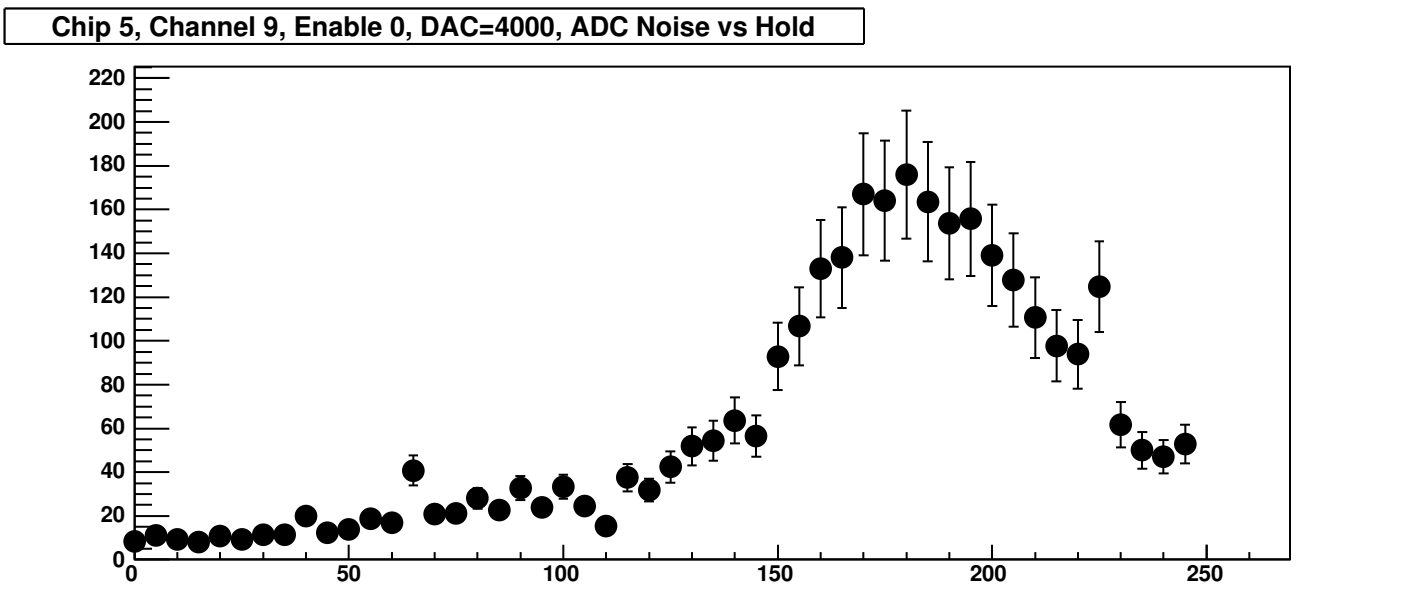
Chip 5, Channel 8, Enable 5!, DAC=4000, ADC Noise vs Hold

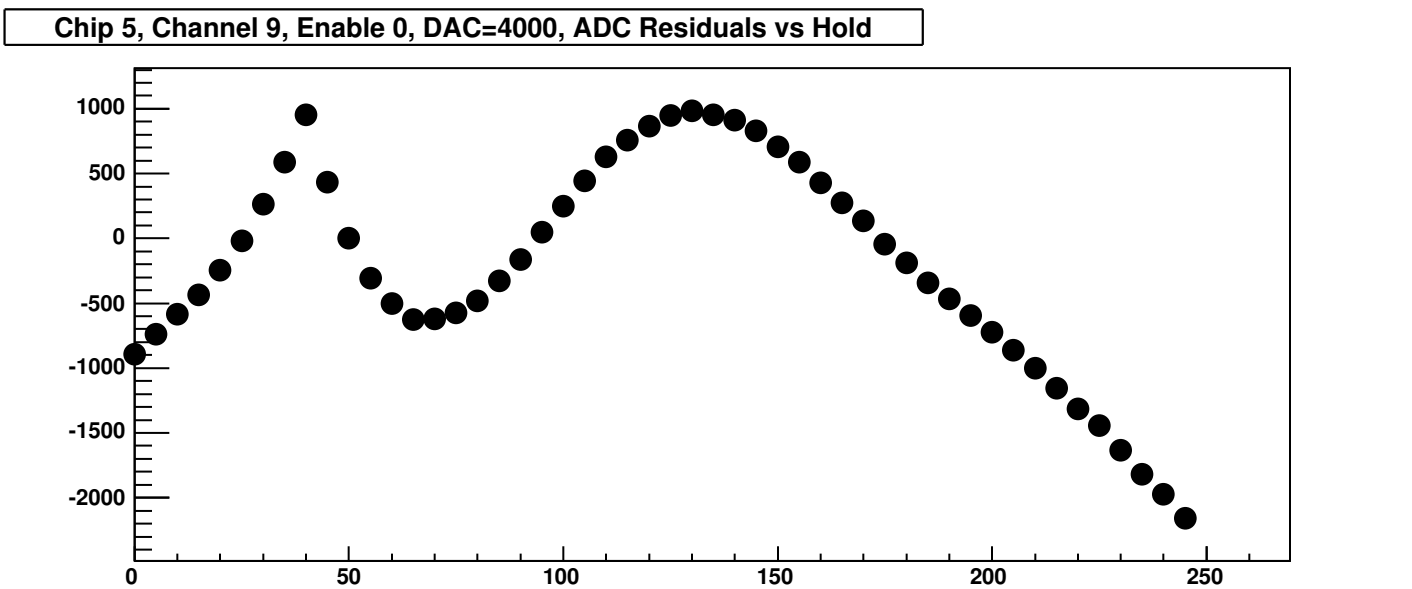


Chip 5, Channel 8, Enable 5!, DAC=4000, ADC Residuals vs Hold

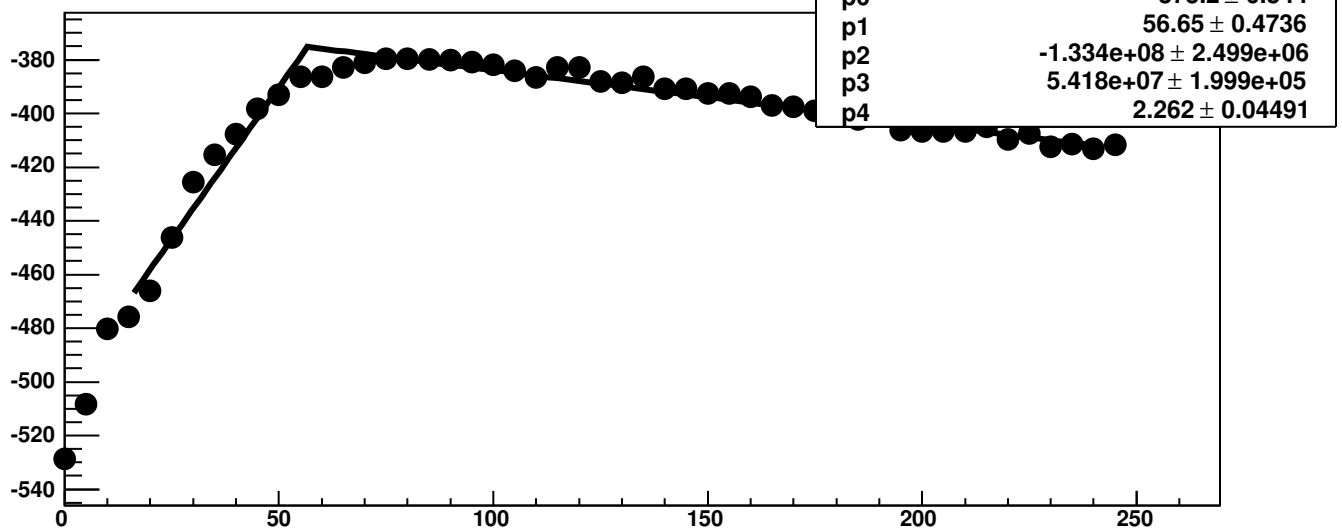



Chip 5, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.407e+05 / 41
p0 -629.1 ± 1.379
p1 40.23 ± 0.01557
p2 9708 ± 13.87
p3 46.47 ± 0.07333
p4 -24.3 ± 0.07326
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

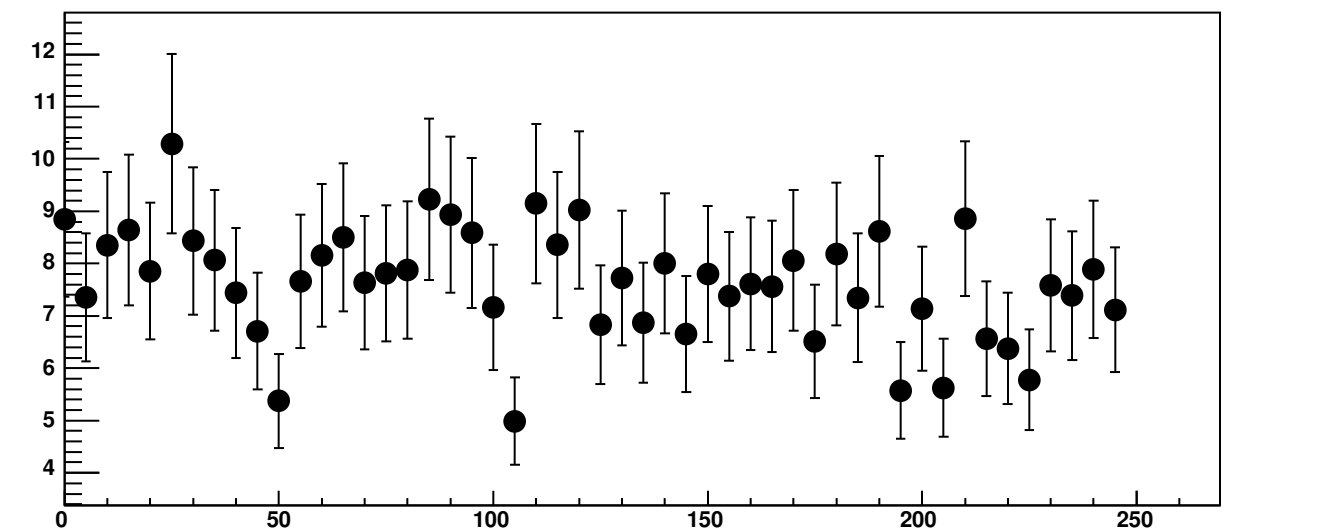

Chip 5, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 5, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

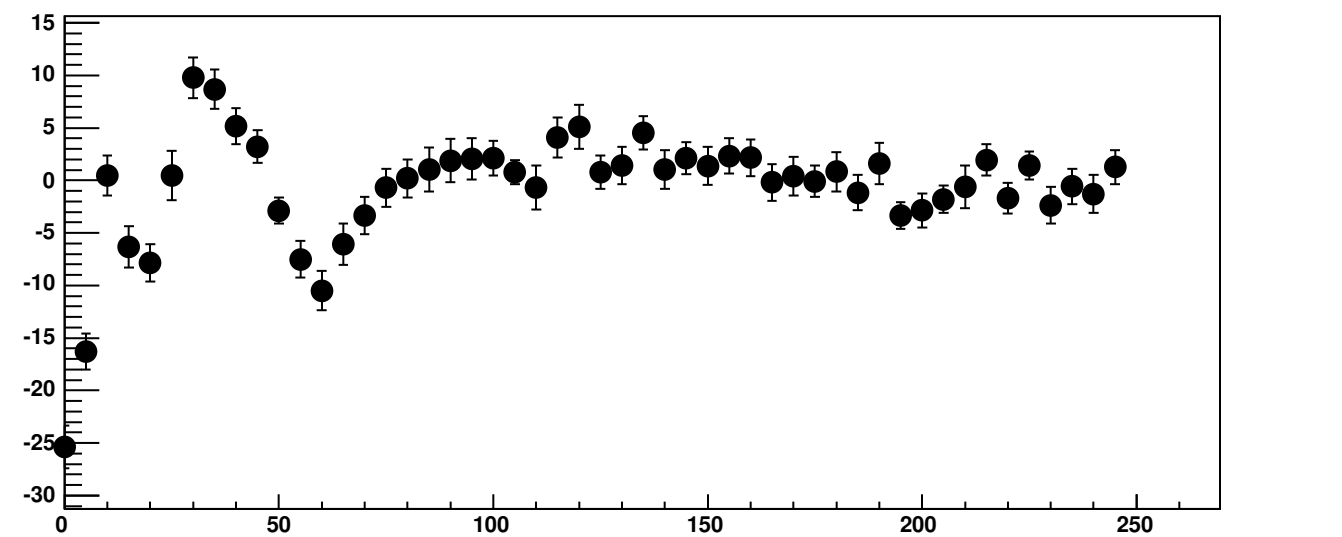
Chip 5, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

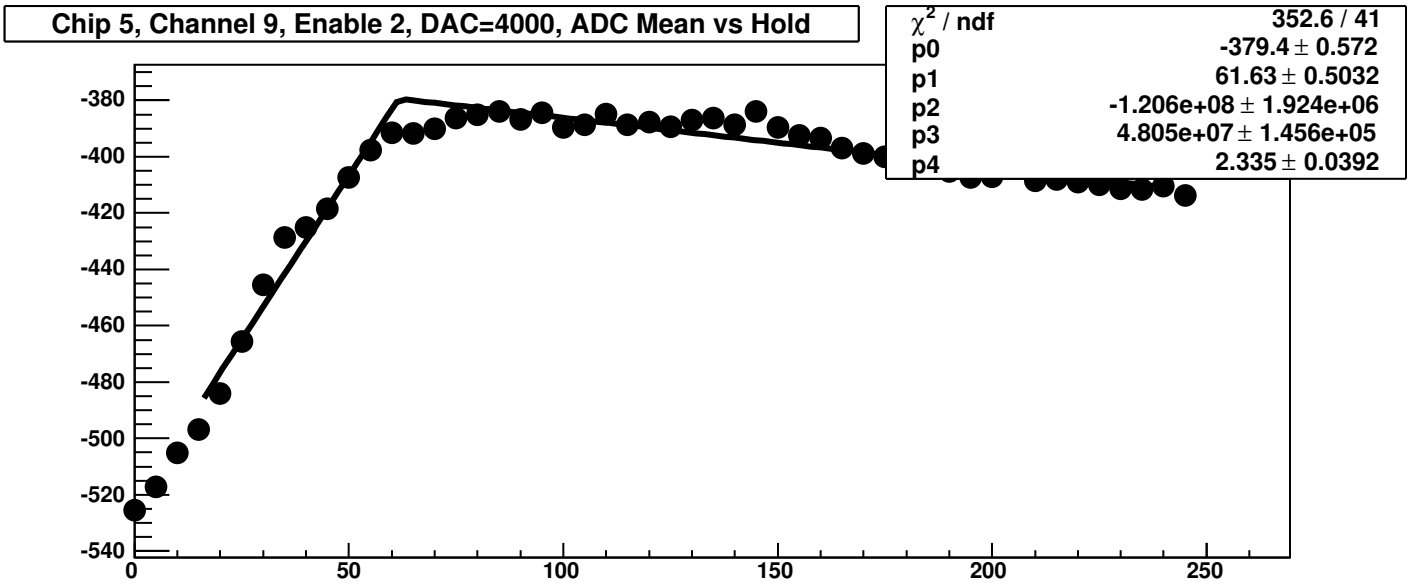


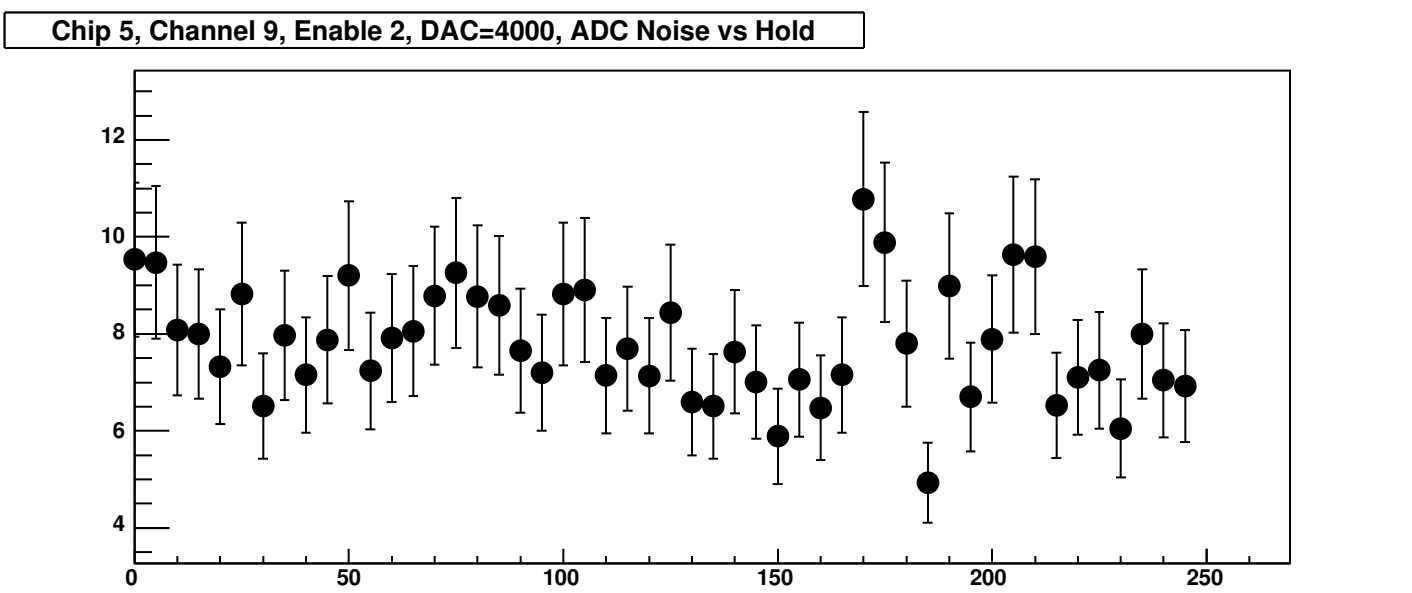
Chip 5, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold

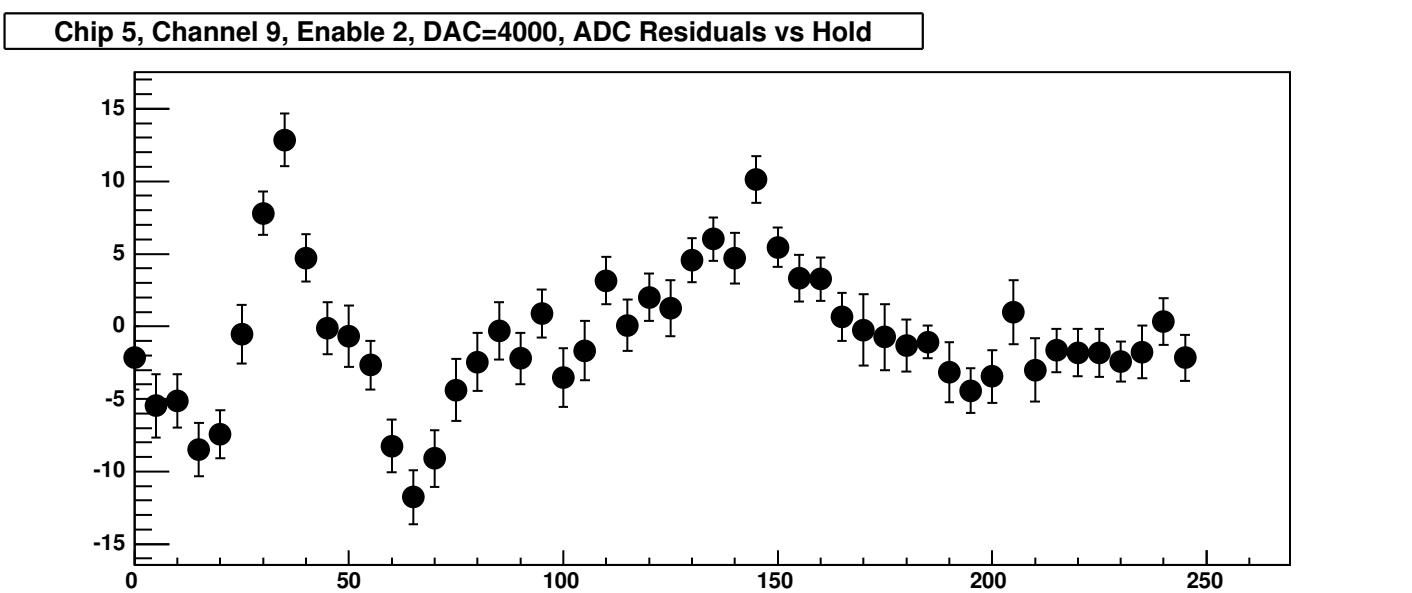


Chip 5, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

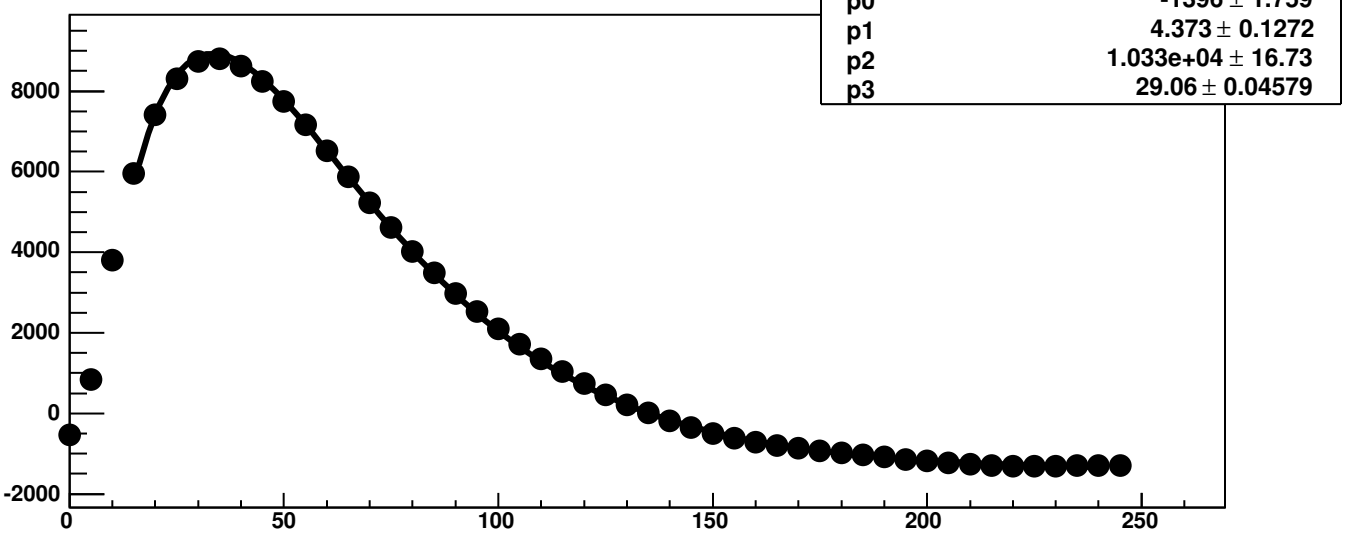



Chip 5, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 352.6 / 41
p0 -379.4 ± 0.572
p1 61.63 ± 0.5032
p2 -1.206e+08 ± 1.924e+06
p3 4.805e+07 ± 1.456e+05
p4 2.335 ± 0.0392
-380
-400
-420
-440
-460
-480
-500
-520
-540
0
50
100
150
200
250

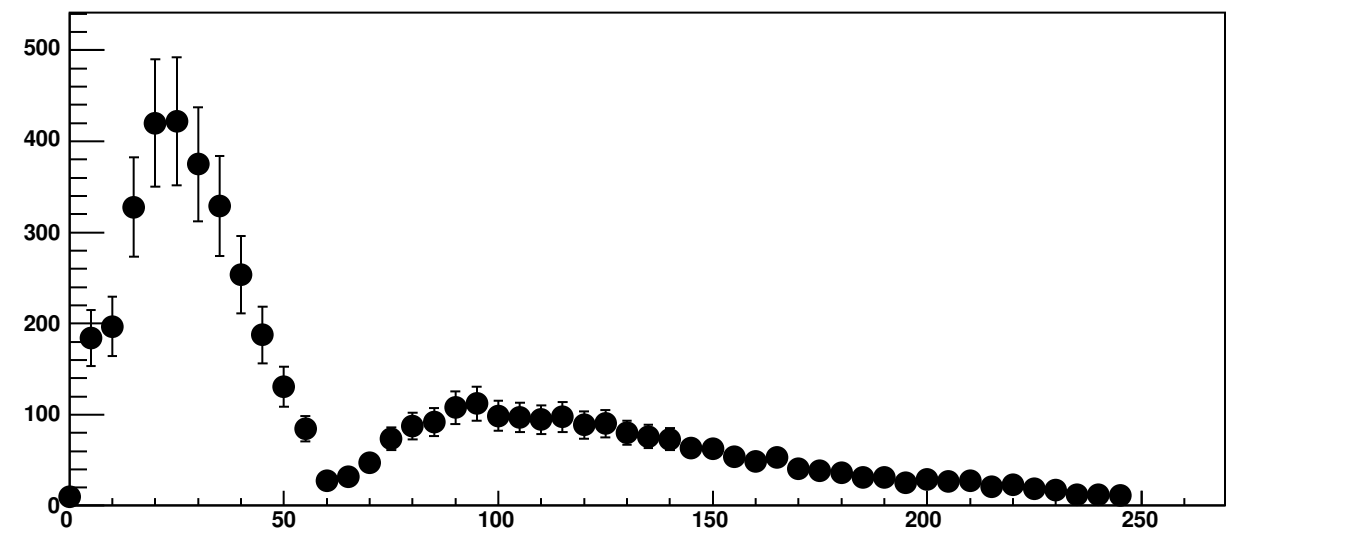

Chip 5, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 5, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

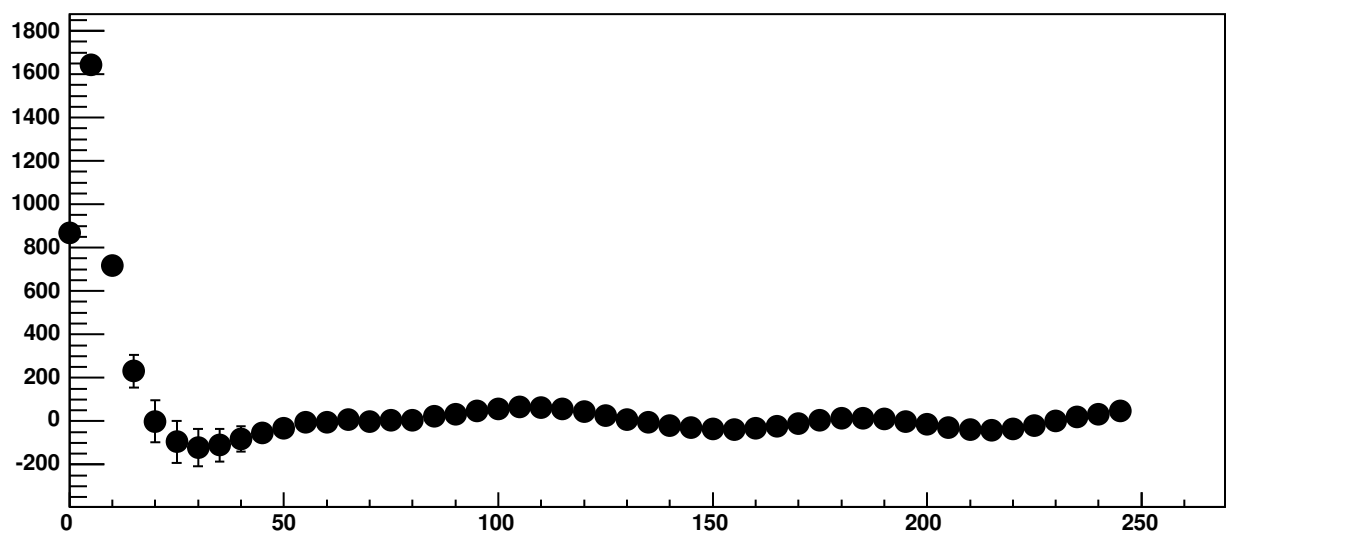
Chip 5, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold



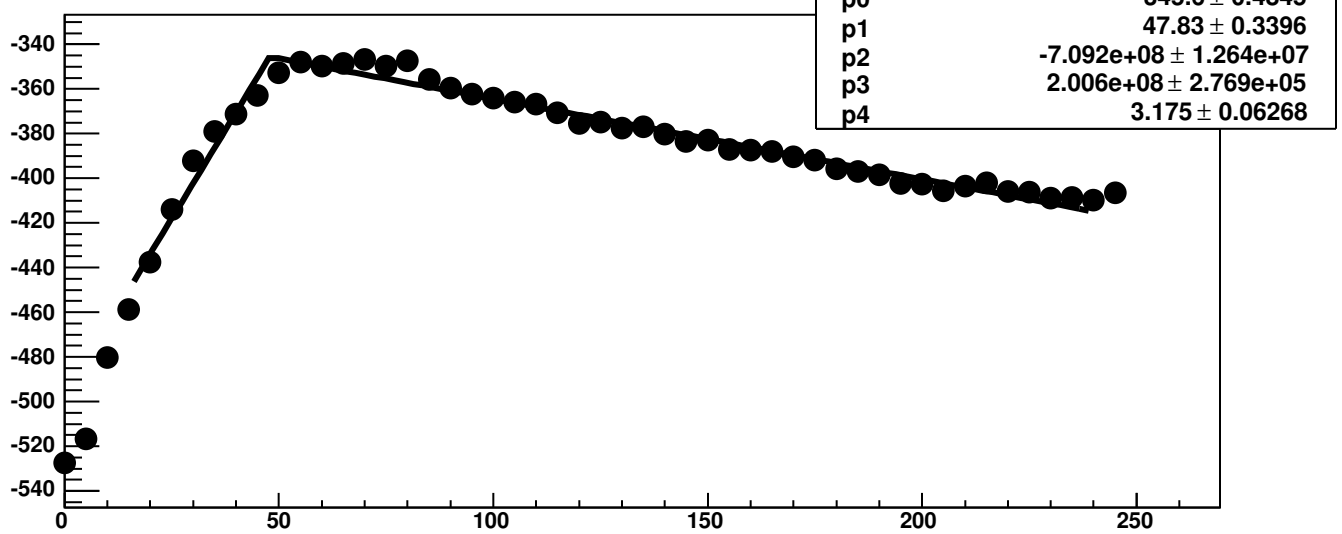
Chip 5, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold



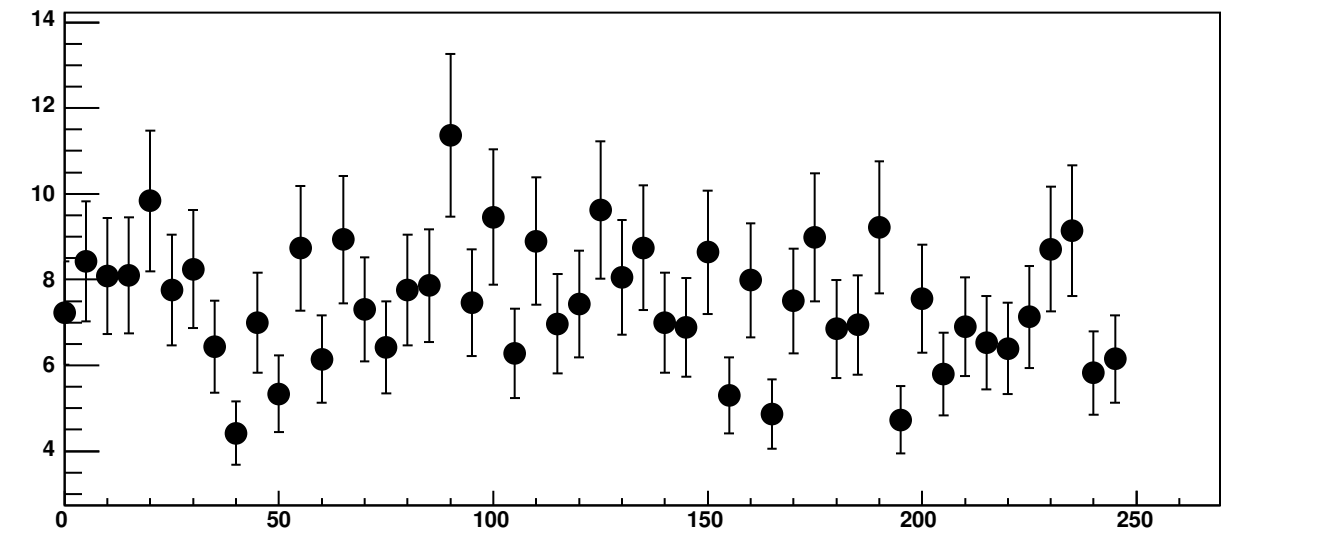
Chip 5, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold



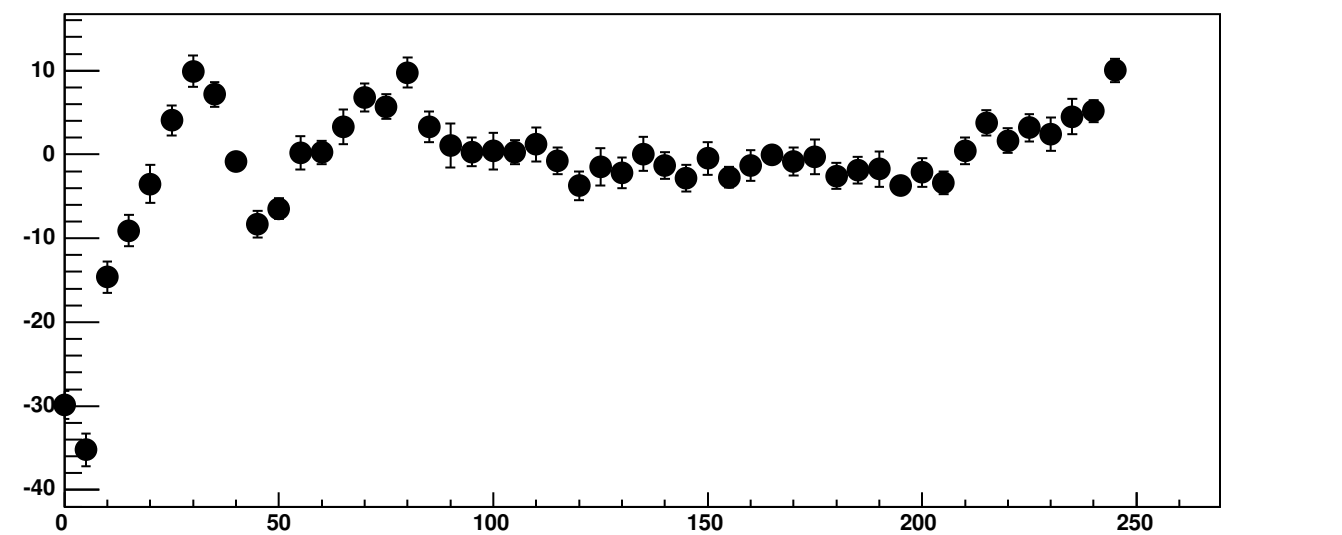
Chip 5, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold



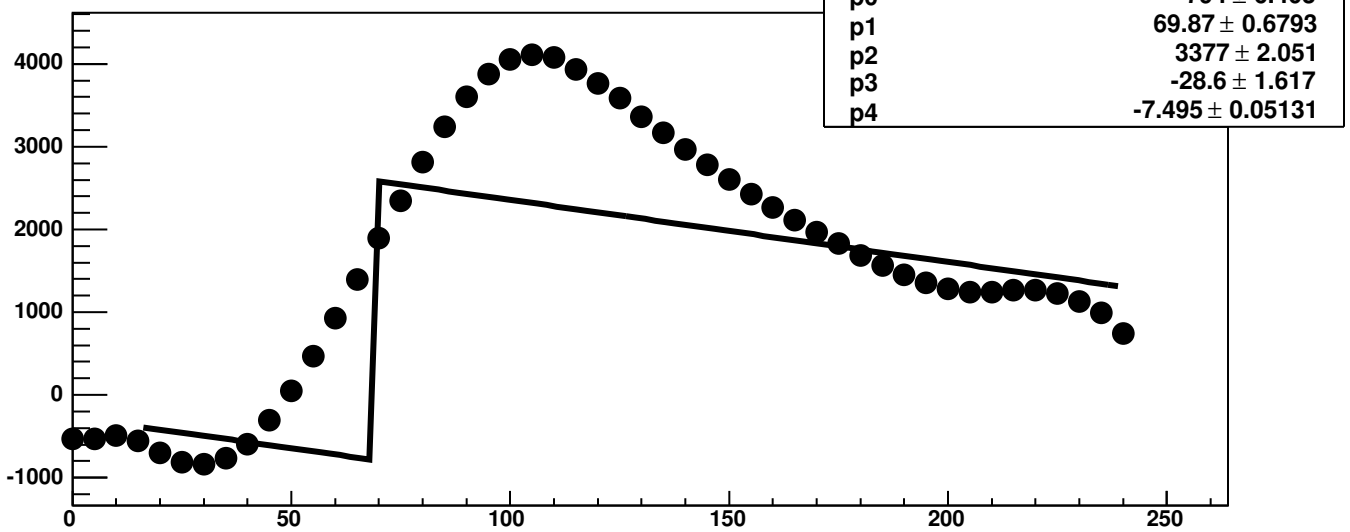
Chip 5, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



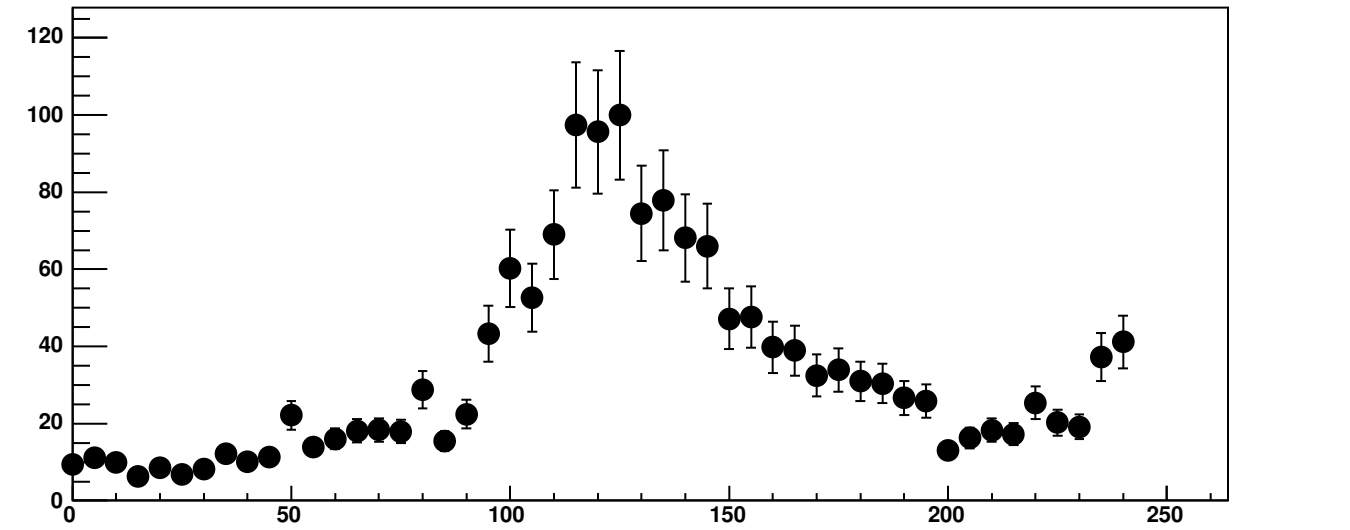
Chip 5, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold



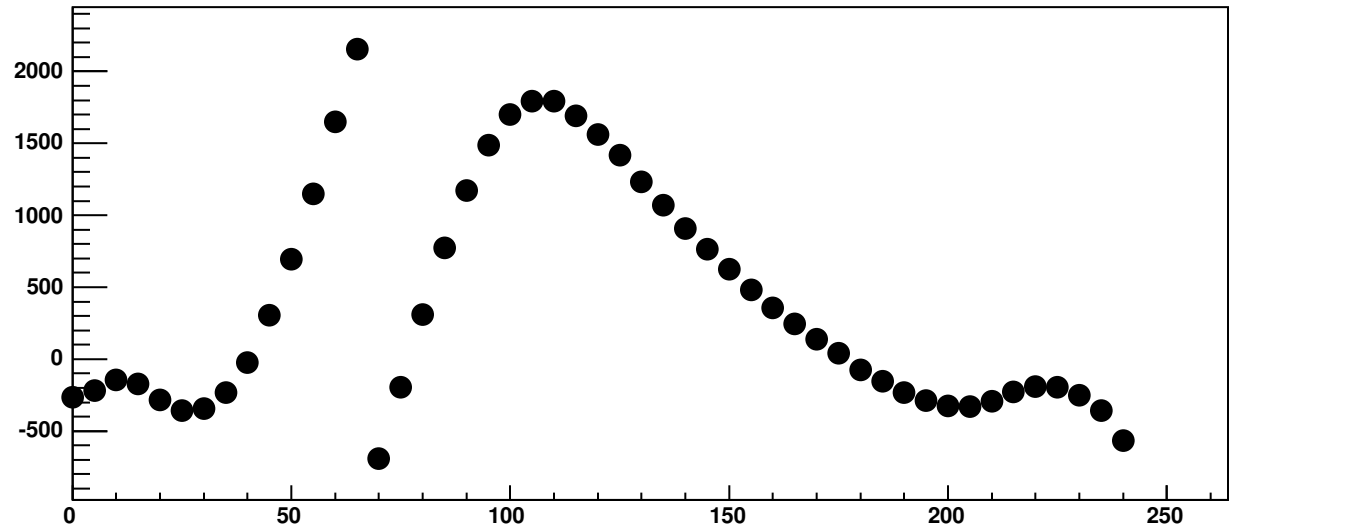
Chip 5, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold

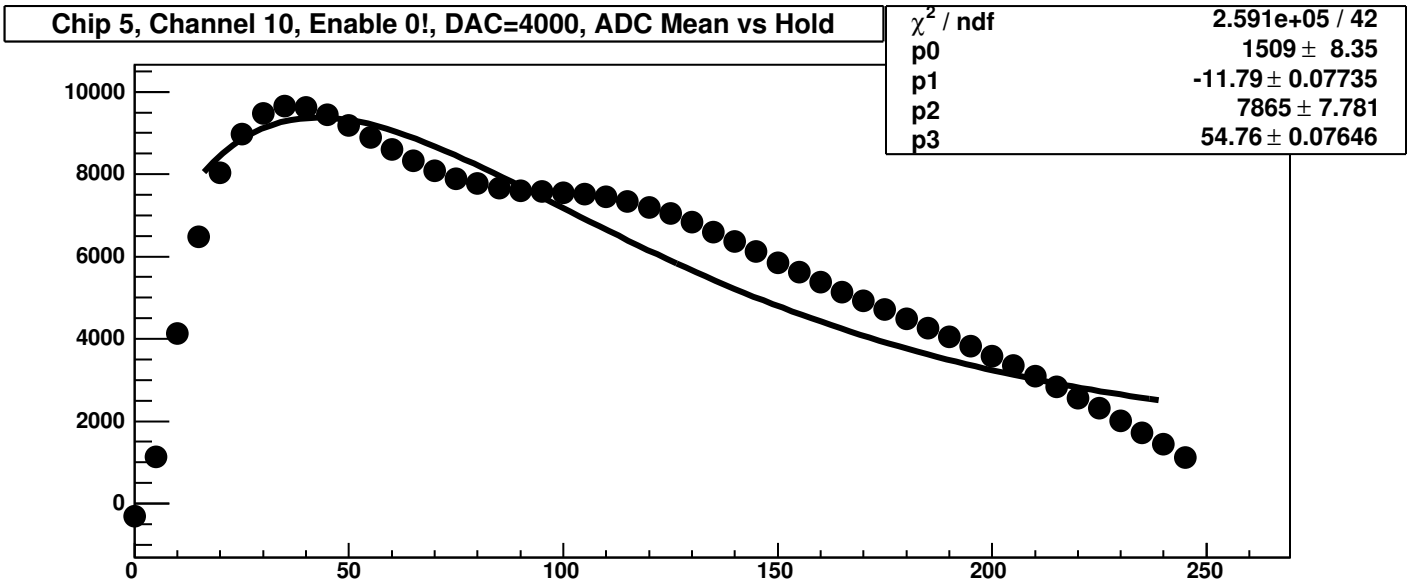


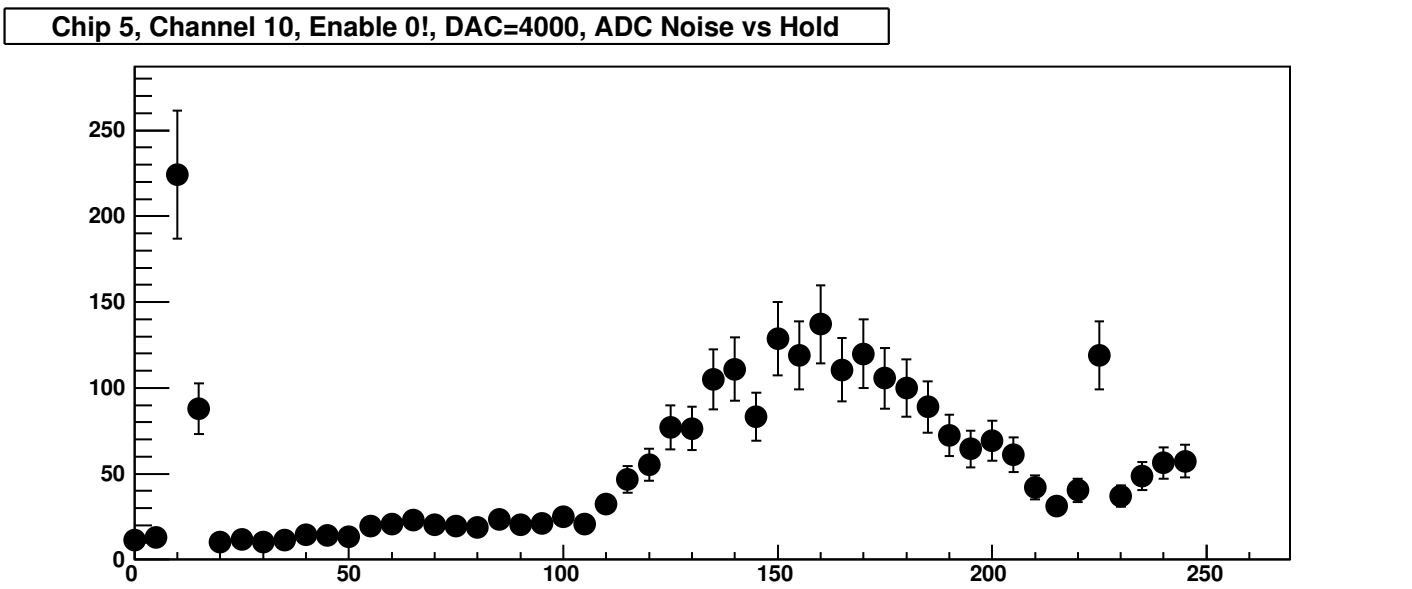
Chip 5, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold

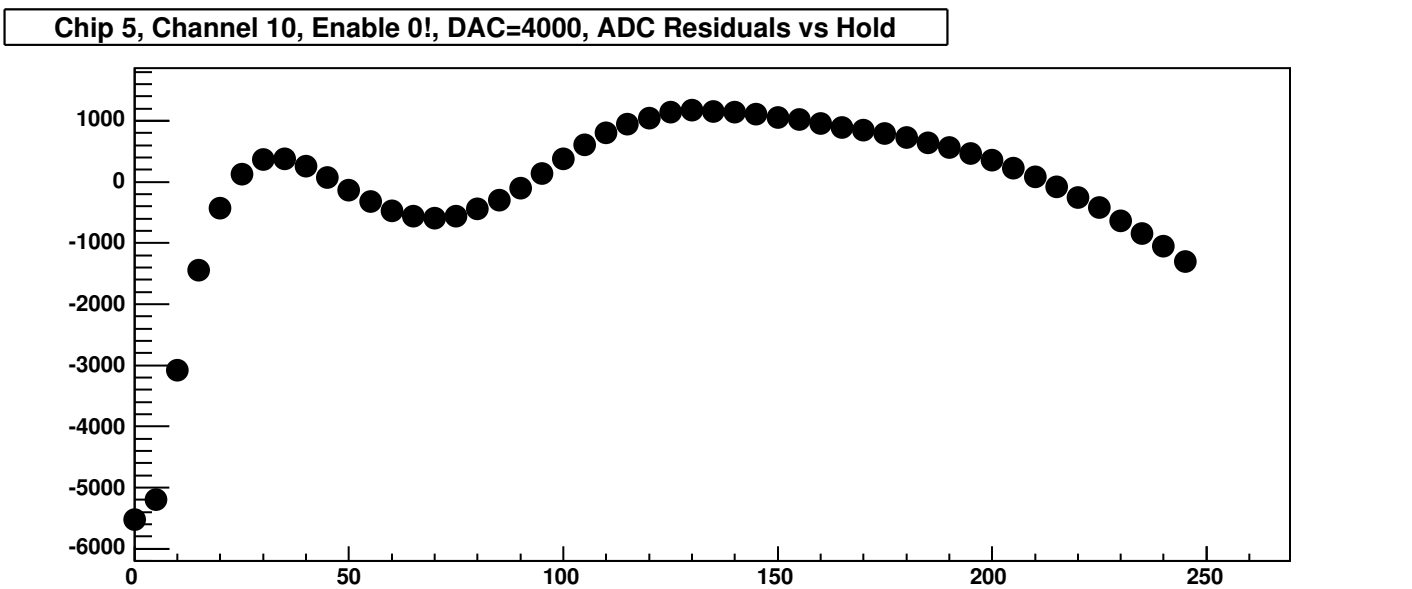


Chip 5, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold

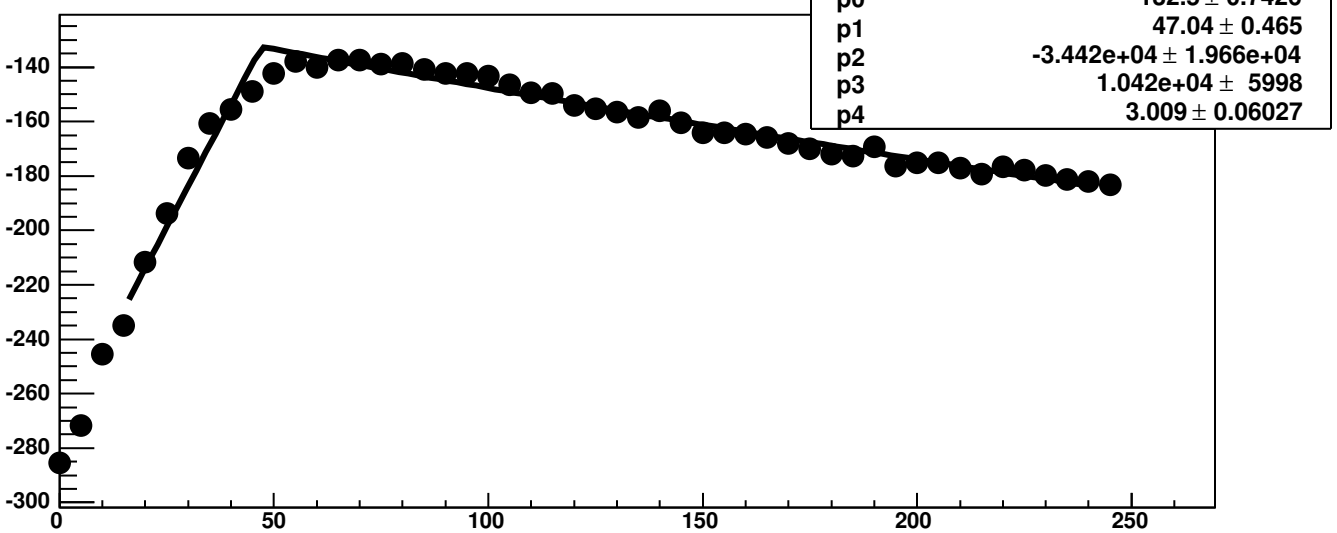



Chip 5, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.591e+05 / 42
p0 1509 ± 8.35
p1 -11.79 ± 0.07735
p2 7865 ± 7.781
p3 54.76 ± 0.07646
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

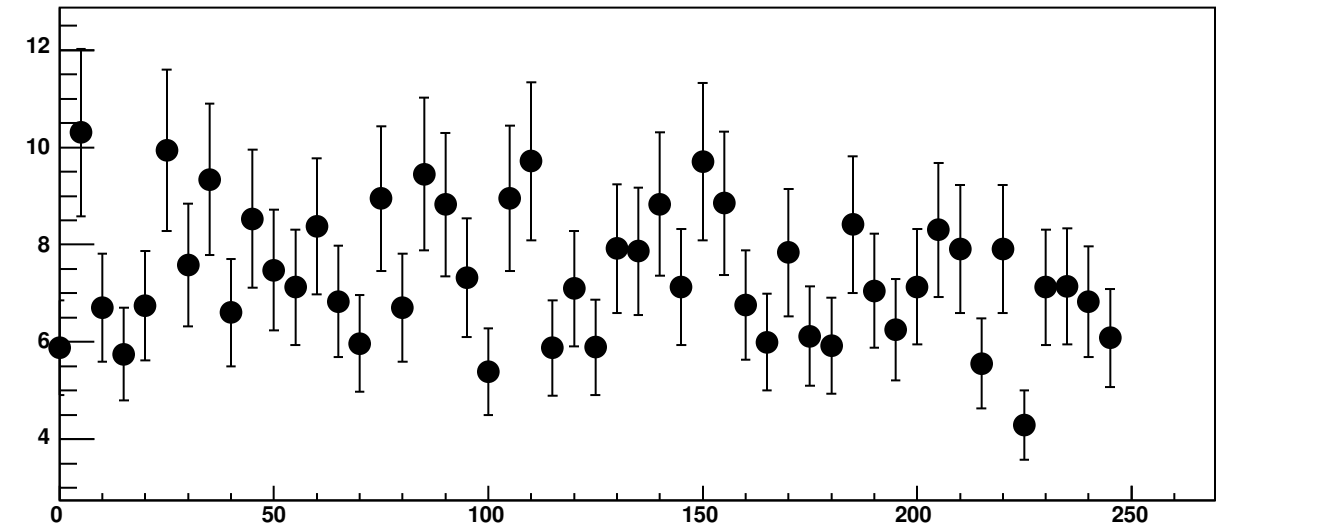

Chip 5, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250


Chip 5, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
50
100
150
200
250

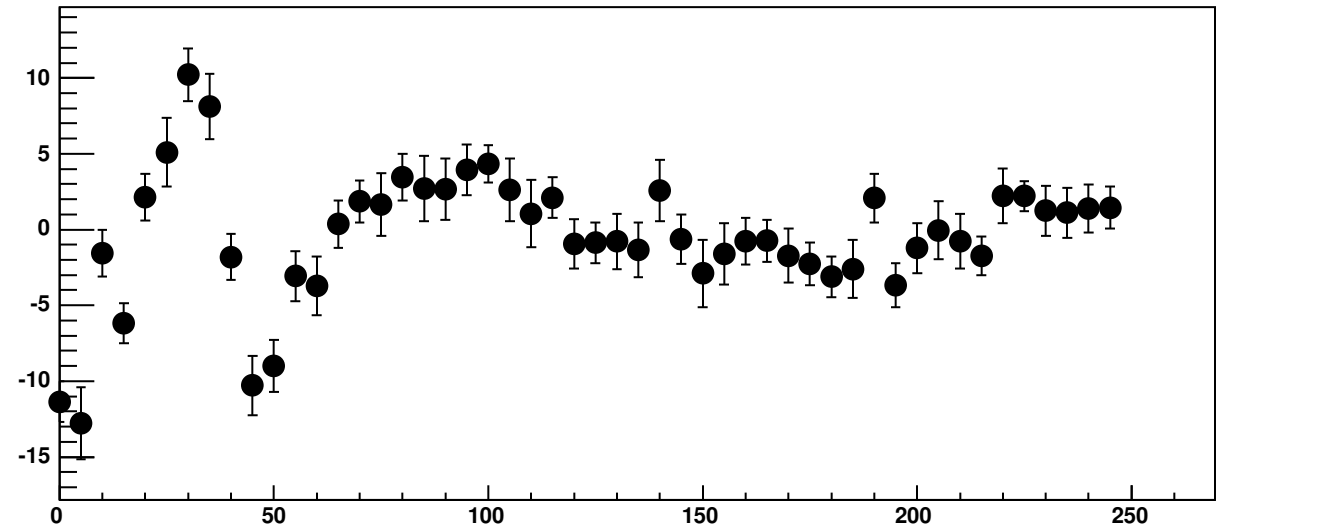
Chip 5, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold

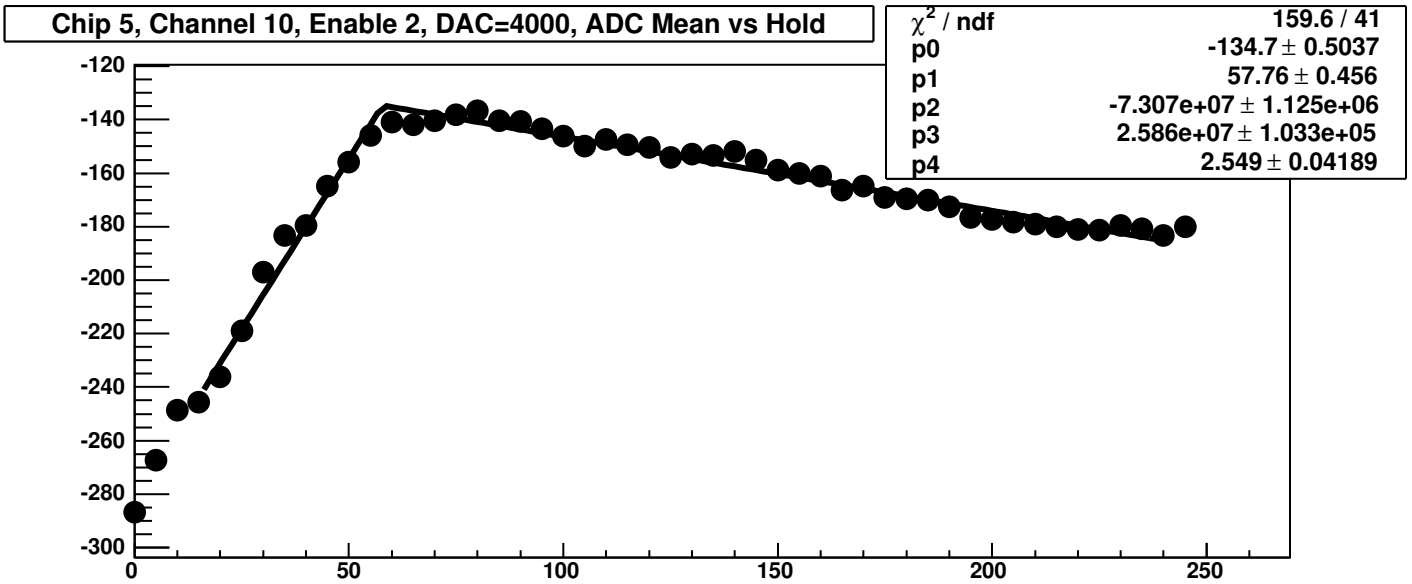


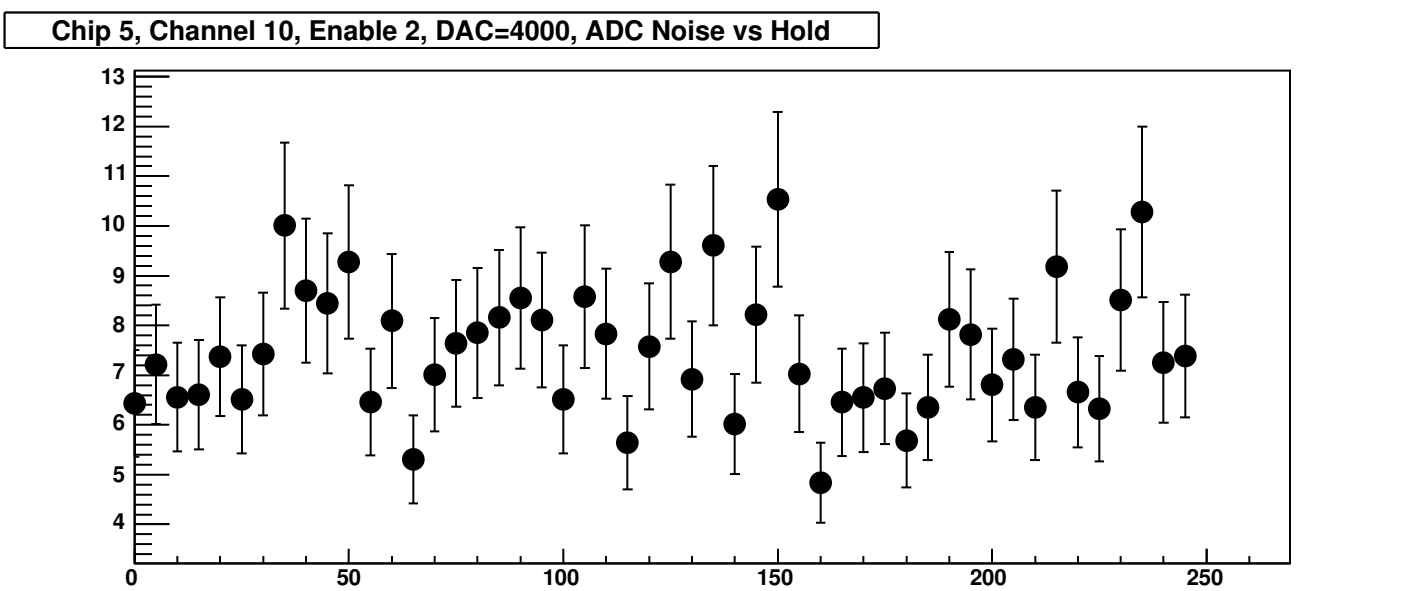
Chip 5, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold

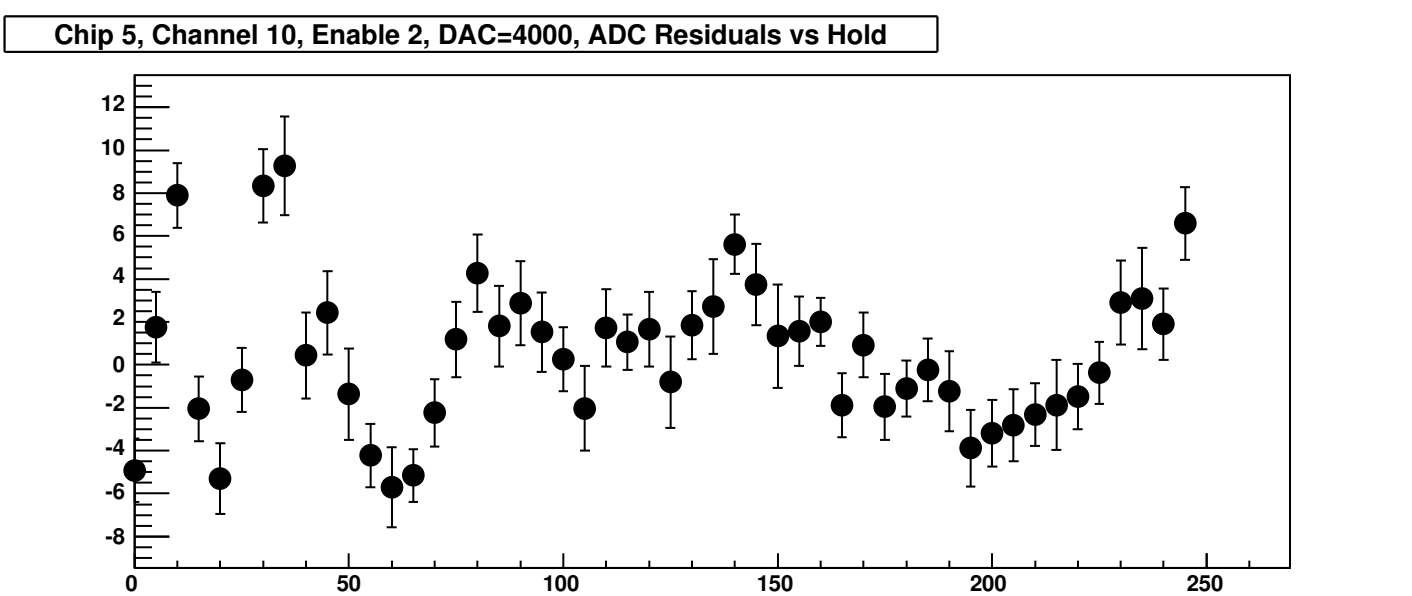


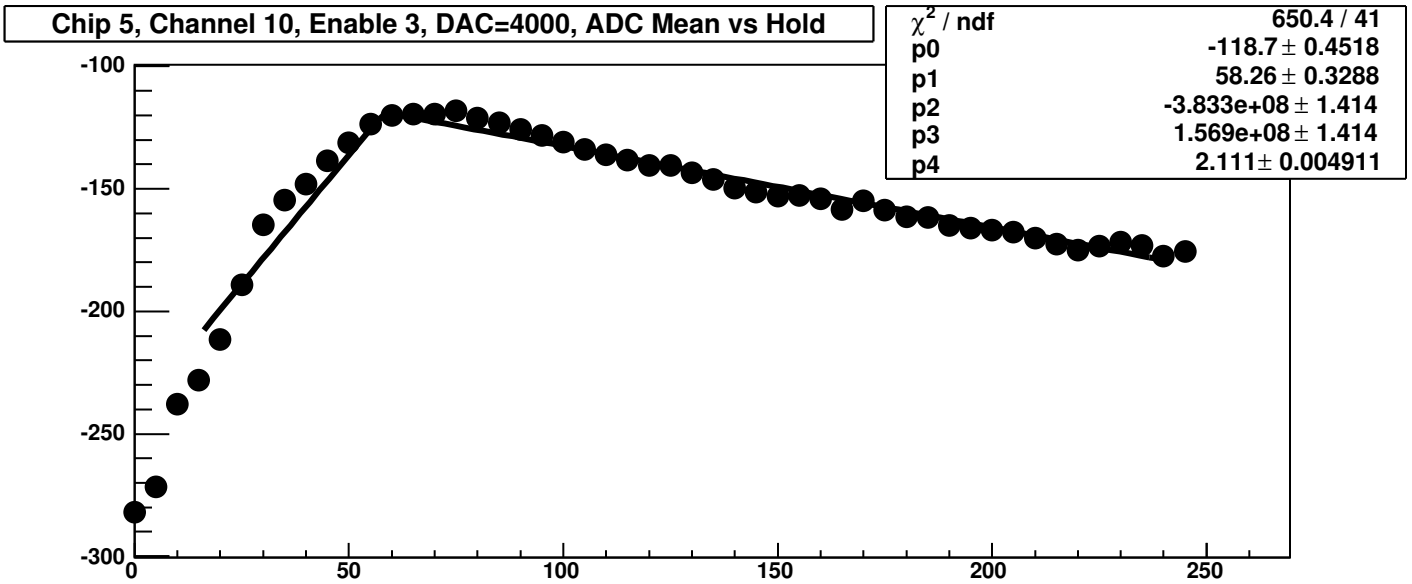
Chip 5, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

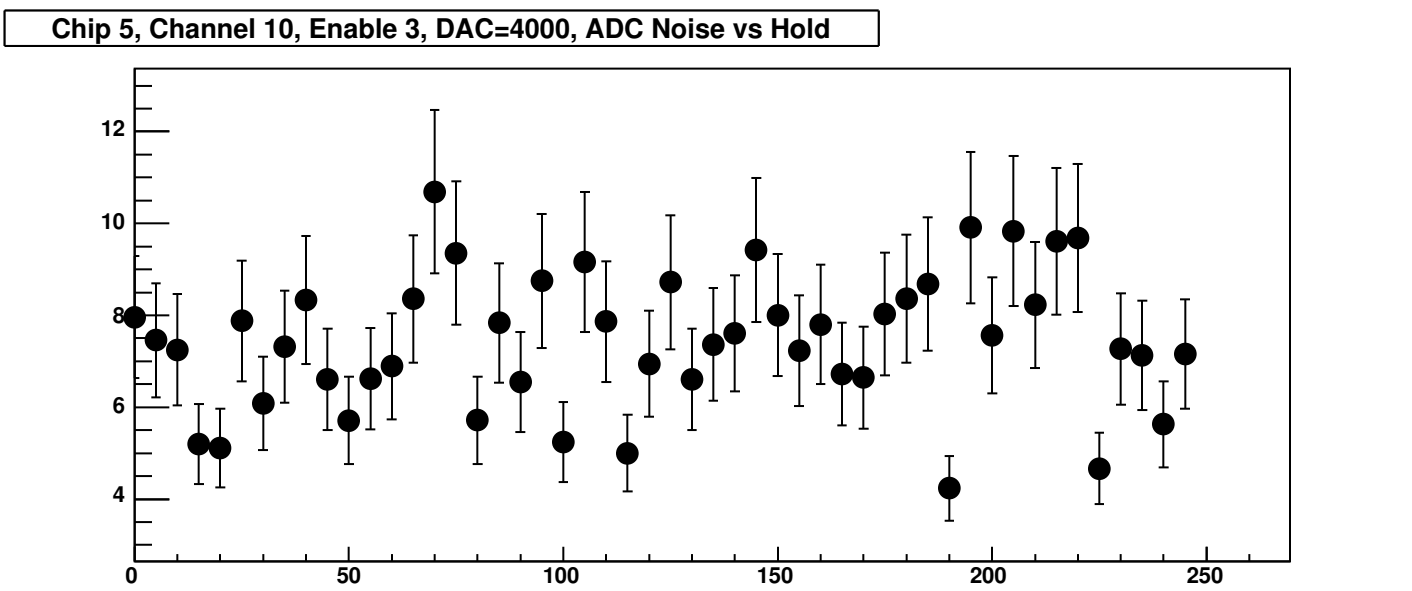


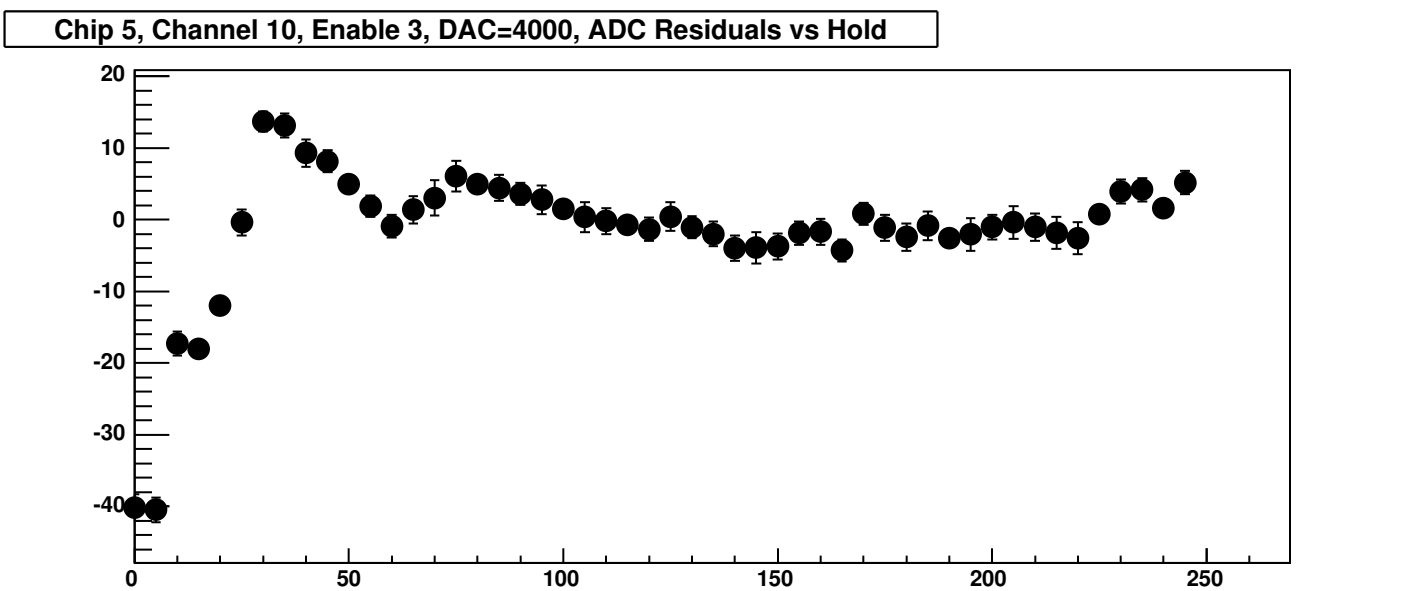

Chip 5, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 159.6 / 41
p0 -134.7 ± 0.5037
p1 57.76 ± 0.456
p2 -7.307e+07 ± 1.125e+06
p3 2.586e+07 ± 1.033e+05
p4 2.549 ± 0.04189
-120
-140
-160
-180
-200
-220
-240
-260
-280
-300
0
50
100
150
200
250


Chip 5, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

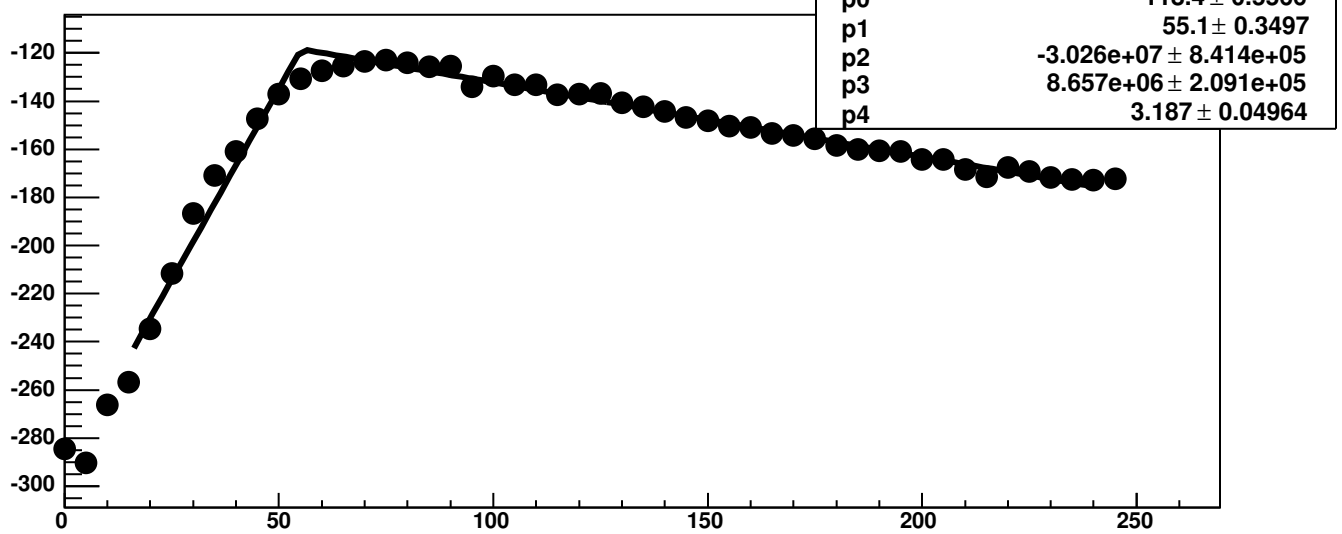

Chip 5, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold
12
10
8
6
4
2
0
-2
-4
-6
-8
0
50
100
150
200
250


Chip 5, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 650.4 / 41
p0 -118.7 ± 0.4518
p1 58.26 ± 0.3288
p2 -3.833e+08 ± 1.414
p3 1.569e+08 ± 1.414
p4 2.111 ± 0.004911
-100
-150
-200
-250
-300
0
50
100
150
200
250

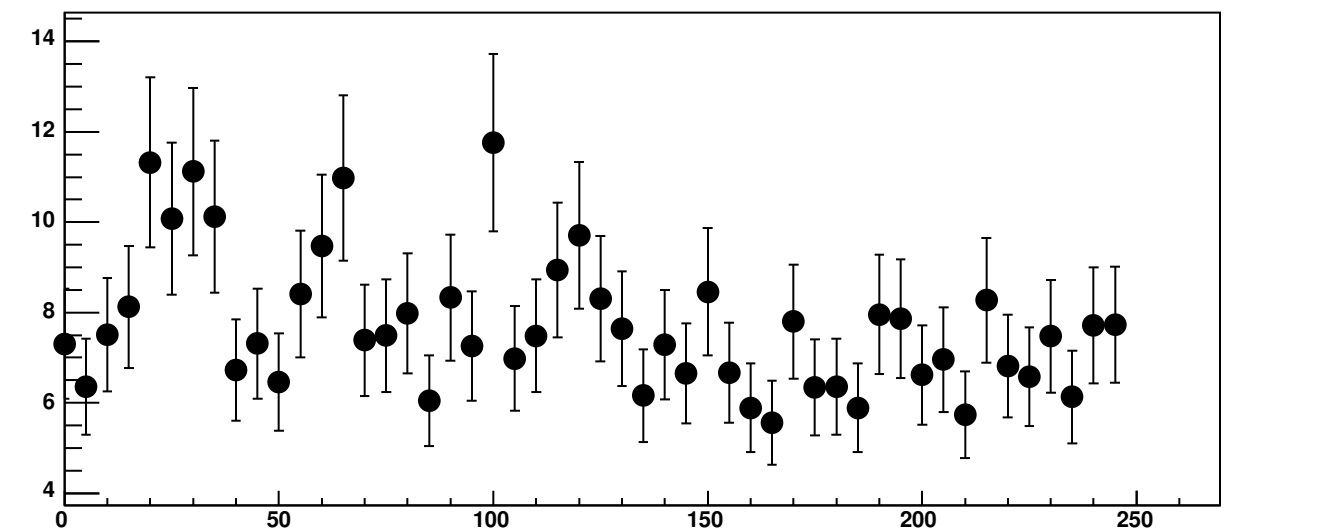

Chip 5, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 5, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
0
50
100
150
200
250

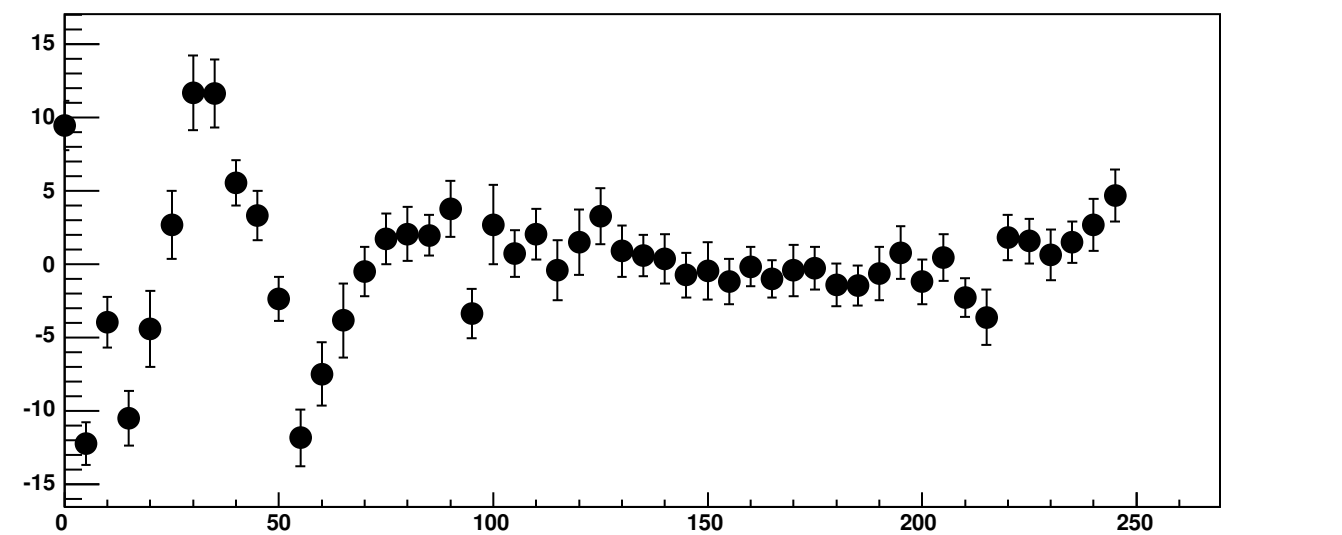
Chip 5, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

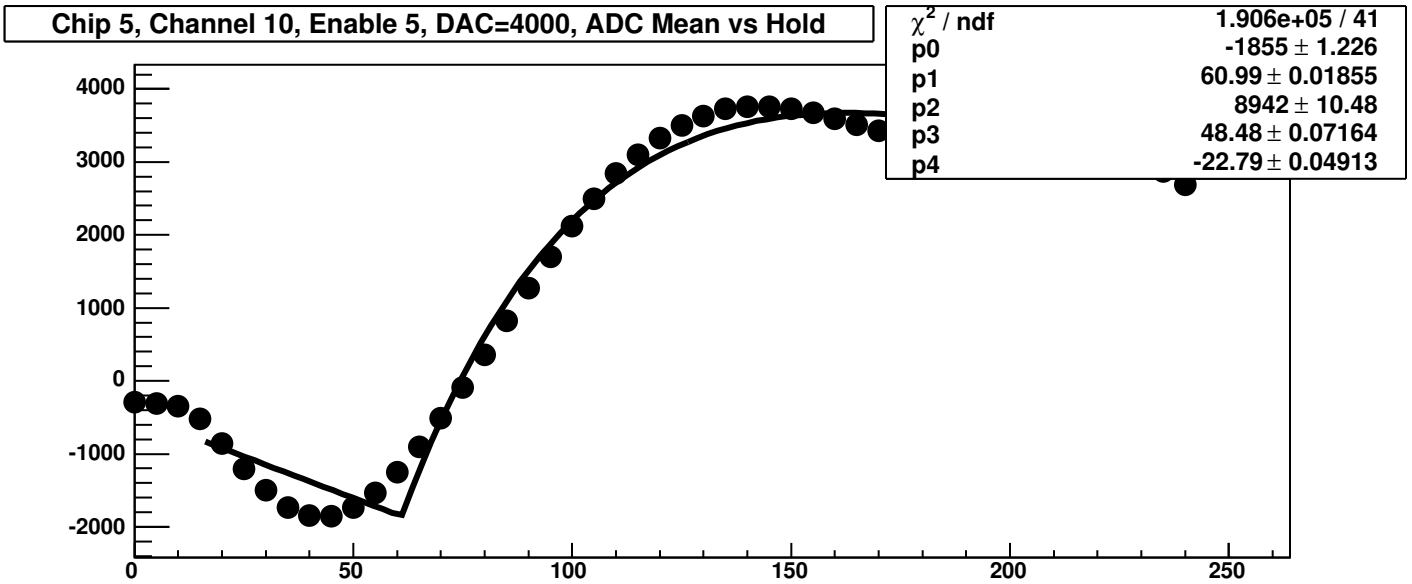


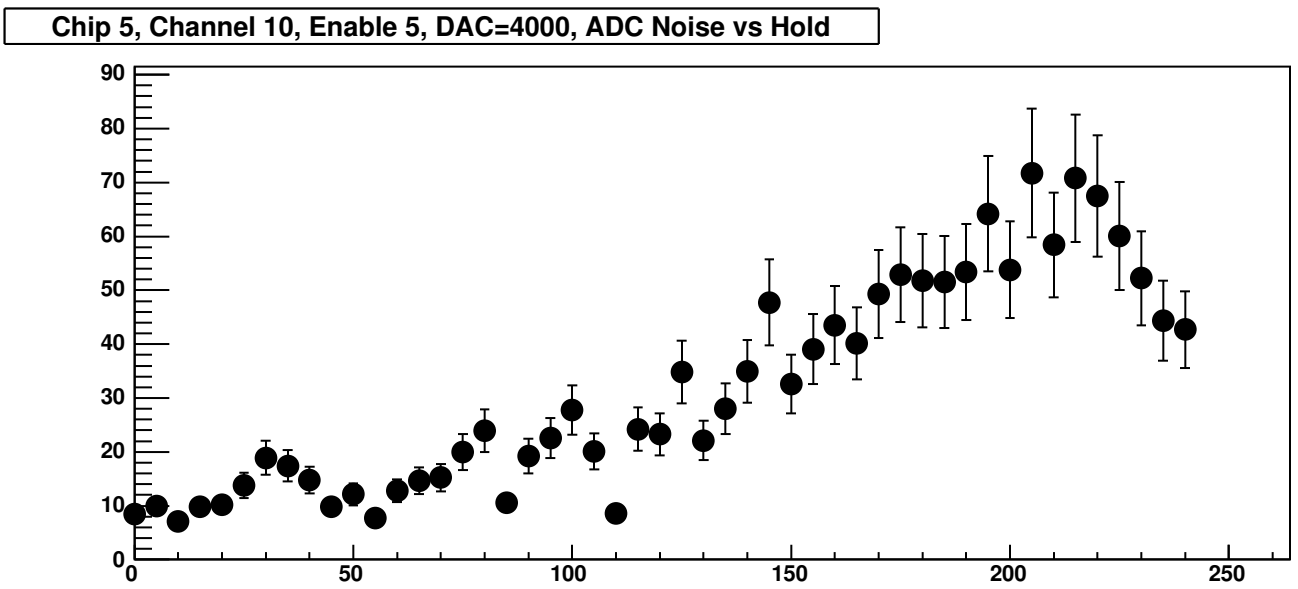
Chip 5, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold

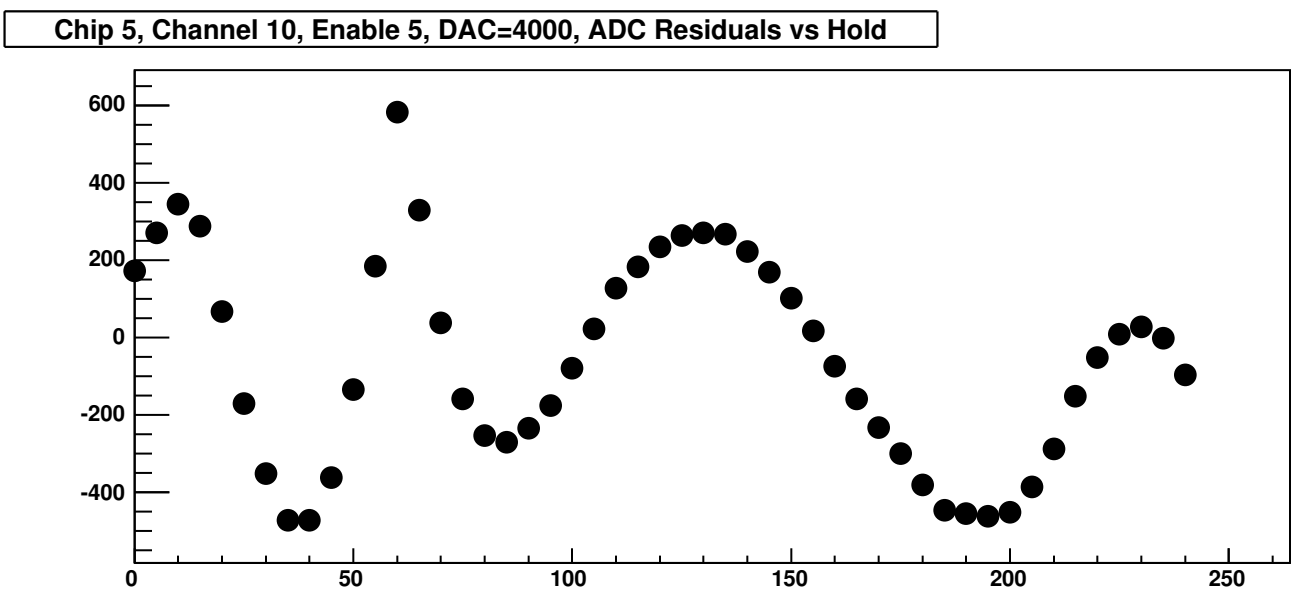


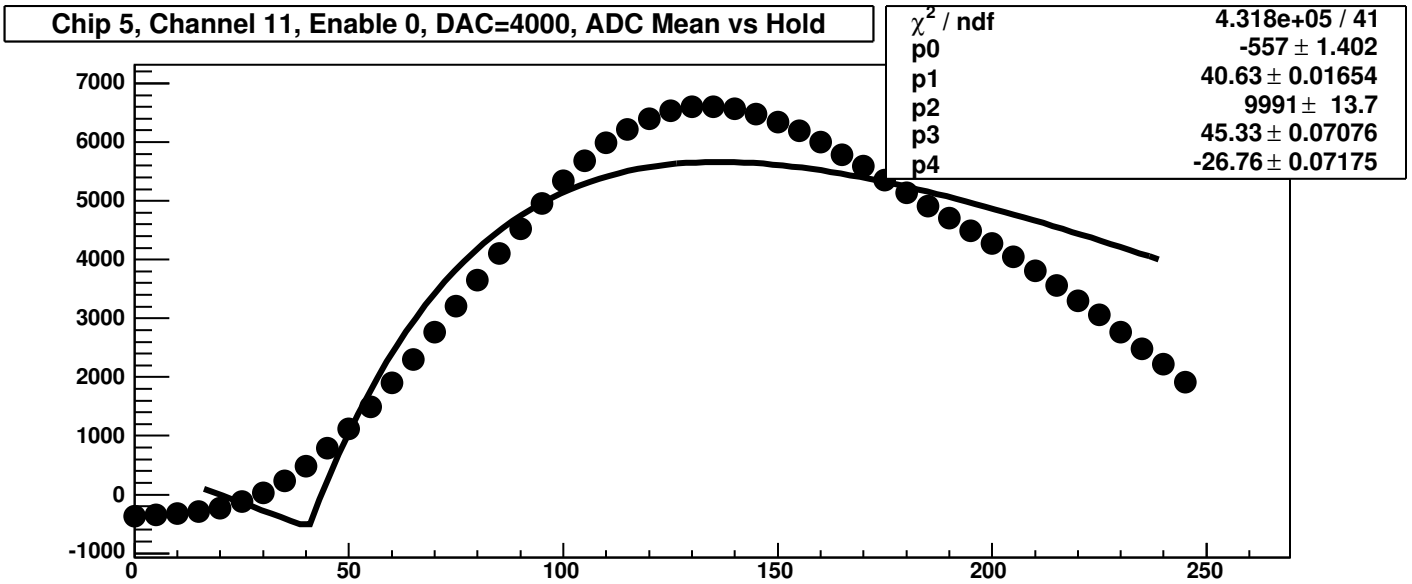
Chip 5, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

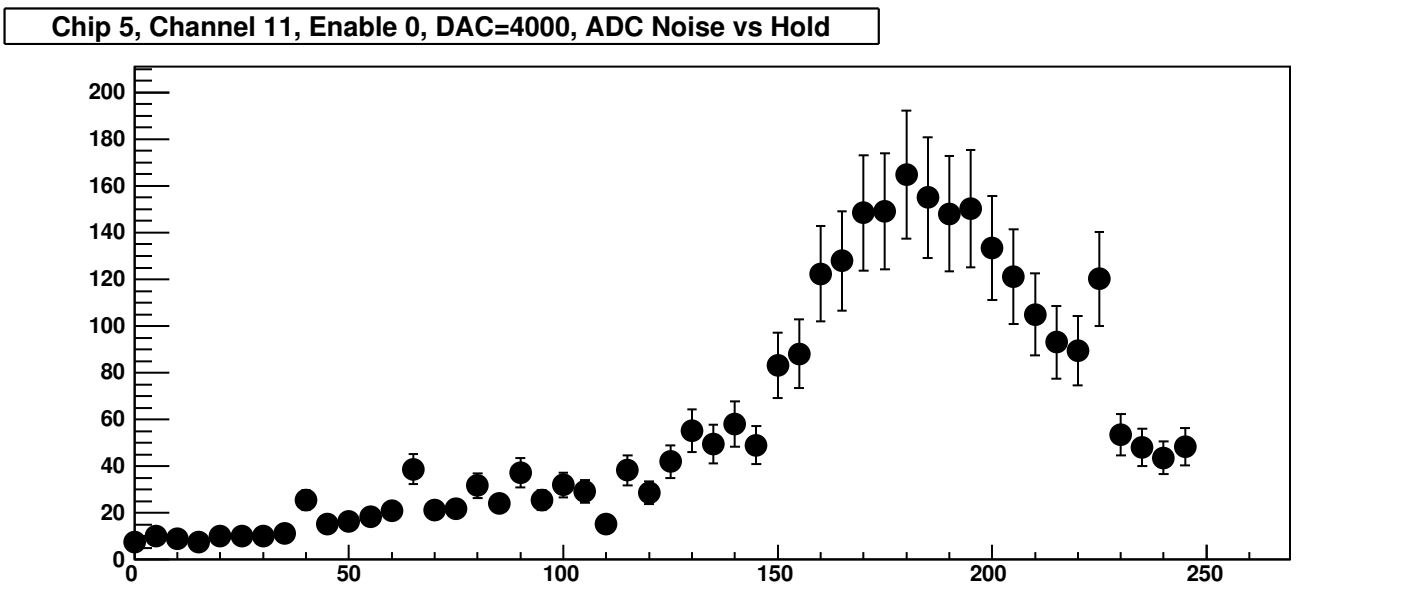


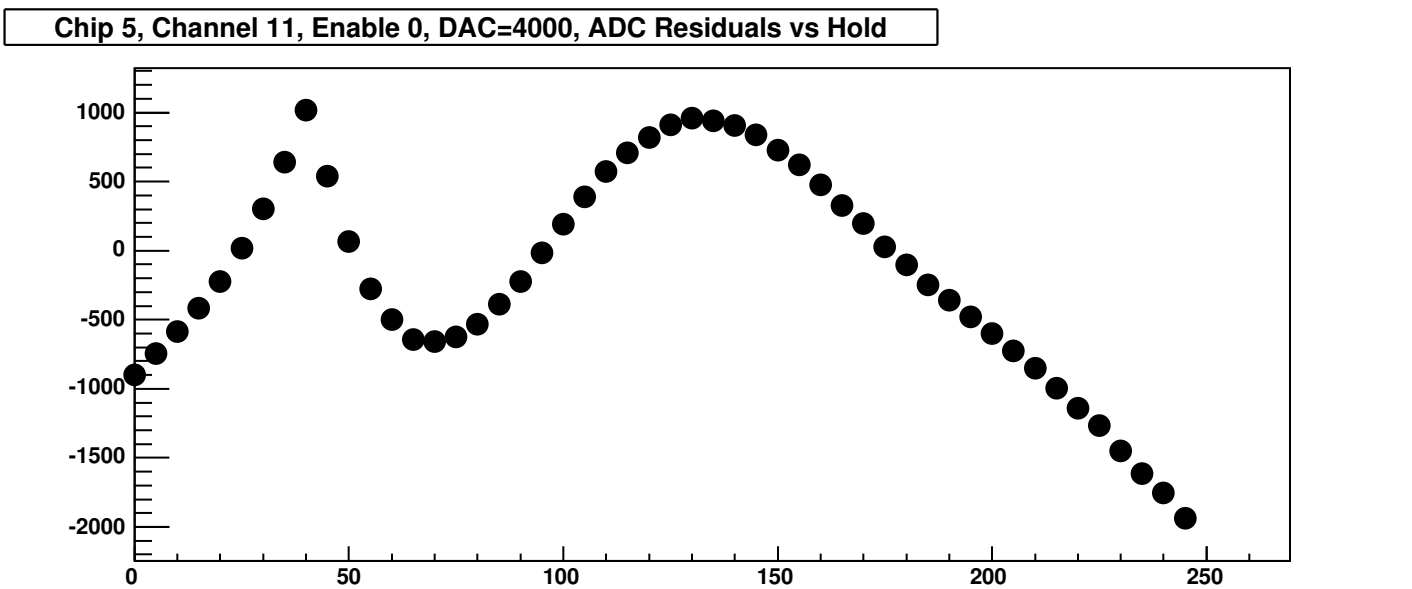

Chip 5, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.906e+05 / 41
p0 -1855 ± 1.226
p1 60.99 ± 0.01855
p2 8942 ± 10.48
p3 48.48 ± 0.07164
p4 -22.79 ± 0.04913
4000
3000
2000
1000
0
-1000
-2000
0
50
100
150
200
250


Chip 5, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold
90
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250

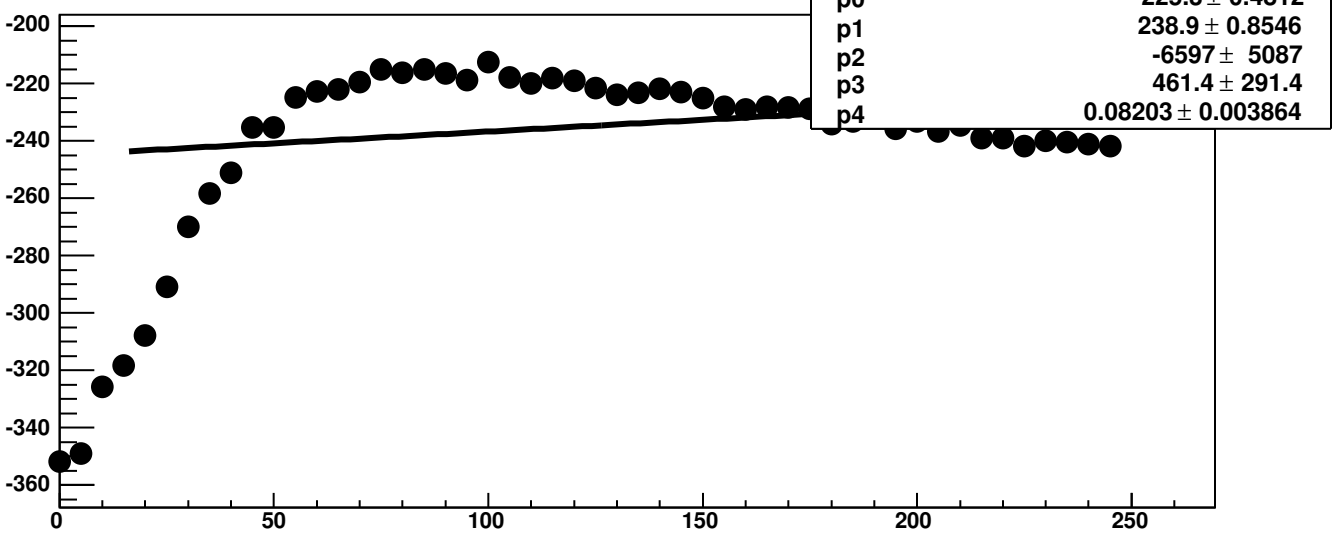

Chip 5, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold
600
400
200
0
-200
-400
0
50
100
150
200
250


Chip 5, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.318e+05 / 41
p0 -557 ± 1.402
p1 40.63 ± 0.01654
p2 9991 ± 13.7
p3 45.33 ± 0.07076
p4 -26.76 ± 0.07175
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

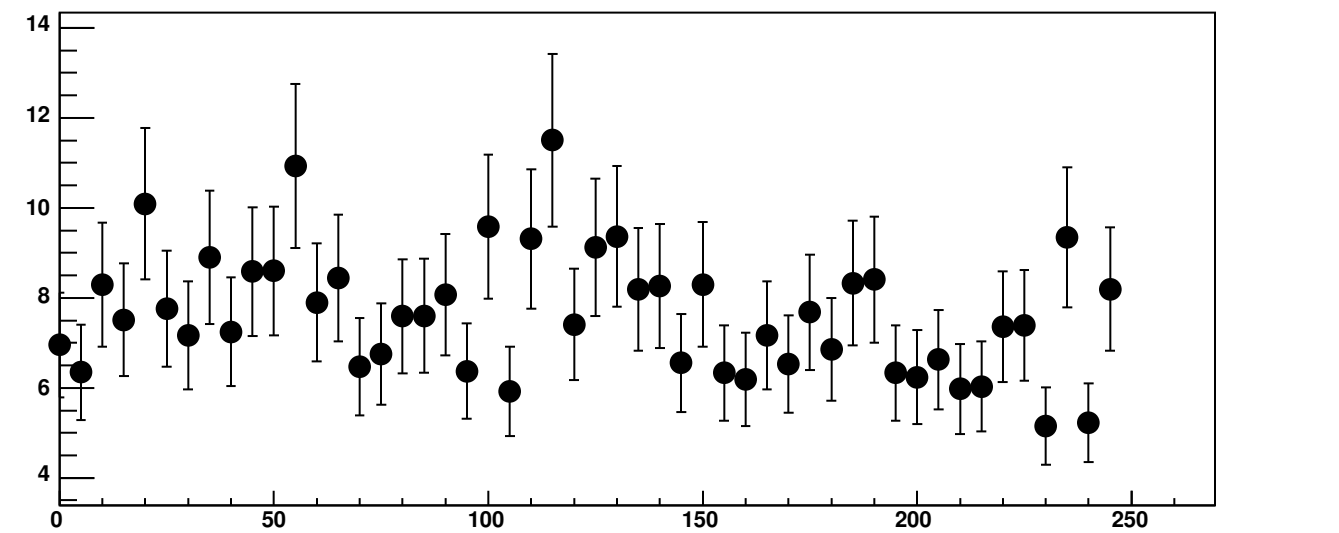

Chip 5, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 5, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

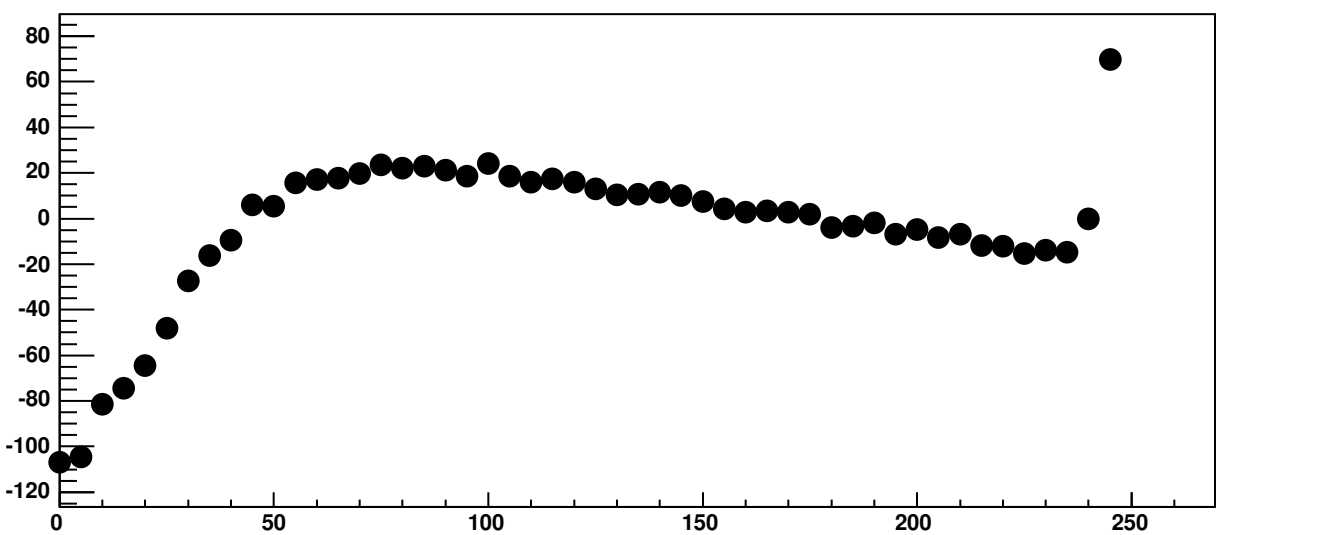
Chip 5, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold



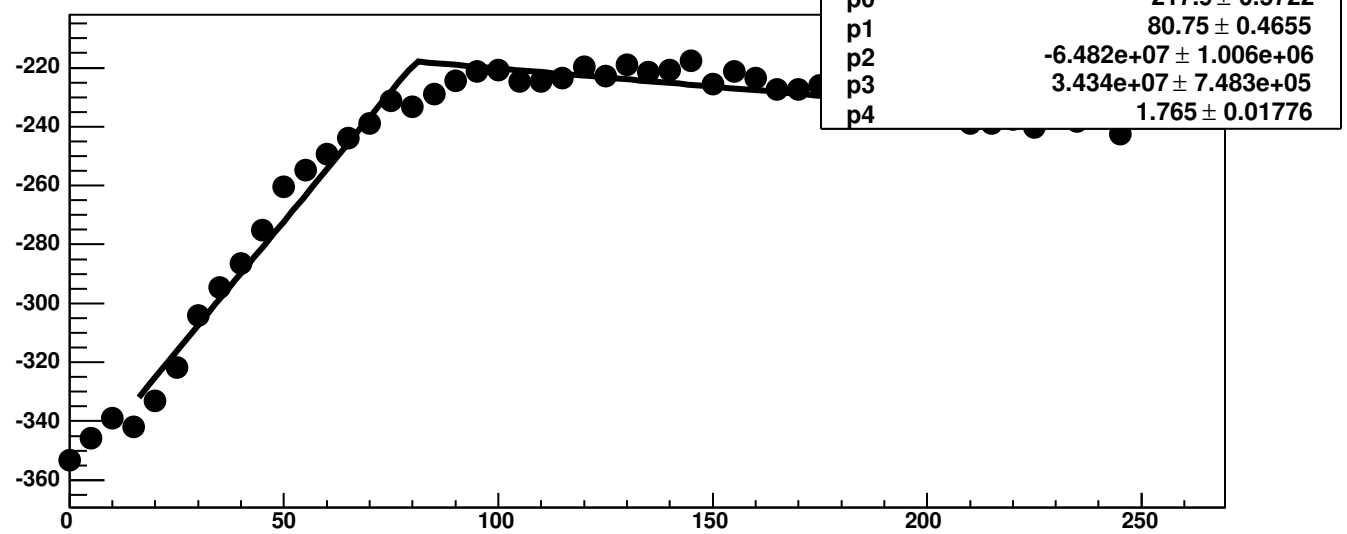
Chip 5, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



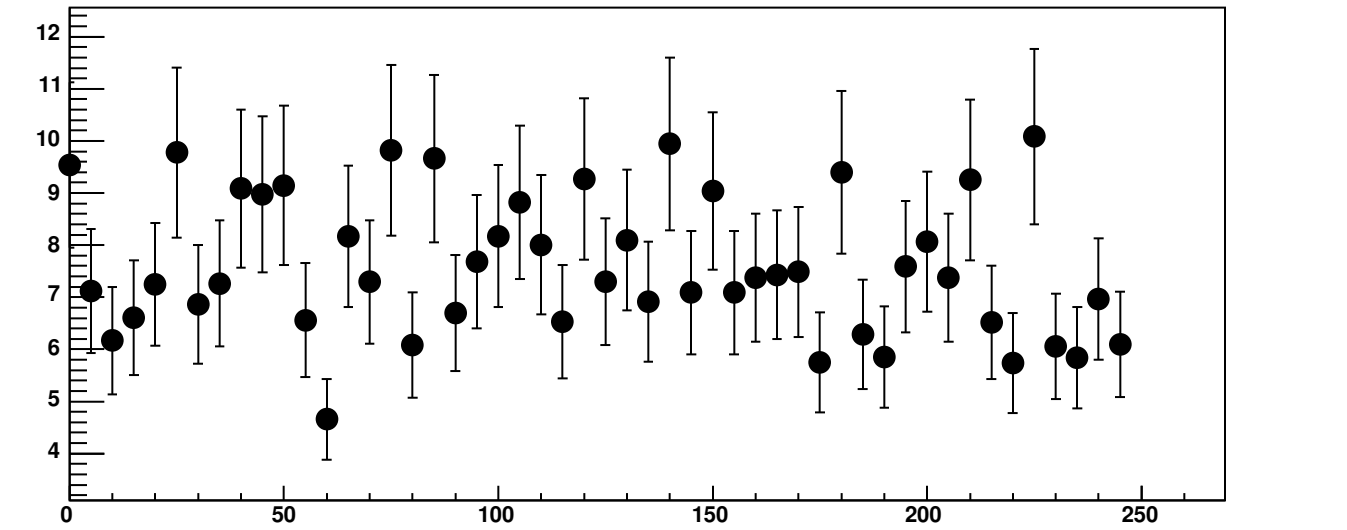
Chip 5, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold



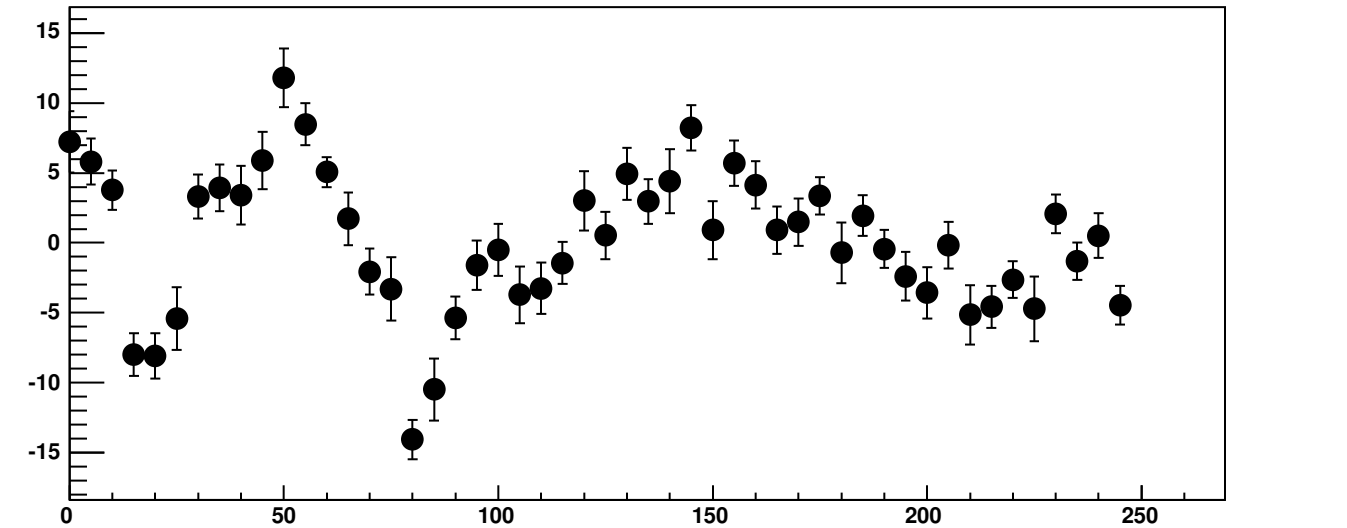
Chip 5, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold

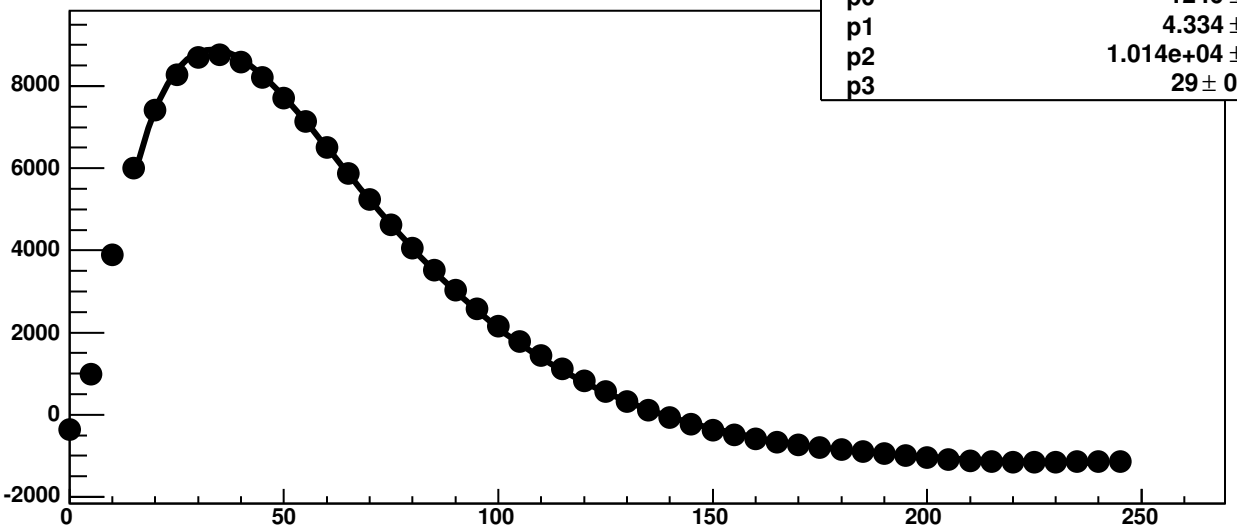


Chip 5, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

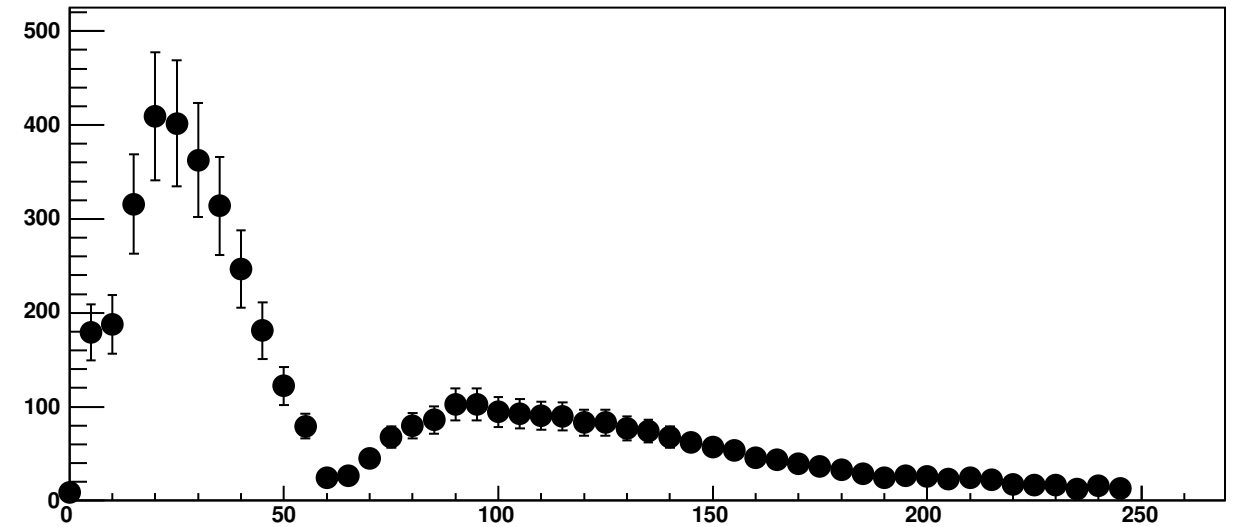


Chip 5, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold

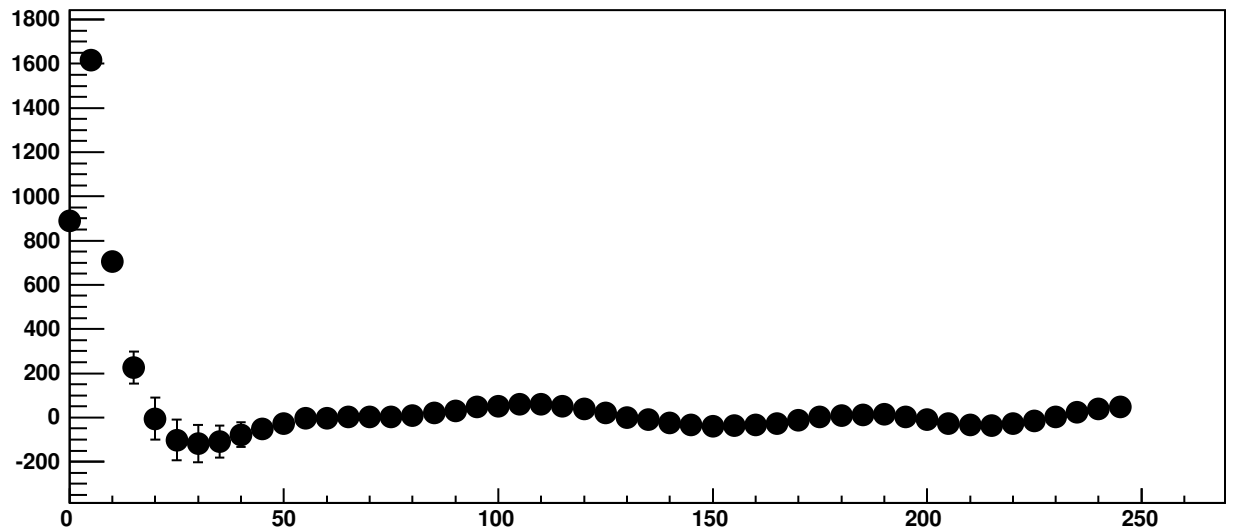
χ^2 / ndf	493.4 / 42
p0	-1246 ± 1.753
p1	4.334 ± 0.125
p2	1.014e+04 ± 16.09
p3	29 ± 0.04475



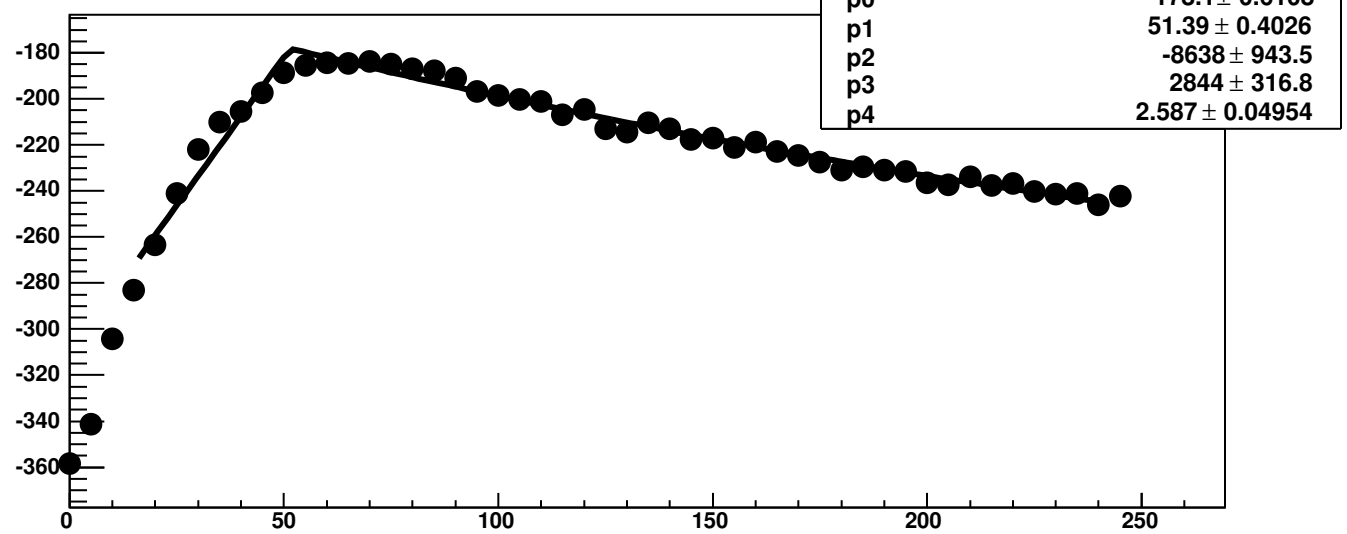
Chip 5, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold



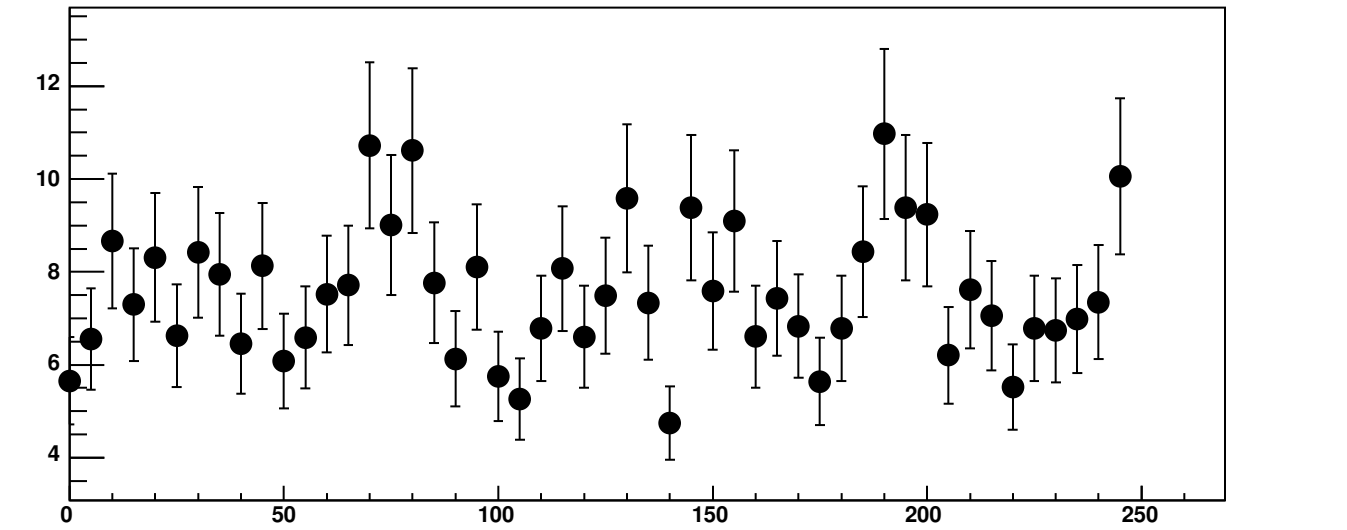
Chip 5, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold



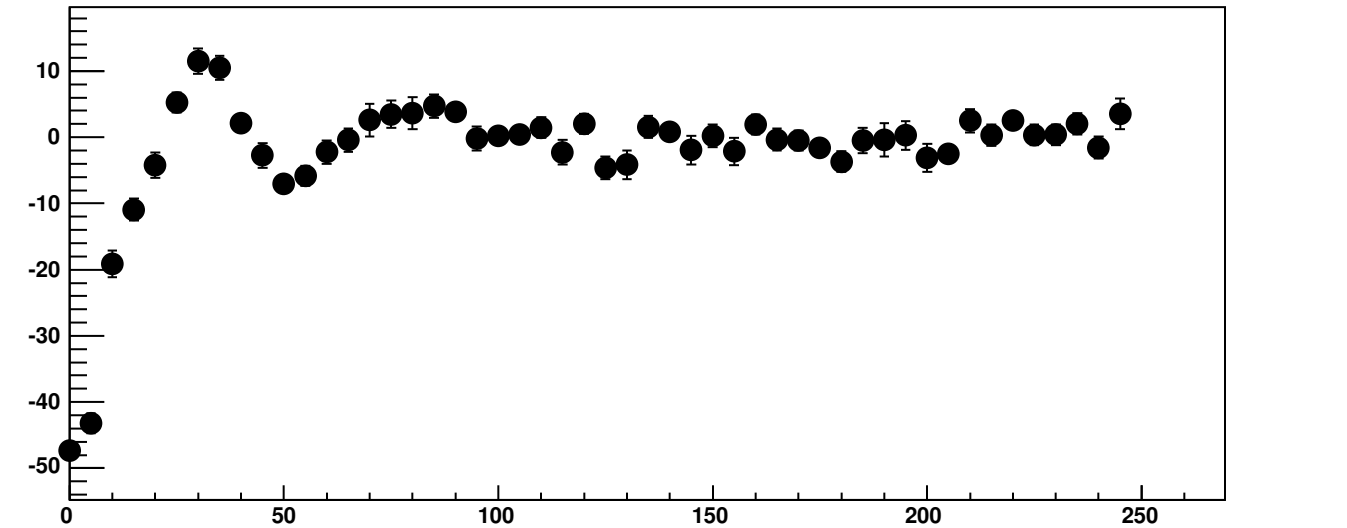
Chip 5, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold



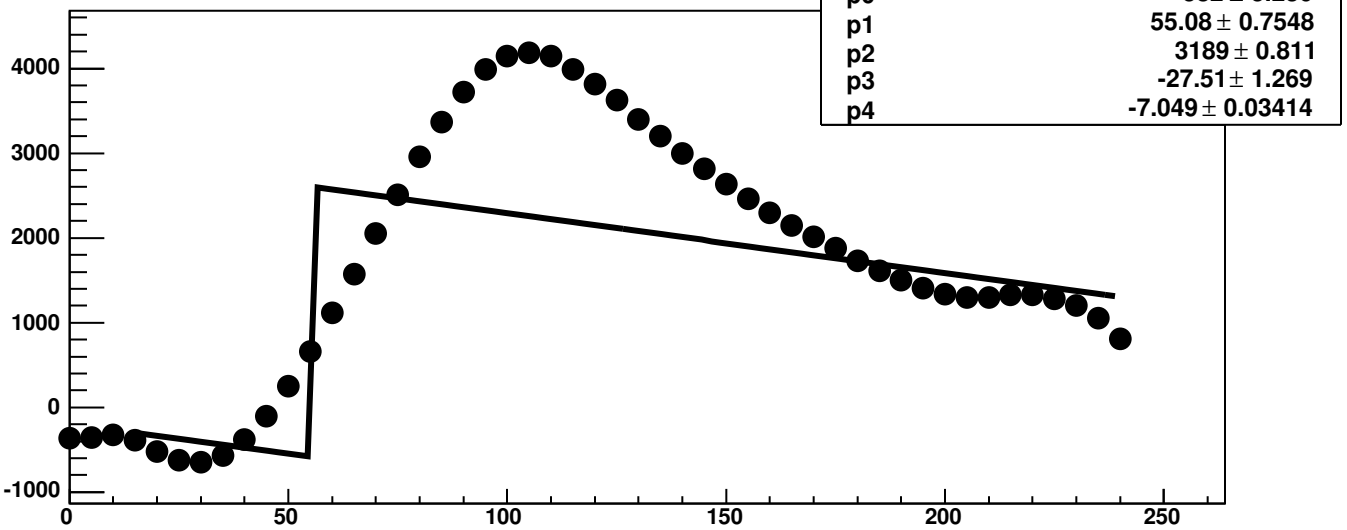
Chip 5, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



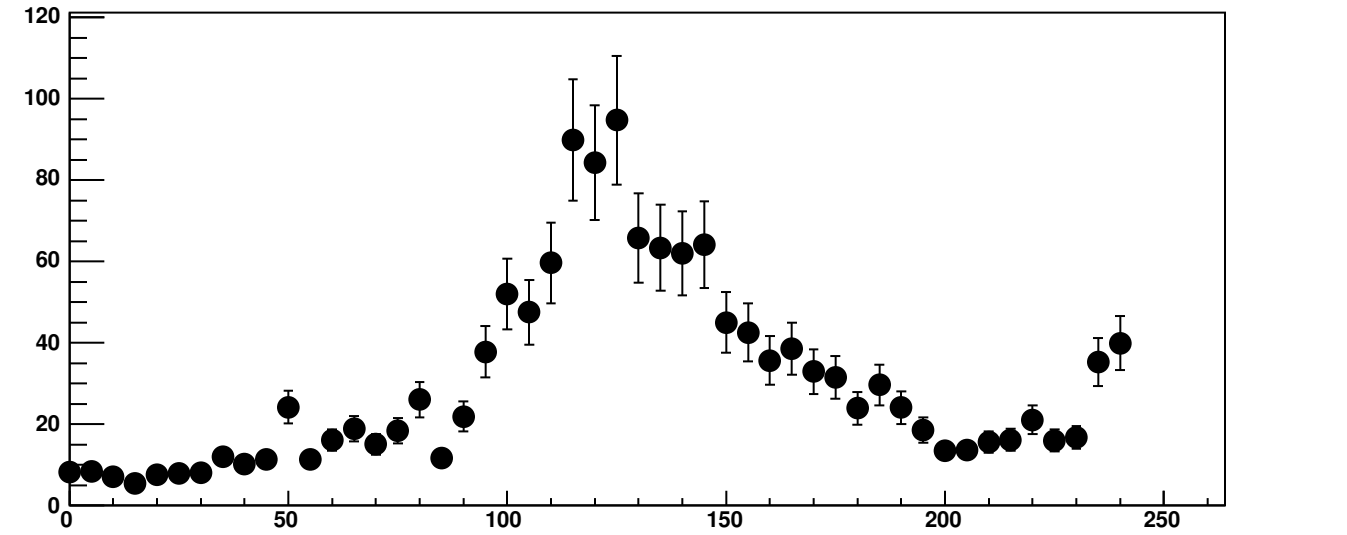
Chip 5, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold



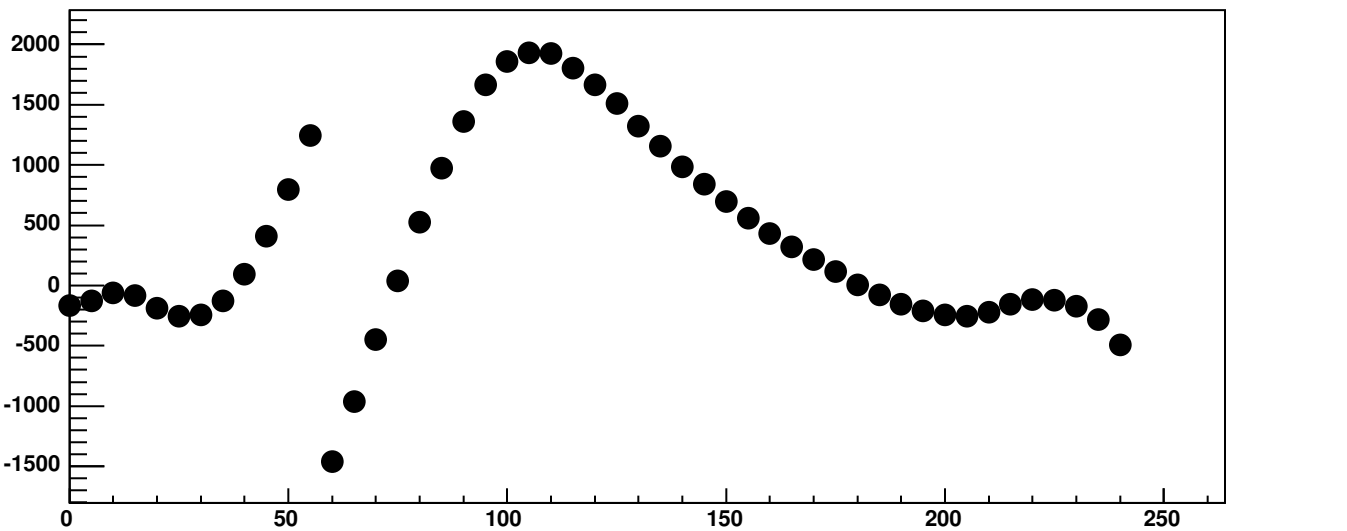
Chip 5, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold

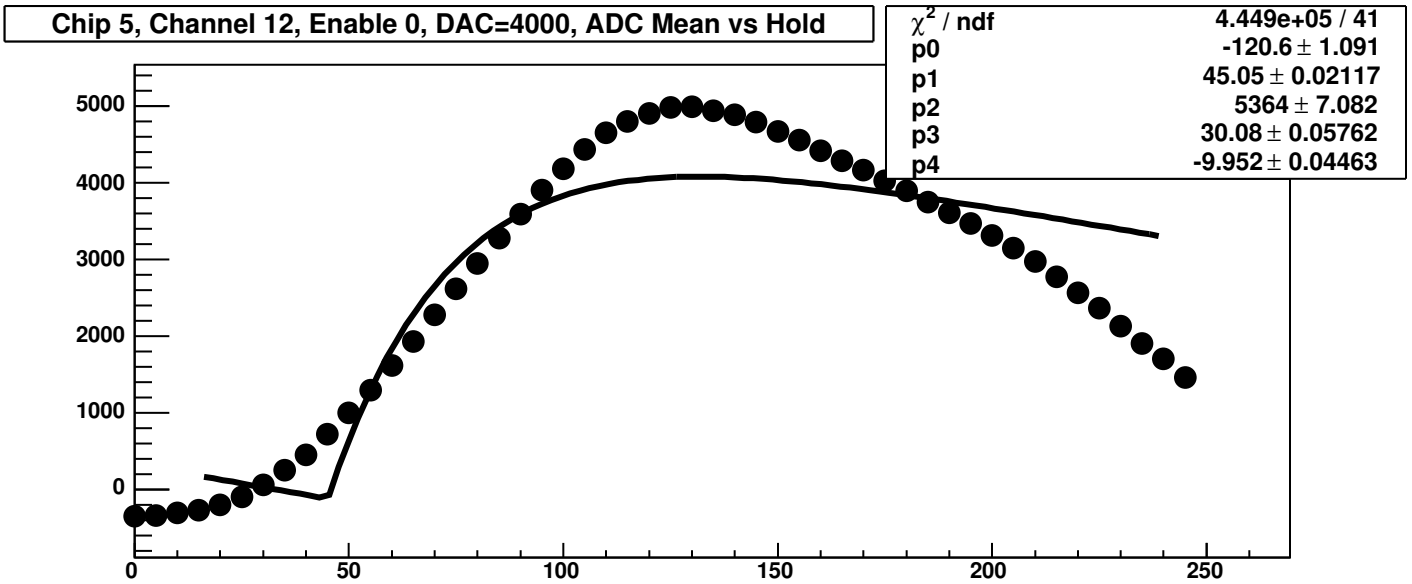


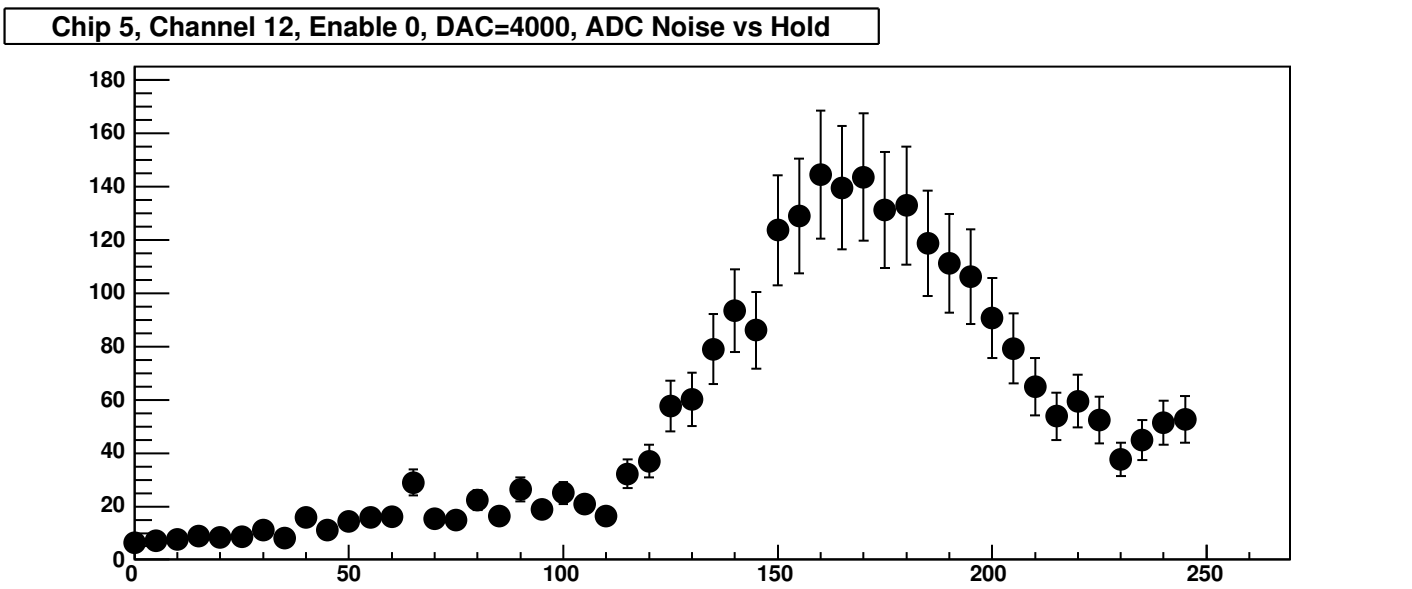
Chip 5, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold

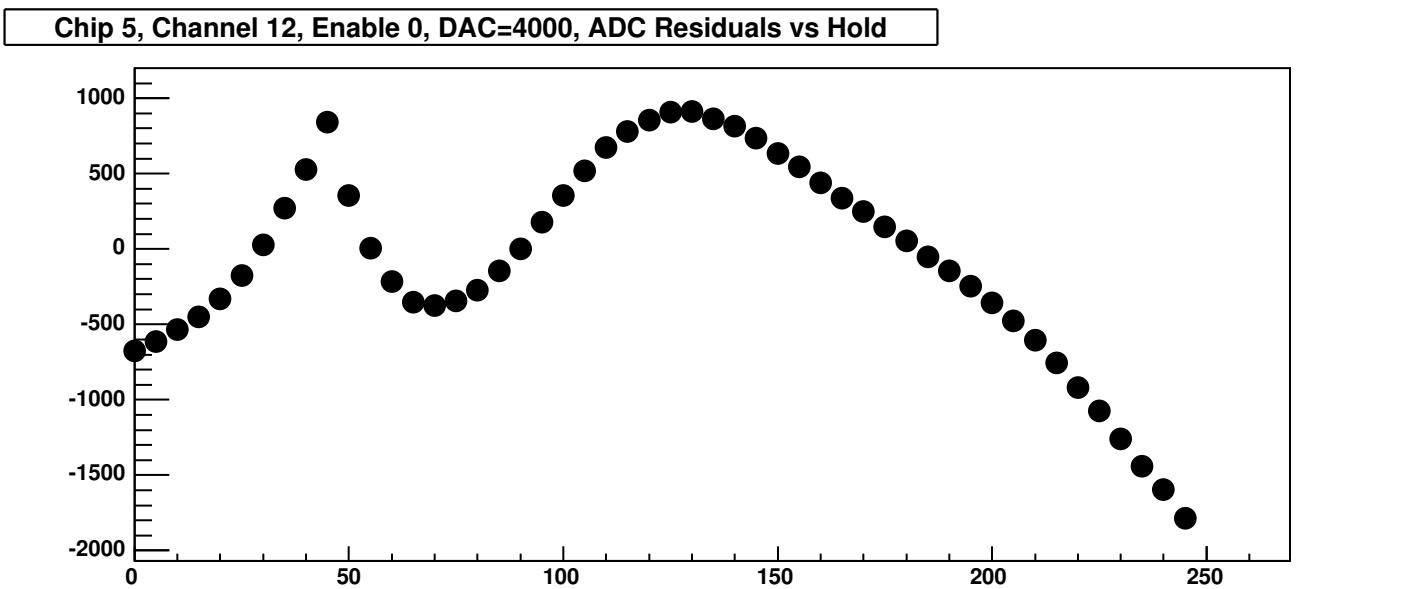


Chip 5, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold

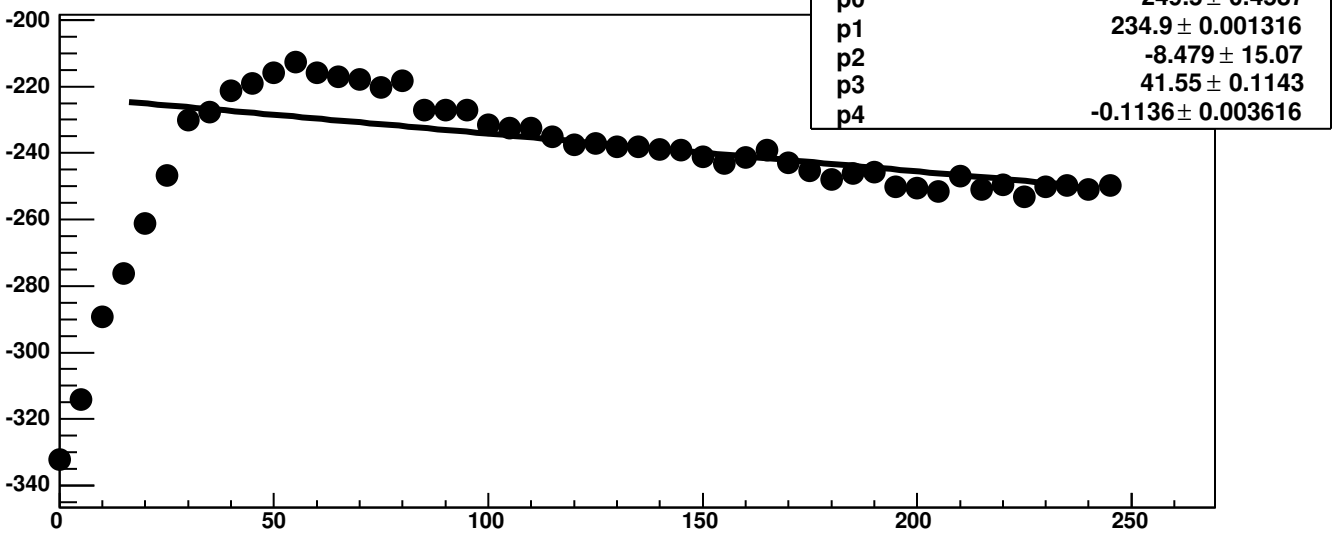



Chip 5, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.449e+05 / 41
p0 -120.6 ± 1.091
p1 45.05 ± 0.02117
p2 5364 ± 7.082
p3 30.08 ± 0.05762
p4 -9.952 ± 0.04463
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

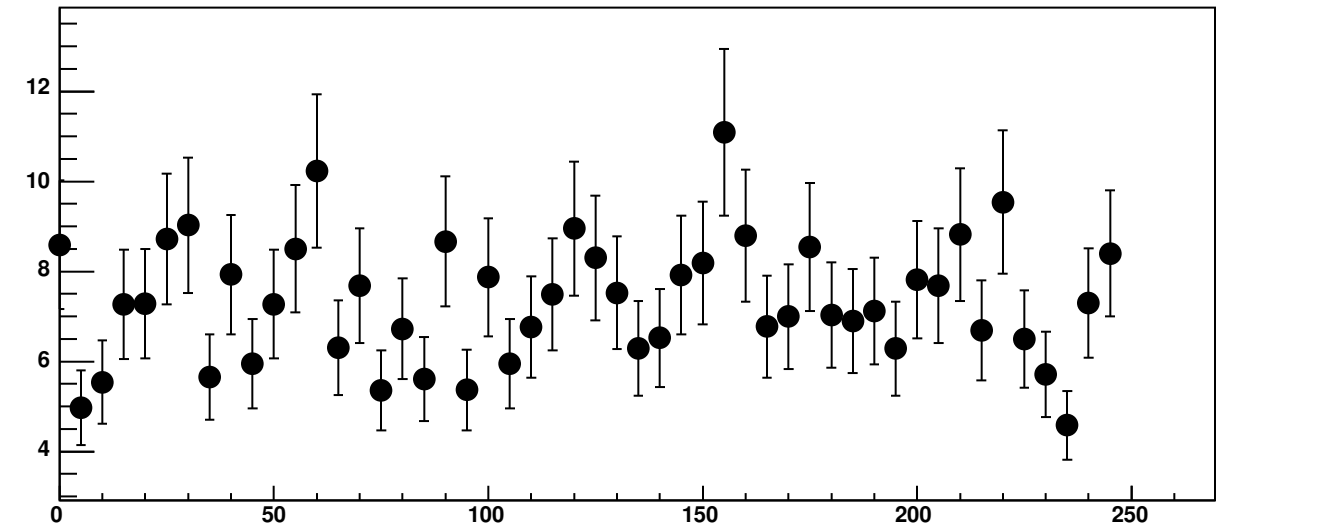

Chip 5, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 5, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

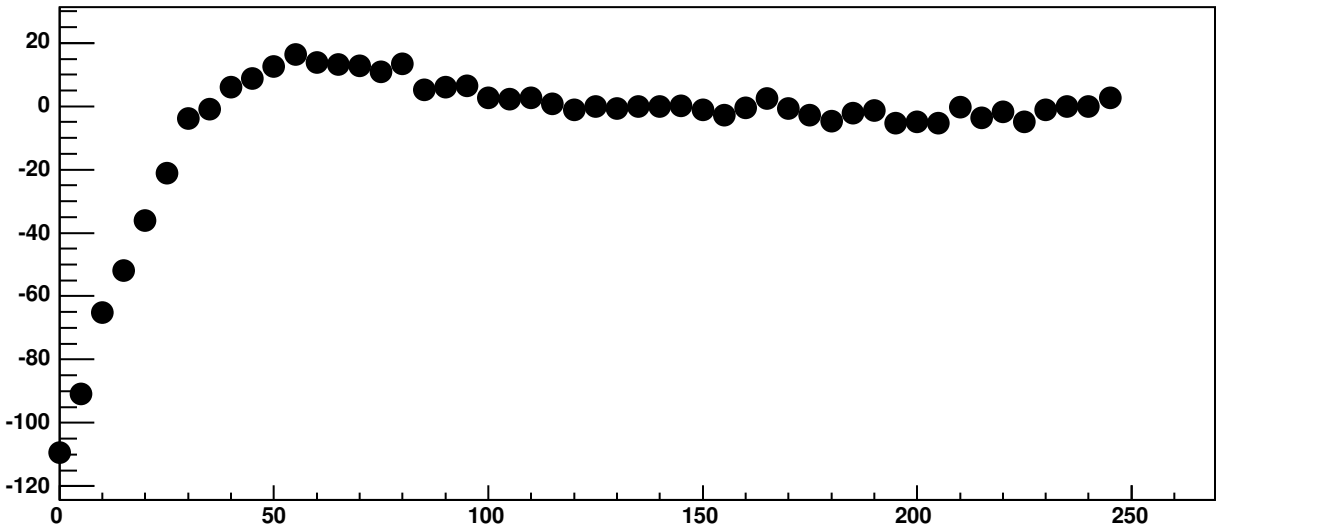
Chip 5, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold



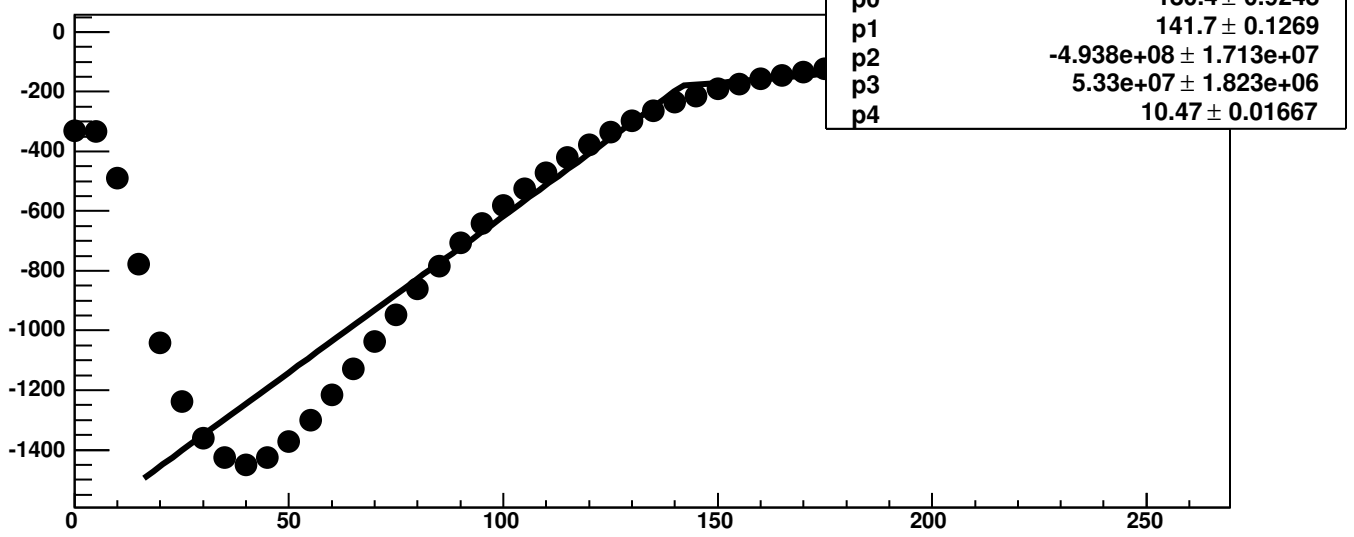
Chip 5, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold



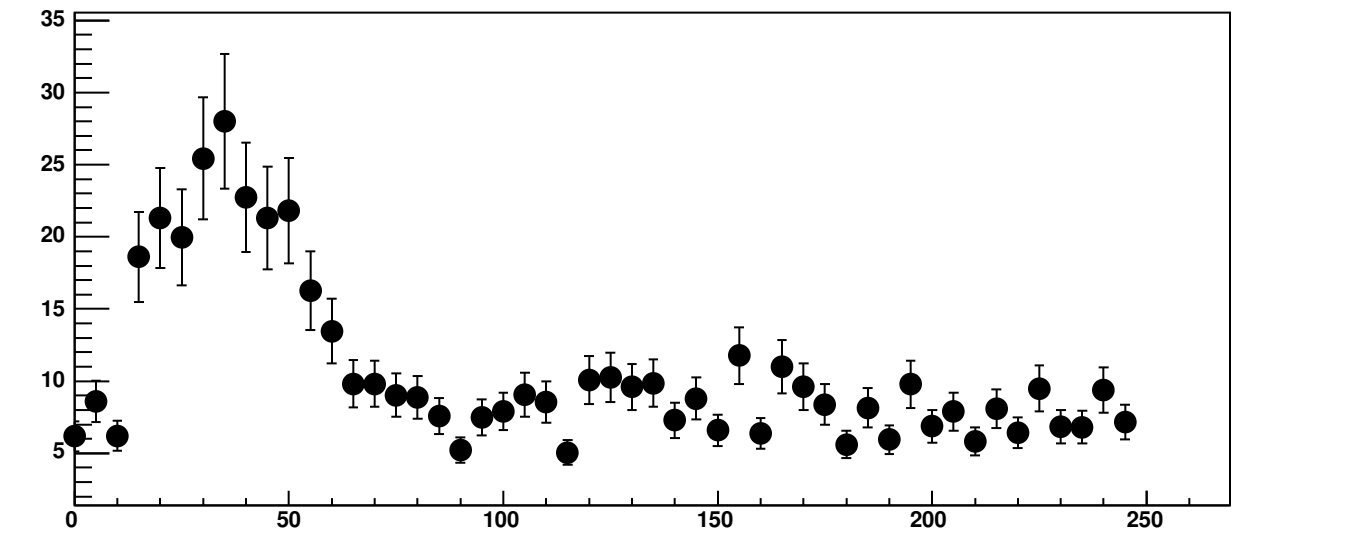
Chip 5, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold



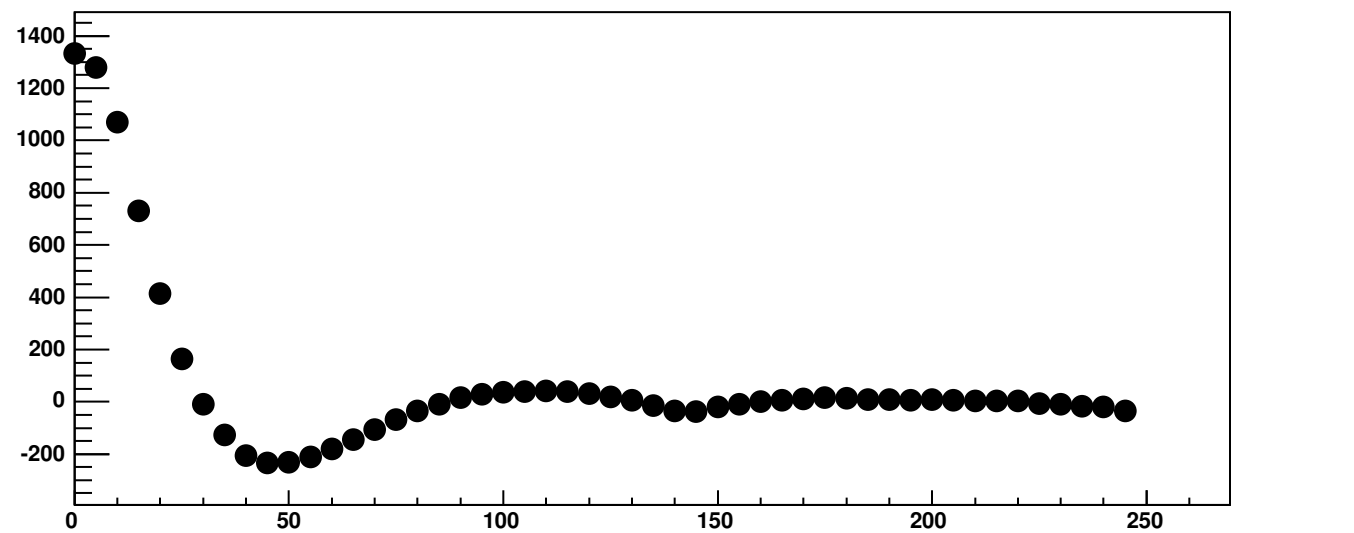
Chip 5, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold



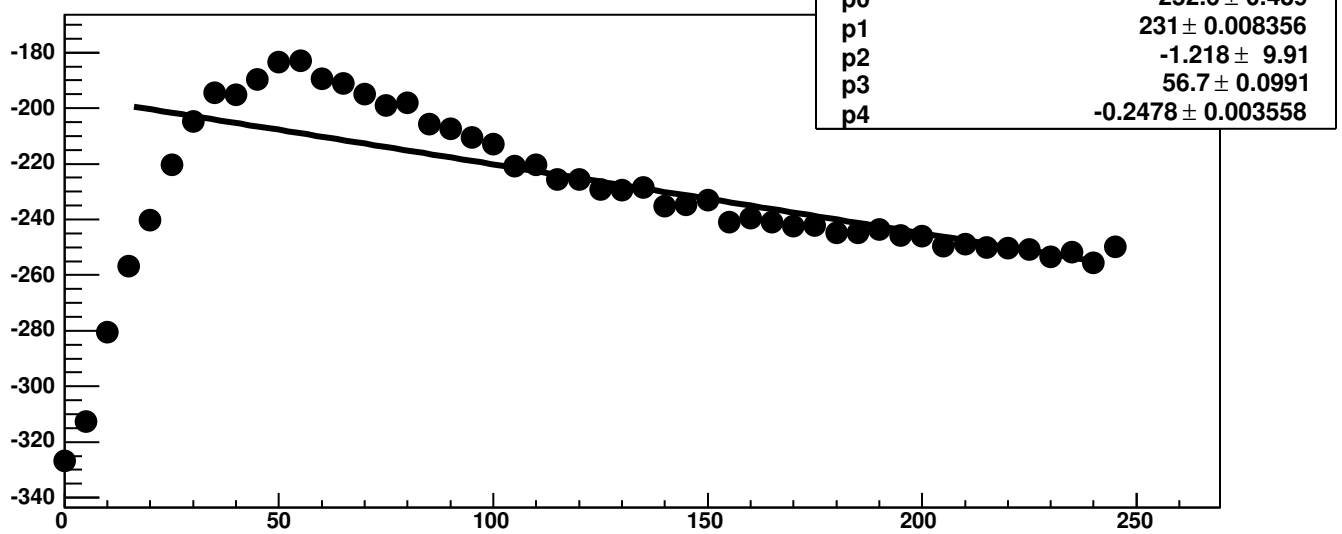
Chip 5, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



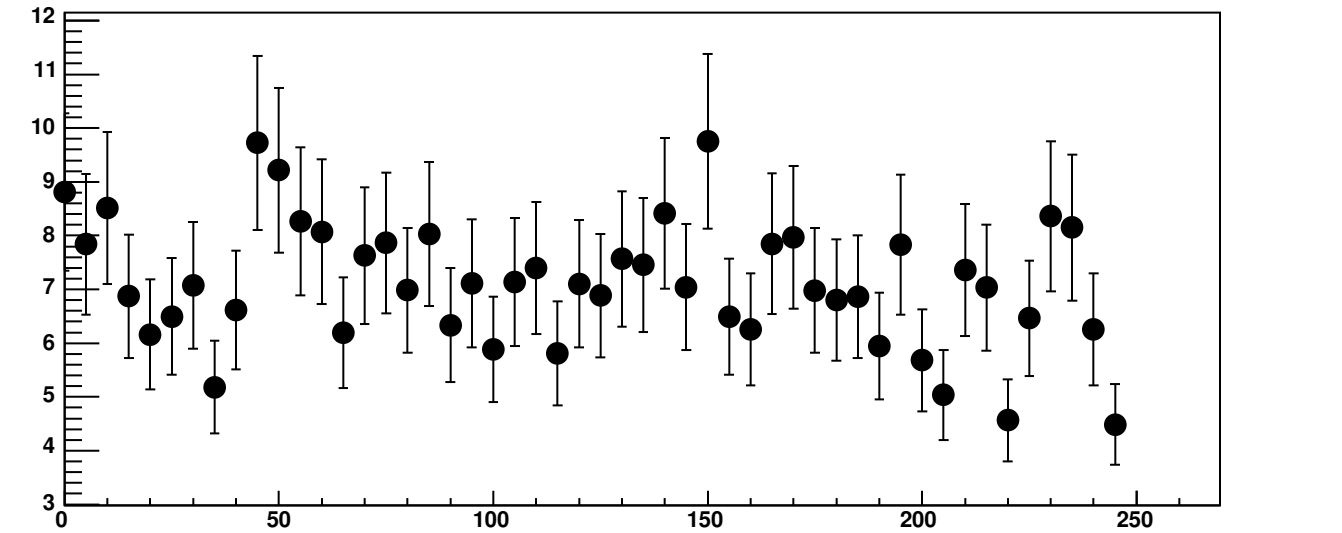
Chip 5, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold



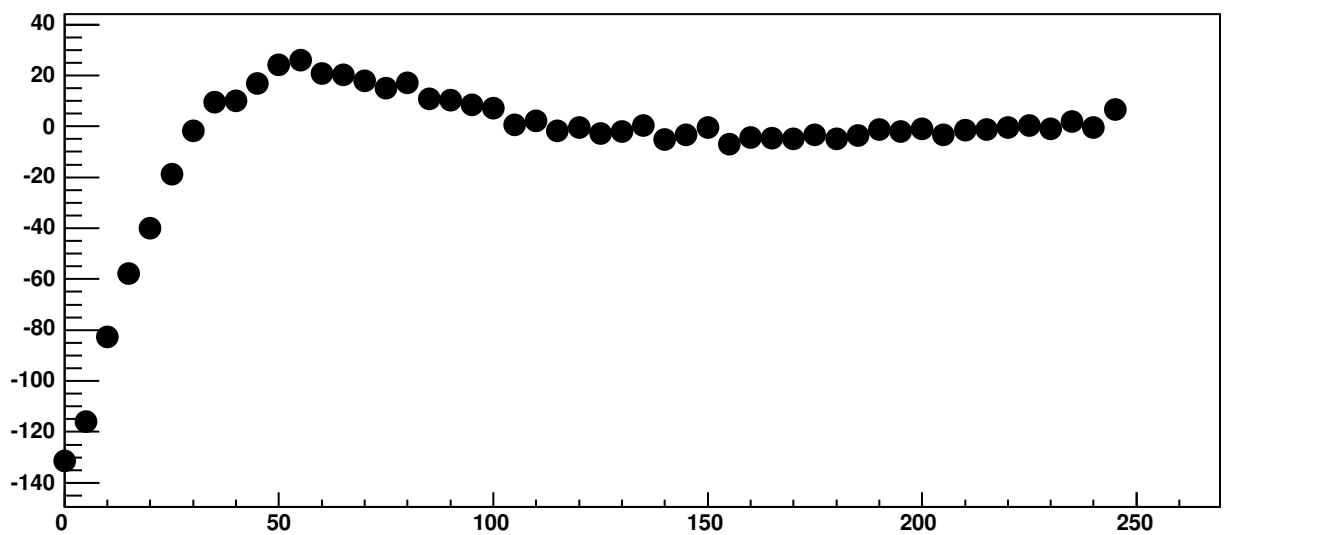
Chip 5, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold

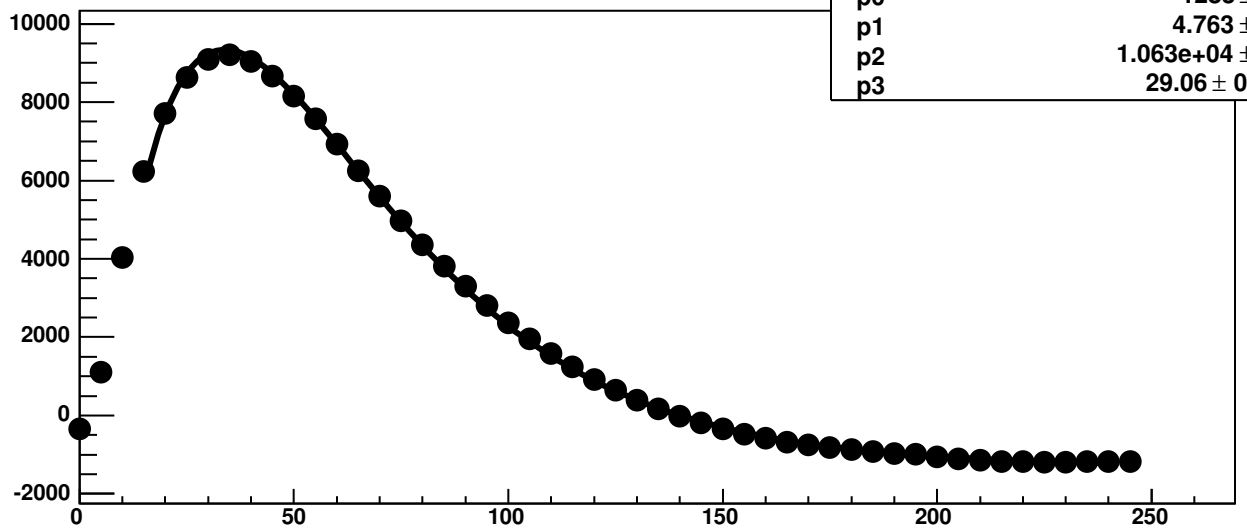


Chip 5, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

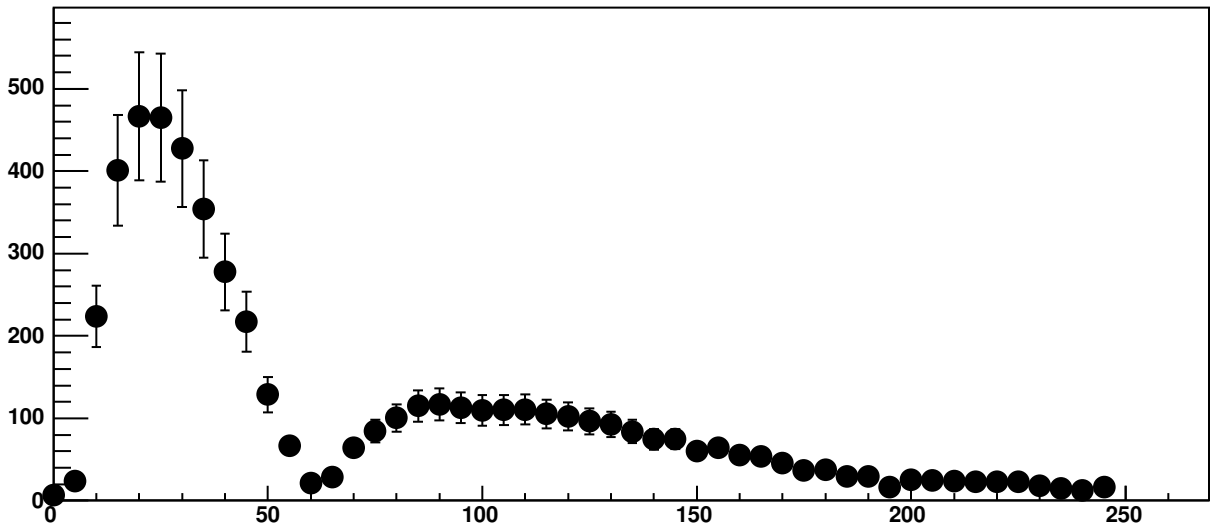


Chip 5, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold

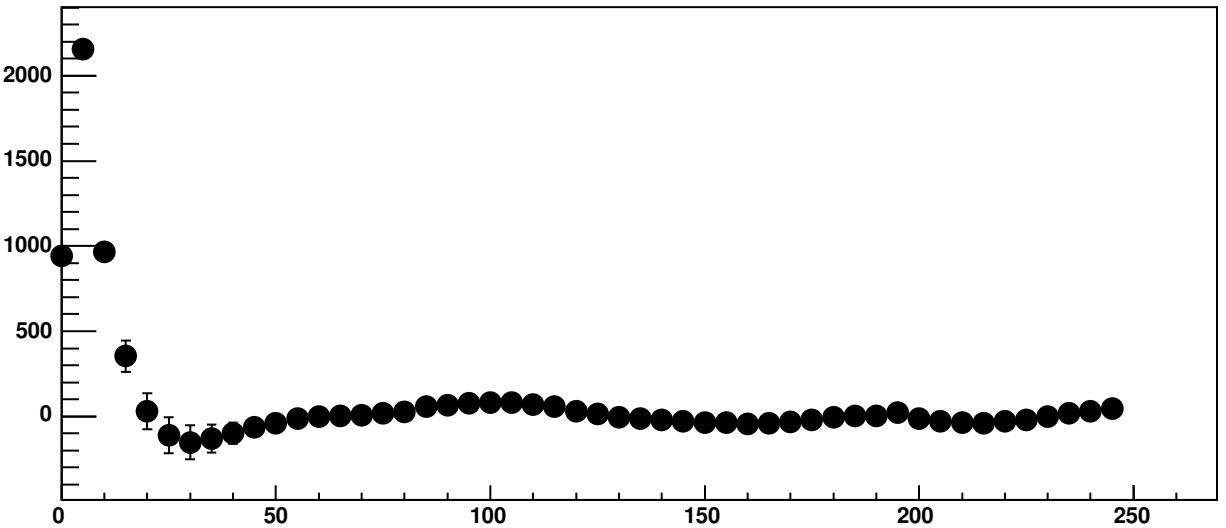
χ^2 / ndf	498.9 / 42
p0	-1283 ± 1.903
p1	4.763 ± 0.139
p2	1.063e+04 ± 18.22
p3	29.06 ± 0.04759



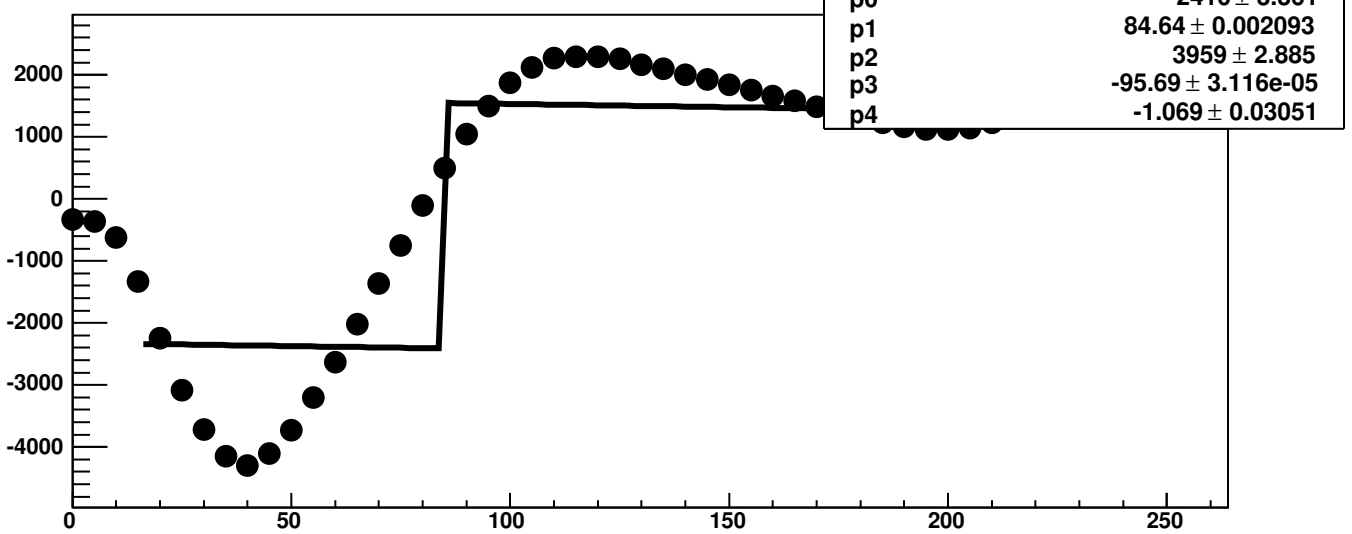
Chip 5, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold



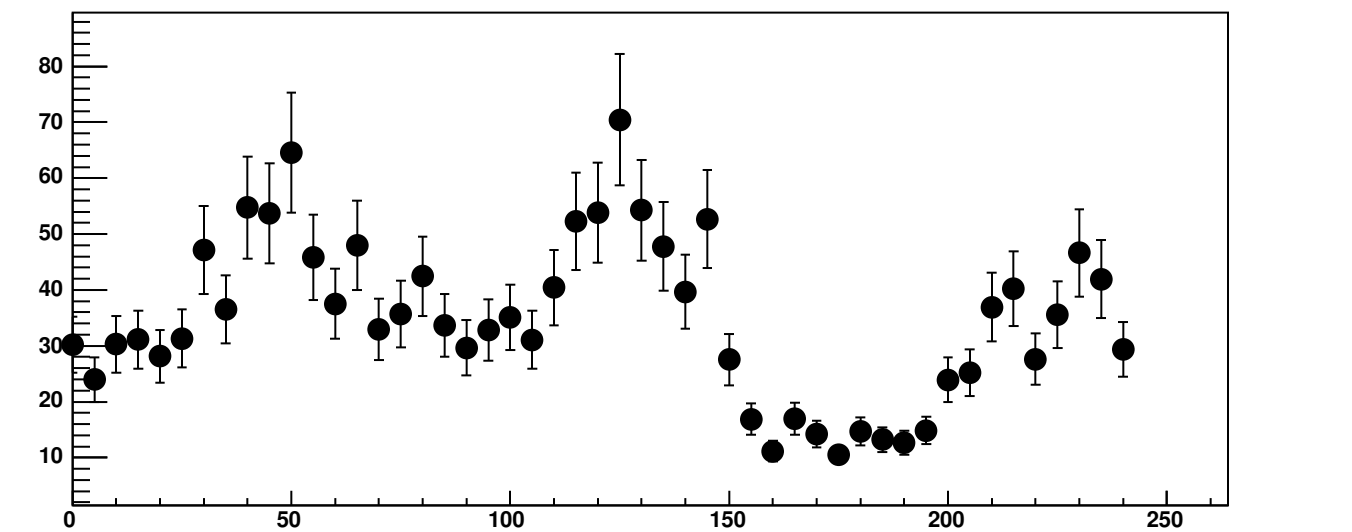
Chip 5, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold



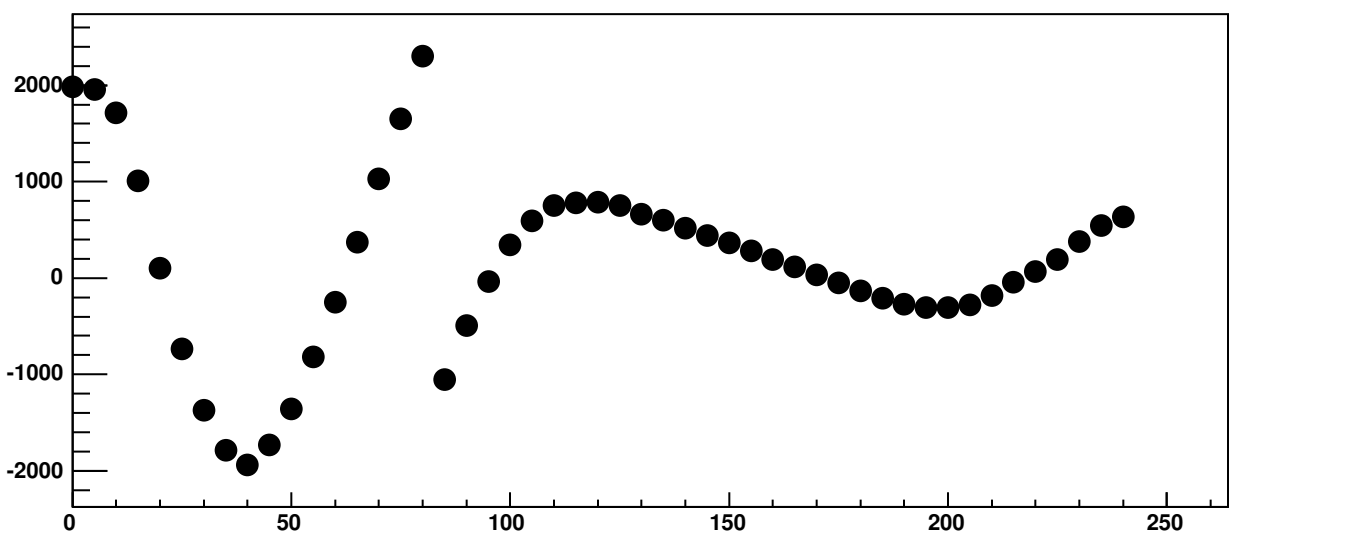
Chip 5, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold

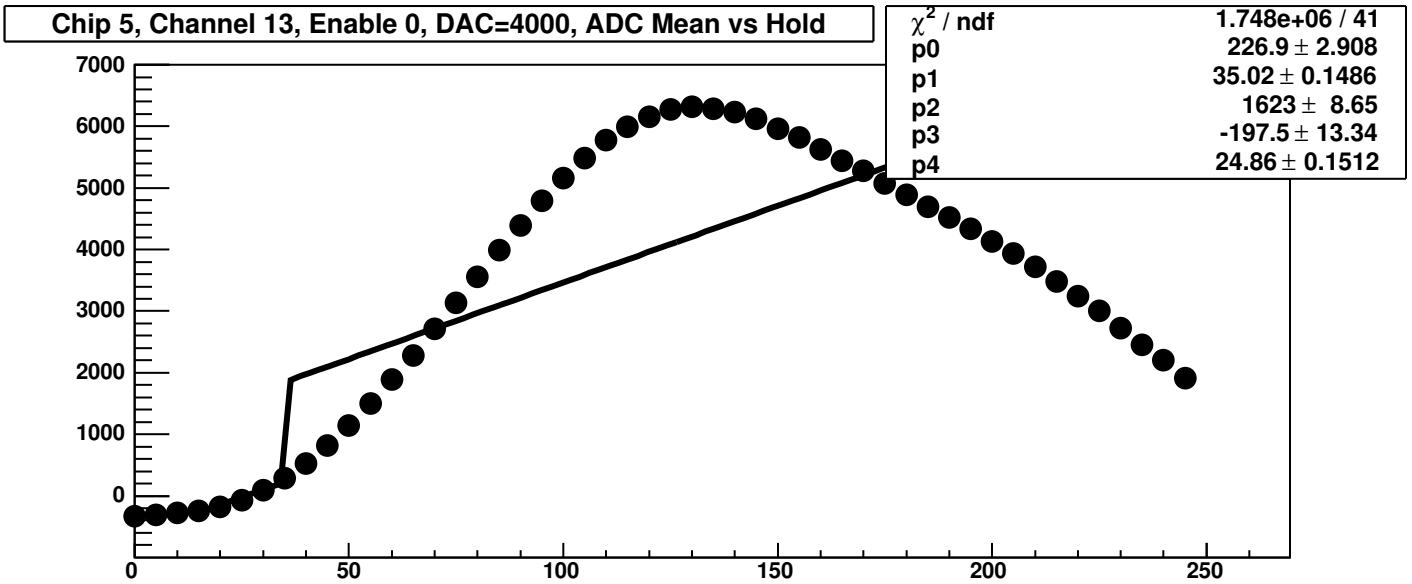


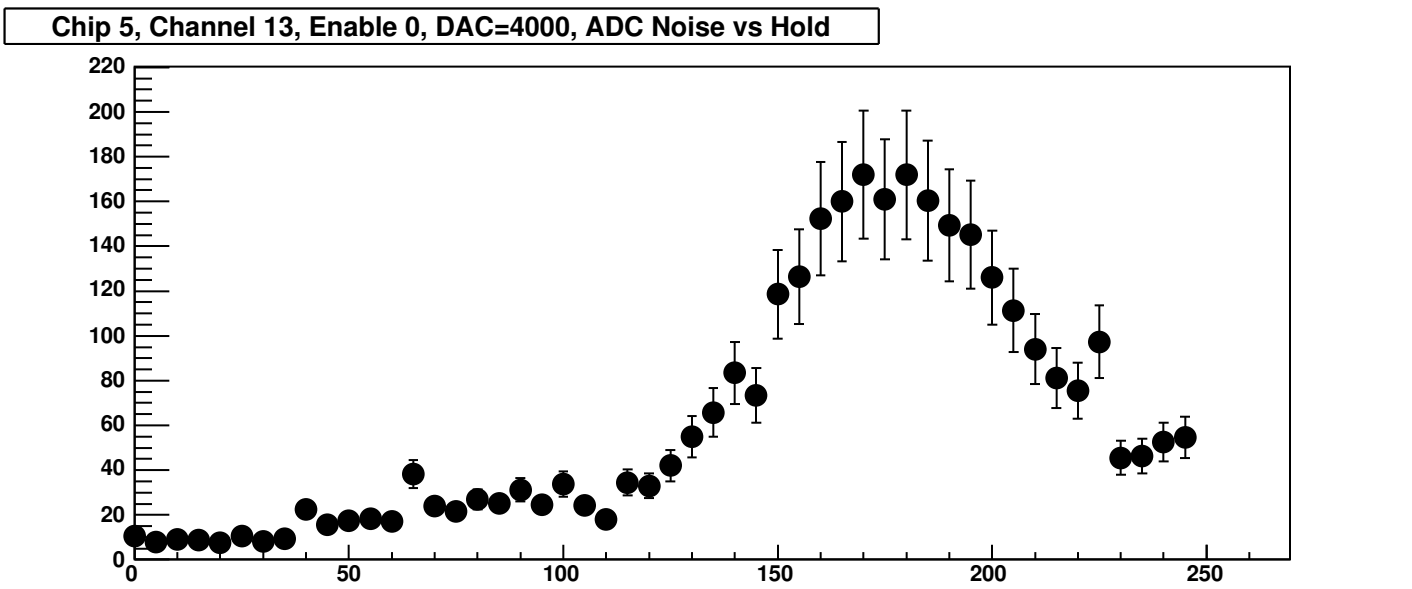
Chip 5, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold

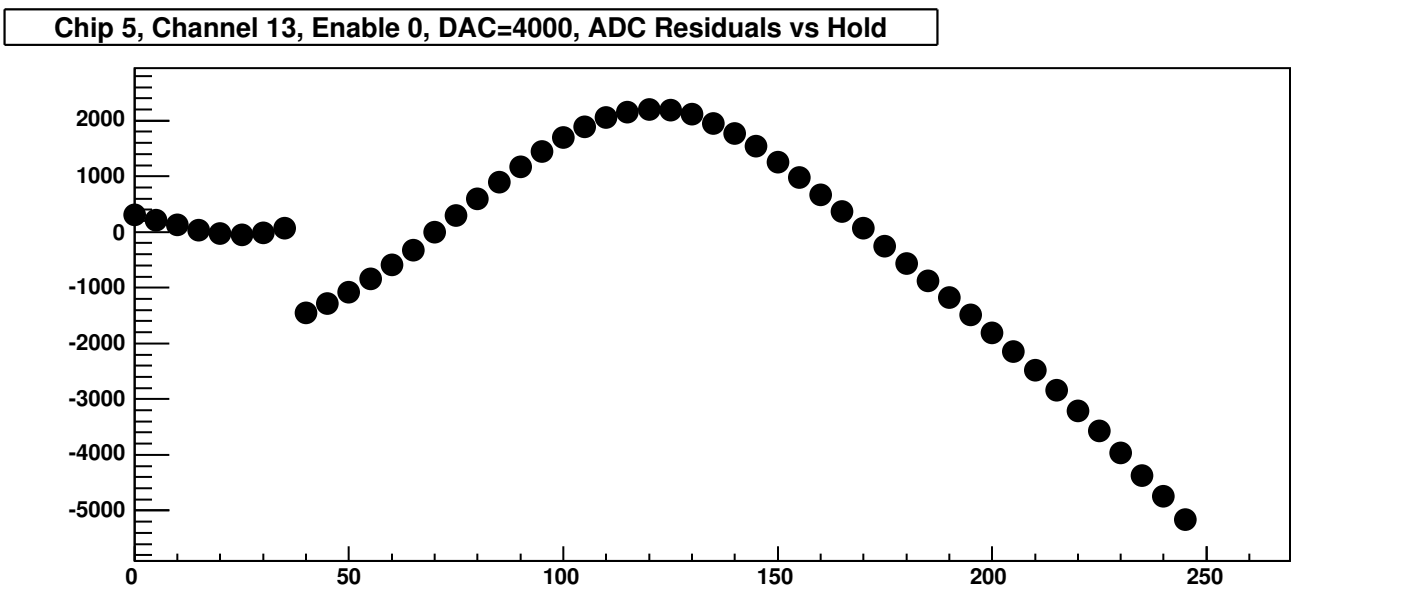


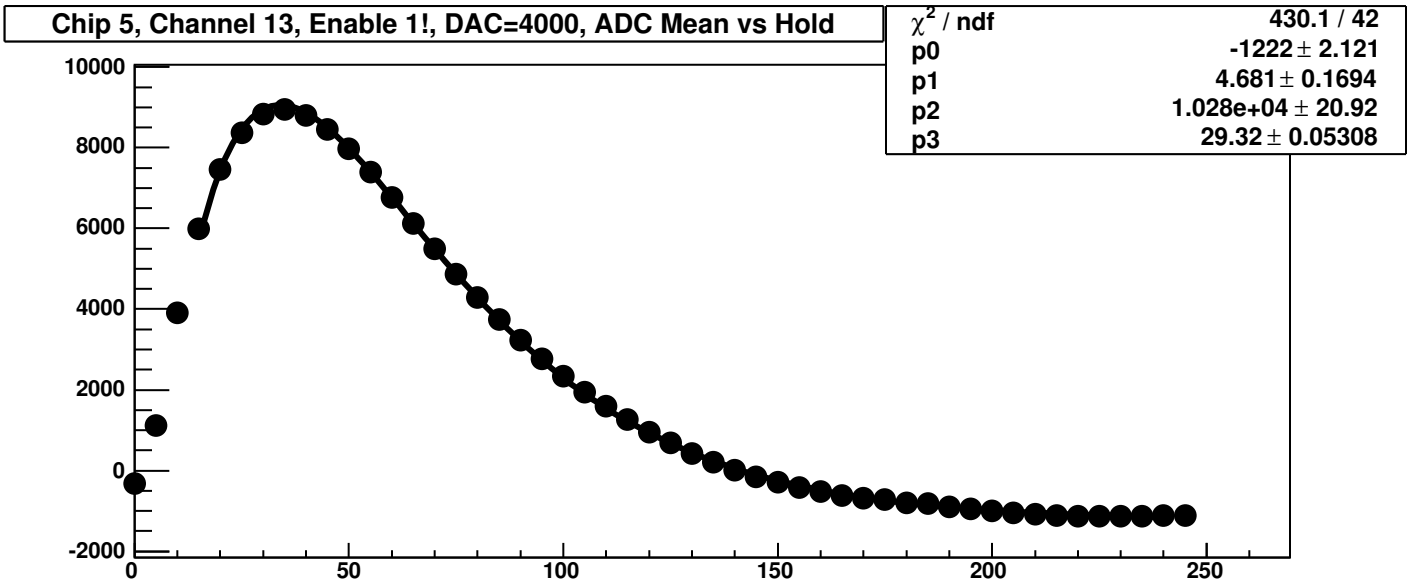
Chip 5, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold

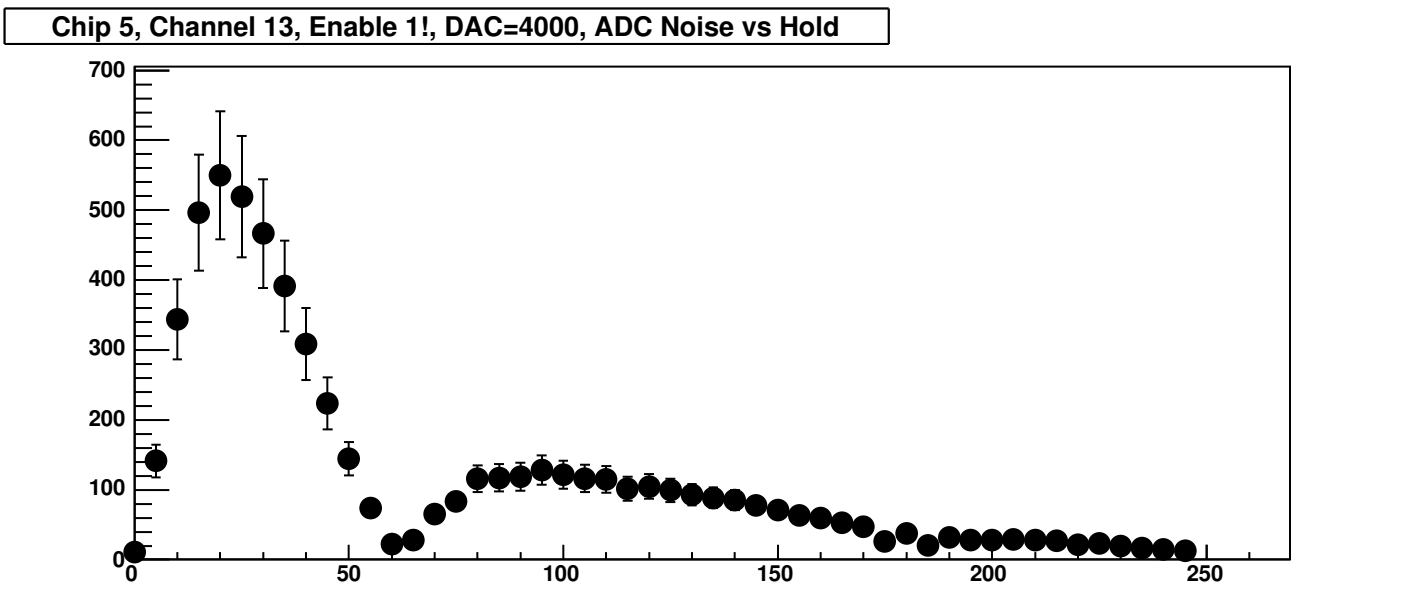


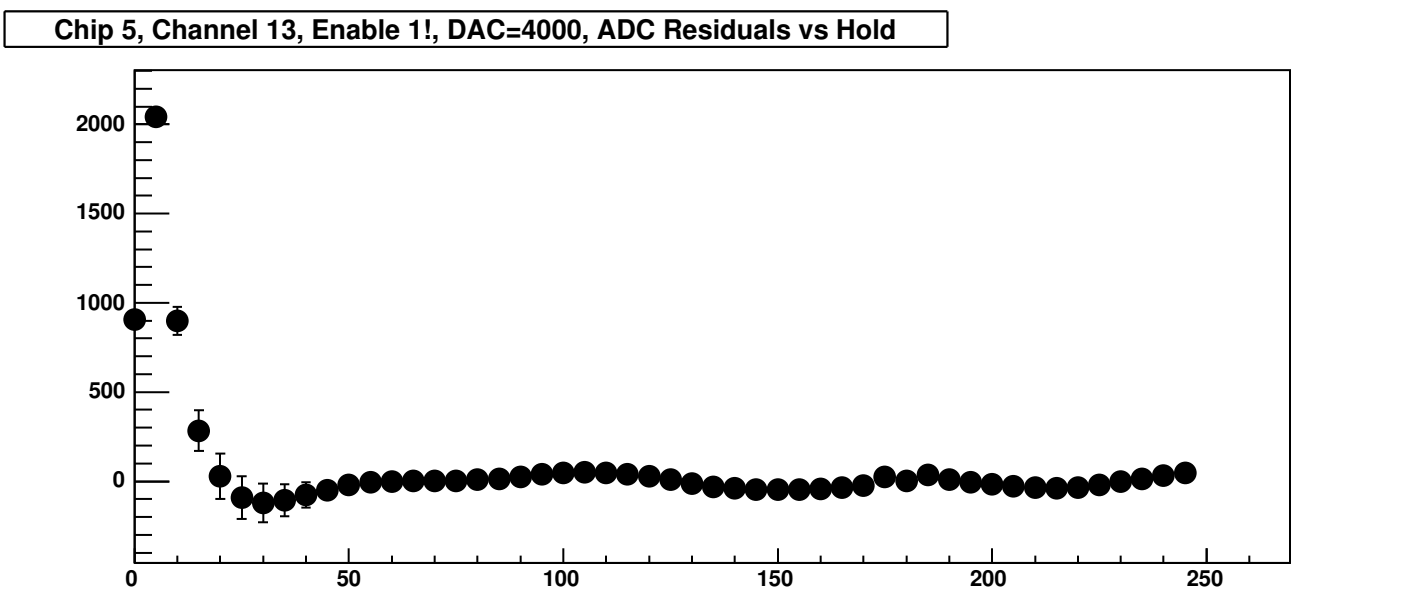

Chip 5, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.748e+06 / 41
p0 226.9 ± 2.908
p1 35.02 ± 0.1486
p2 1623 ± 8.65
p3 -197.5 ± 13.34
p4 24.86 ± 0.1512
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 5, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

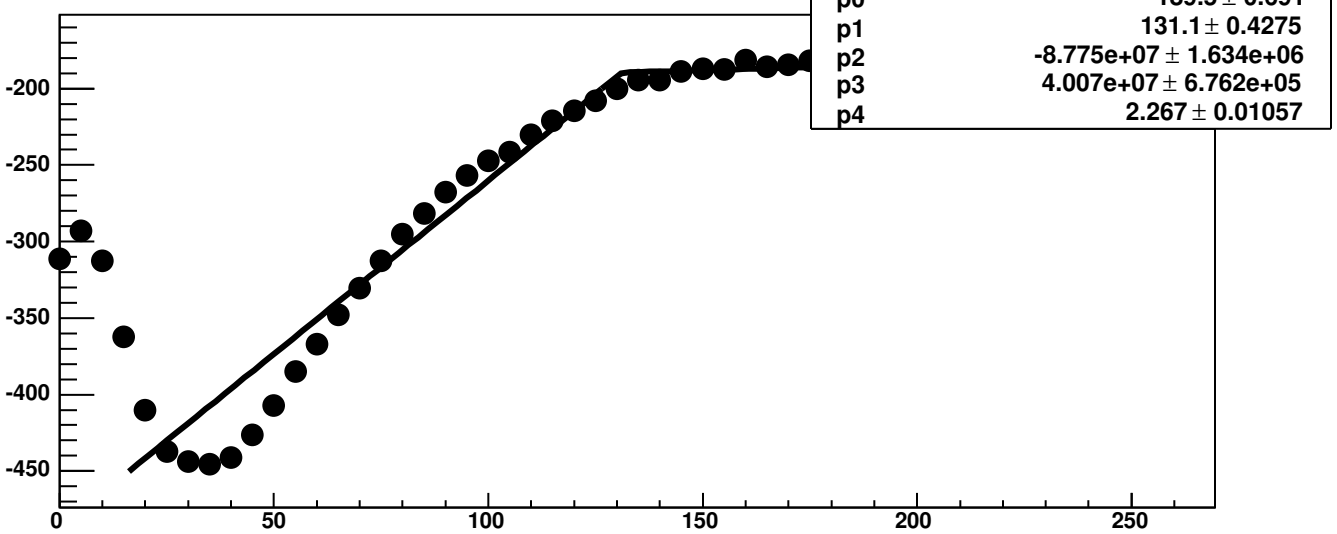

Chip 5, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250


Chip 5, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 430.1 / 42
p0 -1222 ± 2.121
p1 4.681 ± 0.1694
p2 1.028e+04 ± 20.92
p3 29.32 ± 0.05308
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

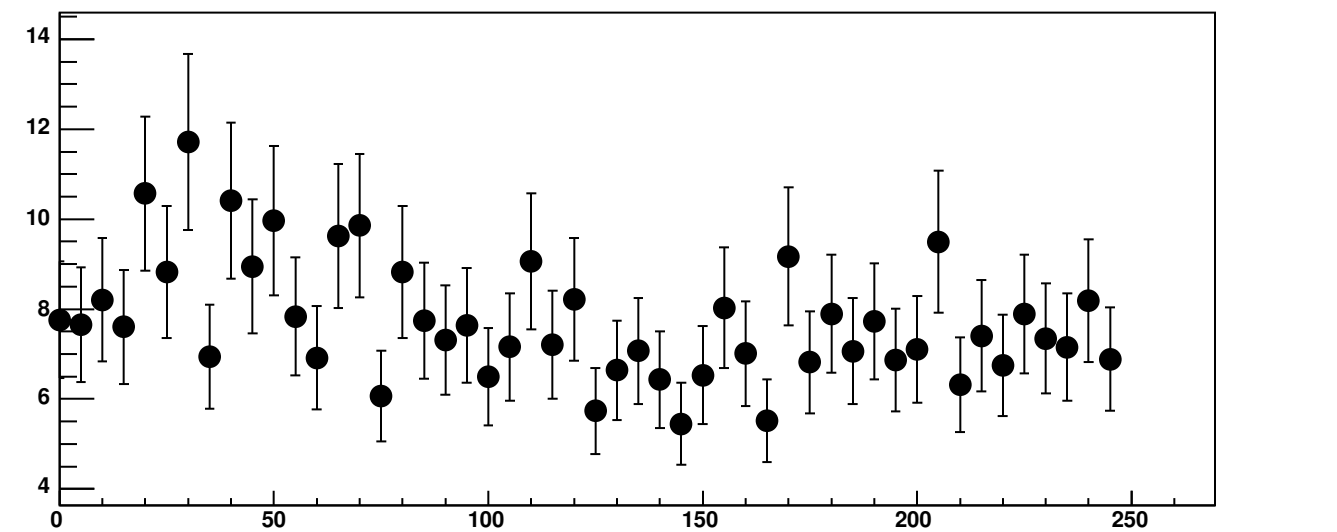

Chip 5, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold
700
600
500
400
300
200
100
0
0
50
100
150
200
250


Chip 5, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
0
50
100
150
200
250

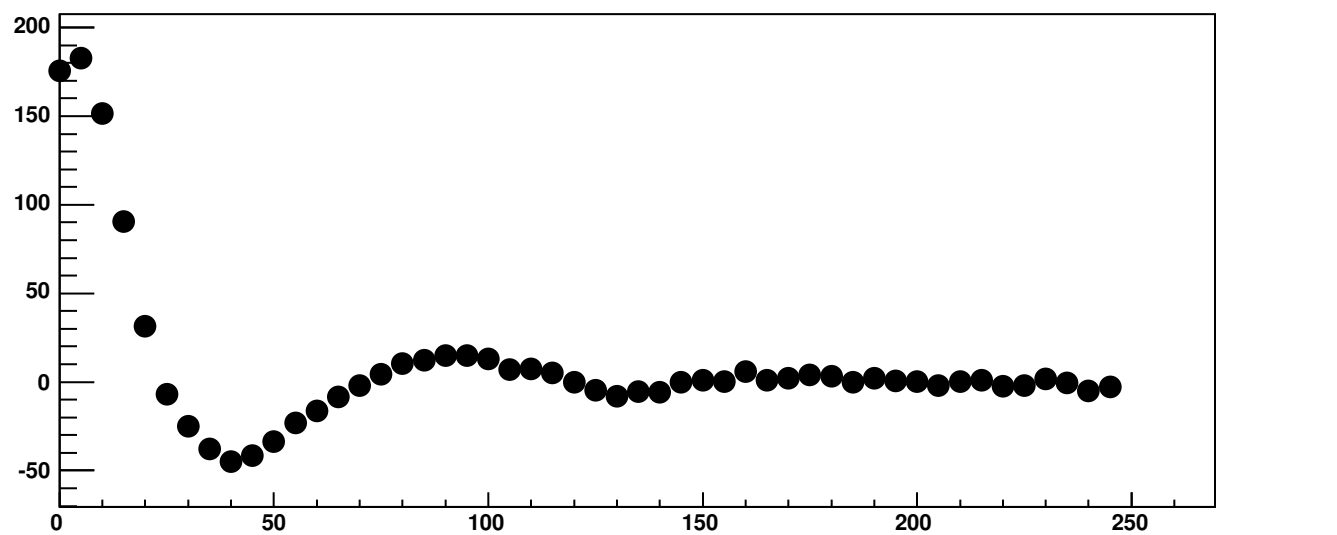
Chip 5, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

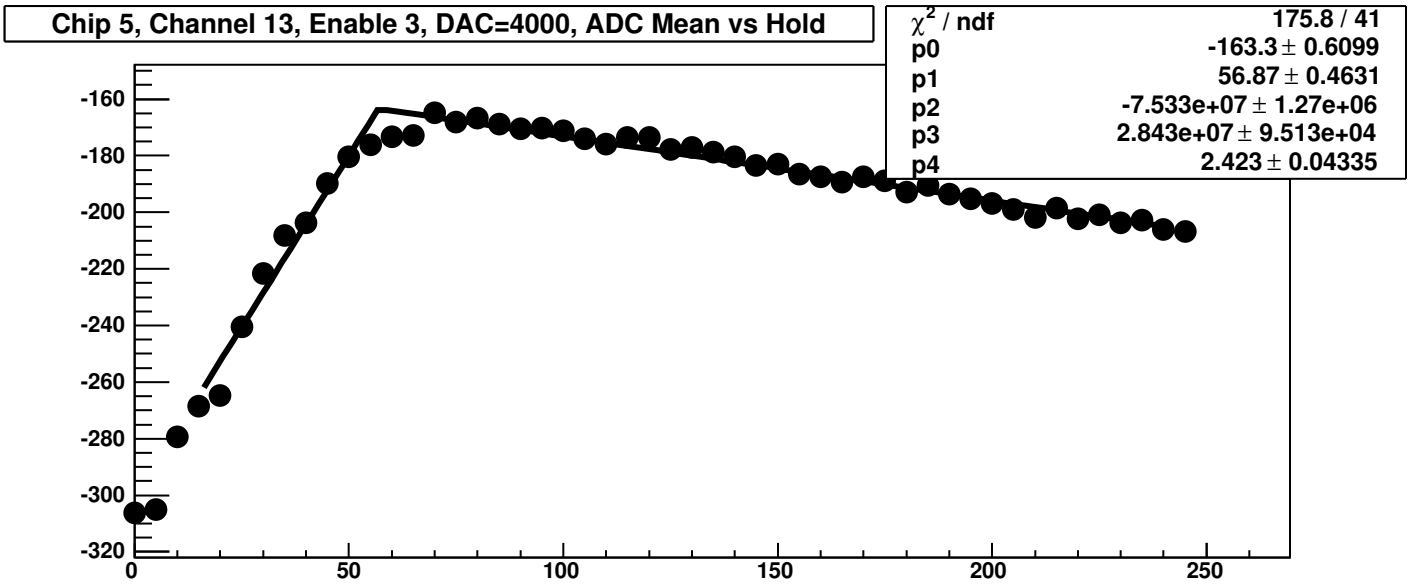


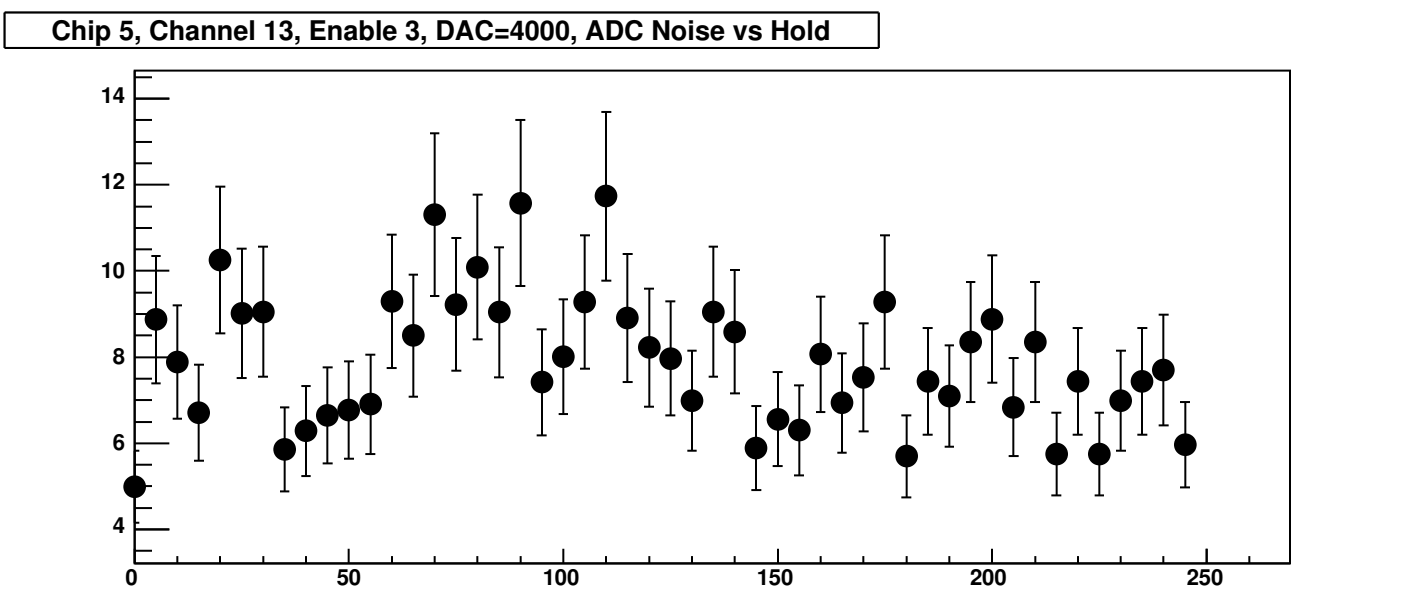
Chip 5, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold

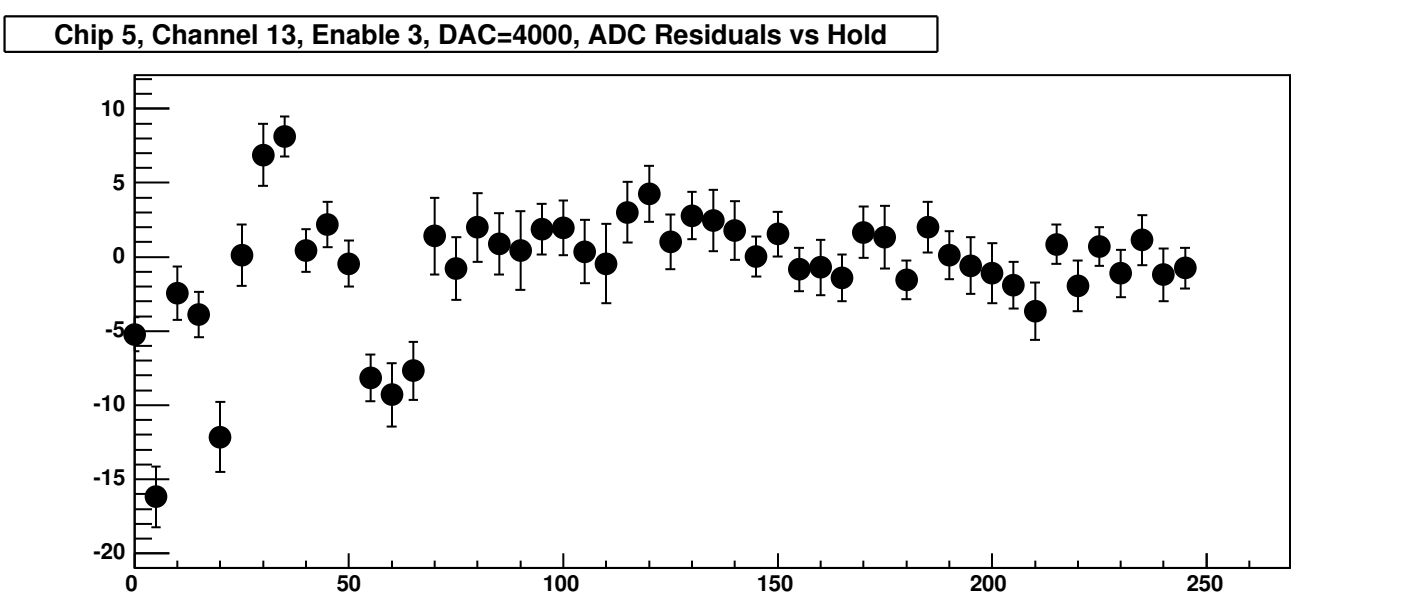


Chip 5, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

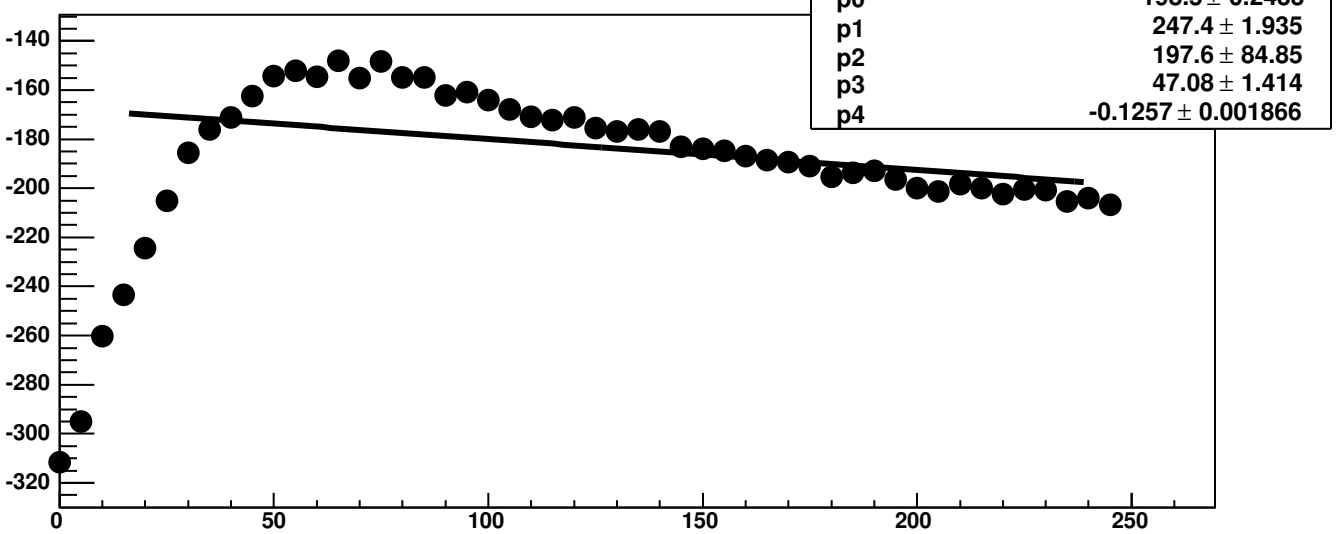



Chip 5, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 175.8 / 41
p0 -163.3 ± 0.6099
p1 56.87 ± 0.4631
p2 -7.533e+07 ± 1.27e+06
p3 2.843e+07 ± 9.513e+04
p4 2.423 ± 0.04335
-160
-180
-200
-220
-240
-260
-280
-300
-320
0
50
100
150
200
250

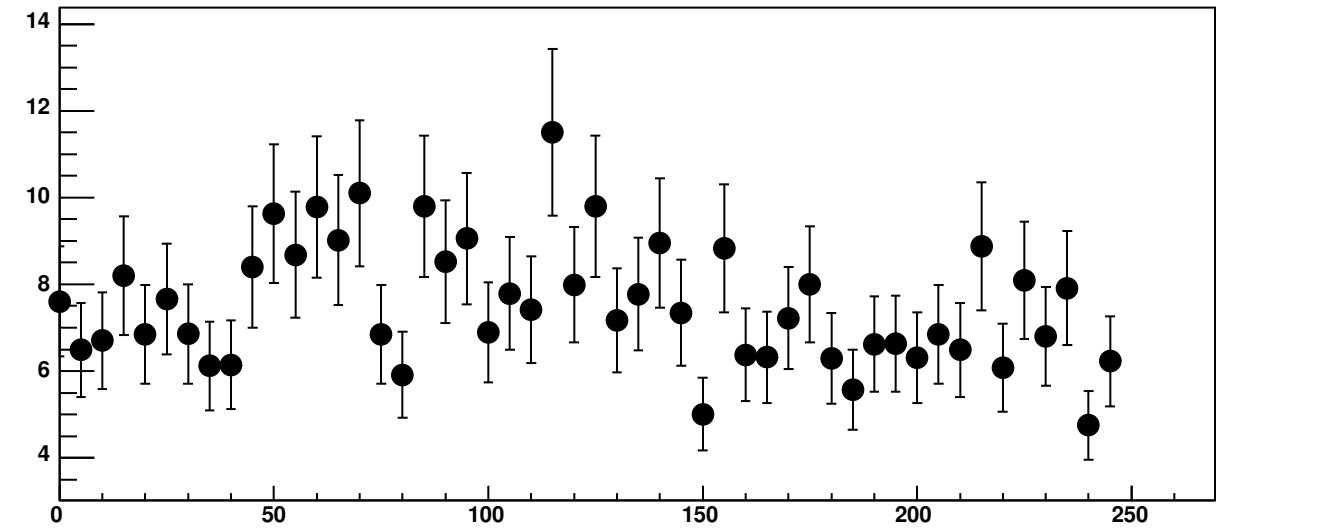

Chip 5, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 5, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
-20
0
50
100
150
200
250

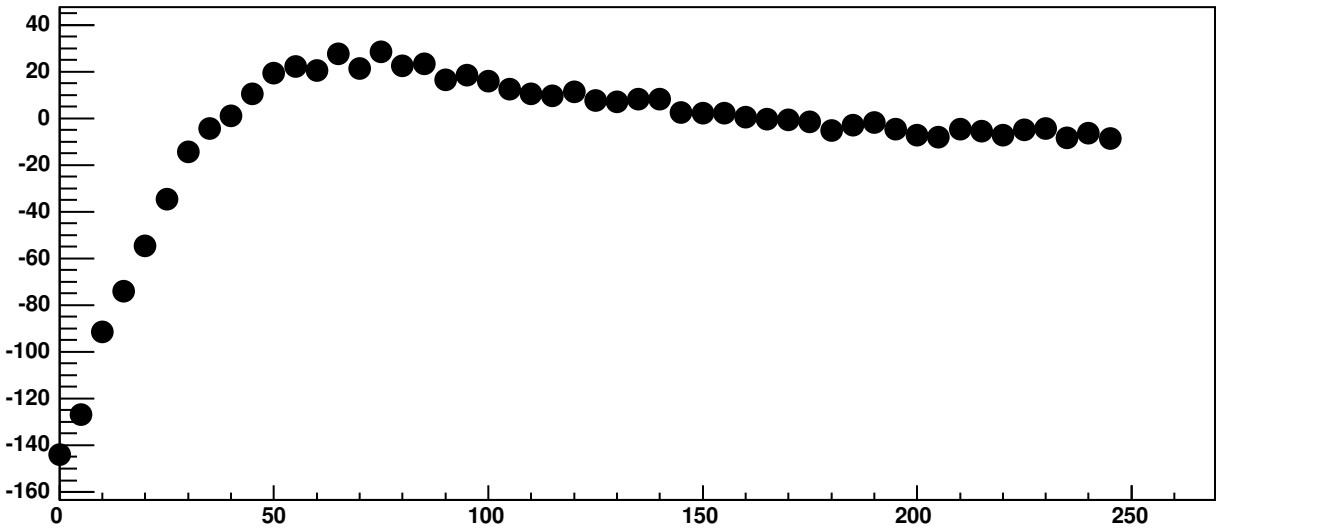
Chip 5, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold

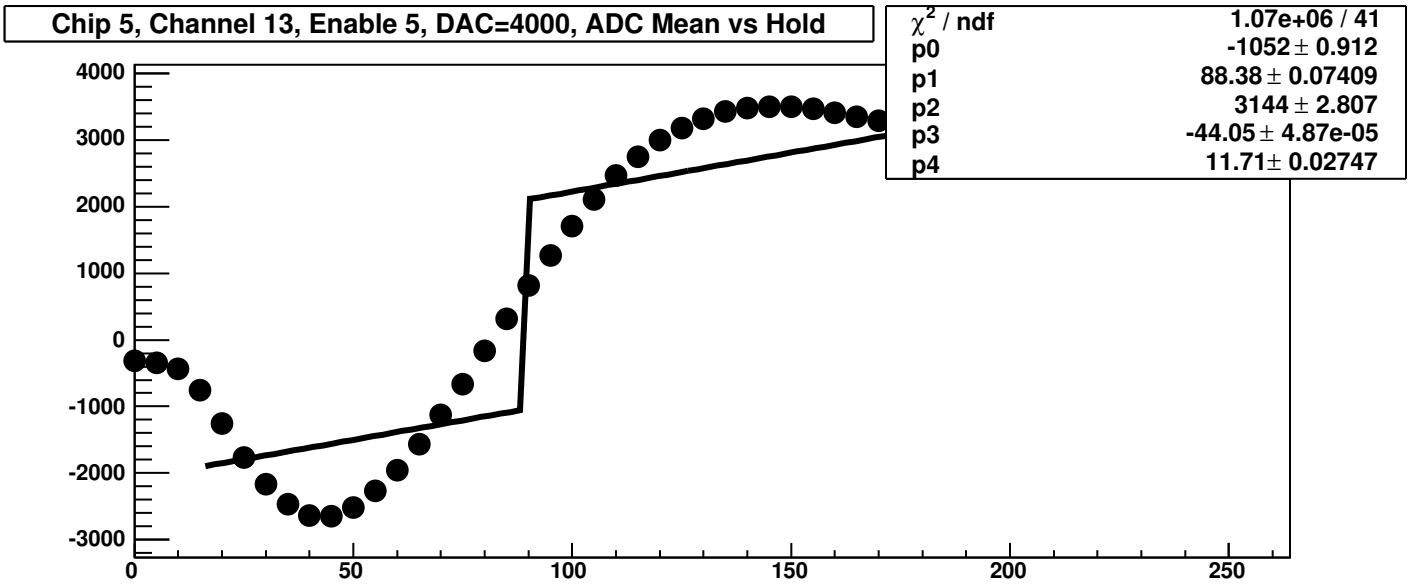


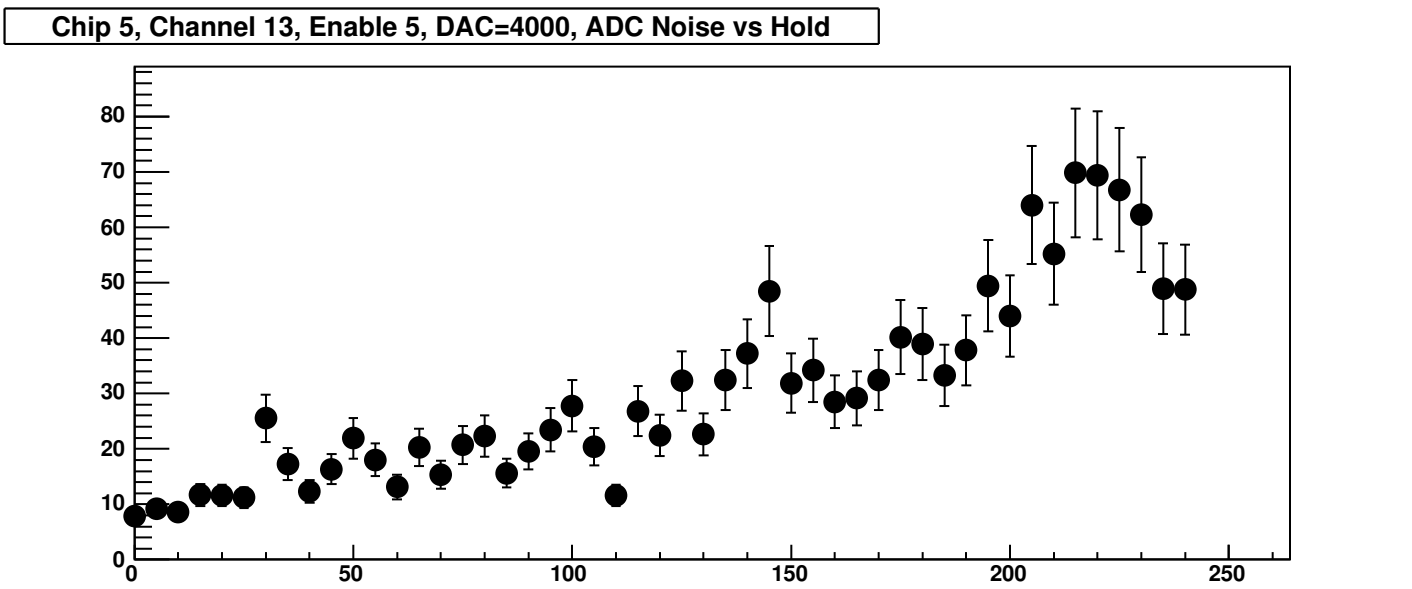
Chip 5, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold

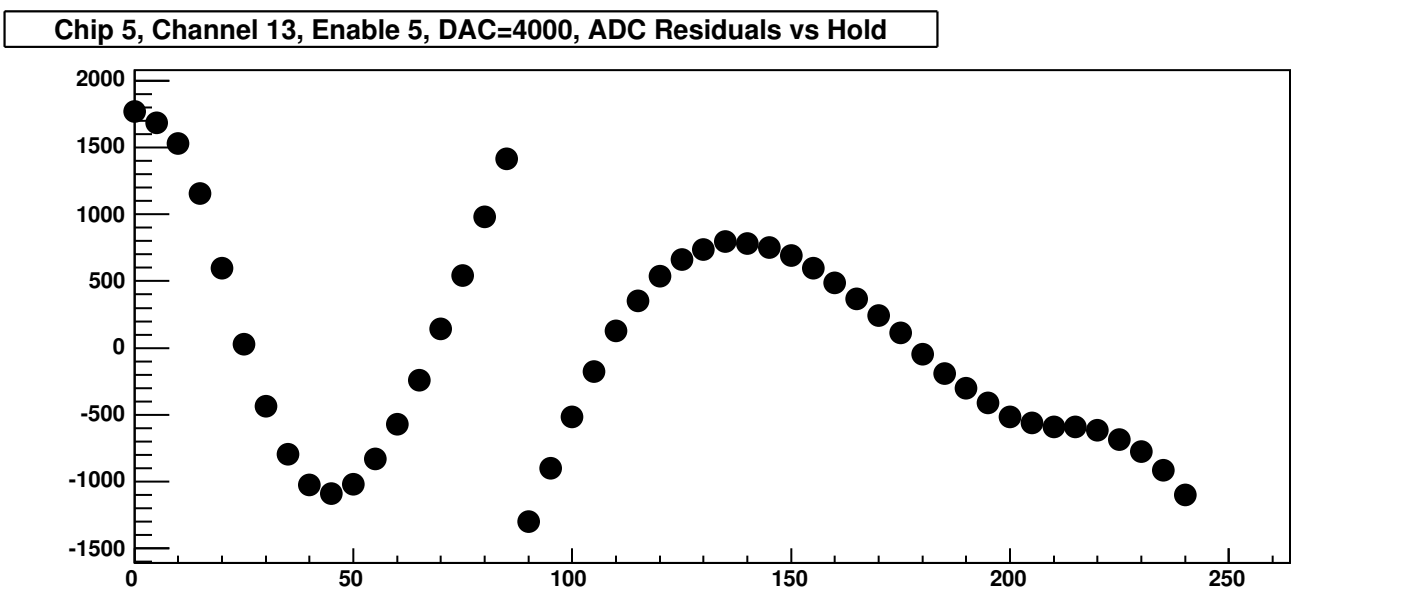


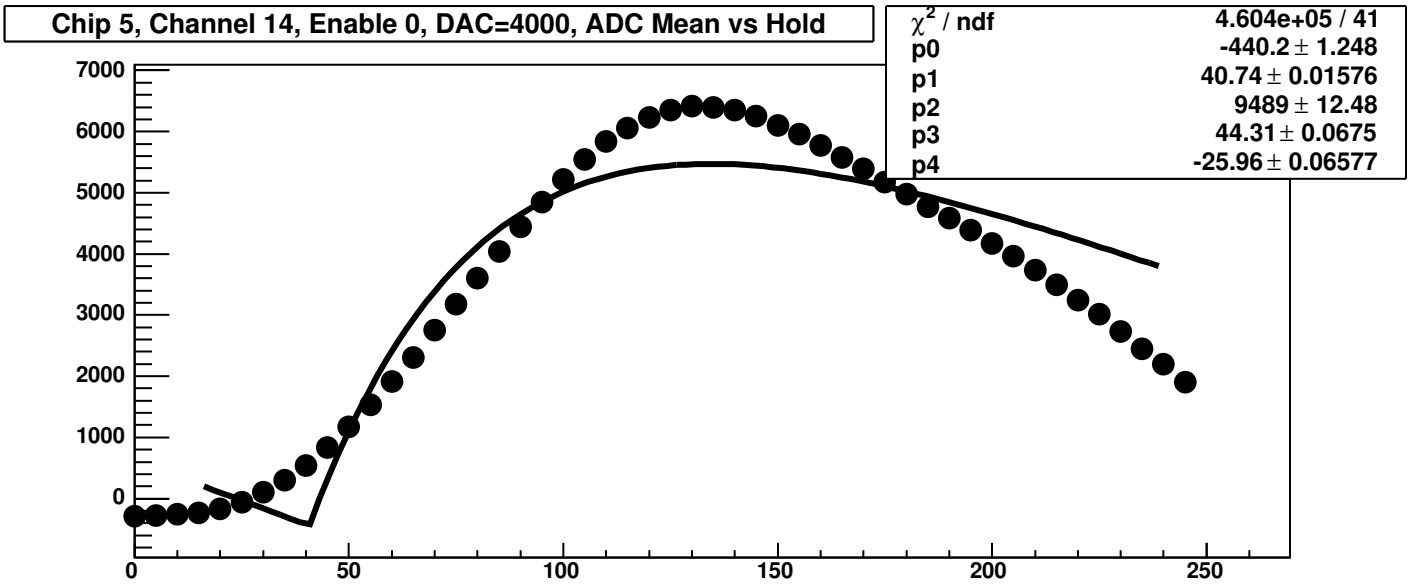
Chip 5, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold

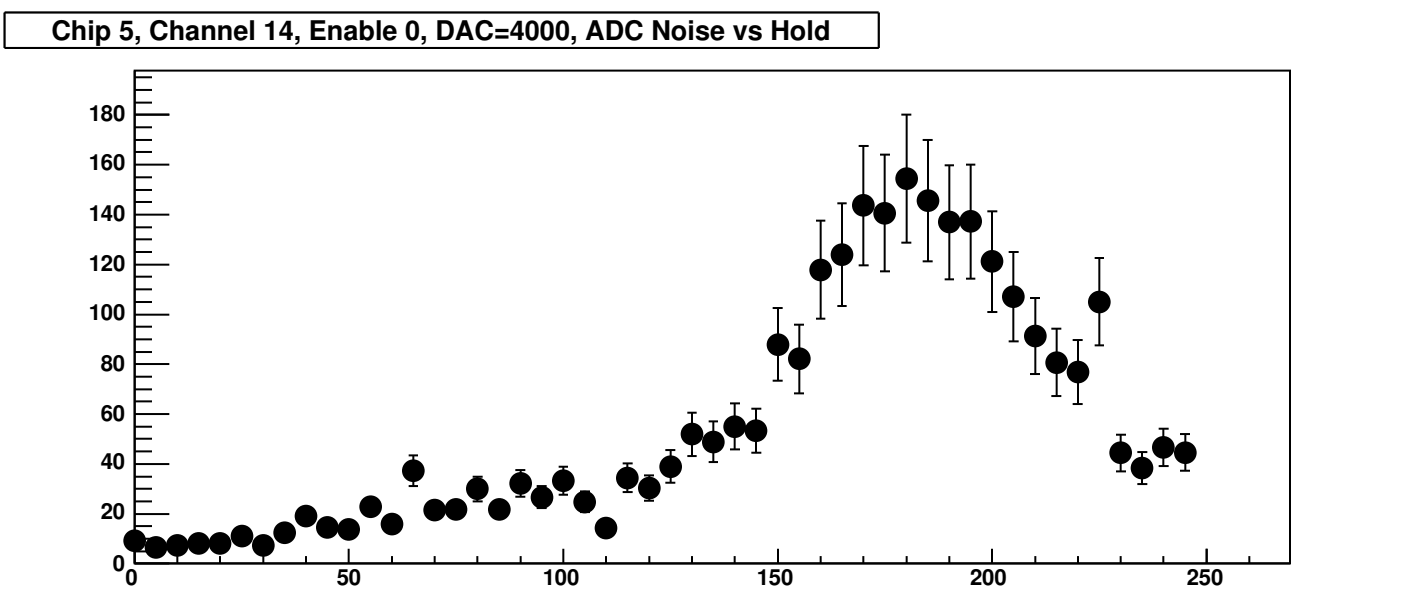


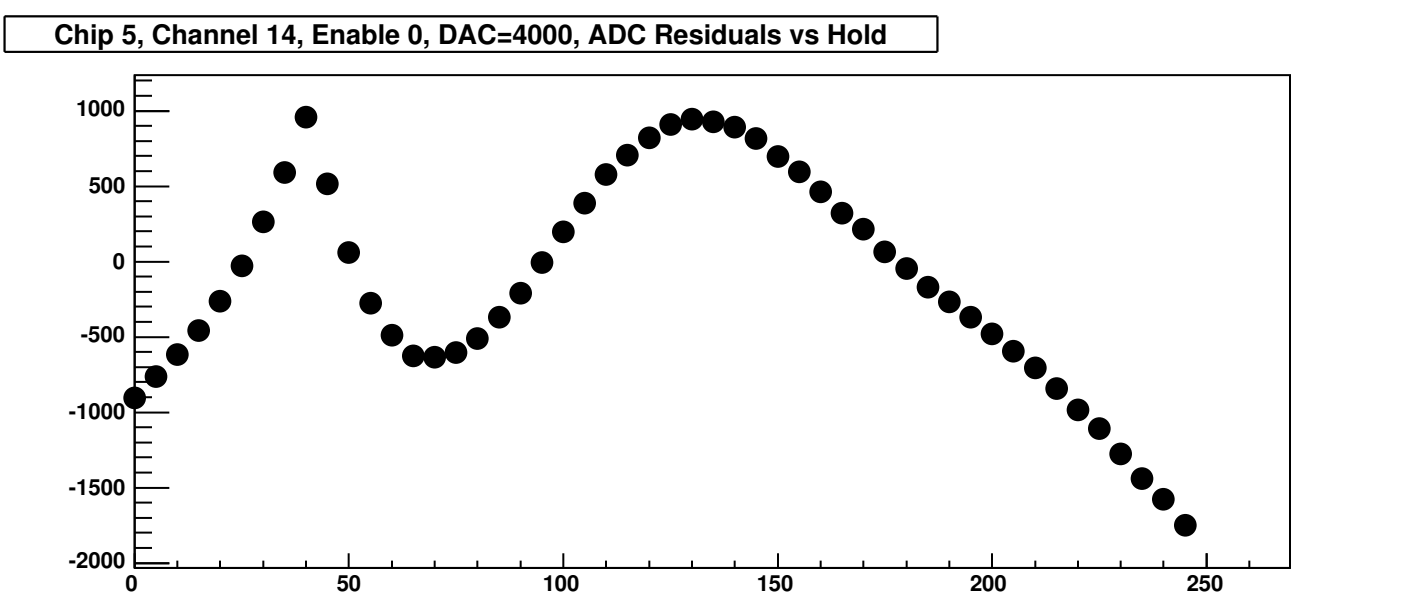

Chip 5, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.07e+06 / 41
p0 -1052 ± 0.912
p1 88.38 ± 0.07409
p2 3144 ± 2.807
p3 -44.05 ± 4.87e-05
p4 11.71 ± 0.02747
4000
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250


Chip 5, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250

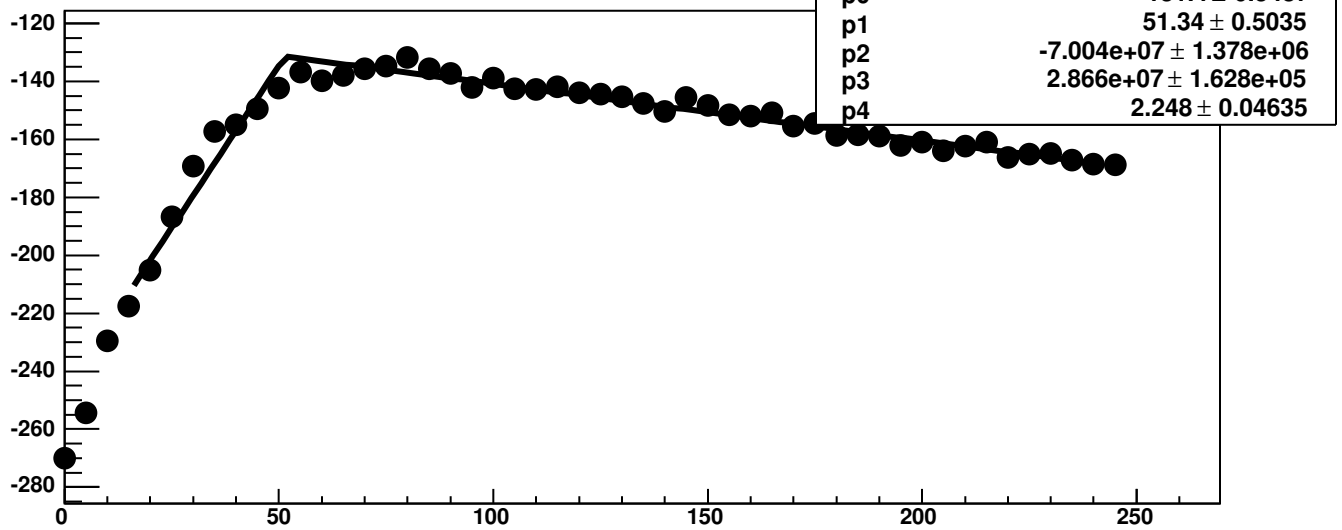

Chip 5, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250


Chip 5, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.604e+05 / 41
p0 -440.2 ± 1.248
p1 40.74 ± 0.01576
p2 9489 ± 12.48
p3 44.31 ± 0.0675
p4 -25.96 ± 0.06577
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

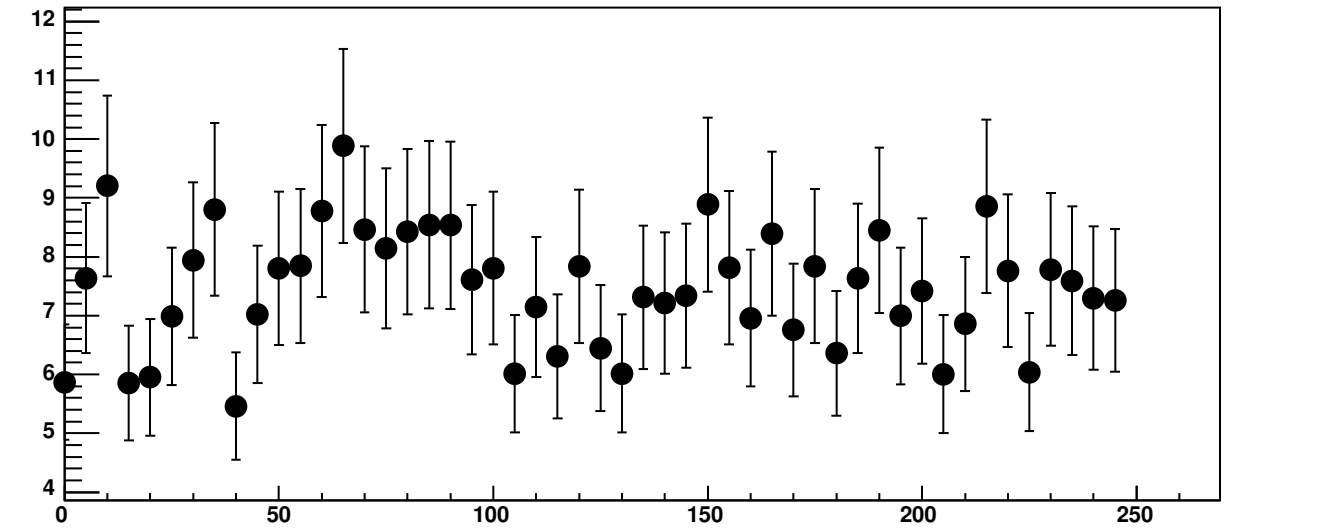

Chip 5, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 5, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

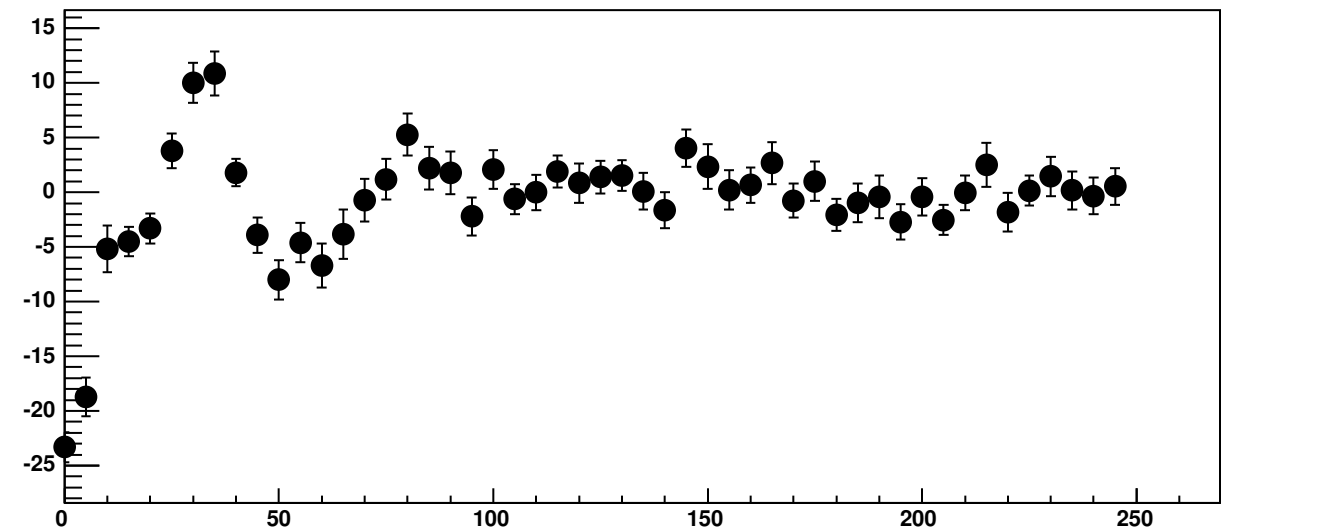
Chip 5, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold



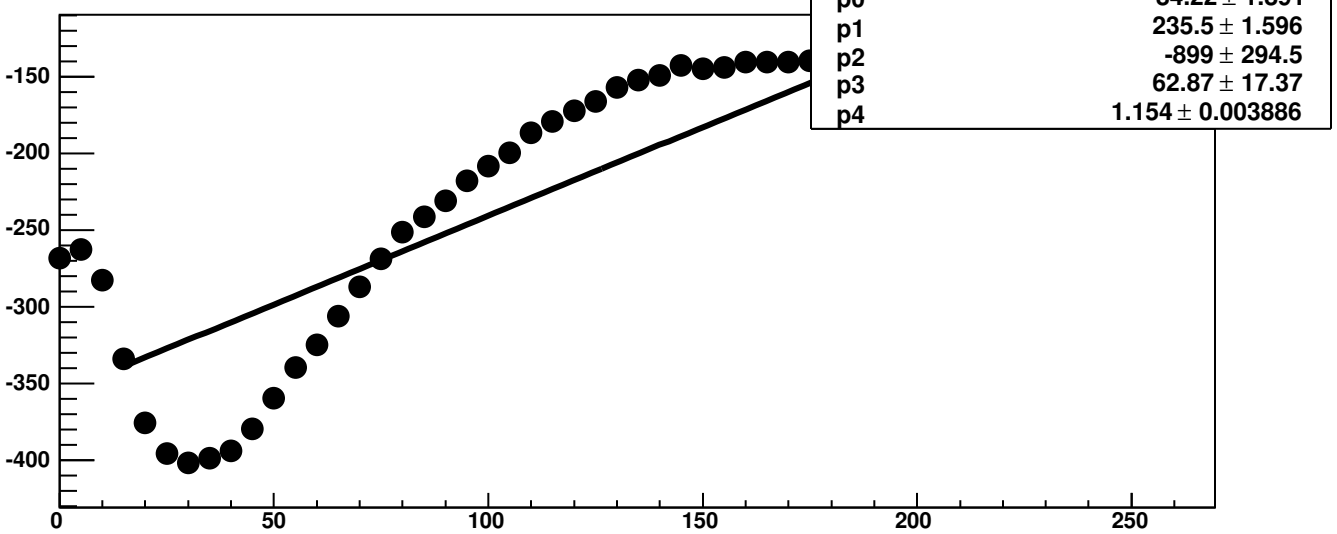
Chip 5, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold



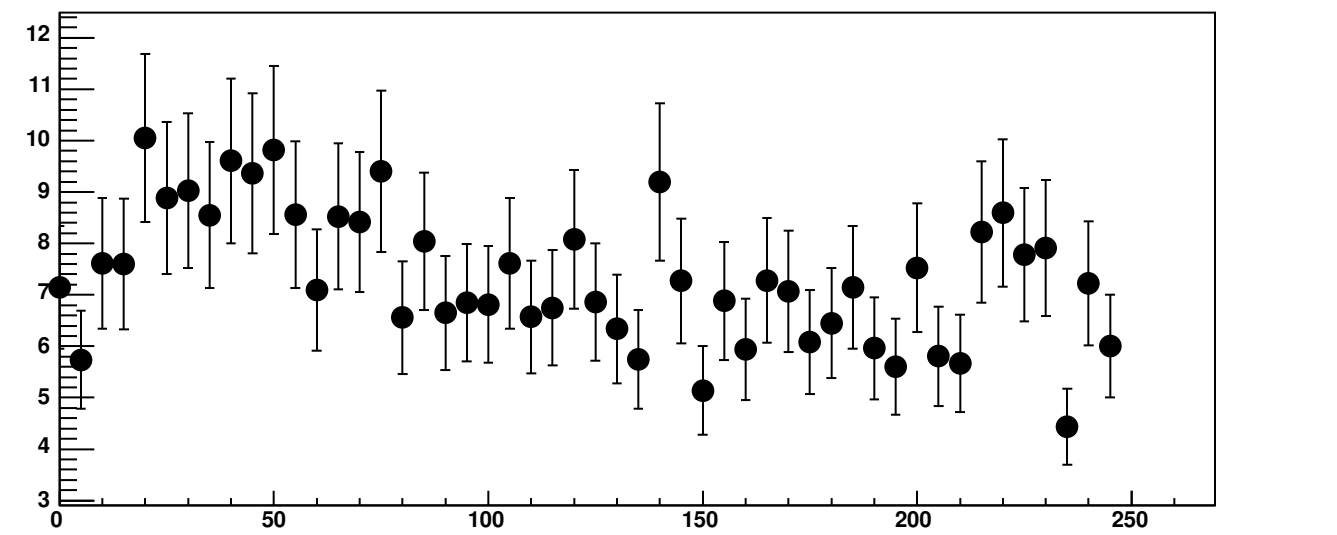
Chip 5, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold



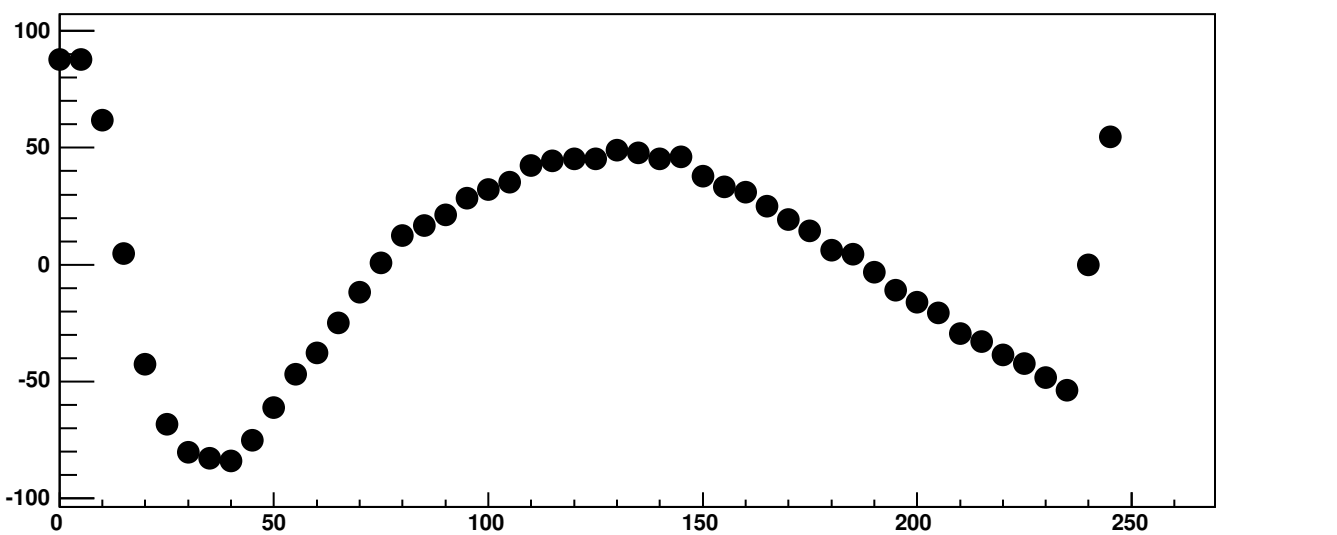
Chip 5, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold



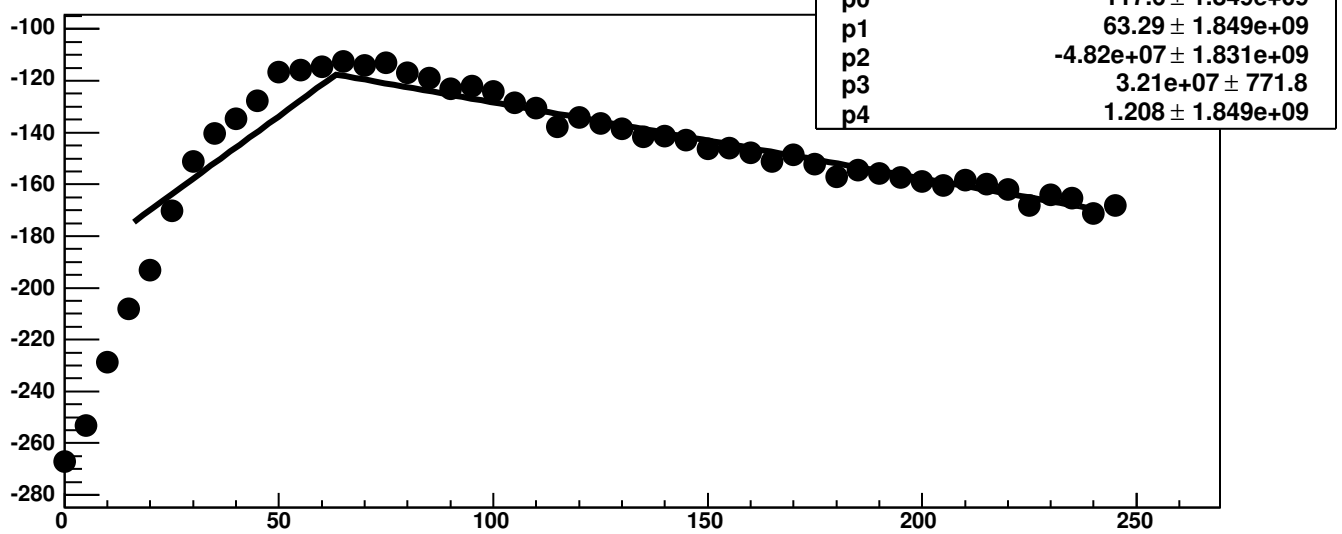
Chip 5, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



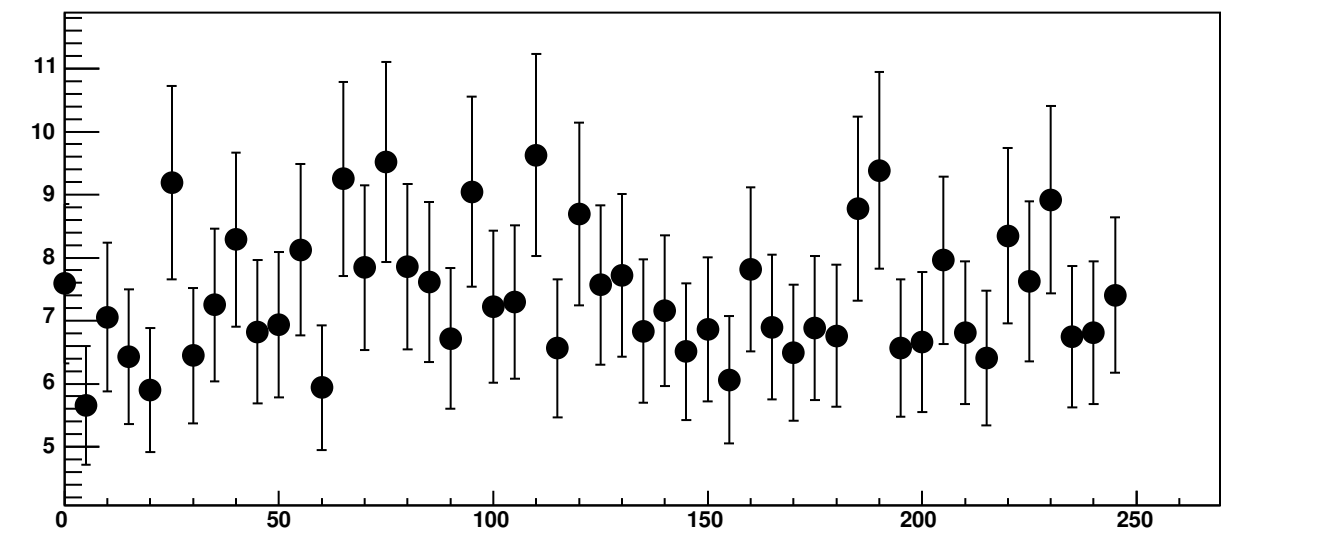
Chip 5, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold



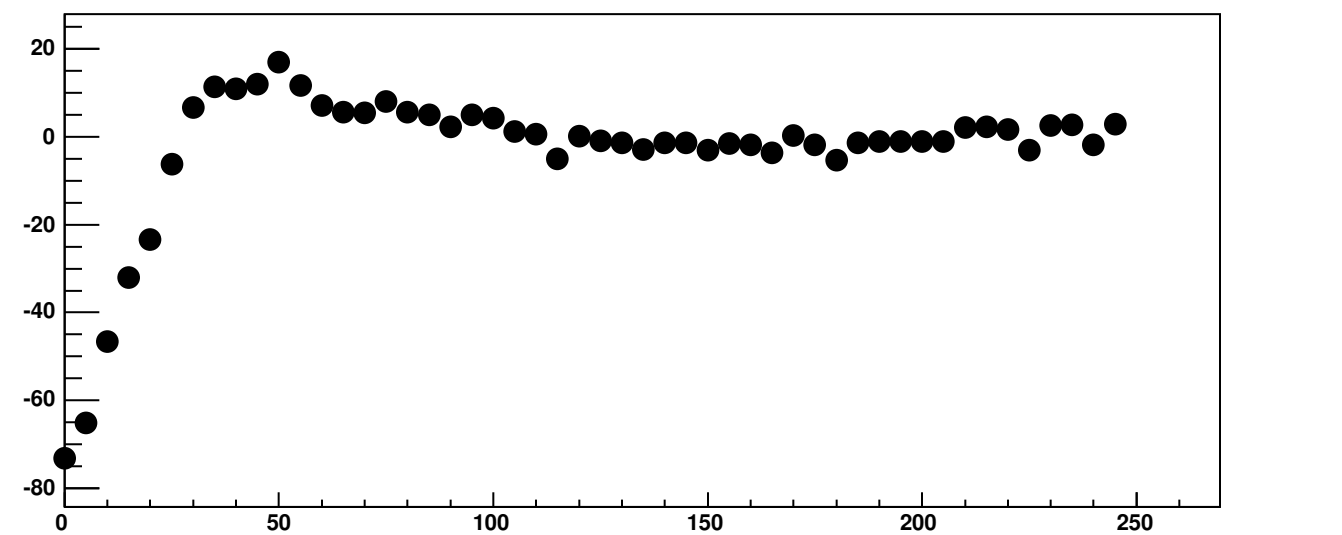
Chip 5, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold



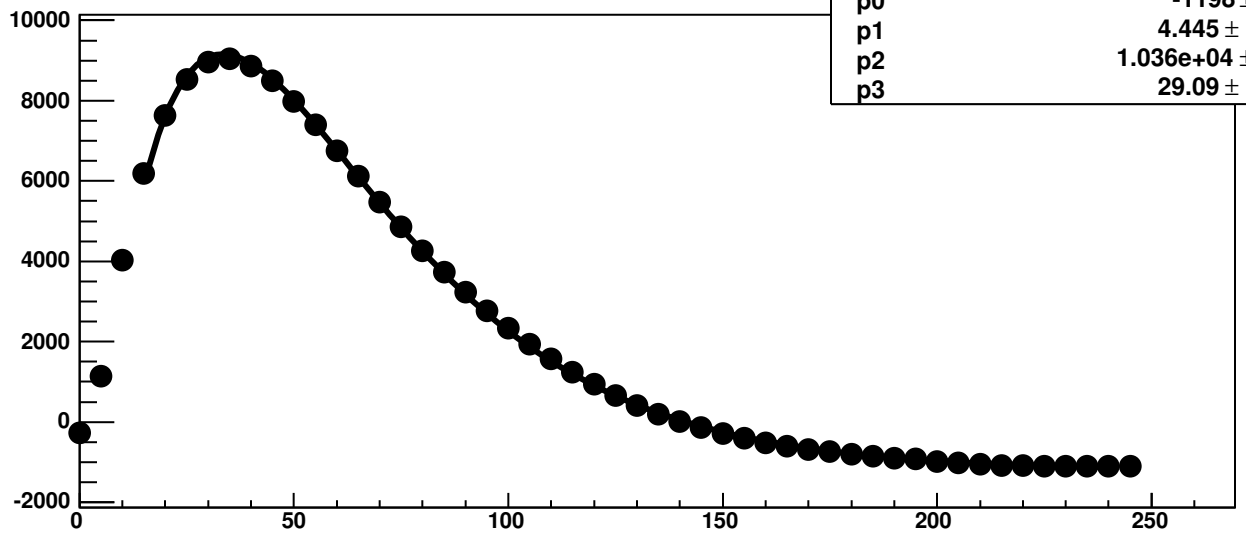
Chip 5, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



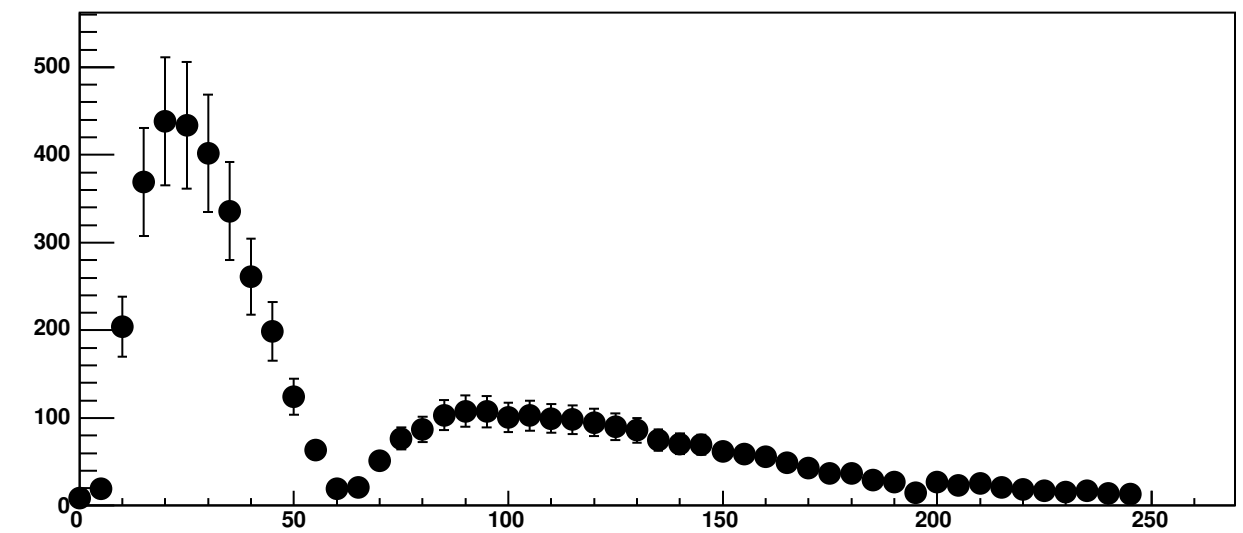
Chip 5, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold



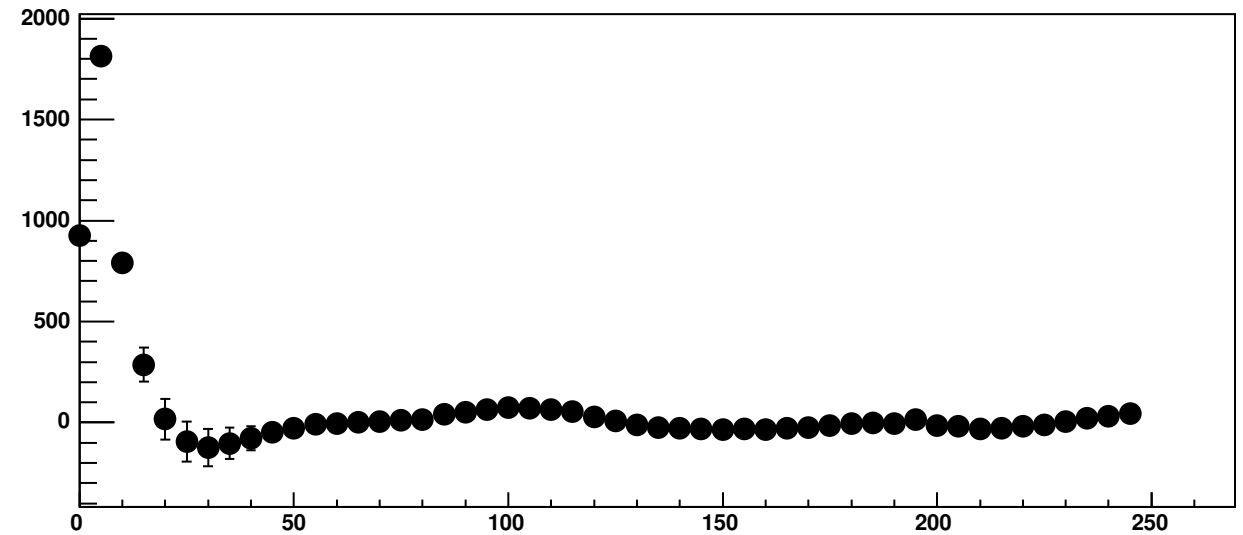
Chip 5, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold



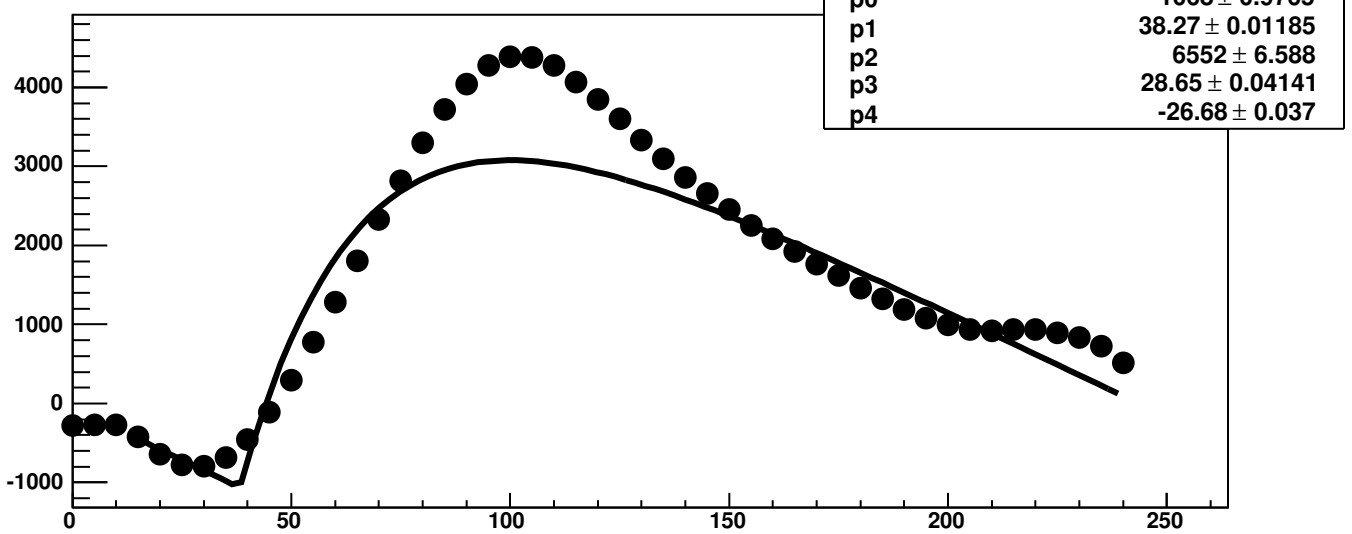
Chip 5, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold



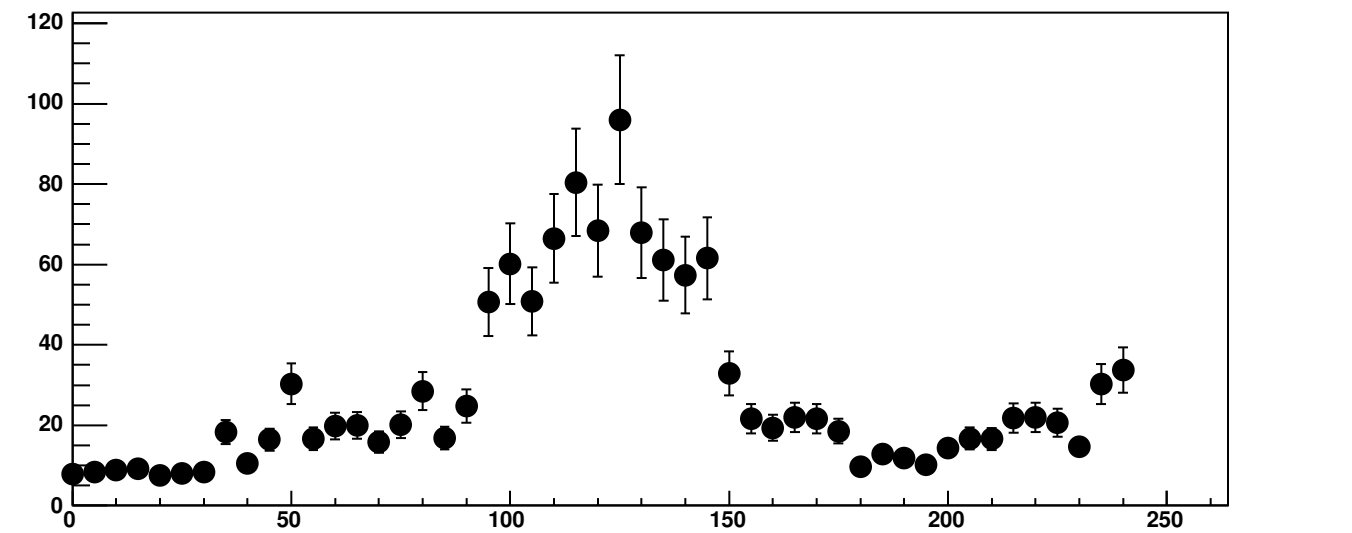
Chip 5, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold



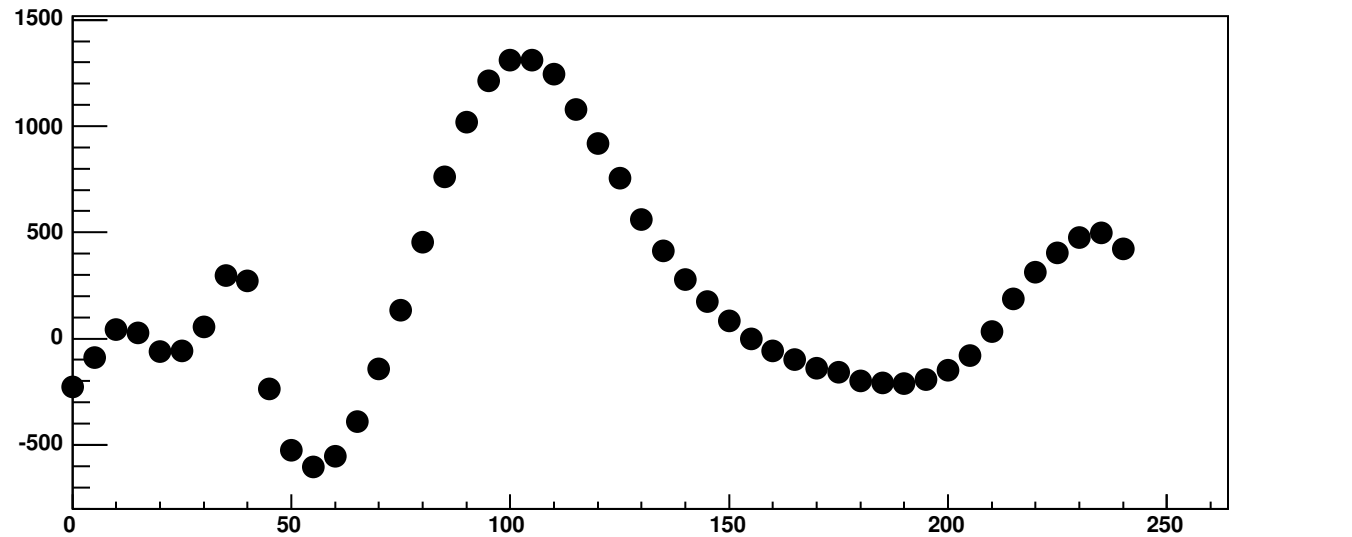
Chip 5, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

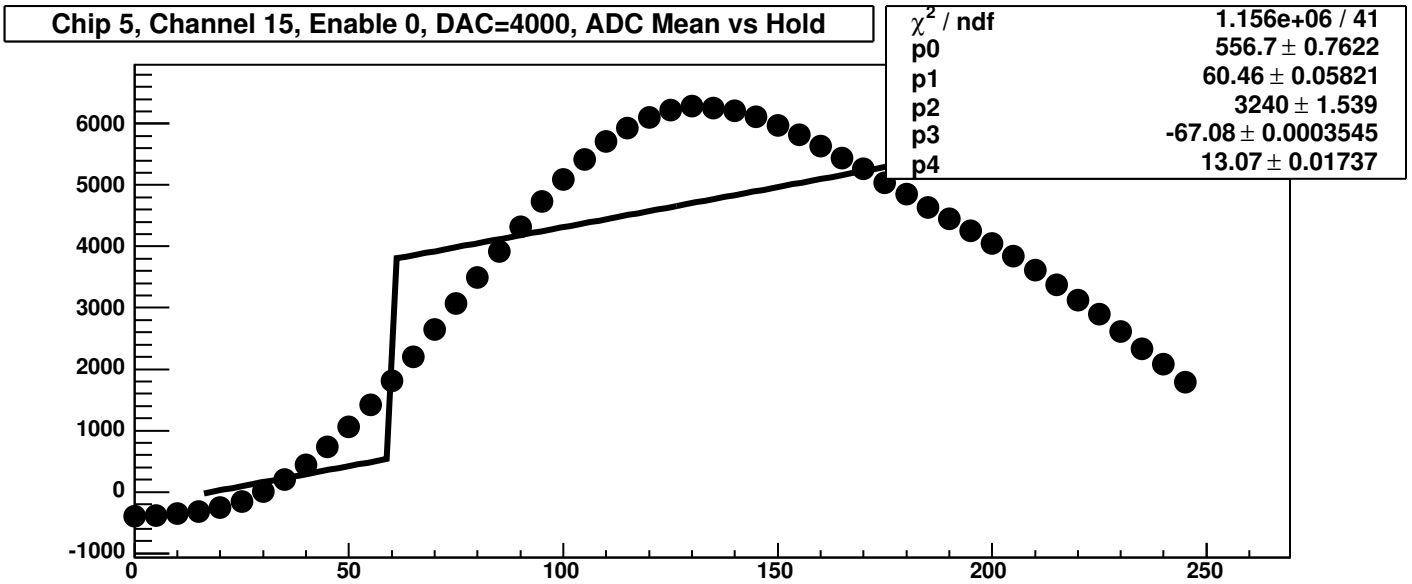


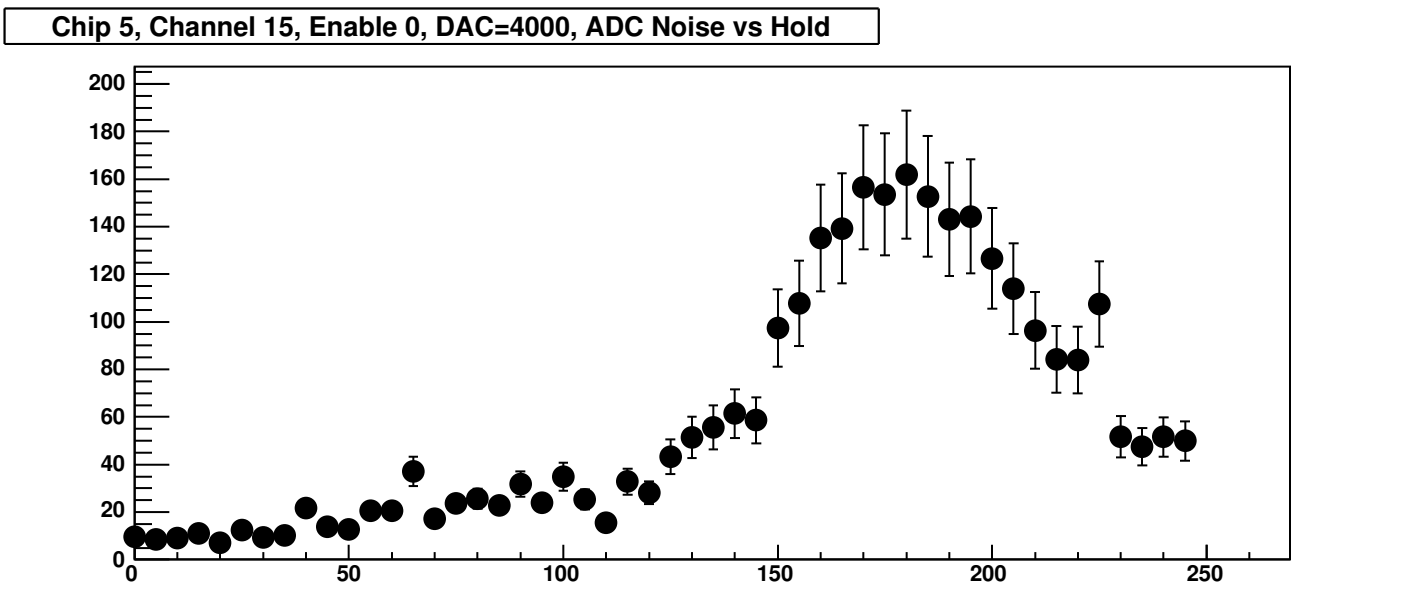
Chip 5, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold

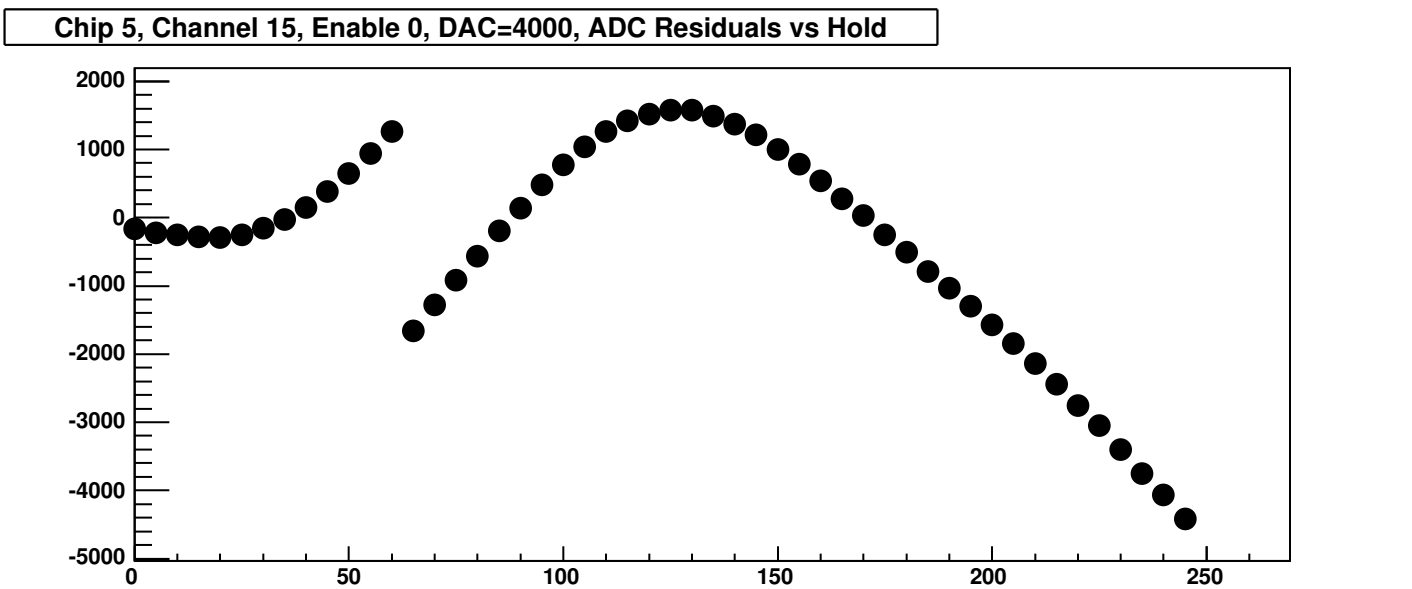


Chip 5, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold

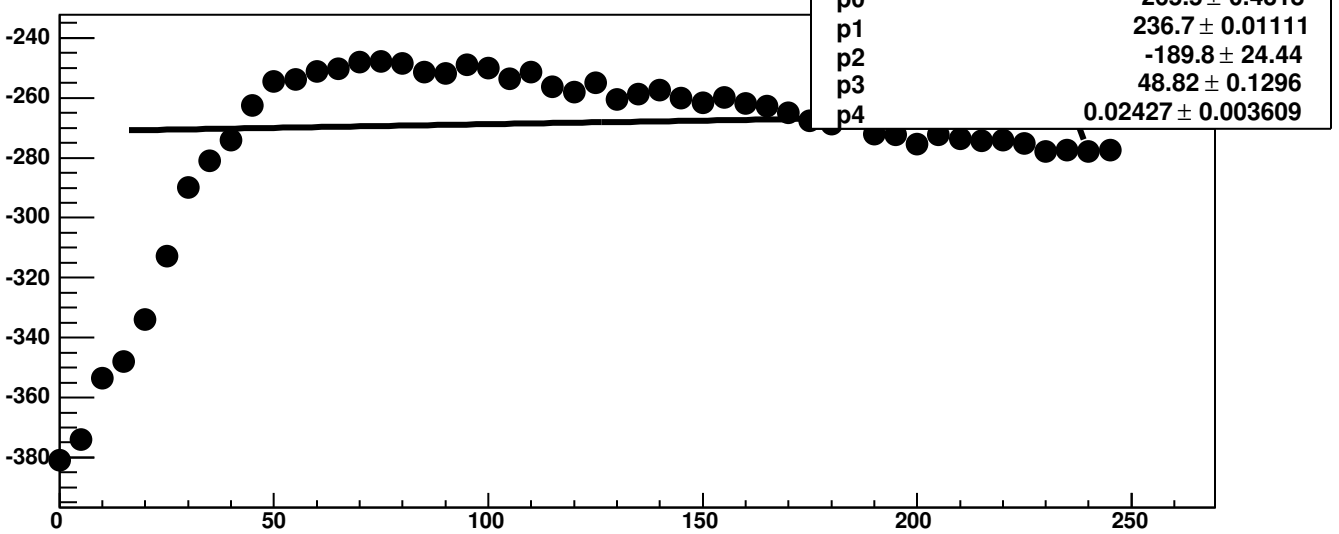



Chip 5, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.156e+06 / 41
p0 556.7 ± 0.7622
p1 60.46 ± 0.05821
p2 3240 ± 1.539
p3 -67.08 ± 0.0003545
p4 13.07 ± 0.01737
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

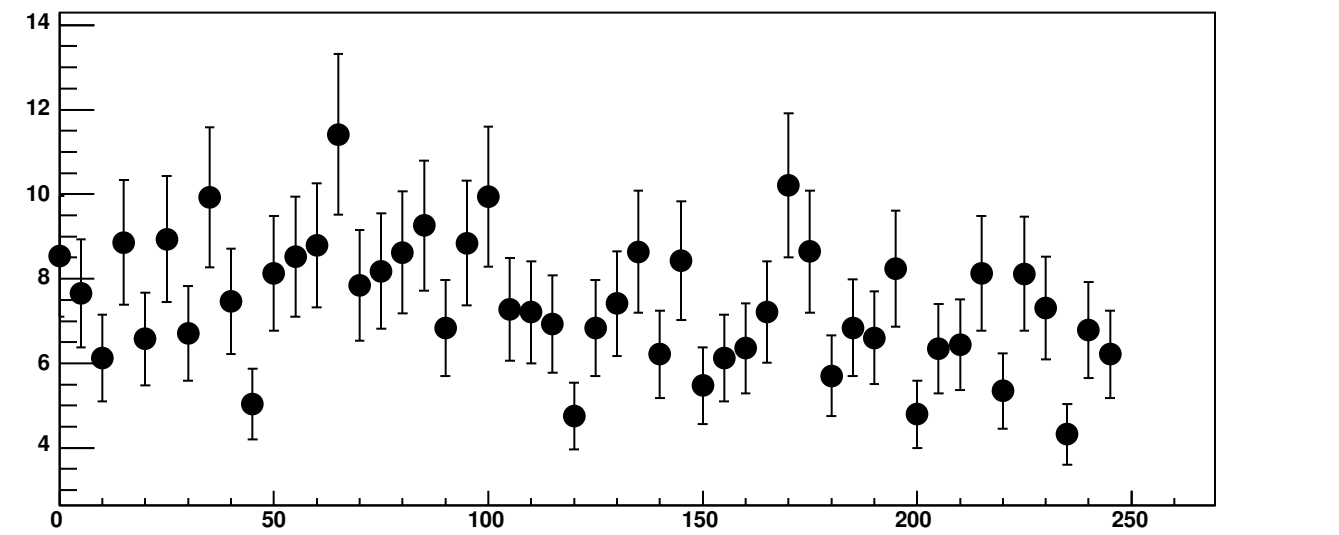

Chip 5, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 5, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

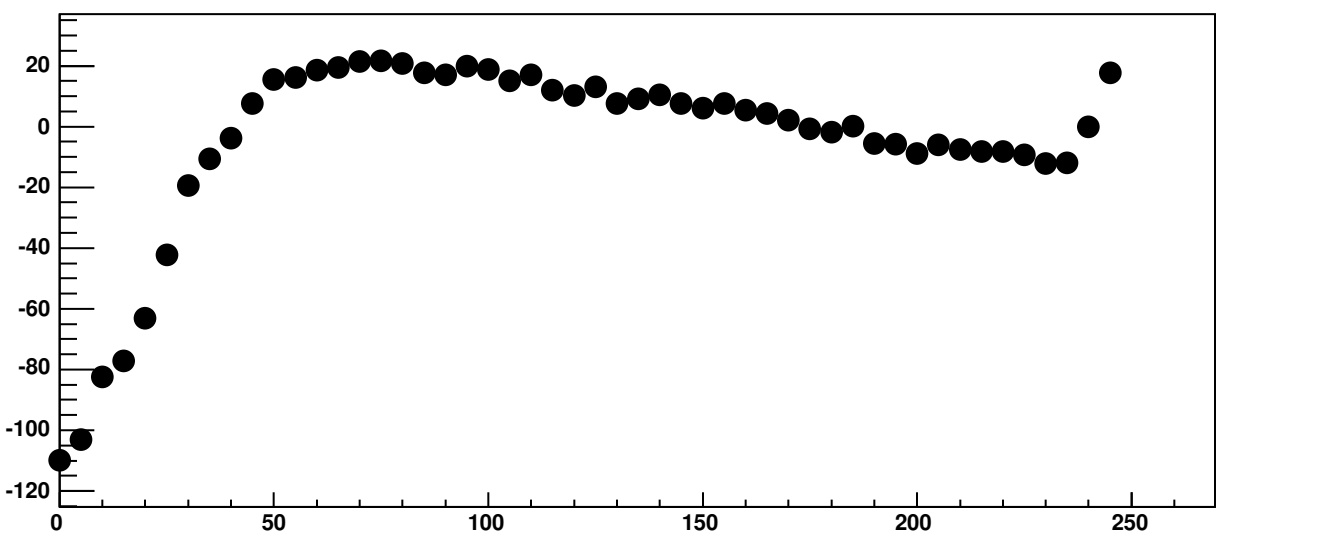
Chip 5, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

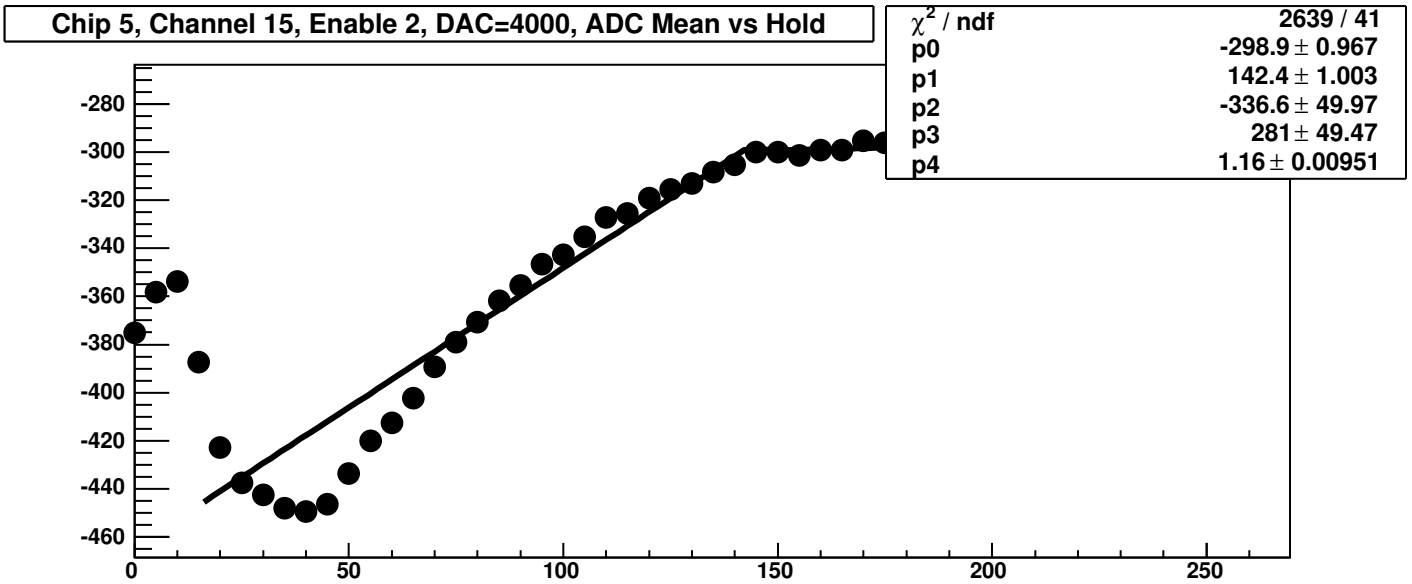


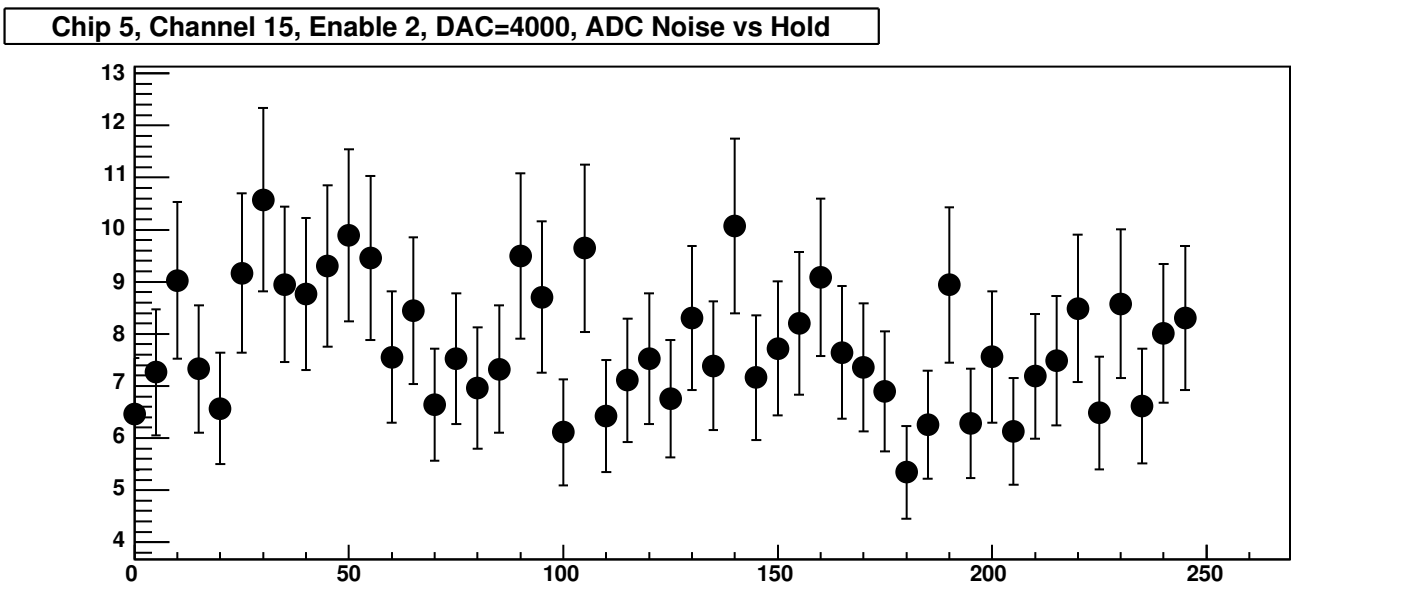
Chip 5, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold

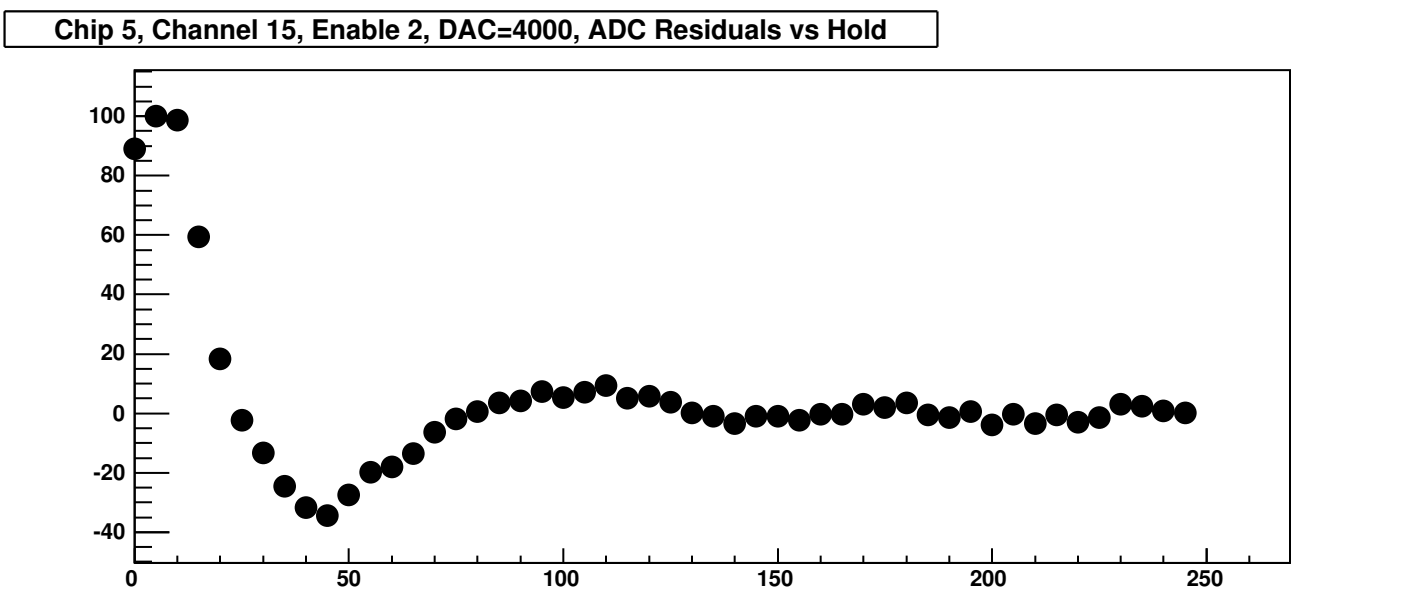


Chip 5, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

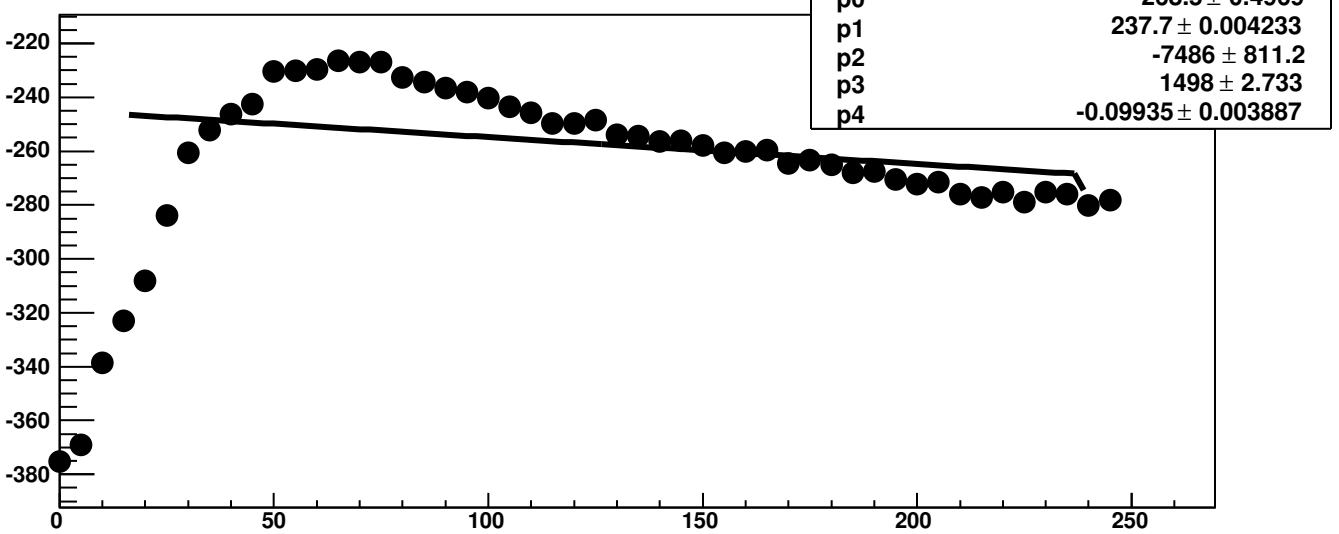



Chip 5, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 2639 / 41
p0 -298.9 ± 0.967
p1 142.4 ± 1.003
p2 -336.6 ± 49.97
p3 281 ± 49.47
p4 1.16 ± 0.00951
-280
-300
-320
-340
-360
-380
-400
-420
-440
-460
0
50
100
150
200
250

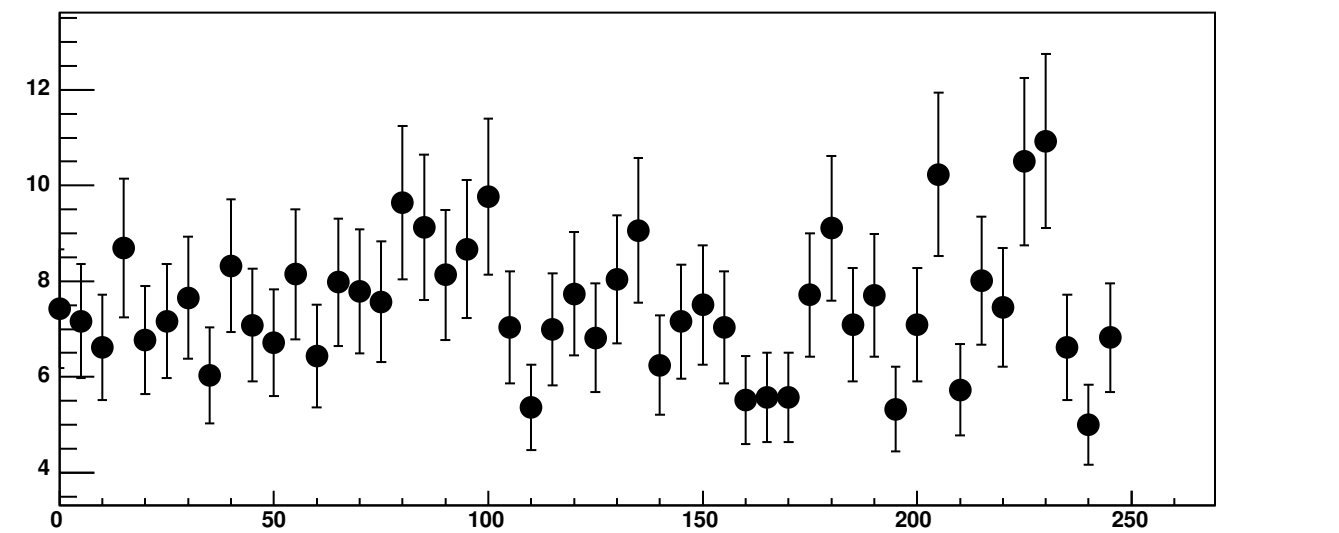

Chip 5, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 5, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold
100
80
60
40
20
0
-20
-40
0
50
100
150
200
250

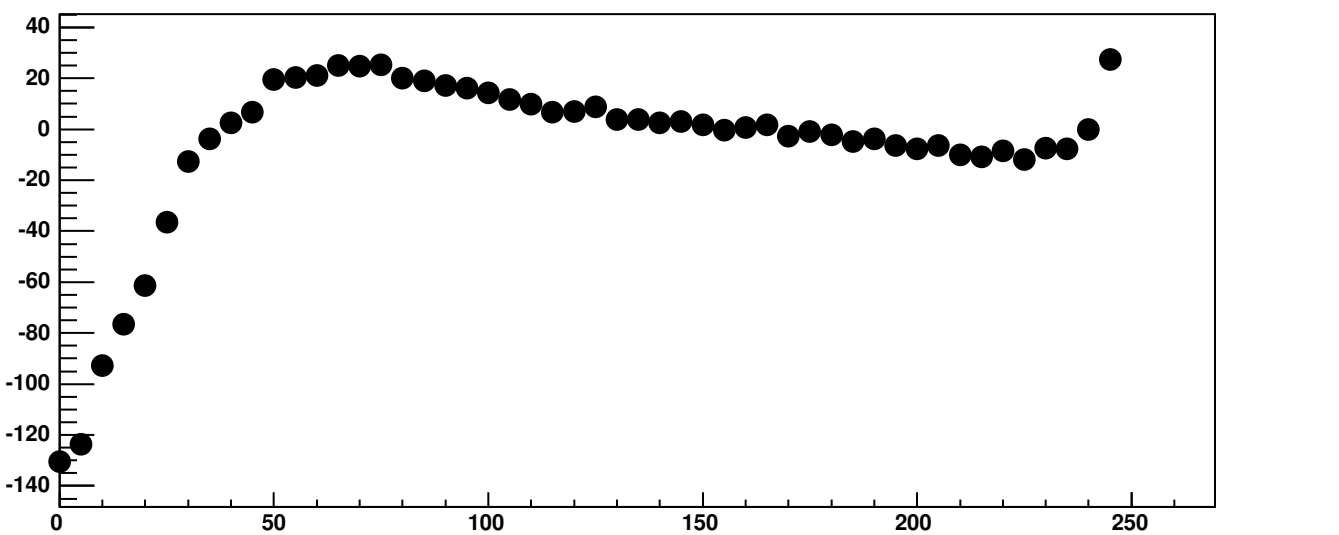
Chip 5, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold



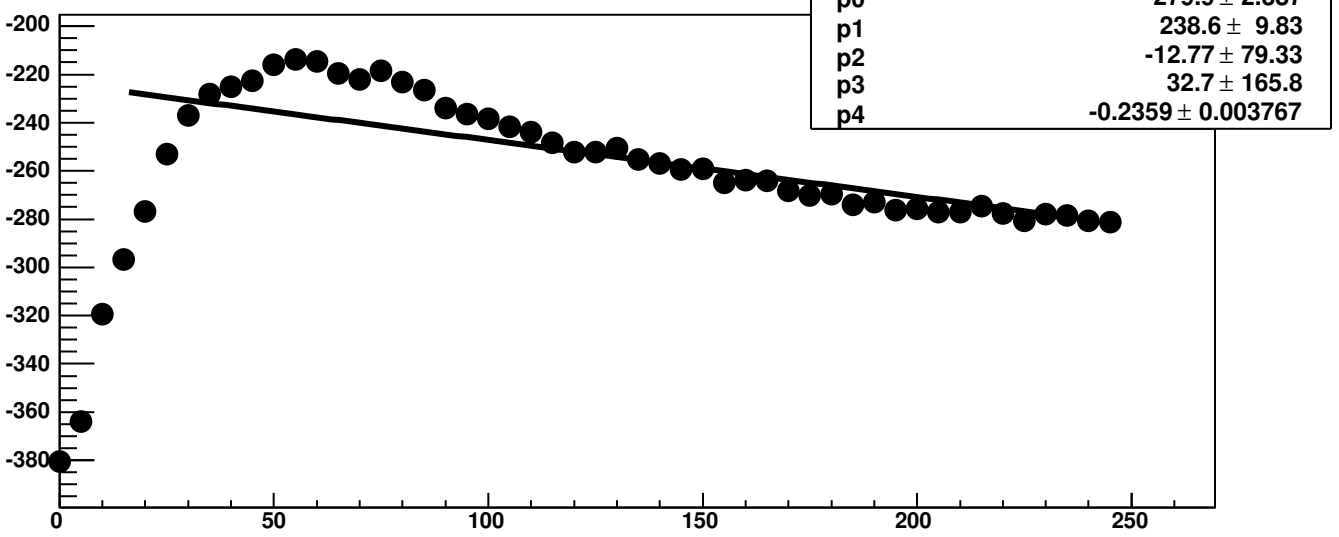
Chip 5, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



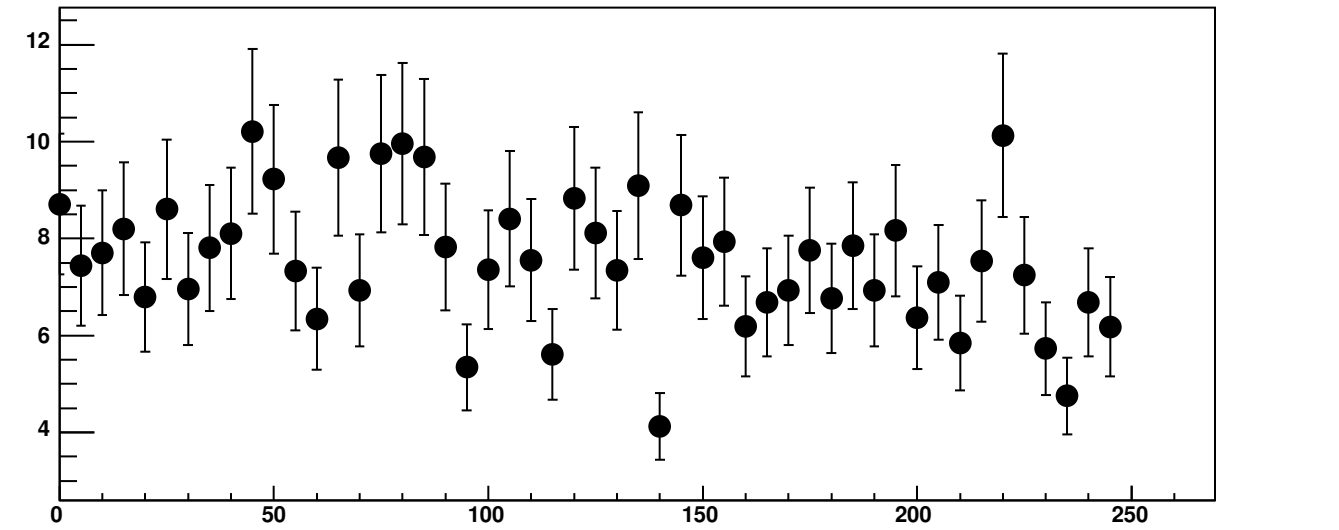
Chip 5, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold



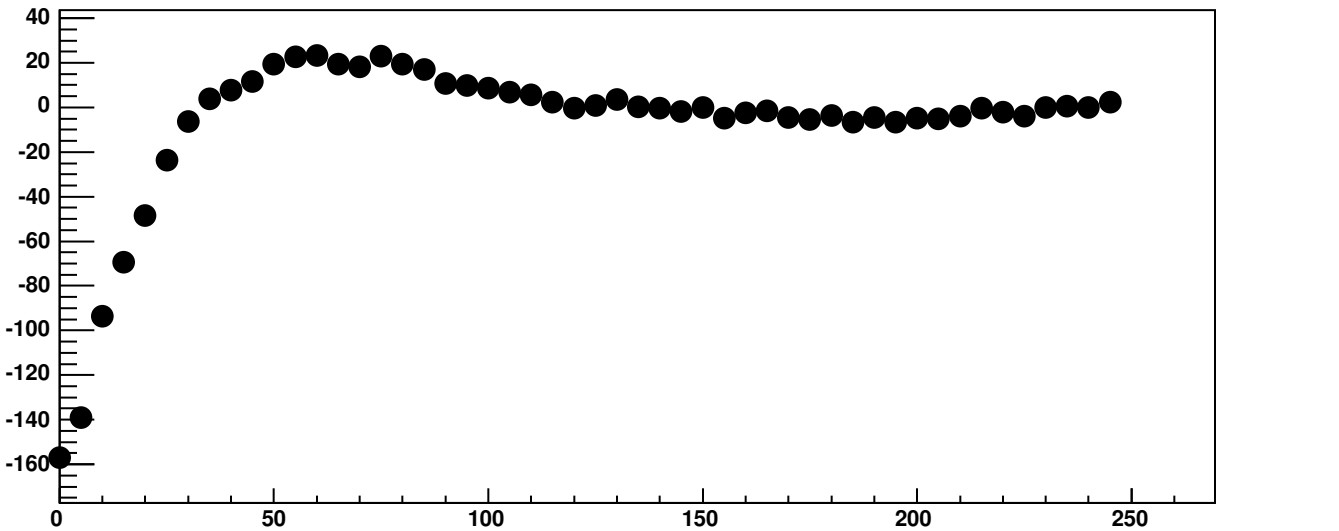
Chip 5, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold

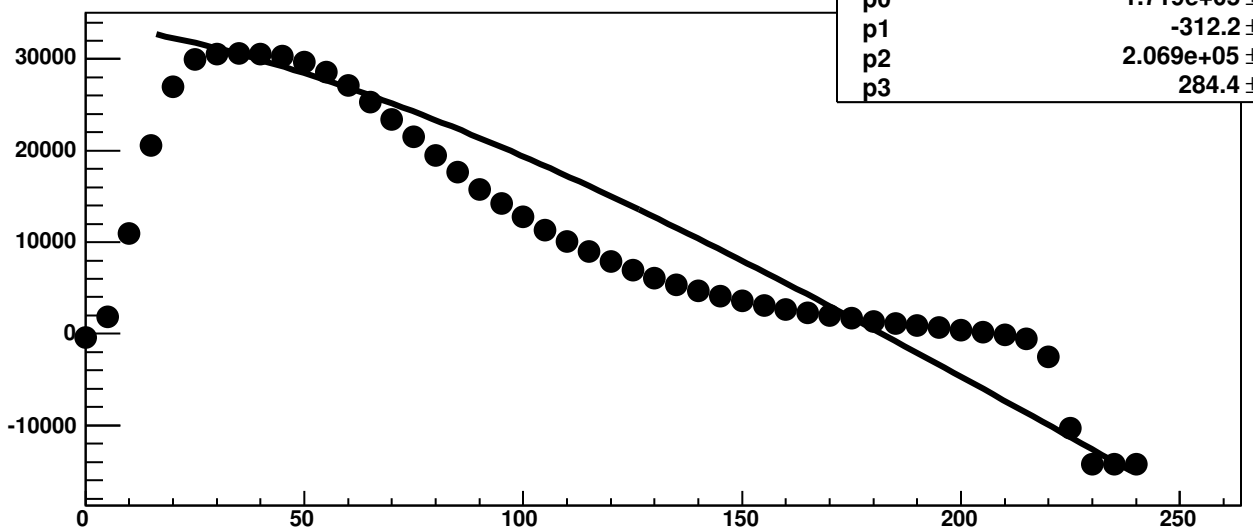


Chip 5, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

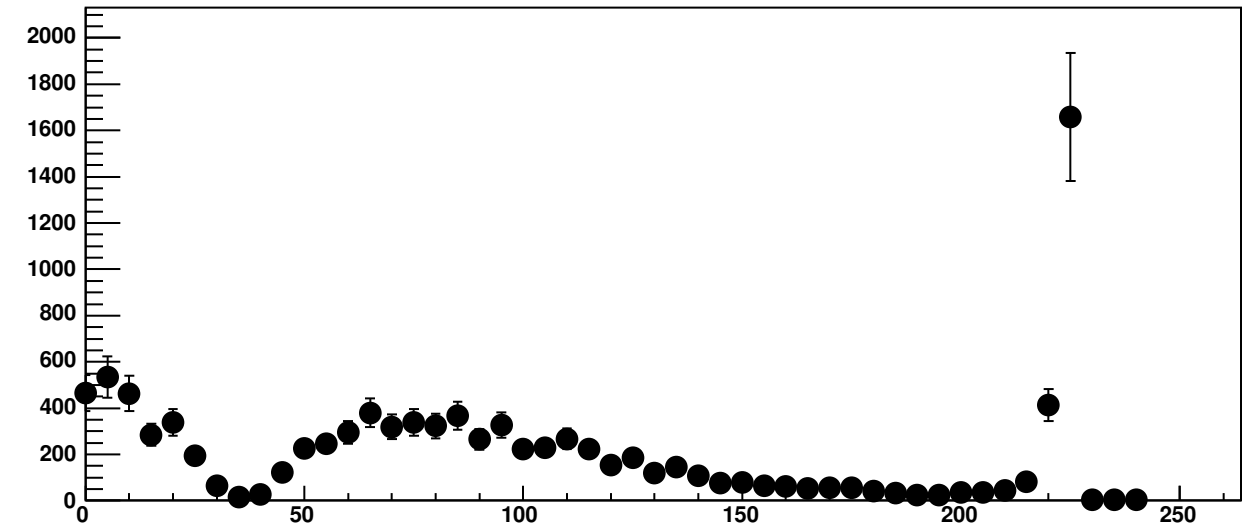


Chip 5, Channel 15, Enable 5!, DAC=4000, ADC Mean vs Hold

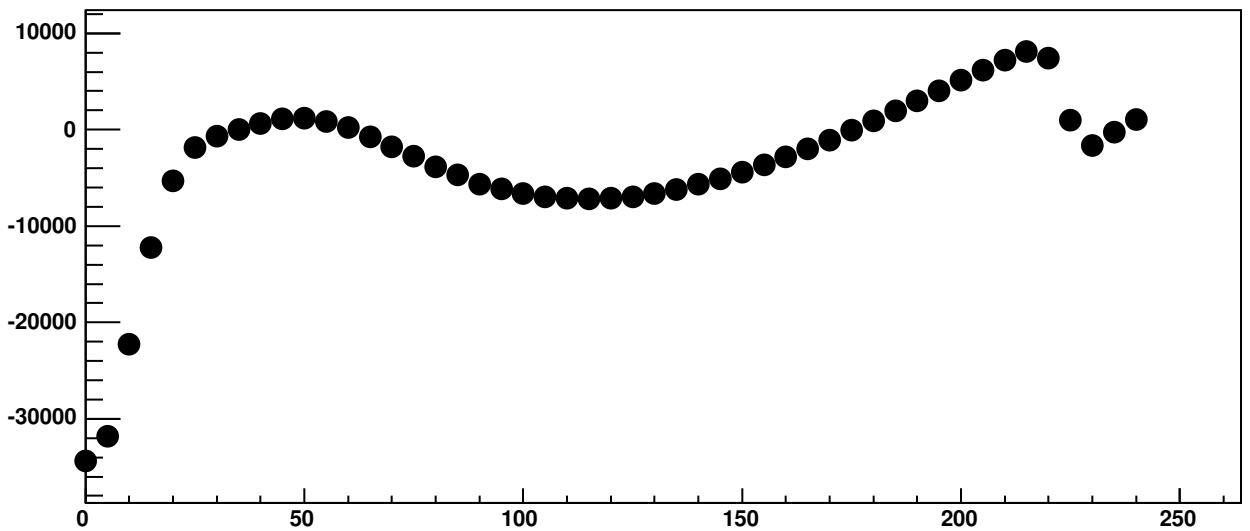
χ^2 / ndf	5.835e+06 / 42
p0	-1.719e+05 ± 1046
p1	-312.2 ± 1.812
p2	2.069e+05 ± 1089
p3	284.4 ± 1.358

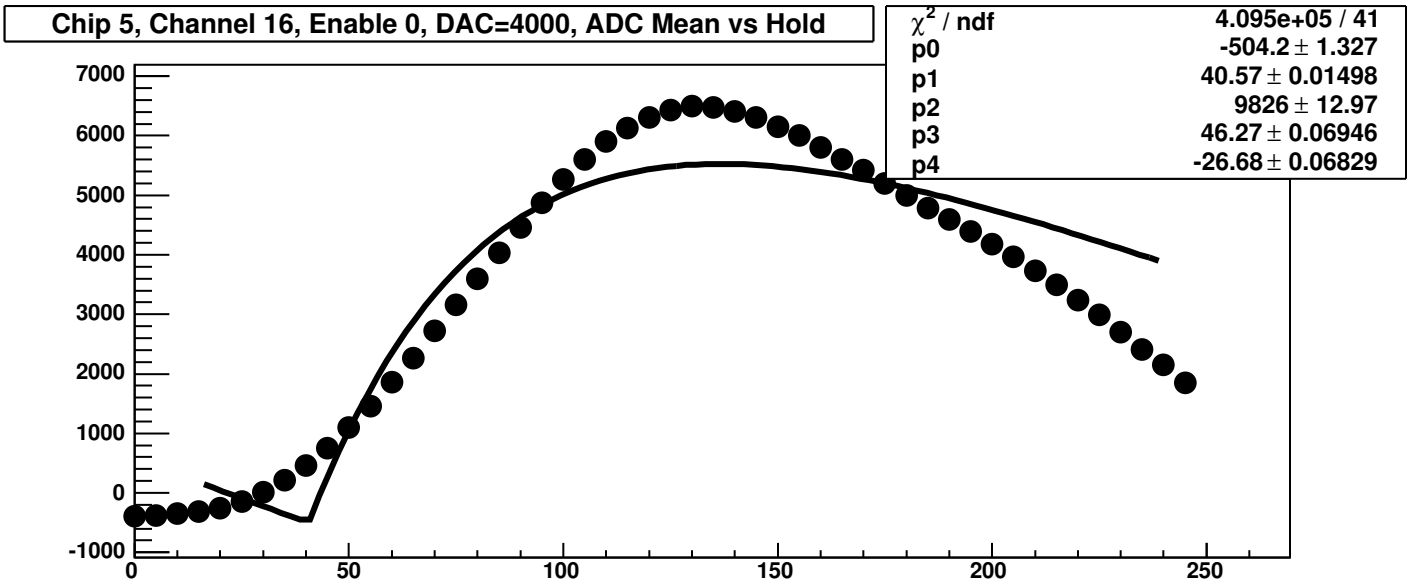


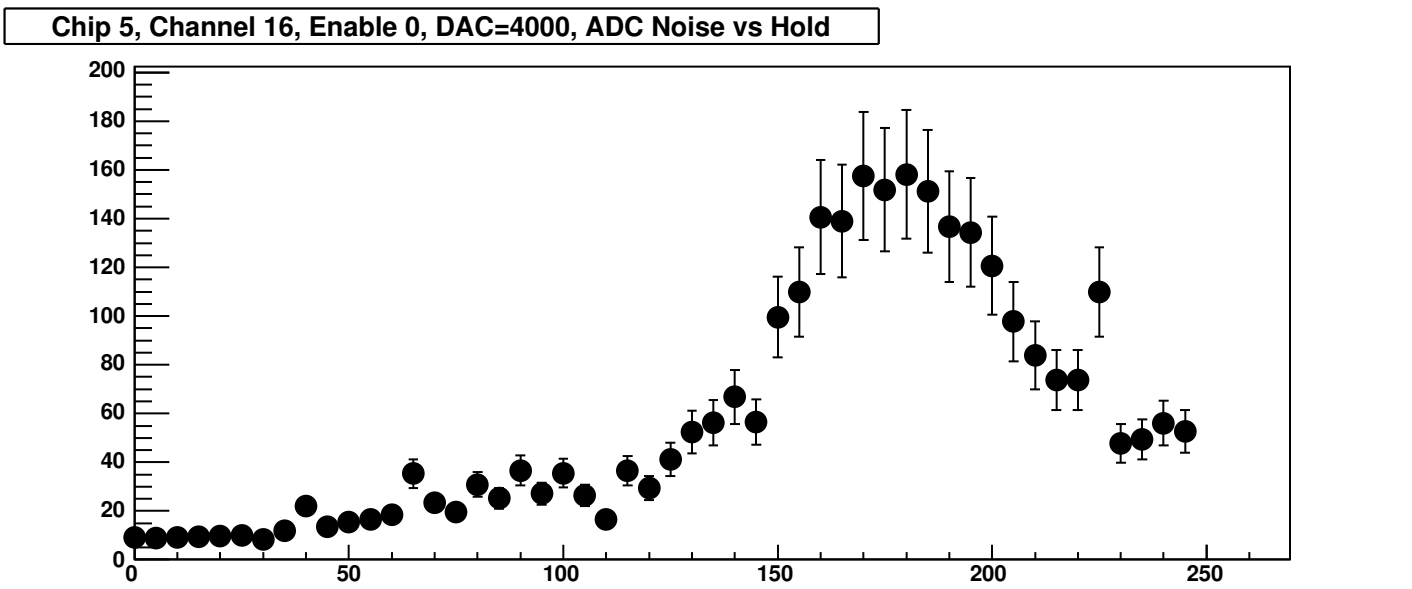
Chip 5, Channel 15, Enable 5!, DAC=4000, ADC Noise vs Hold

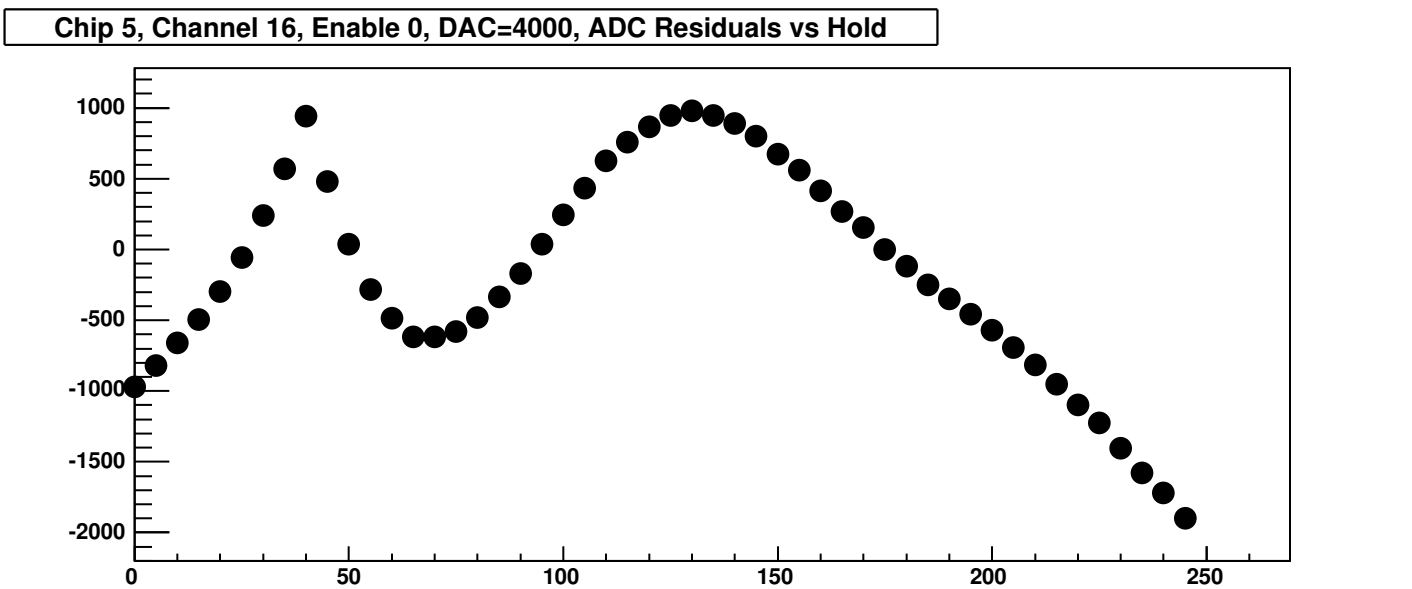


Chip 5, Channel 15, Enable 5!, DAC=4000, ADC Residuals vs Hold

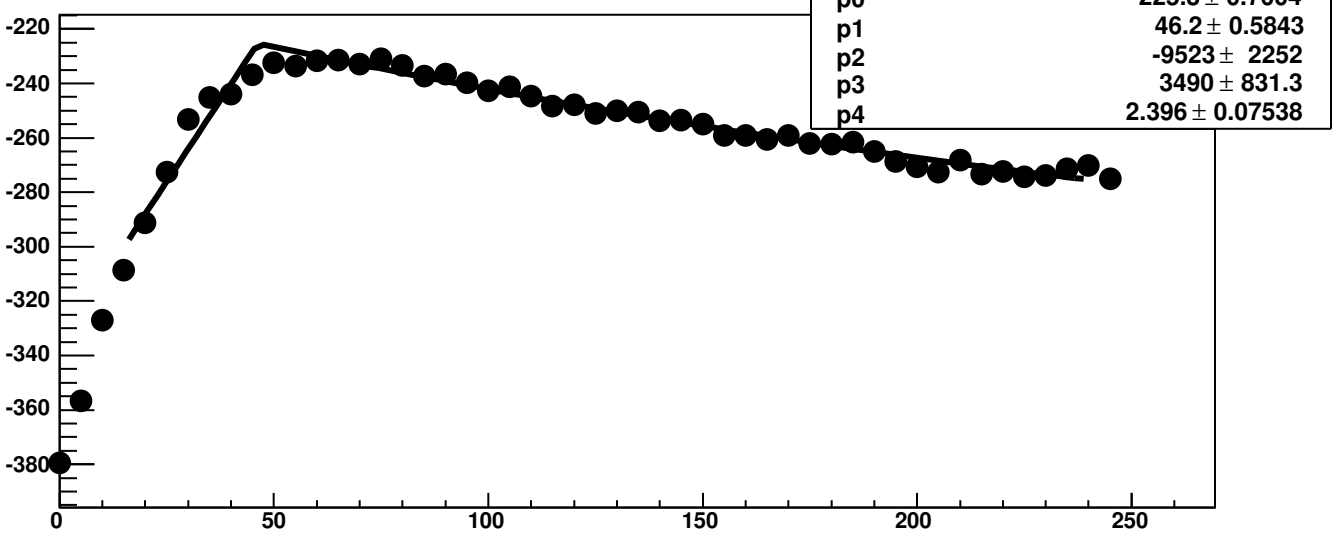



Chip 5, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.095e+05 / 41
p0 -504.2 ± 1.327
p1 40.57 ± 0.01498
p2 9826 ± 12.97
p3 46.27 ± 0.06946
p4 -26.68 ± 0.06829
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

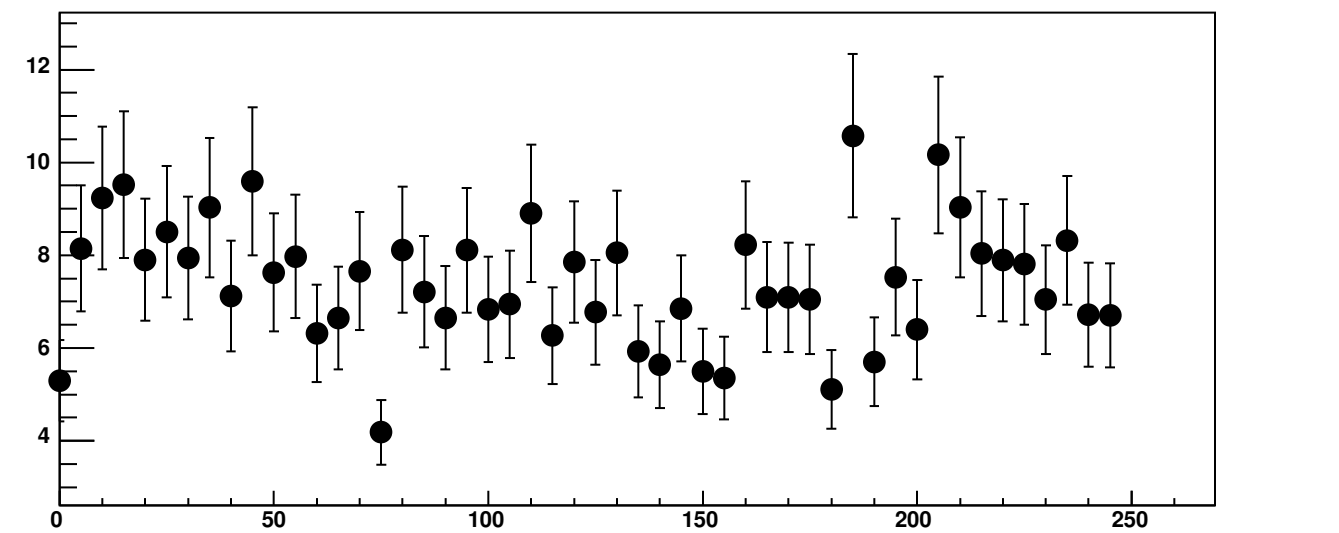

Chip 5, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 5, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

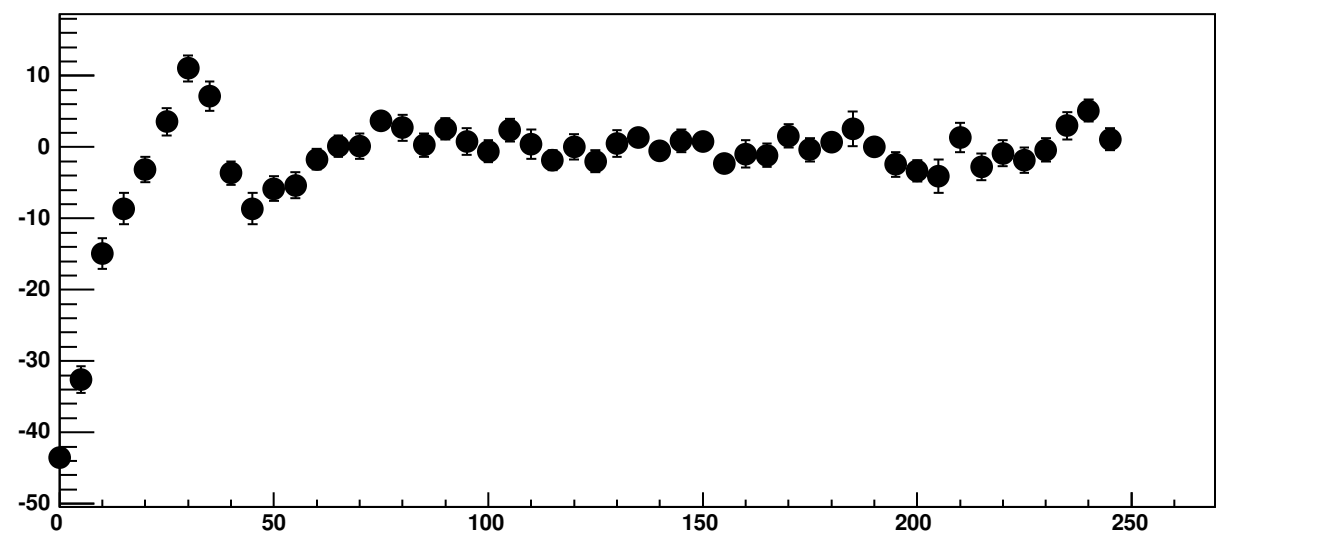
Chip 5, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold

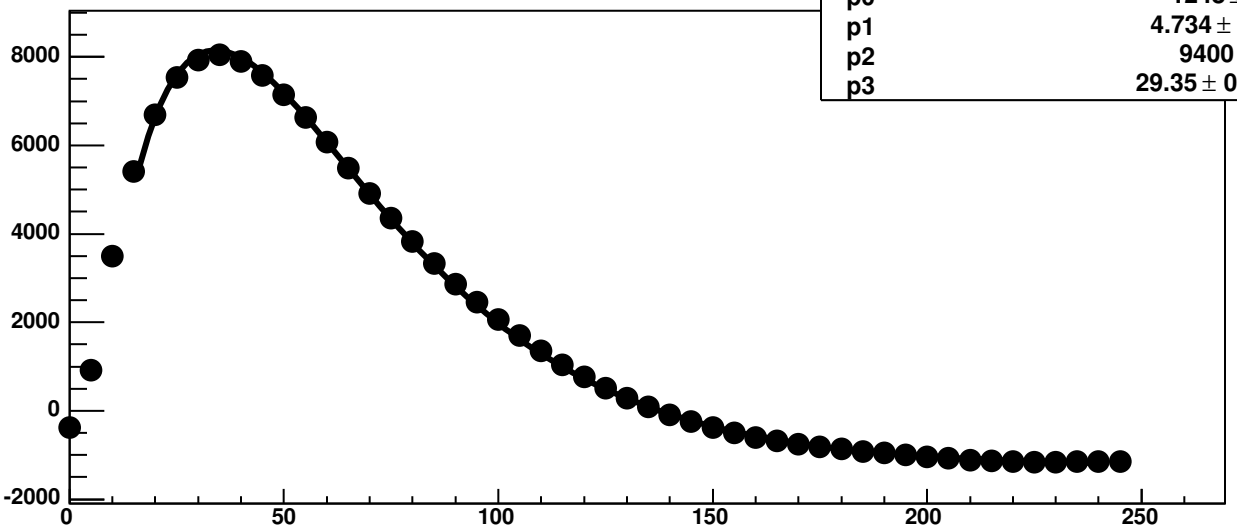


Chip 5, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold

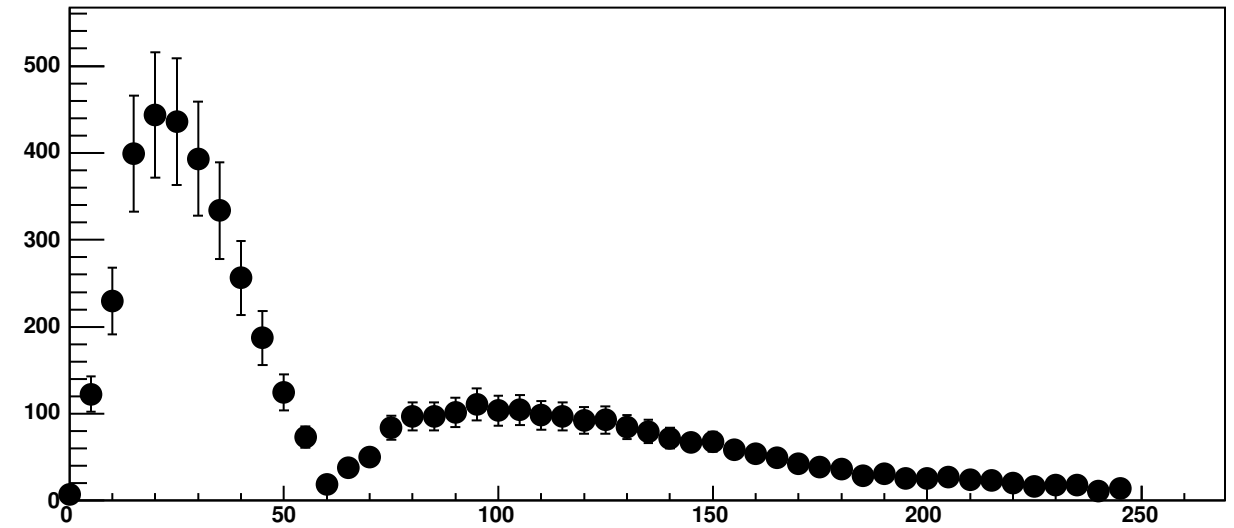


Chip 5, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold

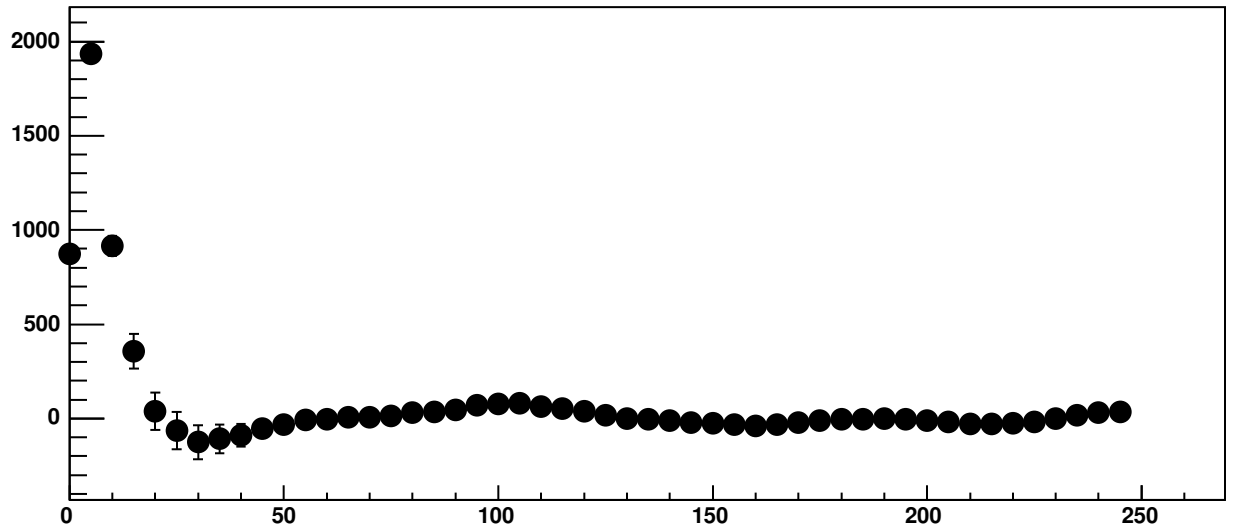
χ^2 / ndf	409.2 / 42
p0	-1248 ± 1.825
p1	4.734 ± 0.1522
p2	9400 ± 17.1
p3	29.35 ± 0.05105



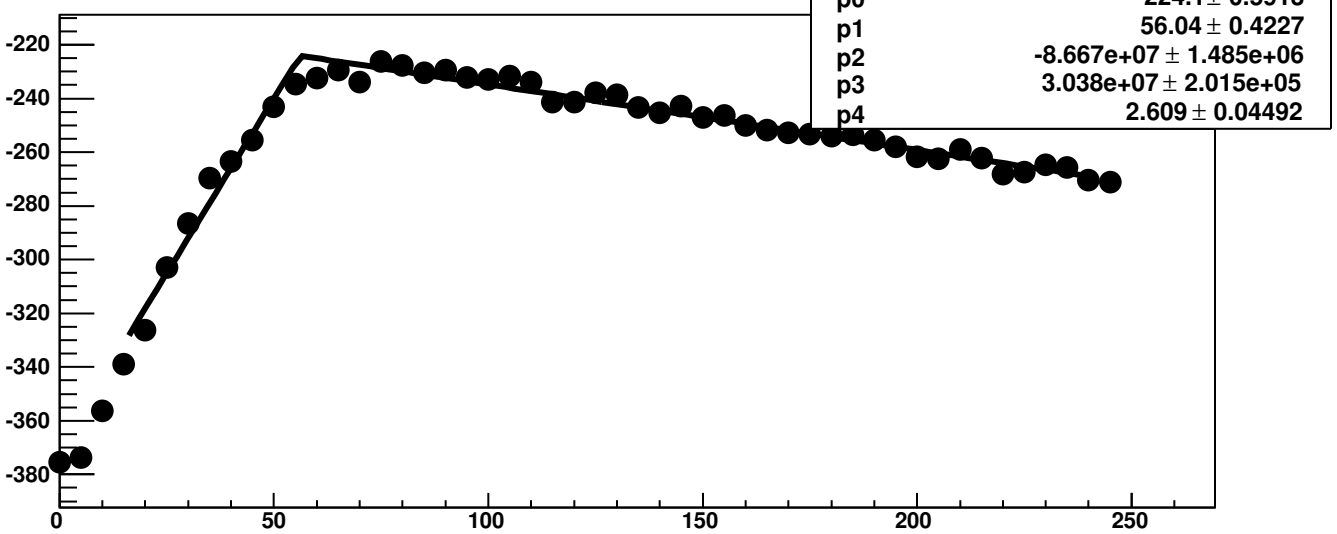
Chip 5, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold



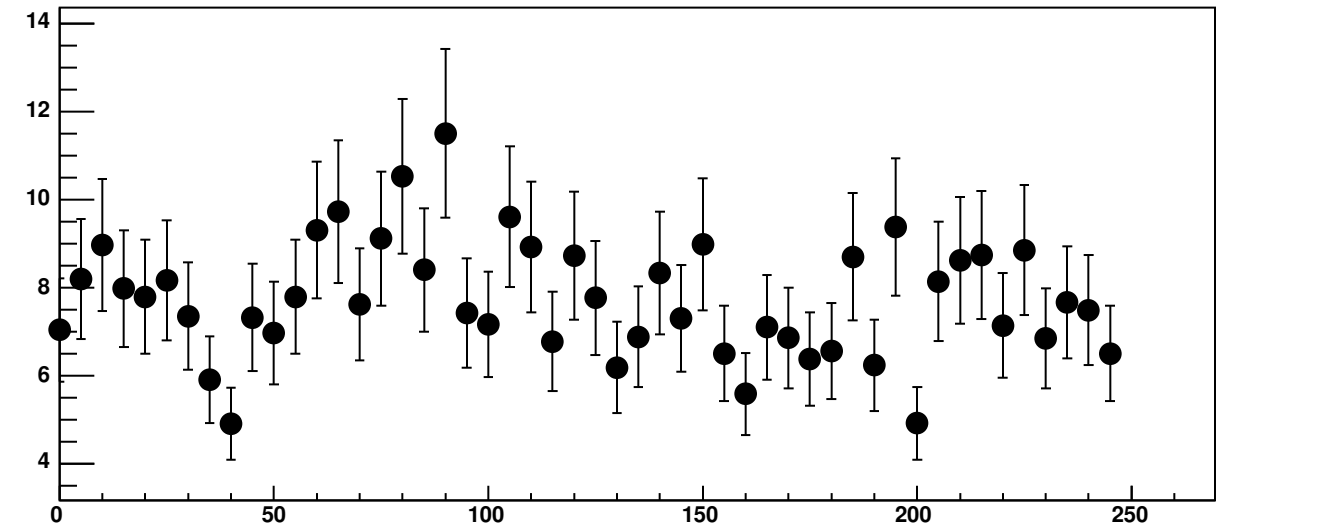
Chip 5, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold



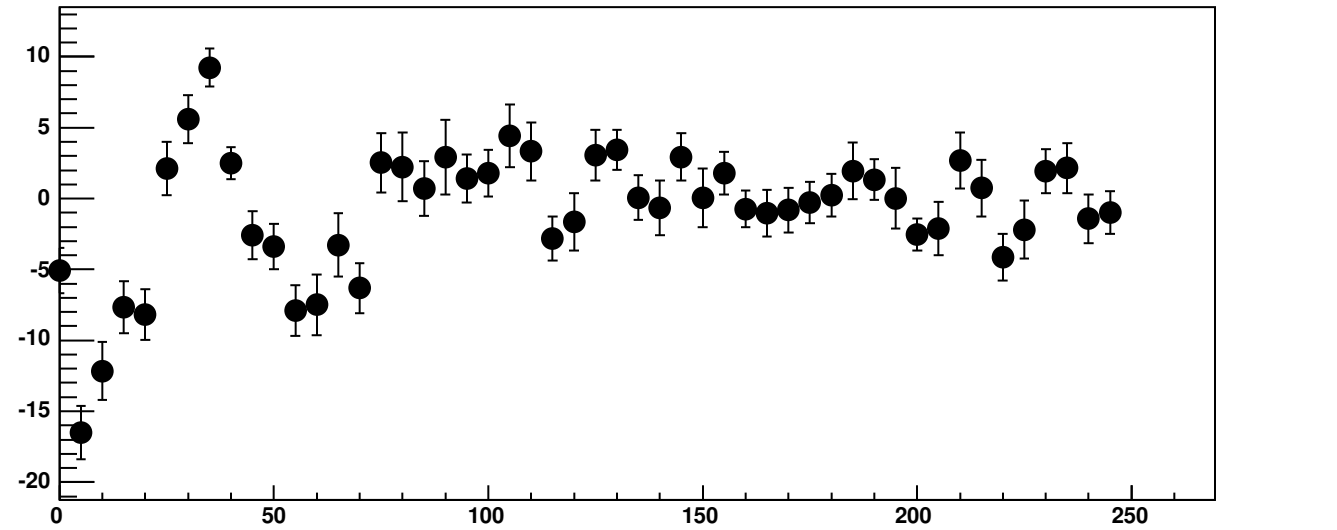
Chip 5, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold



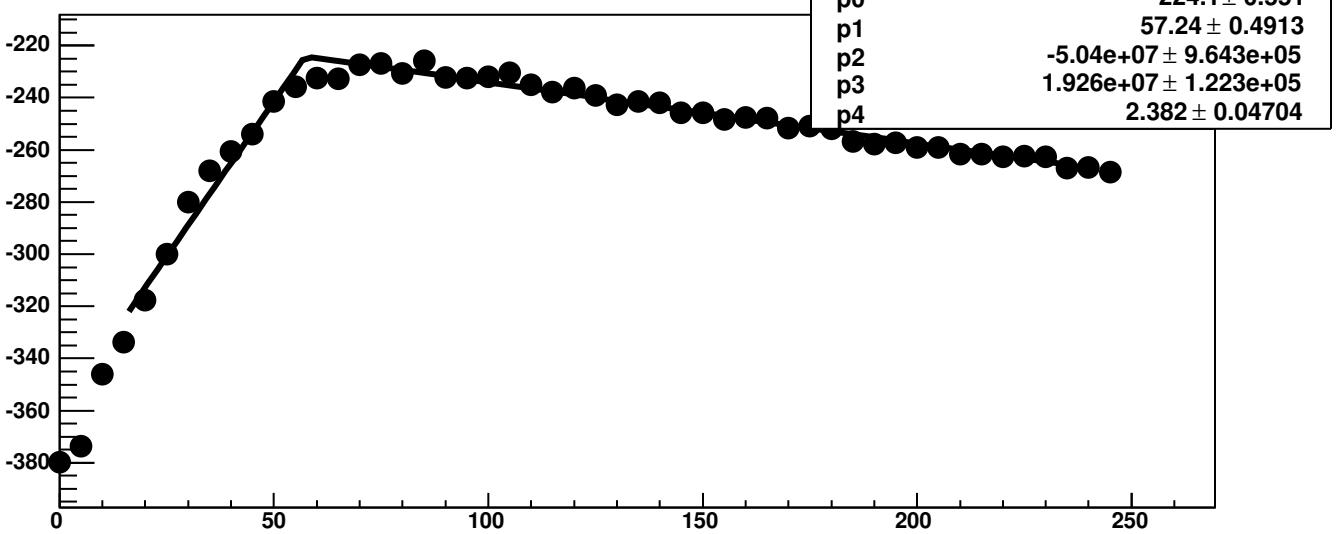
Chip 5, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



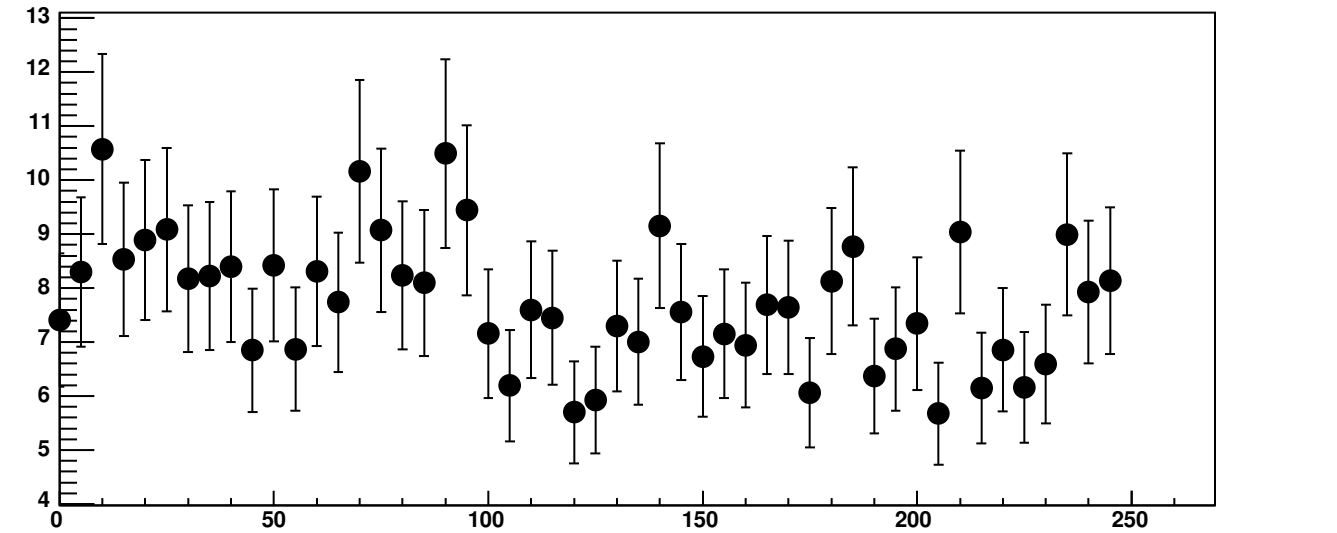
Chip 5, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold



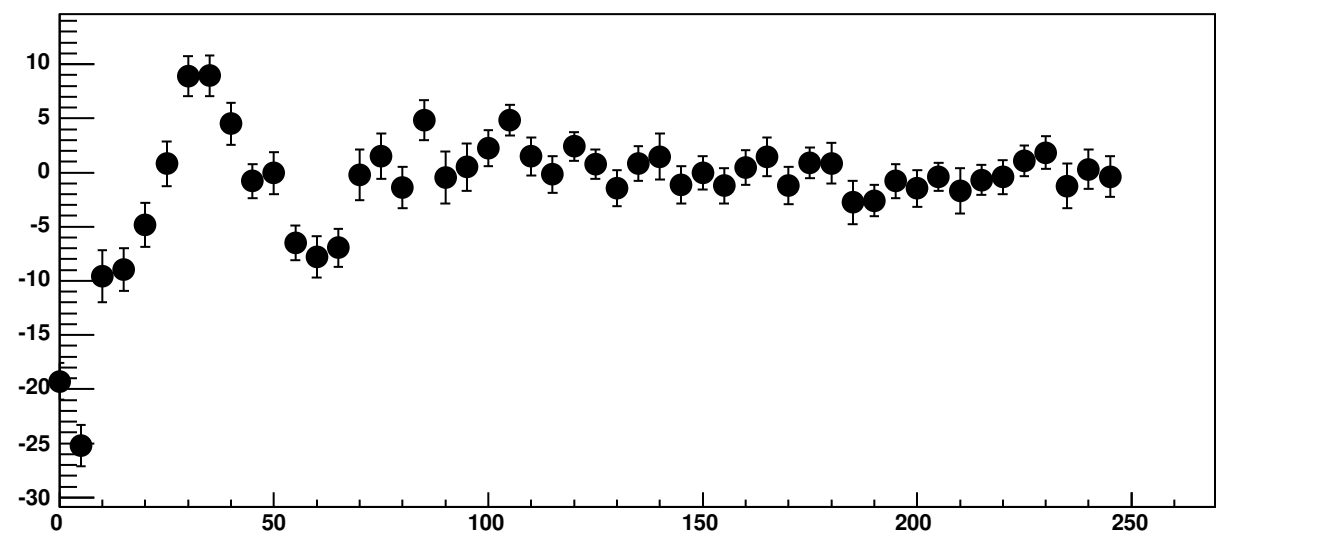
Chip 5, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold



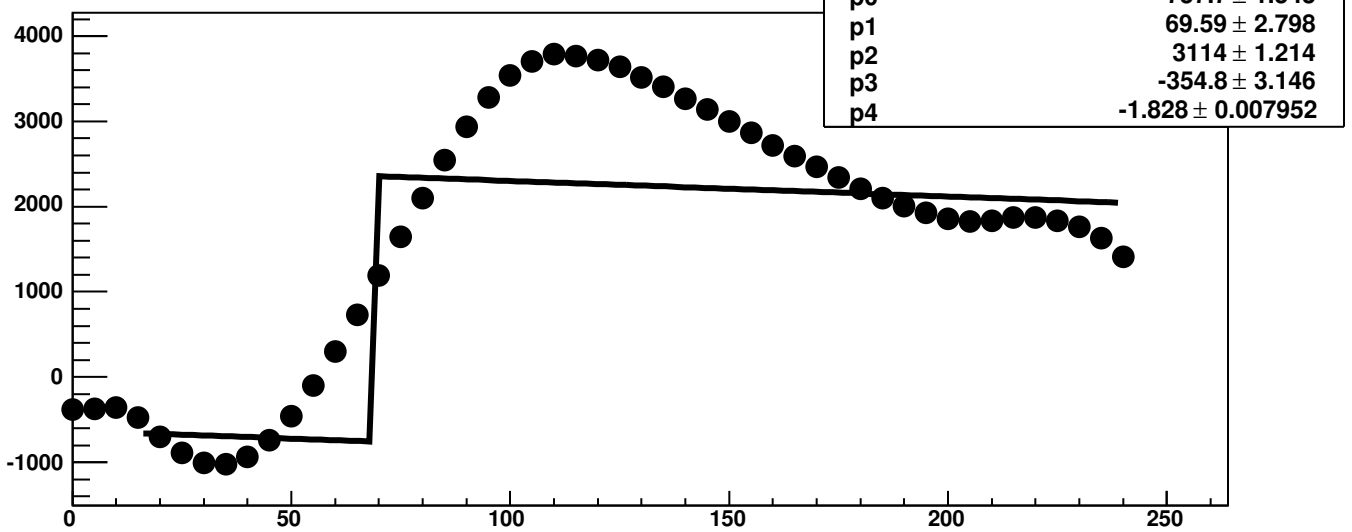
Chip 5, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



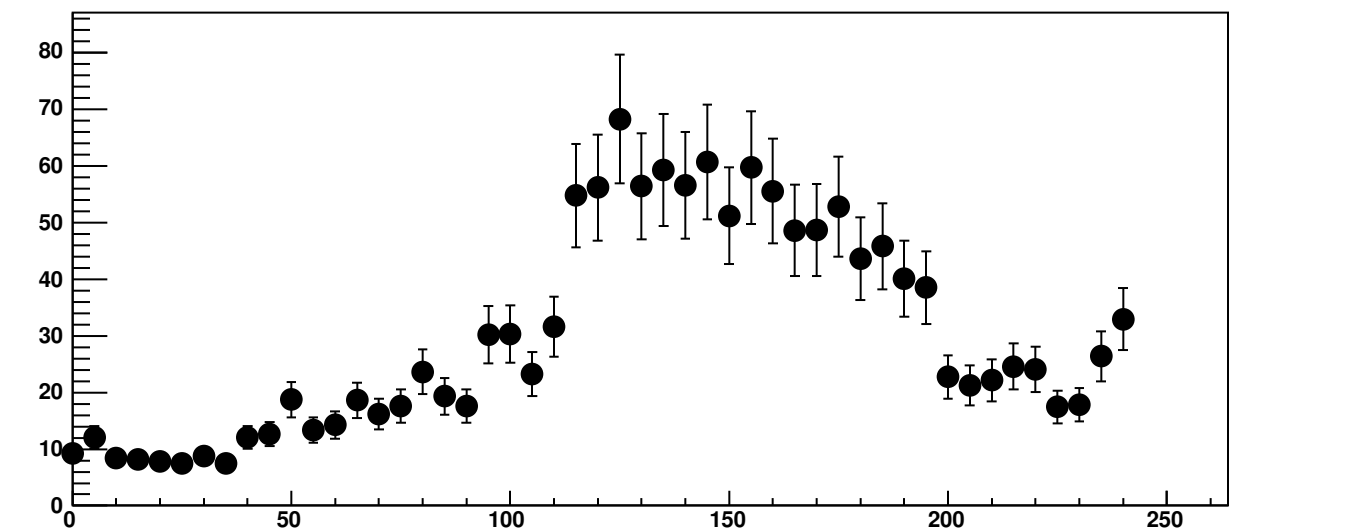
Chip 5, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold



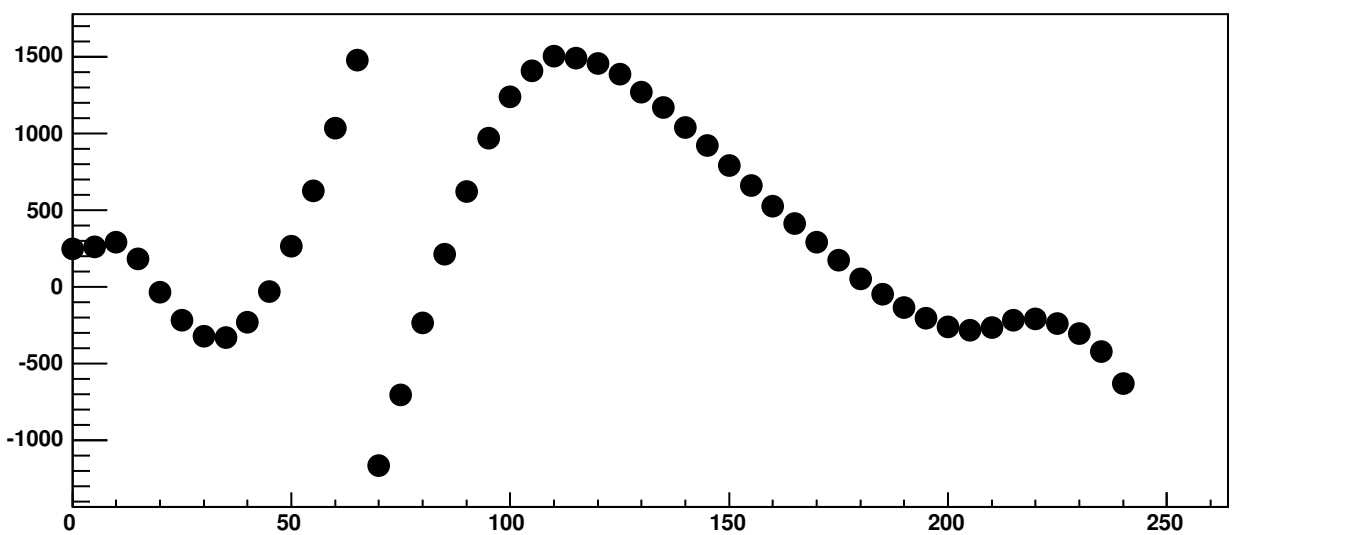
Chip 5, Channel 16, Enable 5, DAC=4000, ADC Mean vs Hold

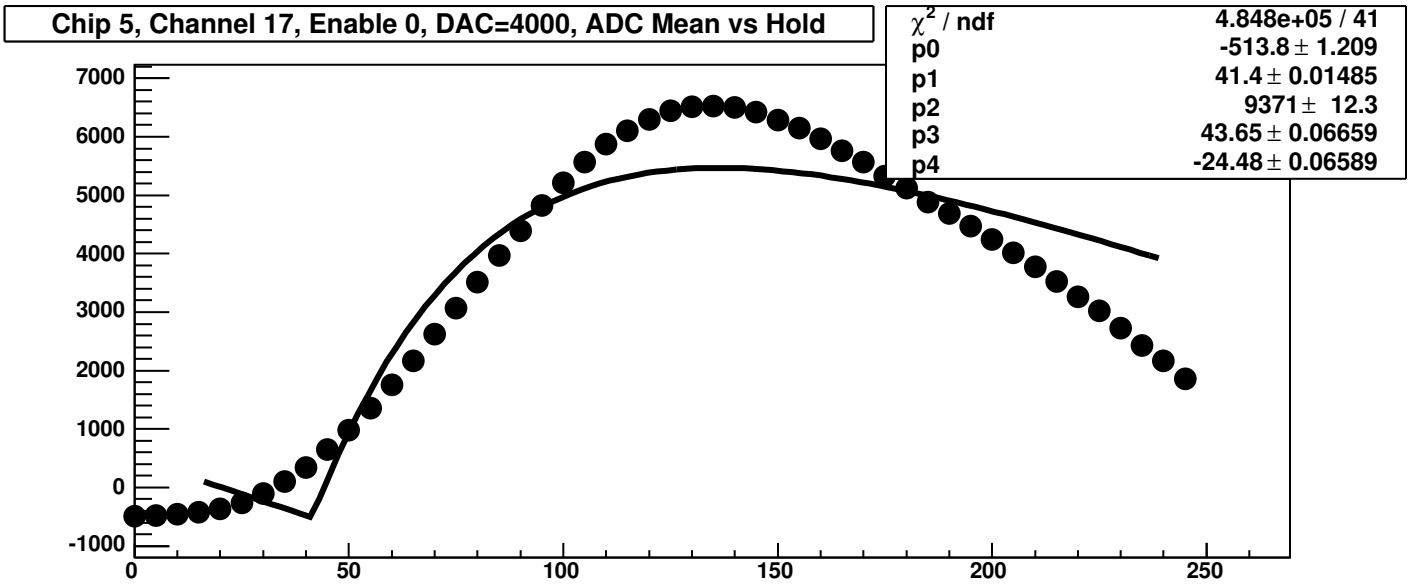


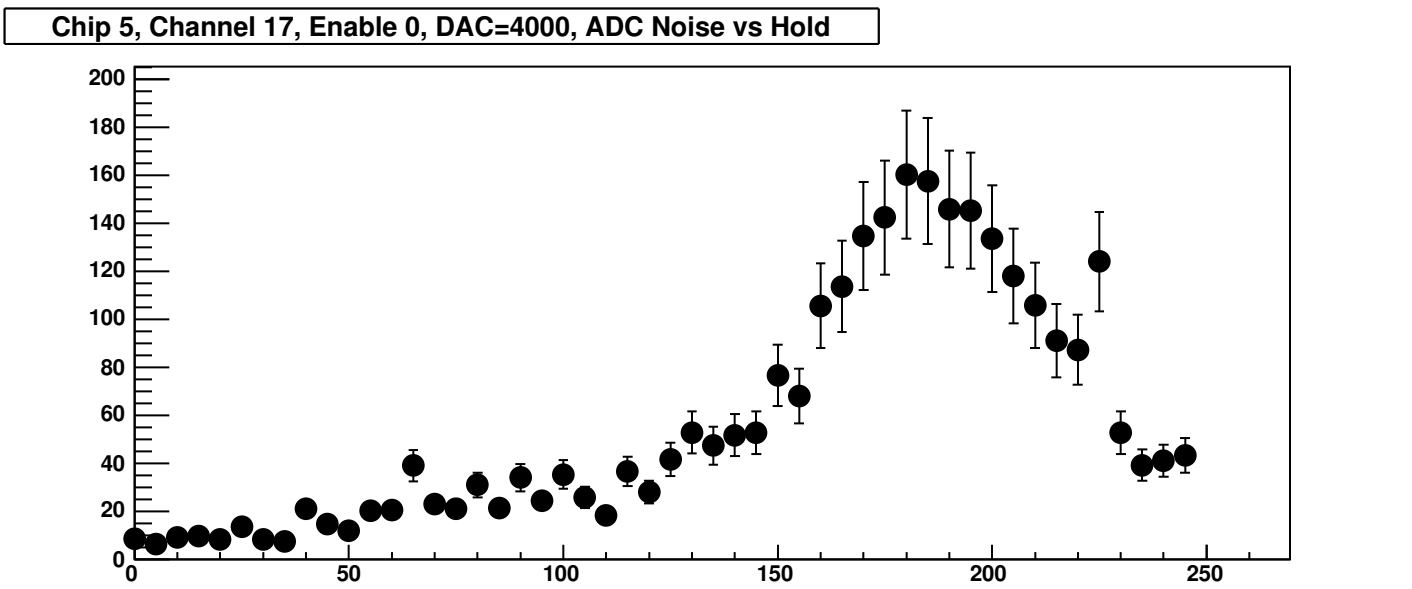
Chip 5, Channel 16, Enable 5, DAC=4000, ADC Noise vs Hold

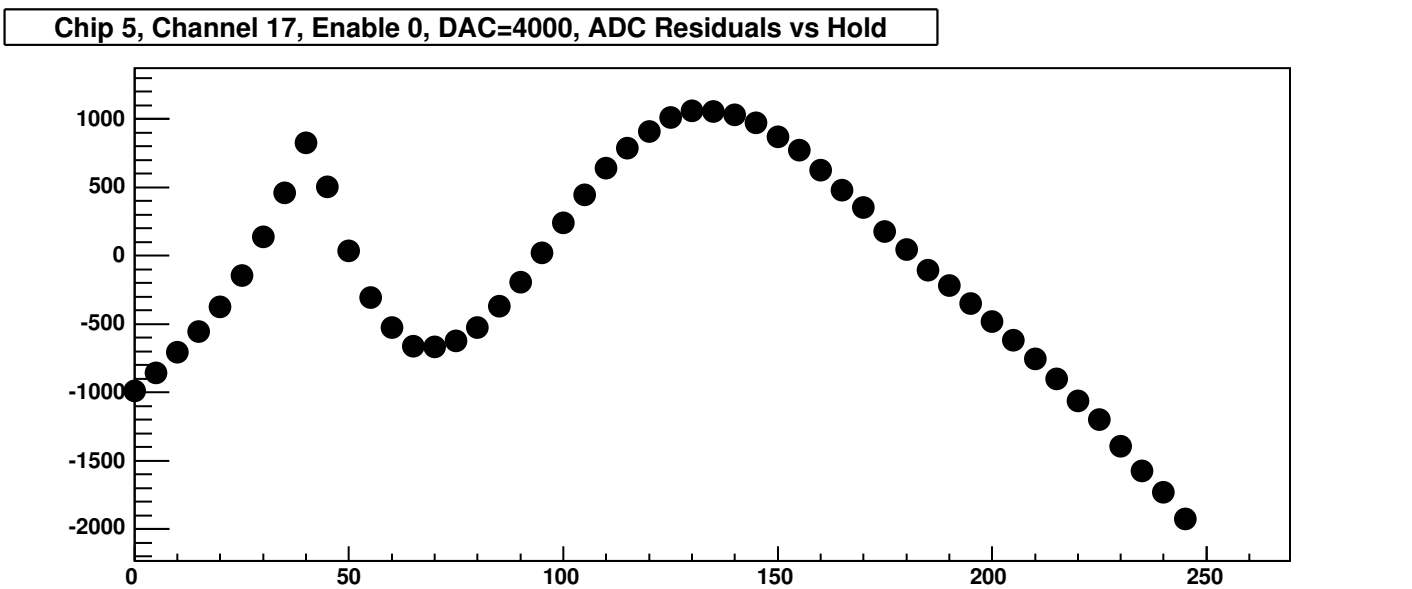


Chip 5, Channel 16, Enable 5, DAC=4000, ADC Residuals vs Hold

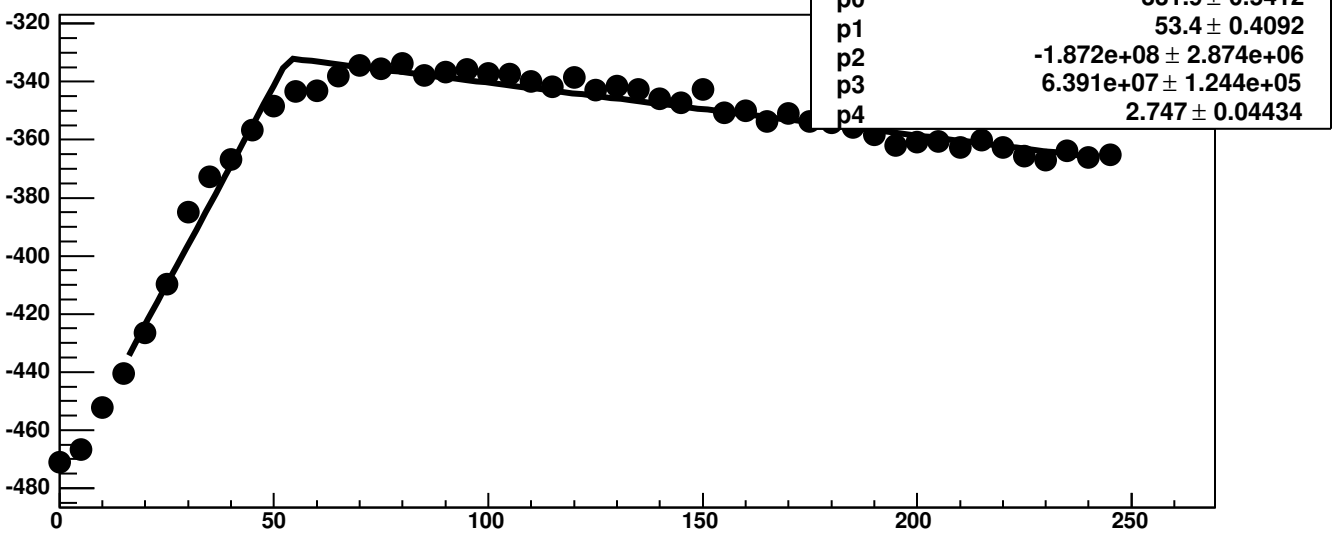



Chip 5, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.848e+05 / 41
p0 -513.8 ± 1.209
p1 41.4 ± 0.01485
p2 9371 ± 12.3
p3 43.65 ± 0.06659
p4 -24.48 ± 0.06589
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

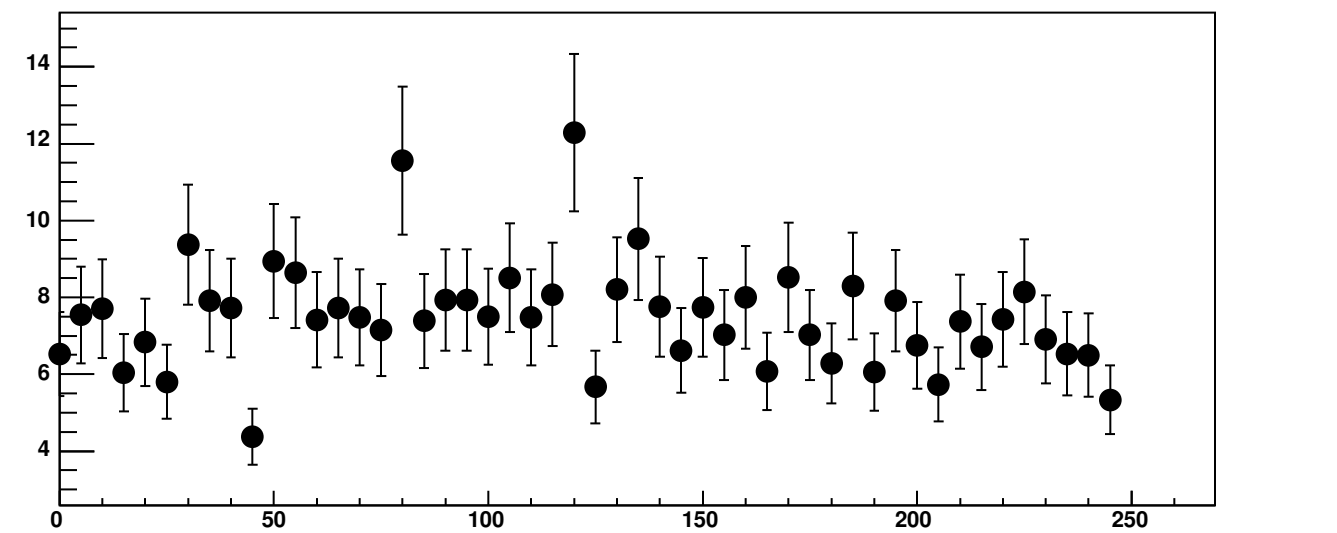

Chip 5, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 5, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

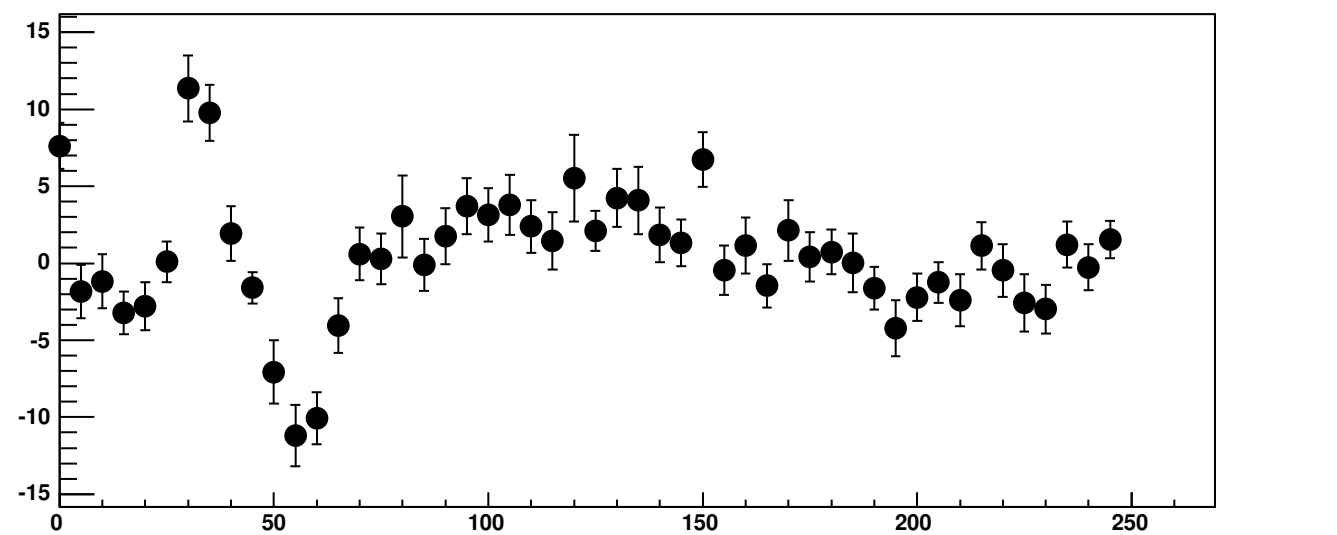
Chip 5, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold



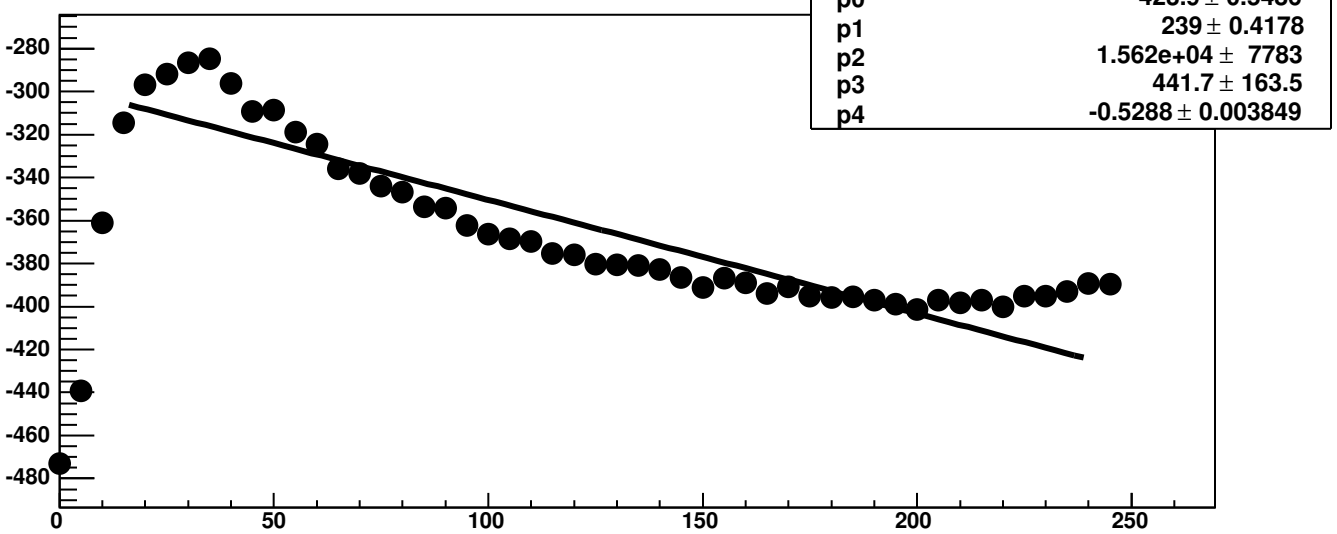
Chip 5, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



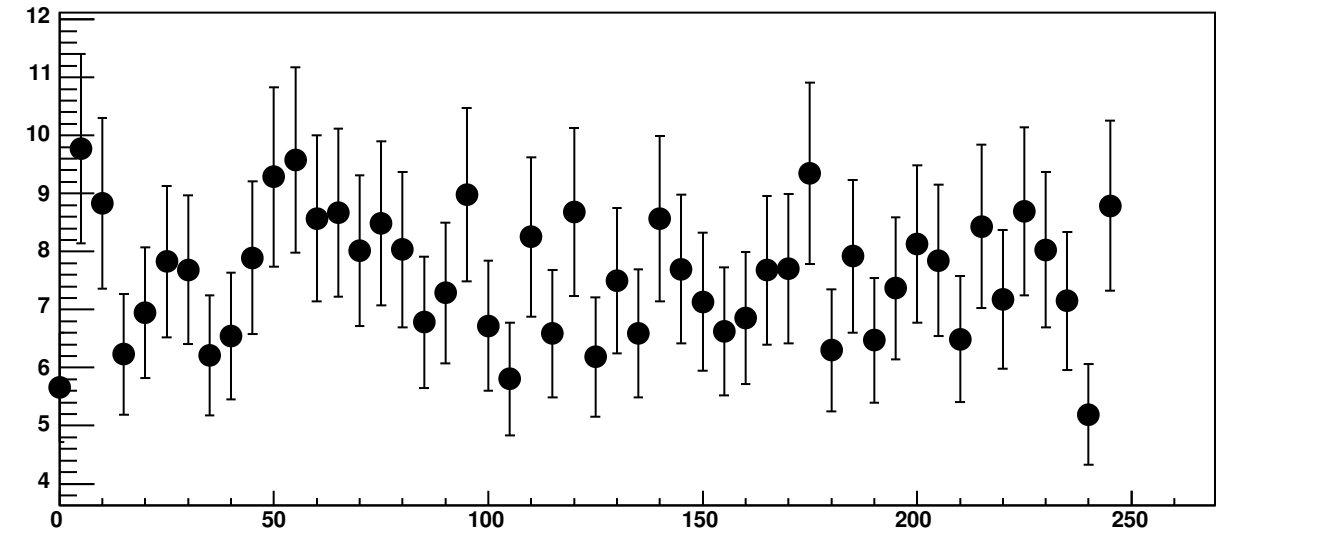
Chip 5, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold



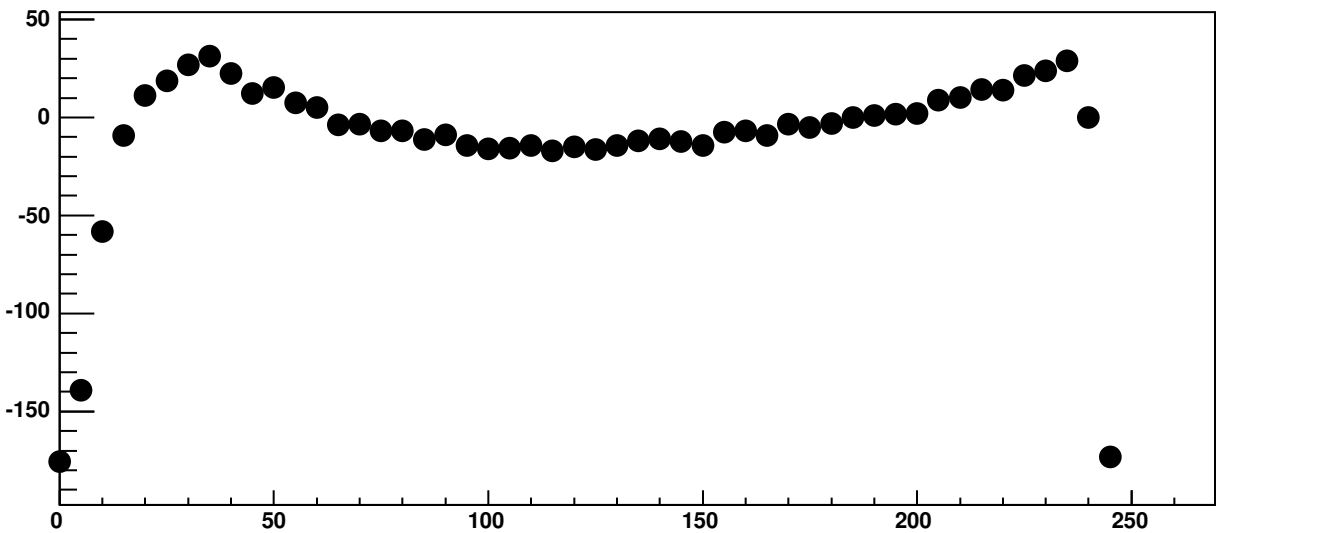
Chip 5, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold



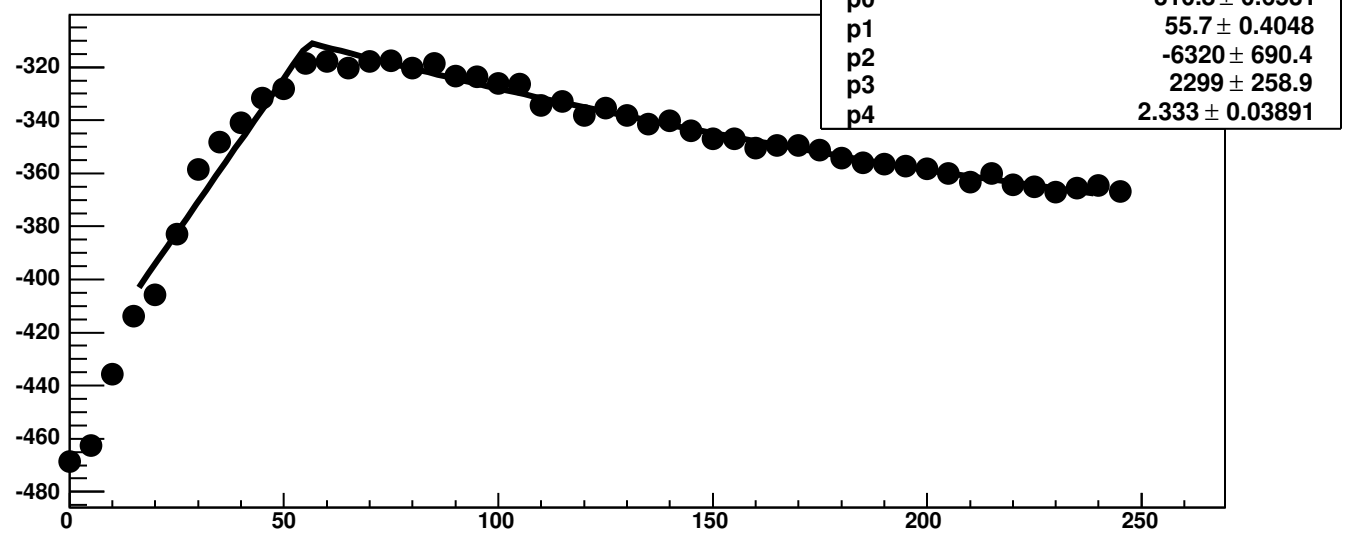
Chip 5, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold



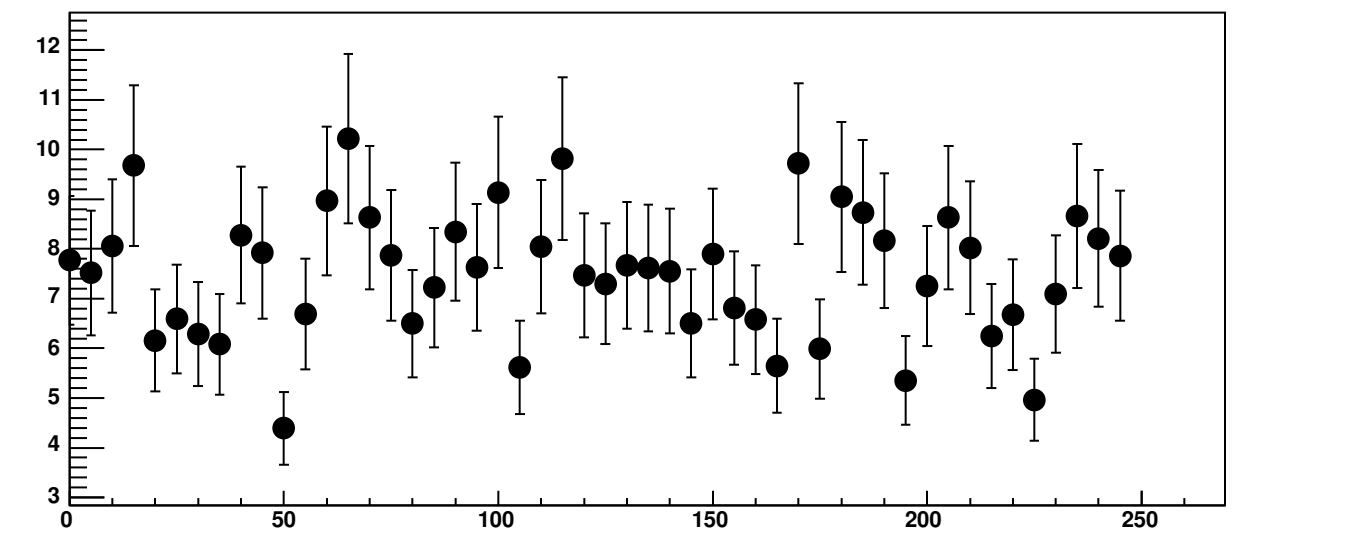
Chip 5, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold



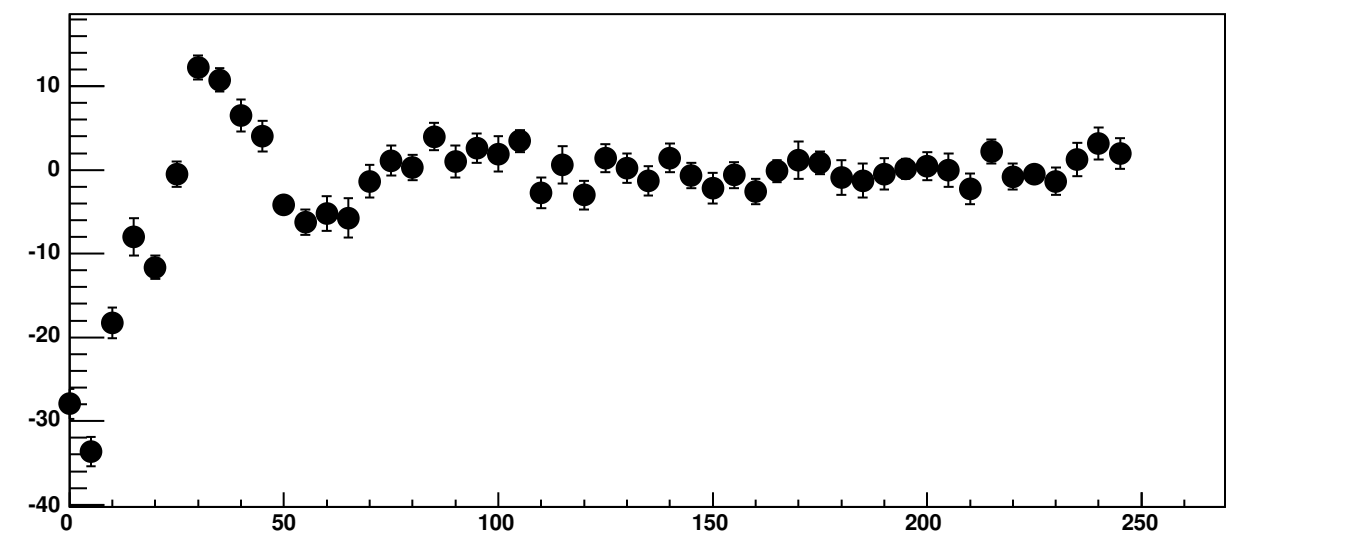
Chip 5, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



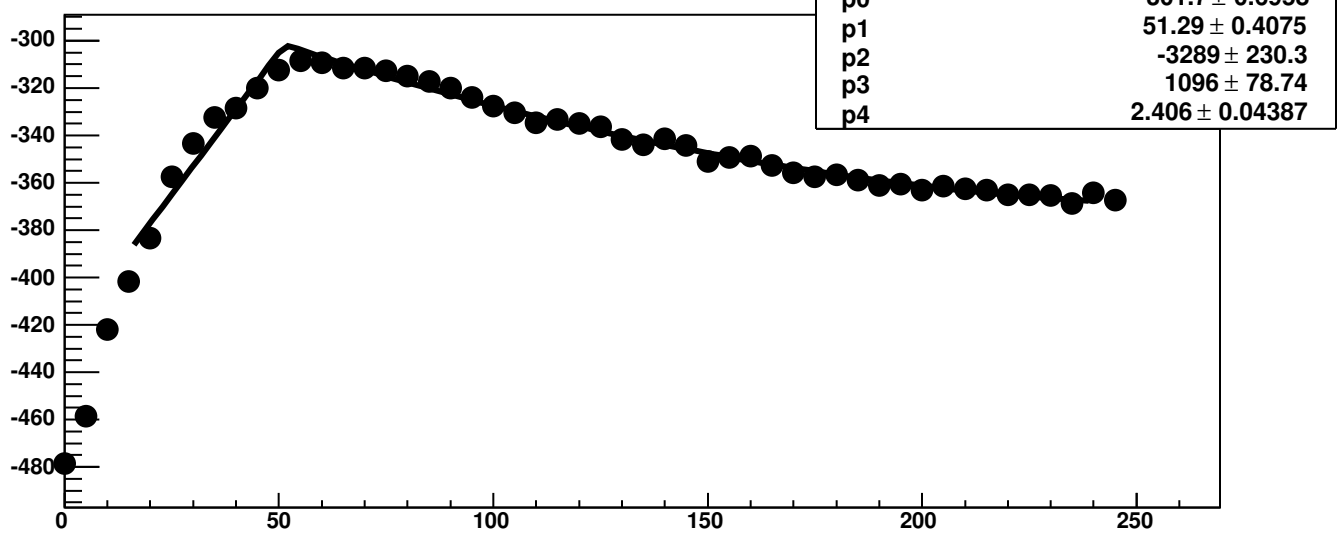
Chip 5, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



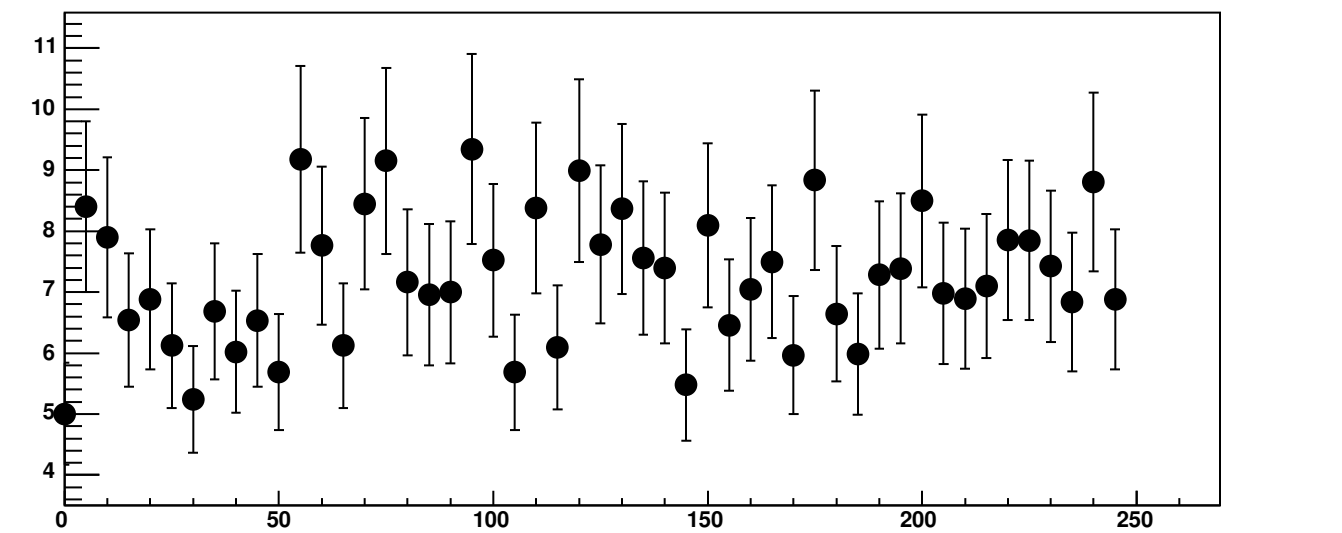
Chip 5, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold



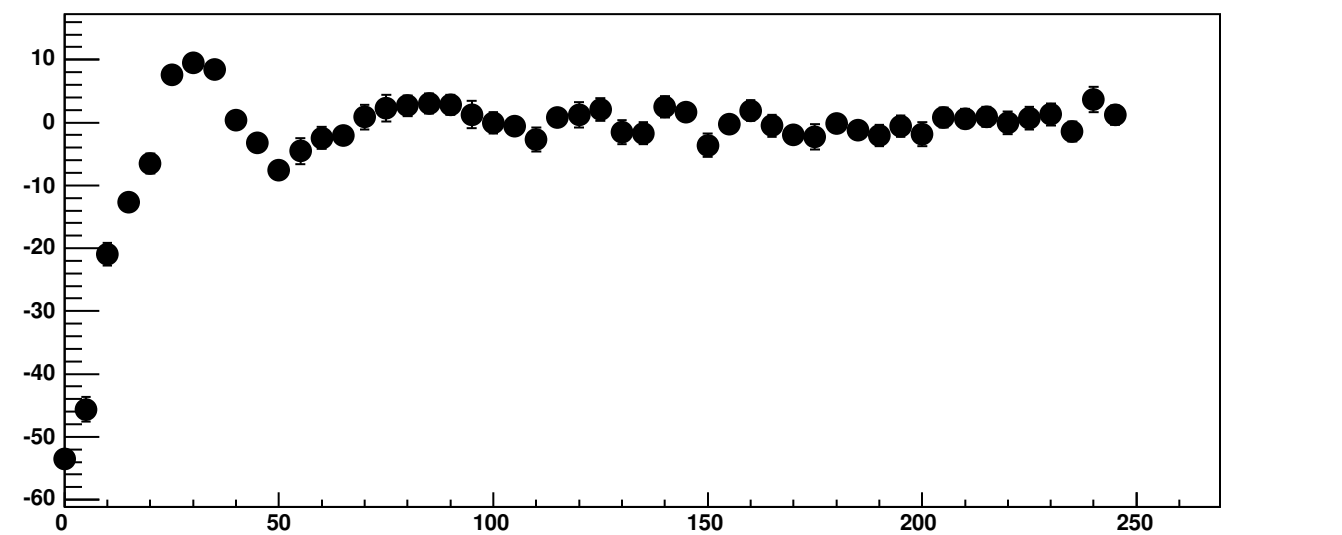
Chip 5, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



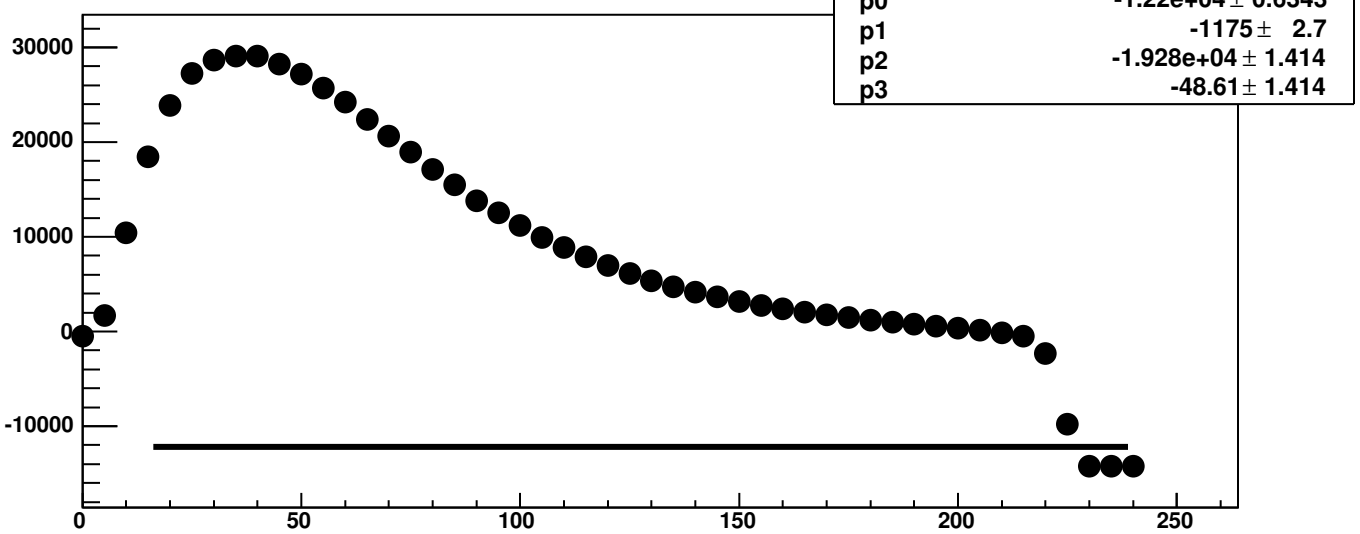
Chip 5, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



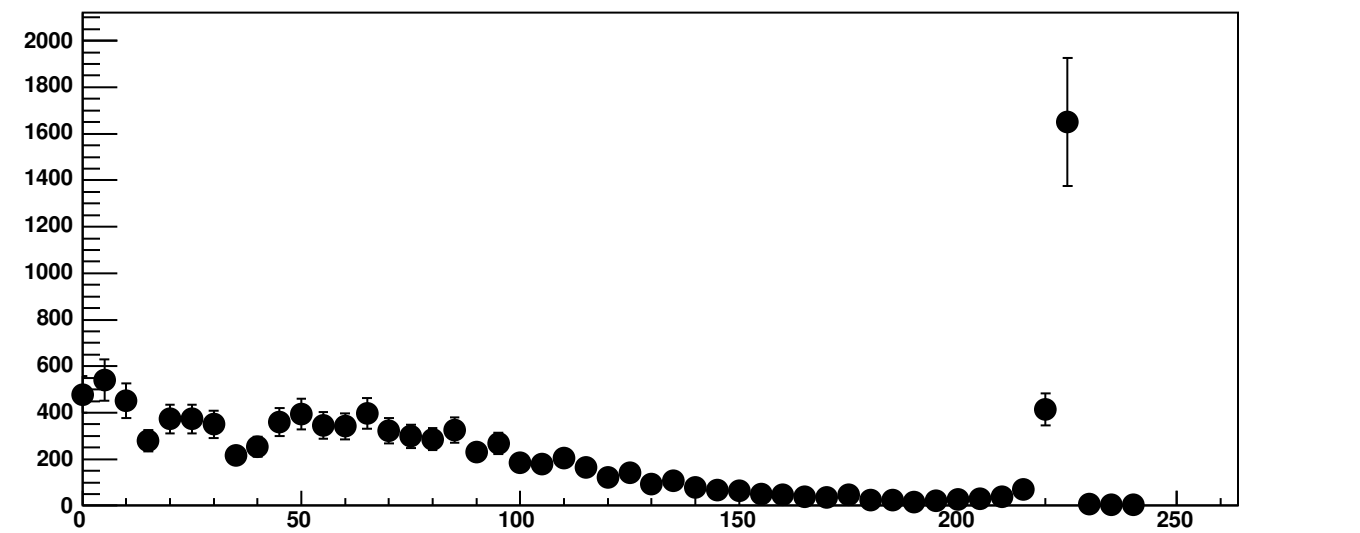
Chip 5, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold



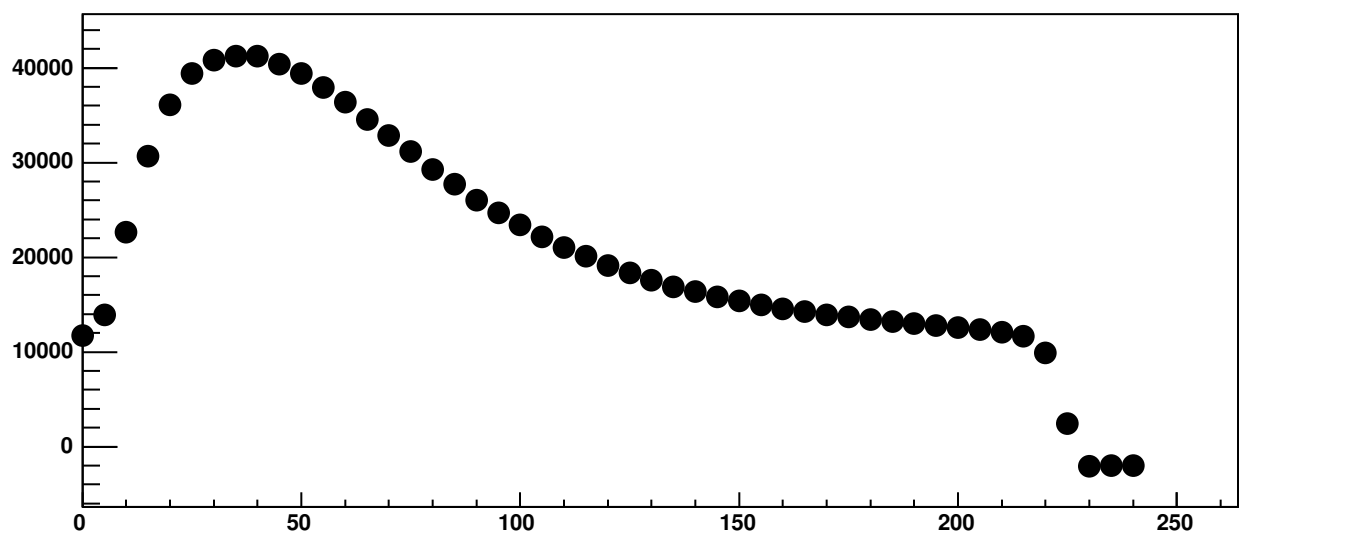
Chip 5, Channel 17, Enable 5!, DAC=4000, ADC Mean vs Hold



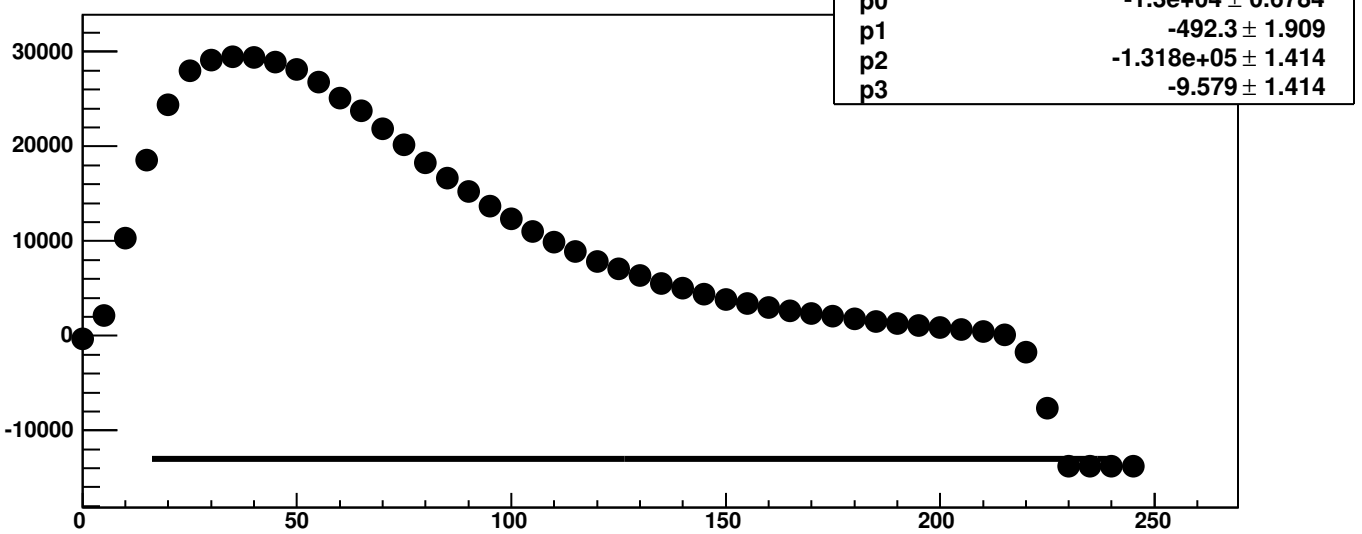
Chip 5, Channel 17, Enable 5!, DAC=4000, ADC Noise vs Hold



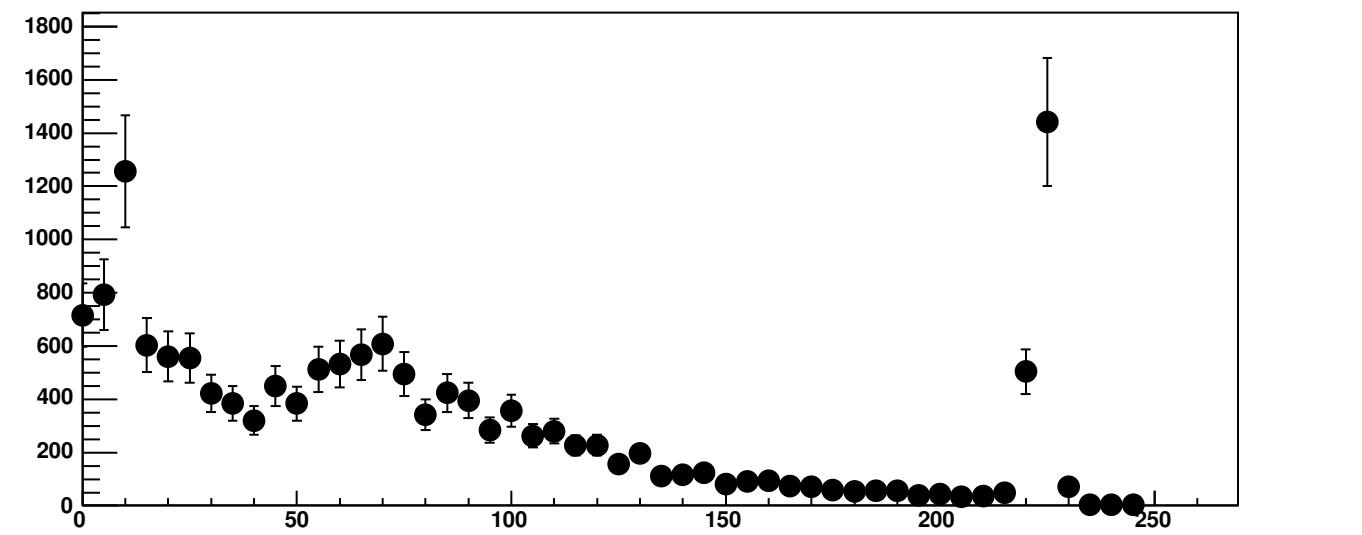
Chip 5, Channel 17, Enable 5!, DAC=4000, ADC Residuals vs Hold



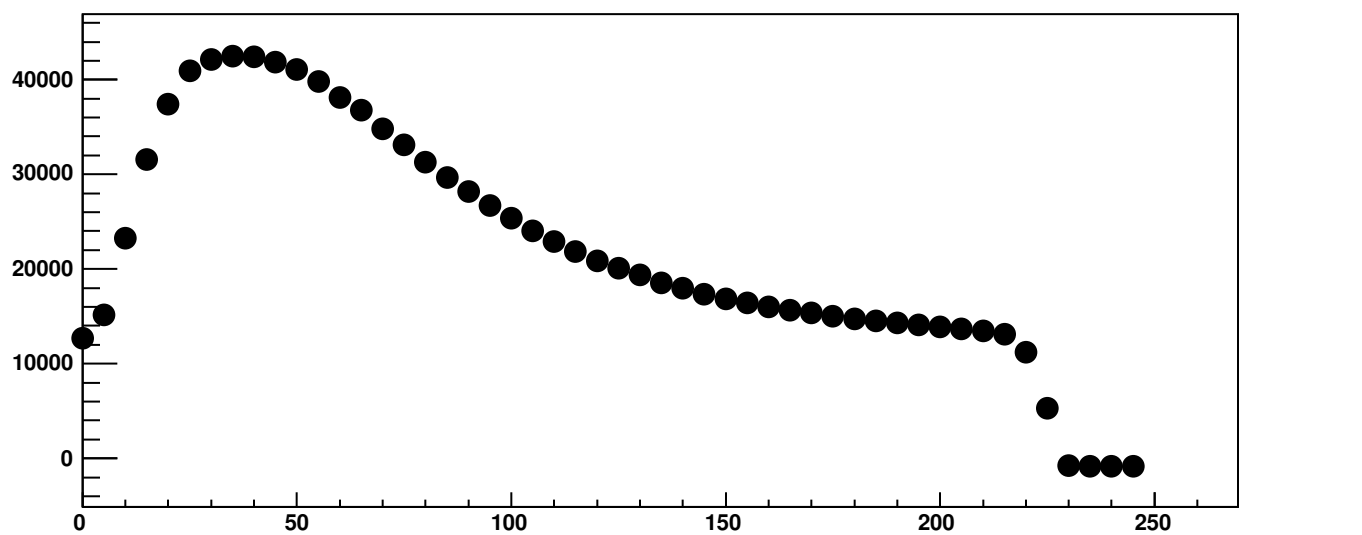
Chip 6, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold

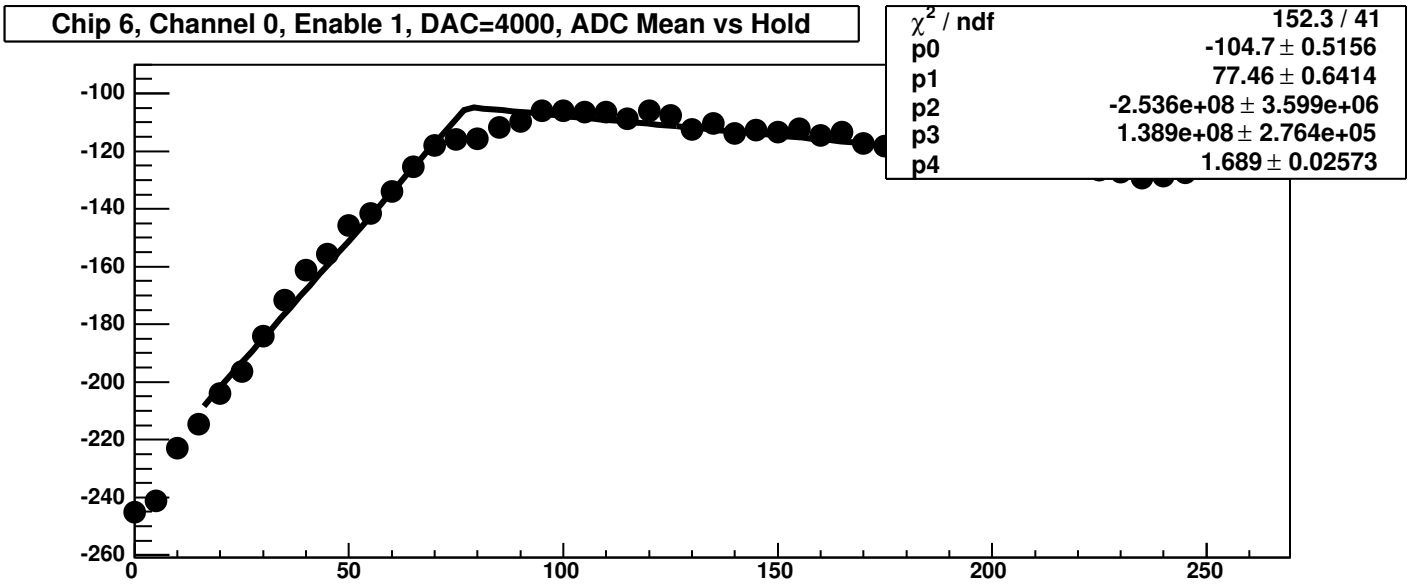


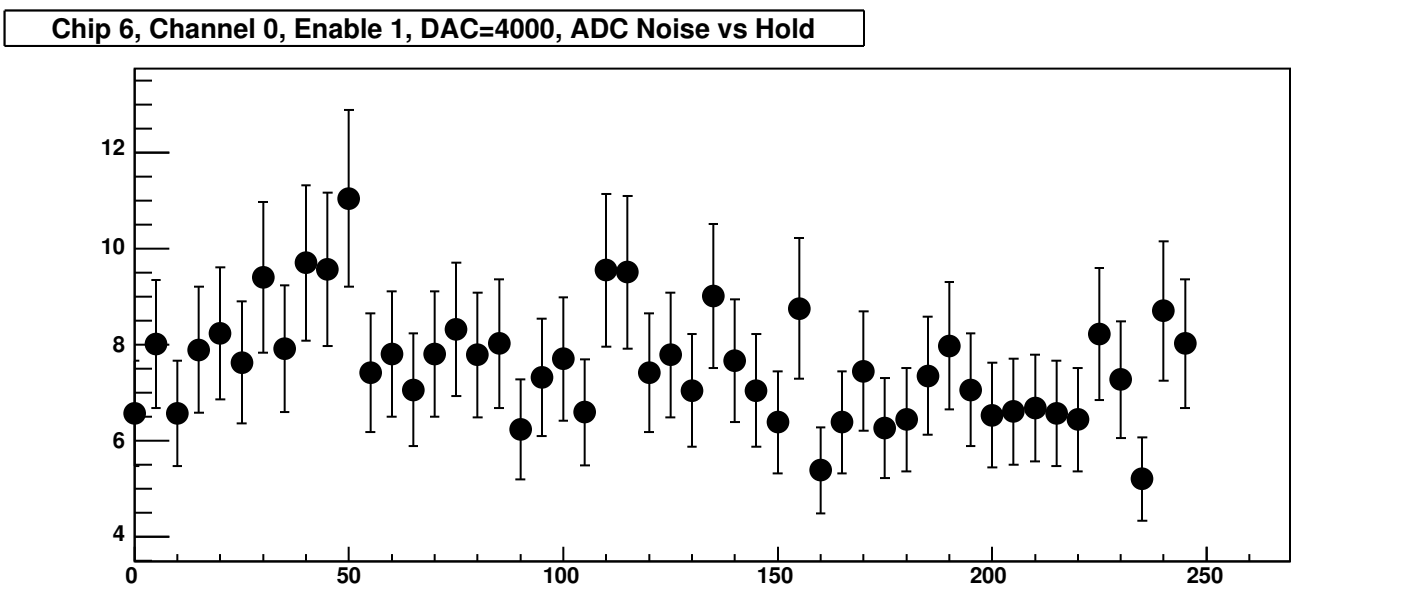
Chip 6, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold

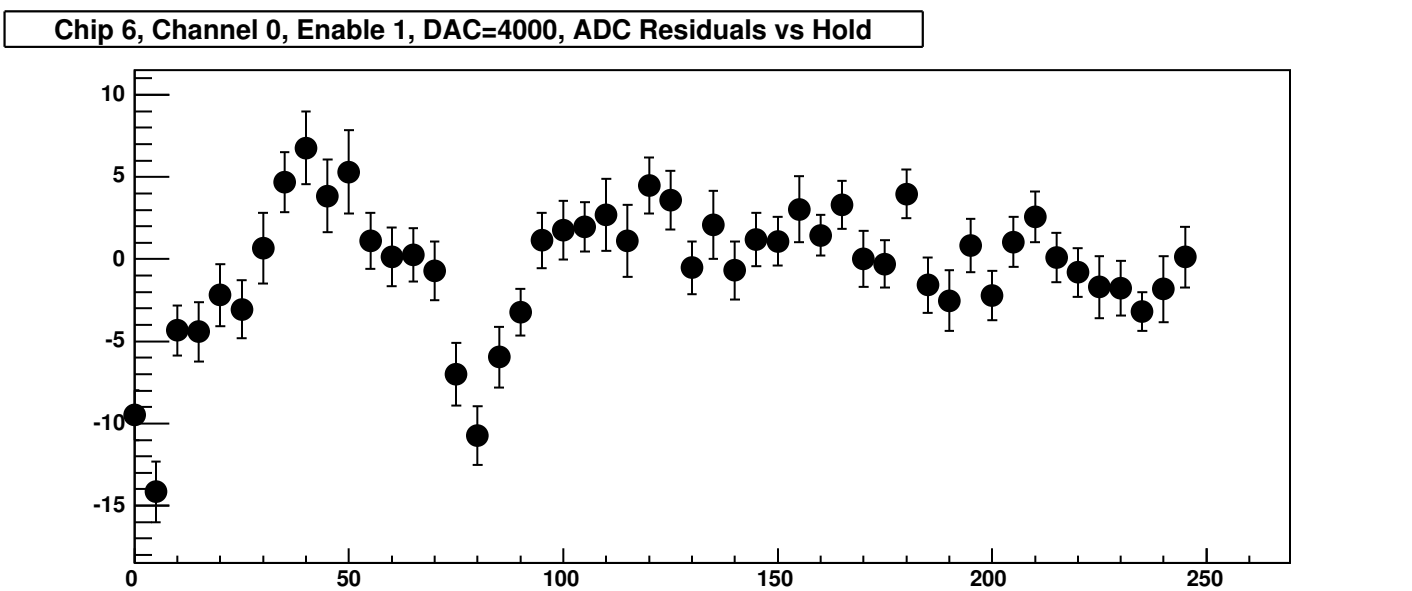


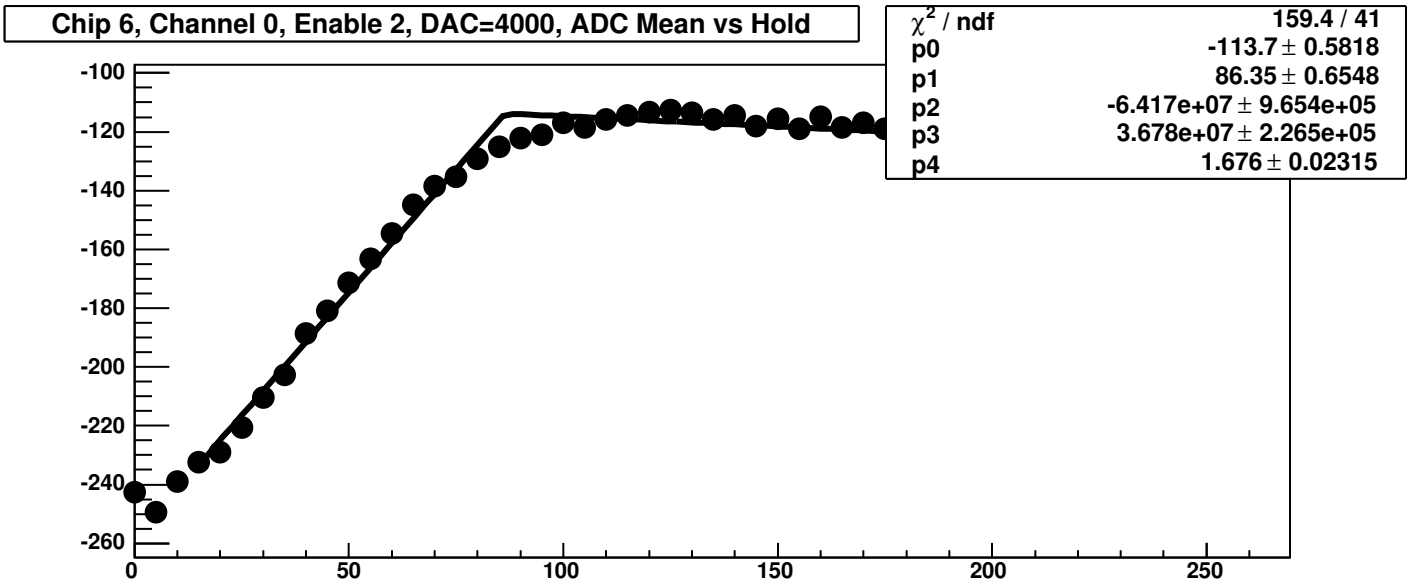
Chip 6, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold

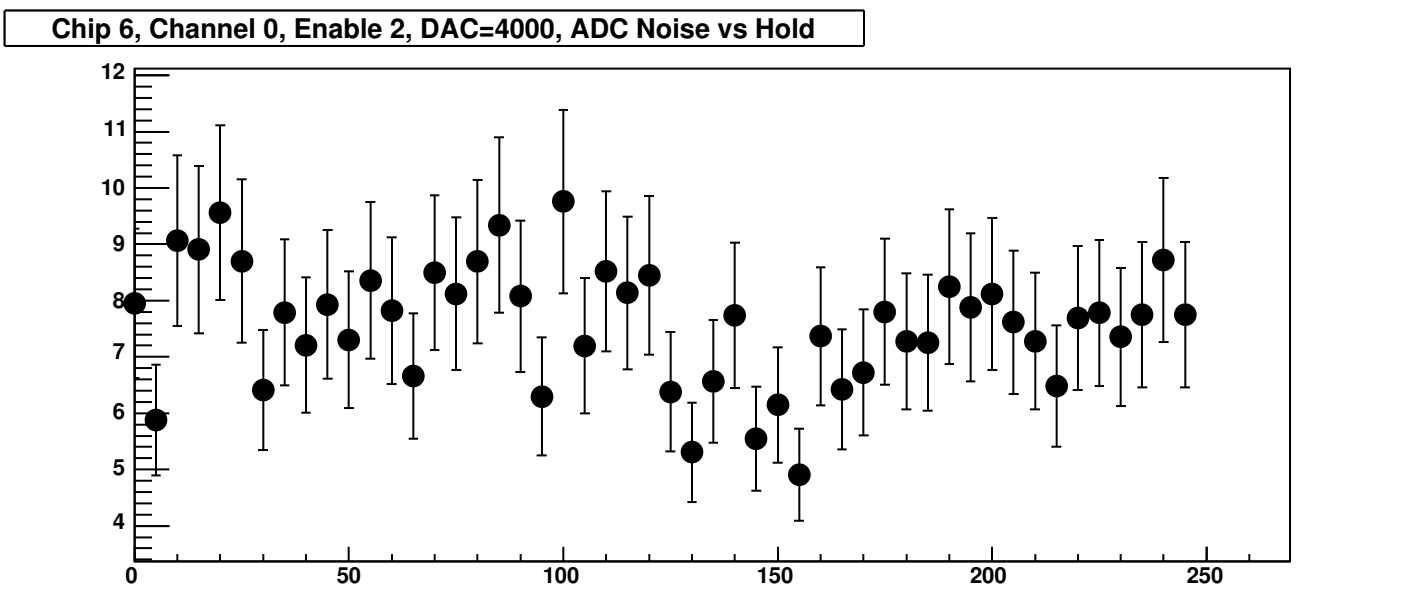


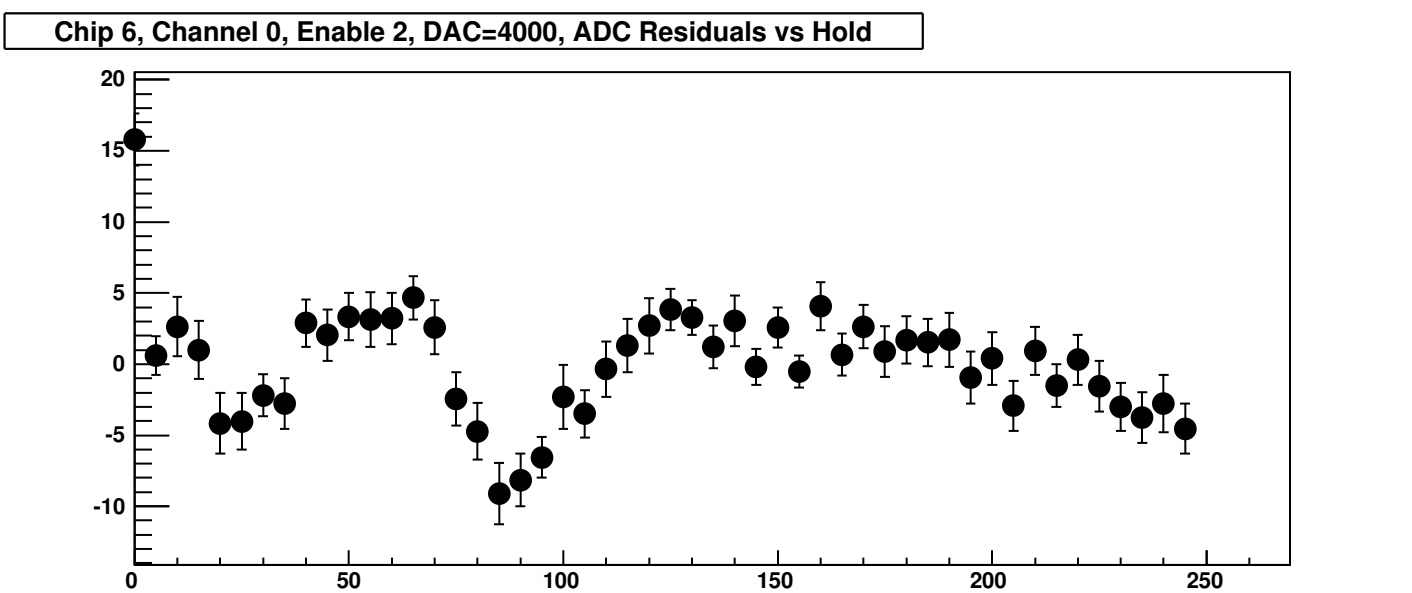

Chip 6, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 152.3 / 41
p0 -104.7 ± 0.5156
p1 77.46 ± 0.6414
p2 -2.536e+08 ± 3.599e+06
p3 1.389e+08 ± 2.764e+05
p4 1.689 ± 0.02573
-100
-120
-140
-160
-180
-200
-220
-240
-260
0
50
100
150
200
250


Chip 6, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

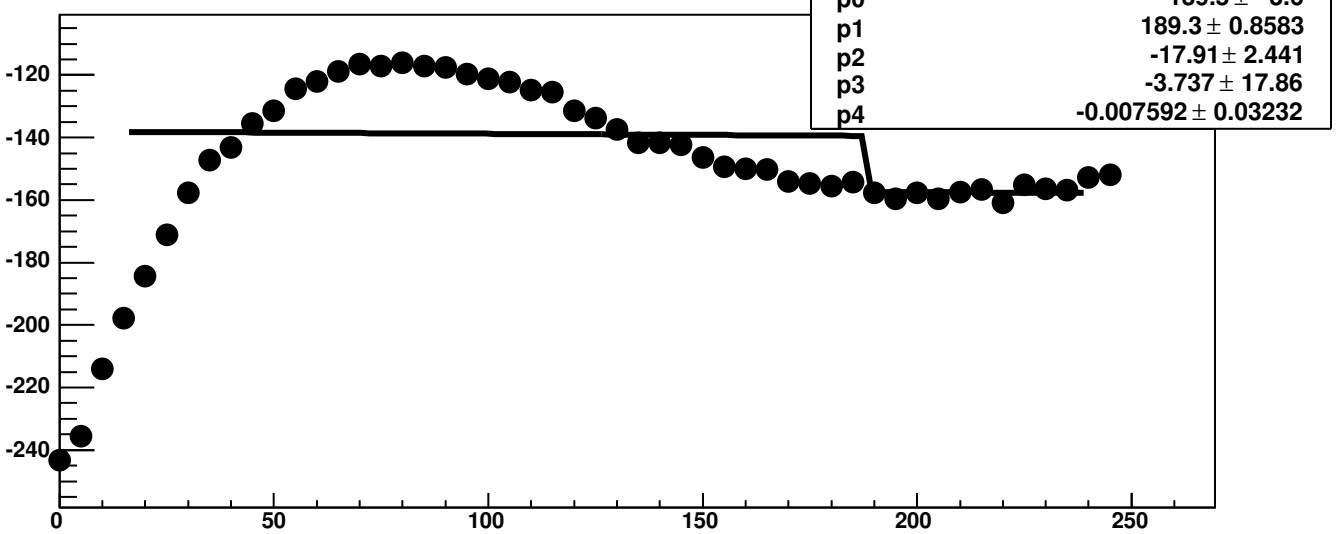

Chip 6, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
0
50
100
150
200
250


Chip 6, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 159.4 / 41
p0 -113.7 ± 0.5818
p1 86.35 ± 0.6548
p2 -6.417e+07 ± 9.654e+05
p3 3.678e+07 ± 2.265e+05
p4 1.676 ± 0.02315
-100
-120
-140
-160
-180
-200
-220
-240
-260
0
50
100
150
200
250

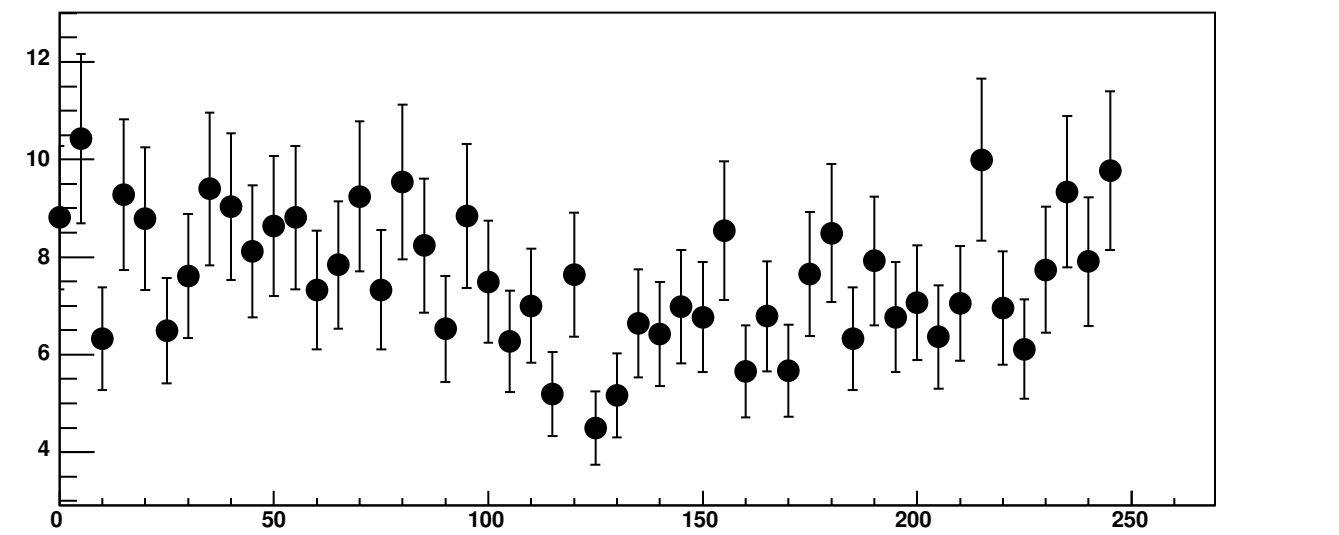

Chip 6, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 6, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
0
50
100
150
200
250

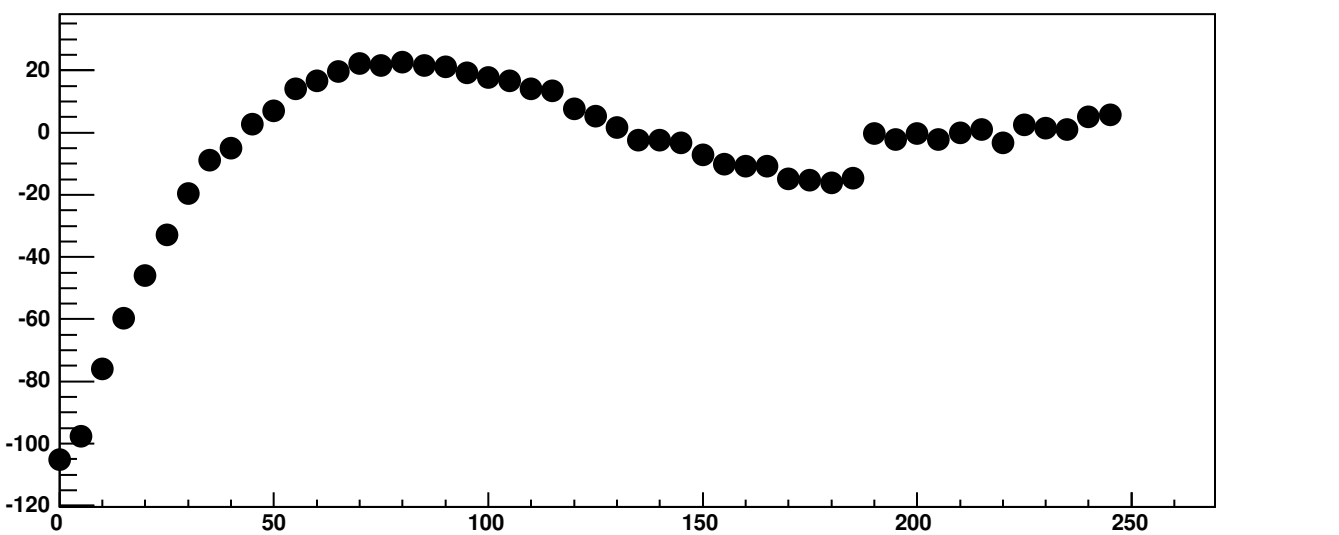
Chip 6, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold



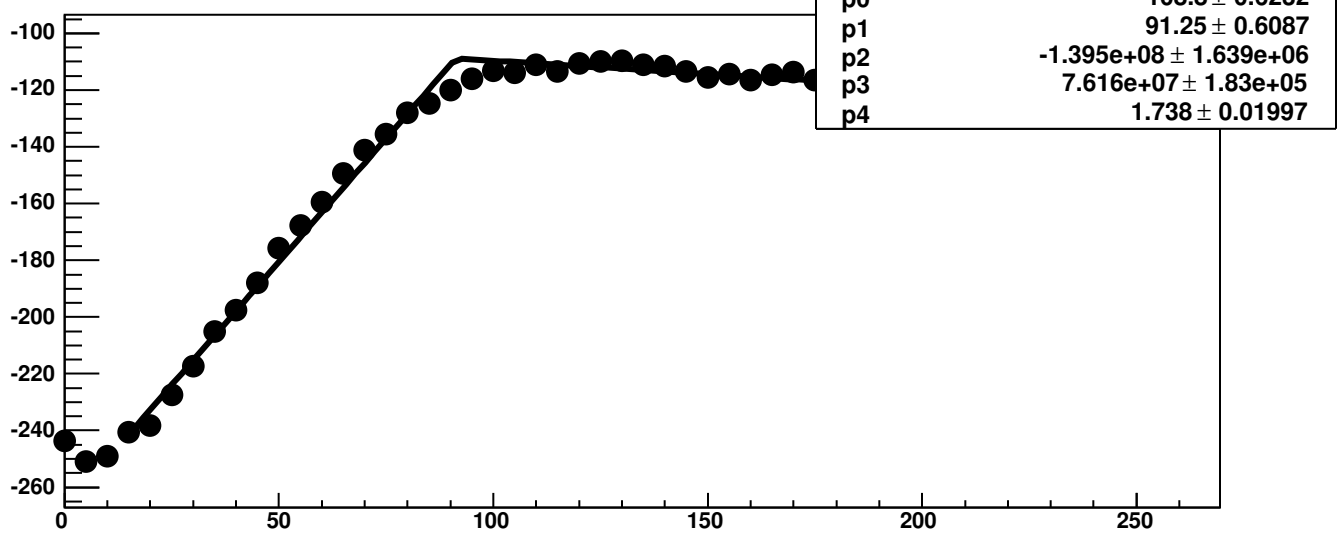
Chip 6, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold



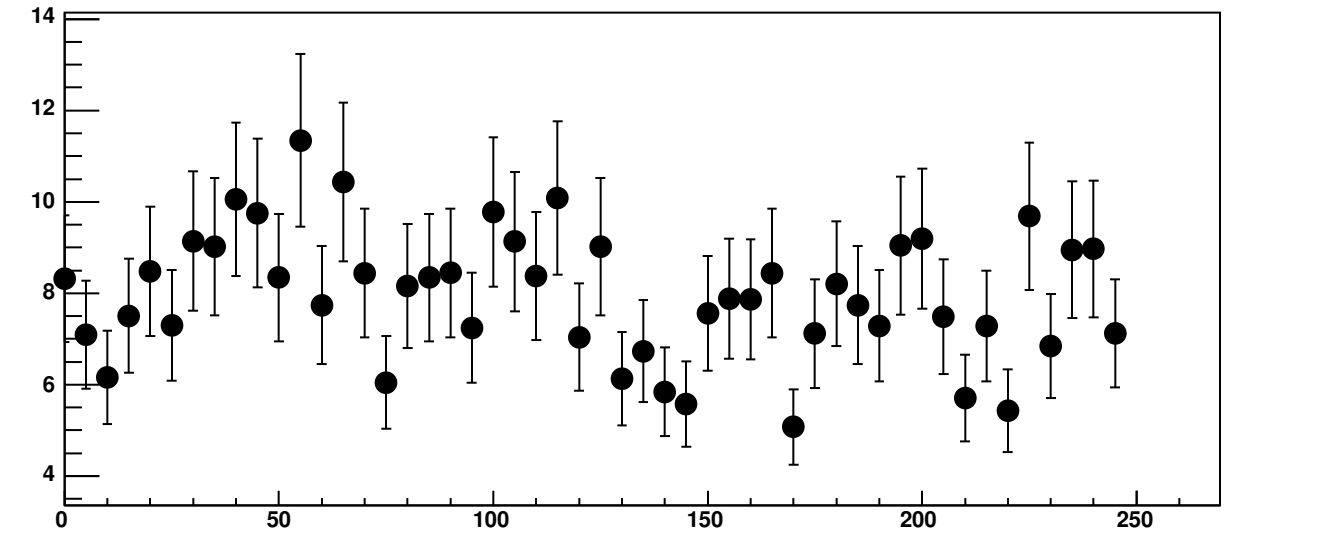
Chip 6, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold



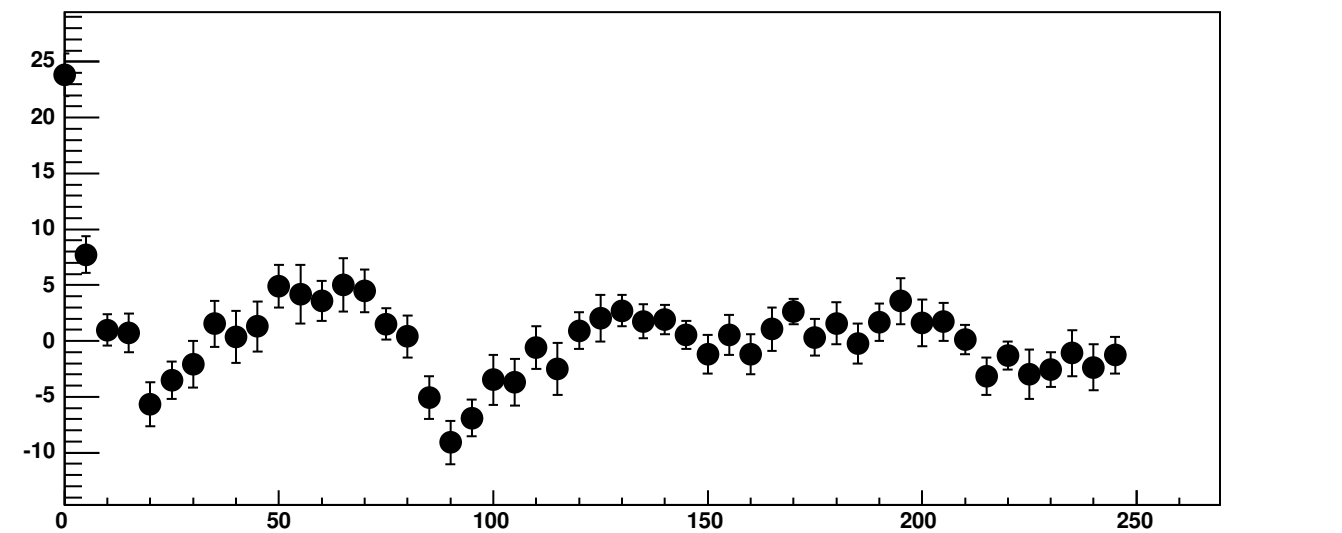
Chip 6, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

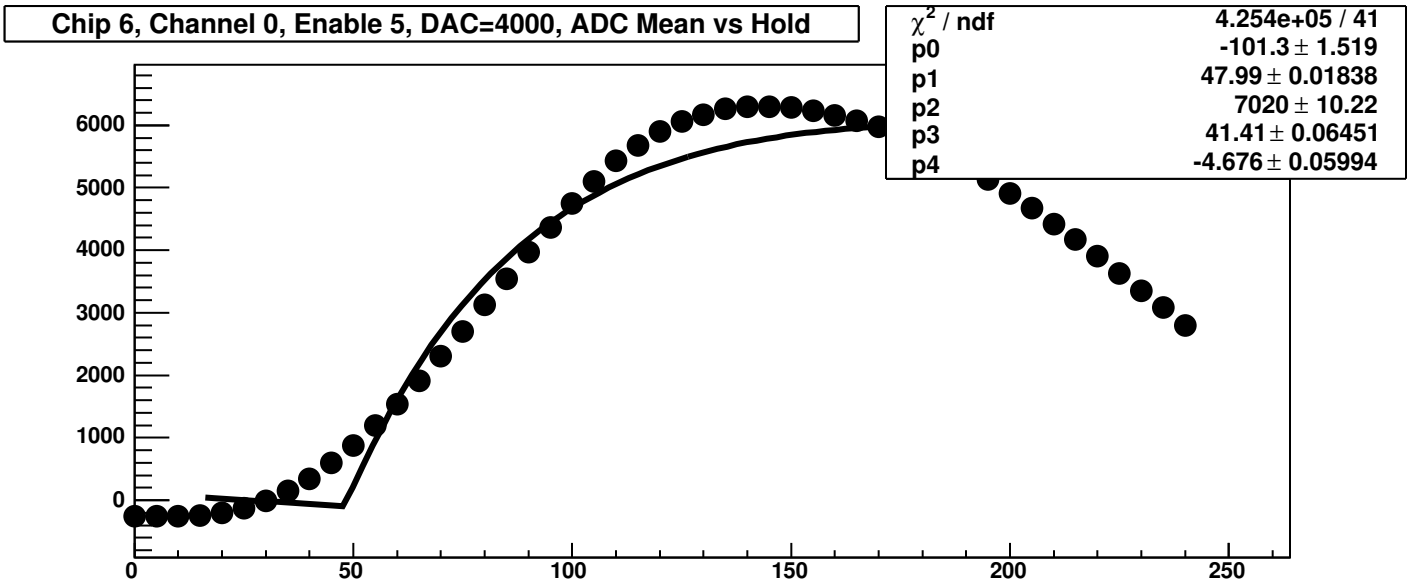


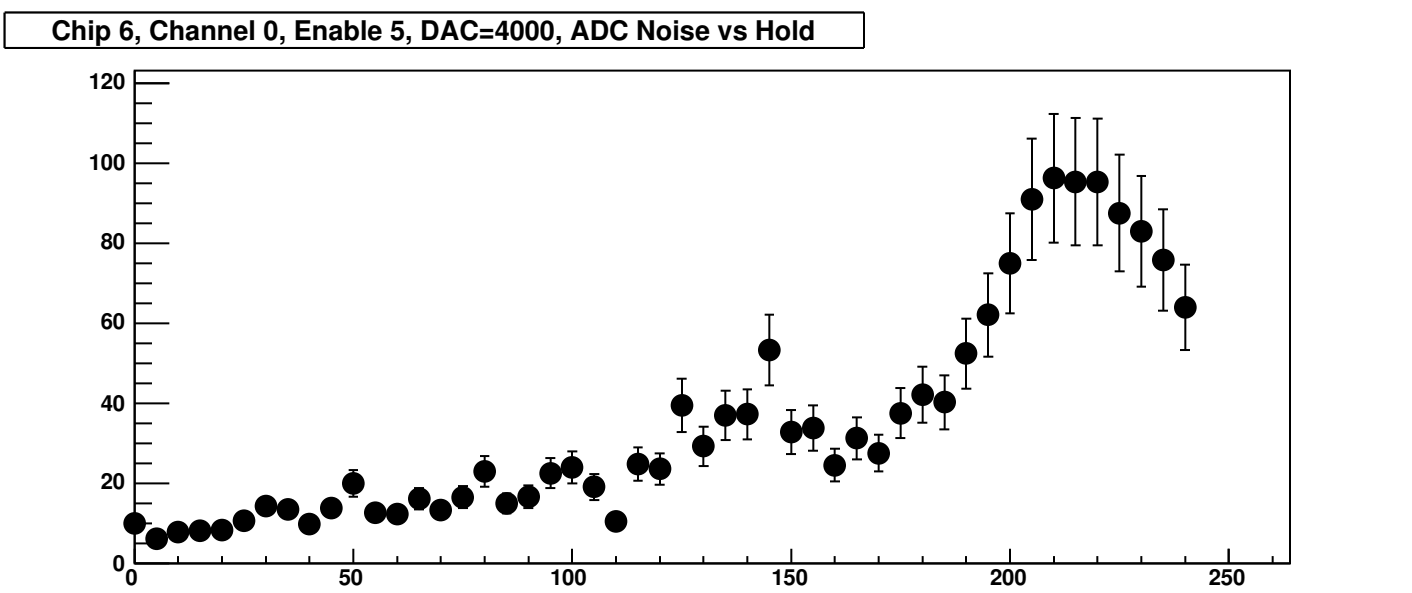
Chip 6, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold

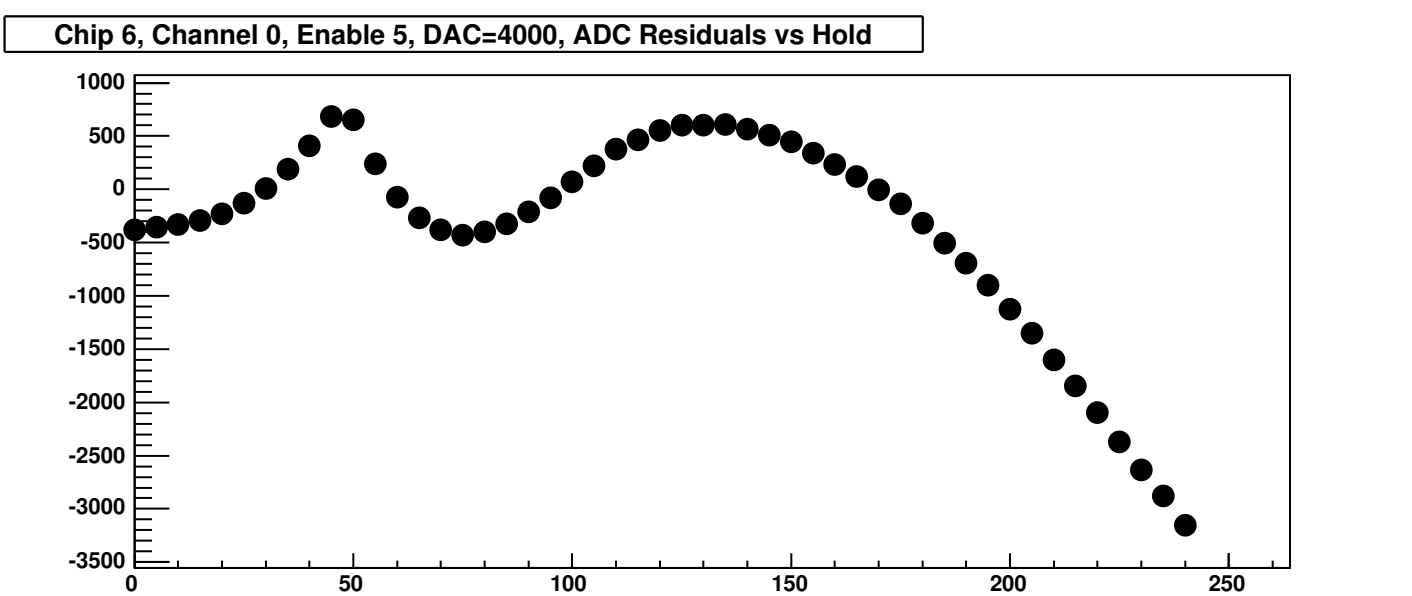


Chip 6, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

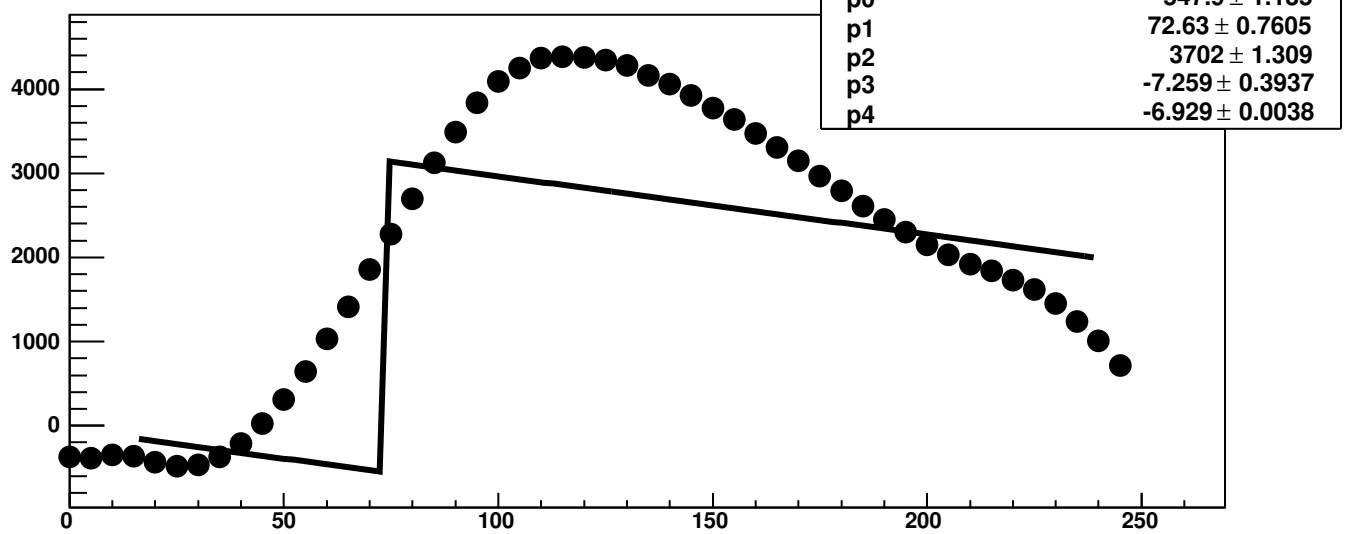



Chip 6, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.254e+05 / 41
p0 -101.3 ± 1.519
p1 47.99 ± 0.01838
p2 7020 ± 10.22
p3 41.41 ± 0.06451
p4 -4.676 ± 0.05994
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

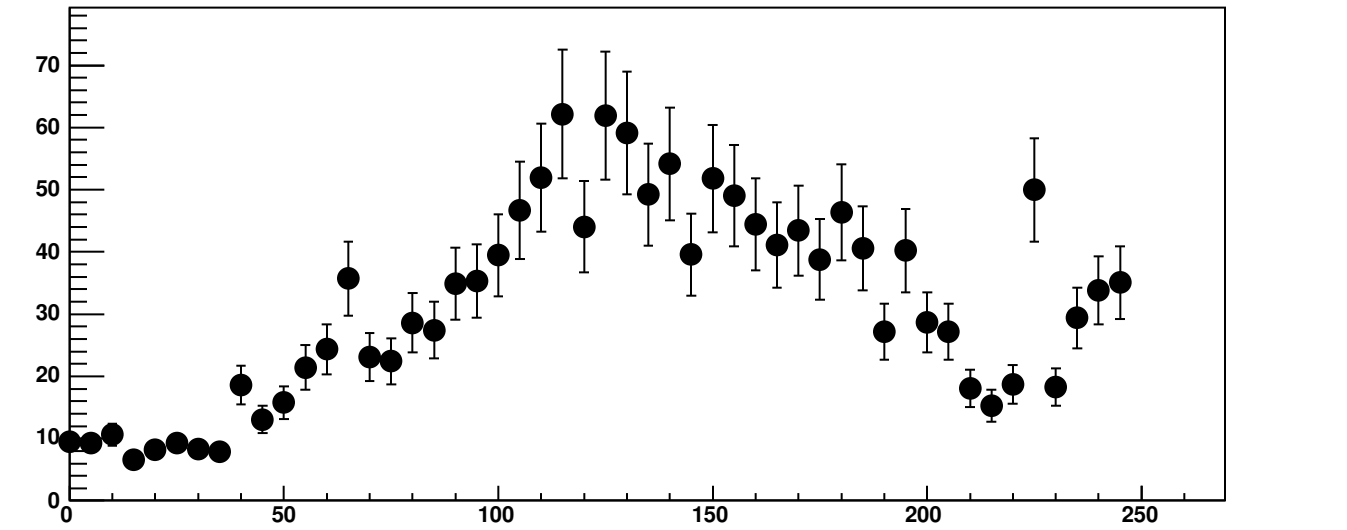

Chip 6, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 6, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
0
50
100
150
200
250

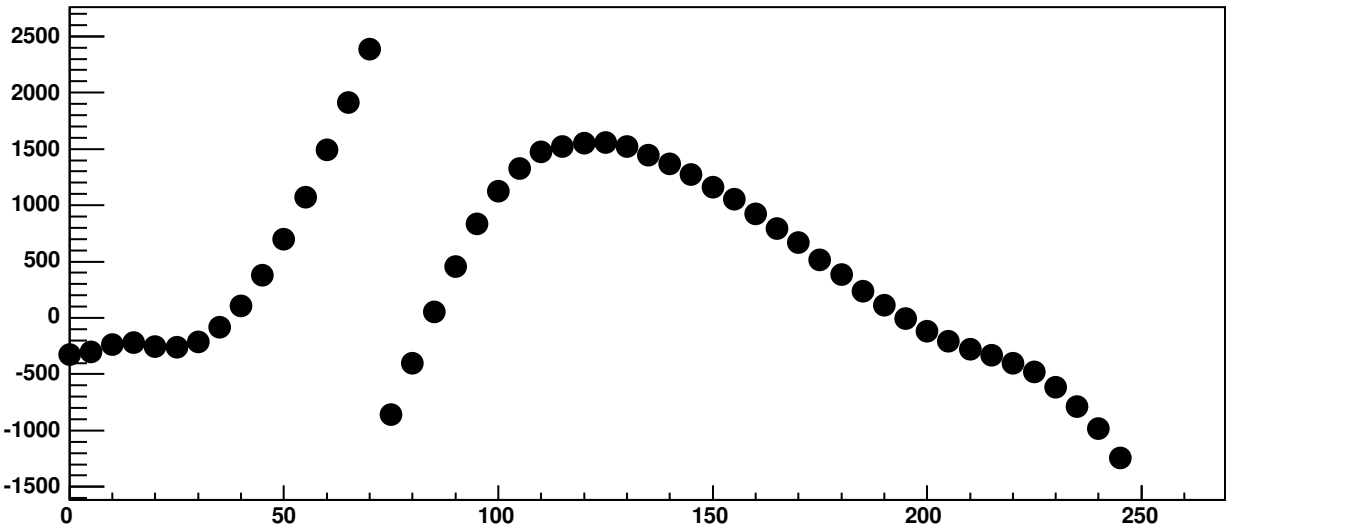
Chip 6, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold

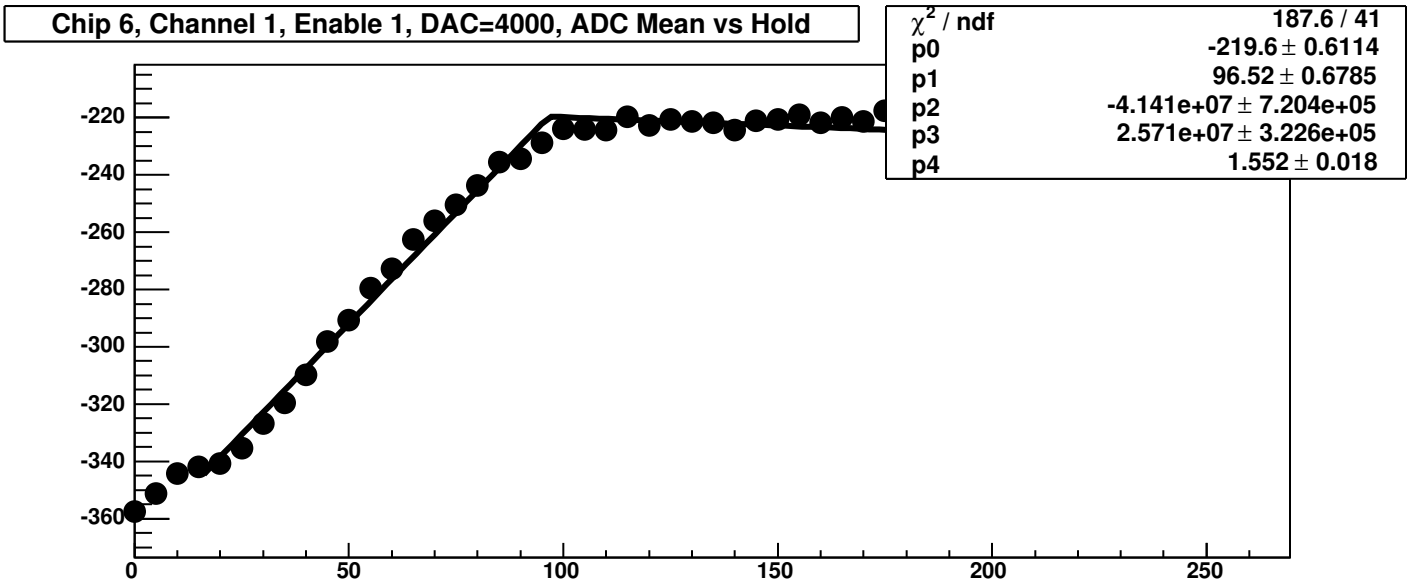


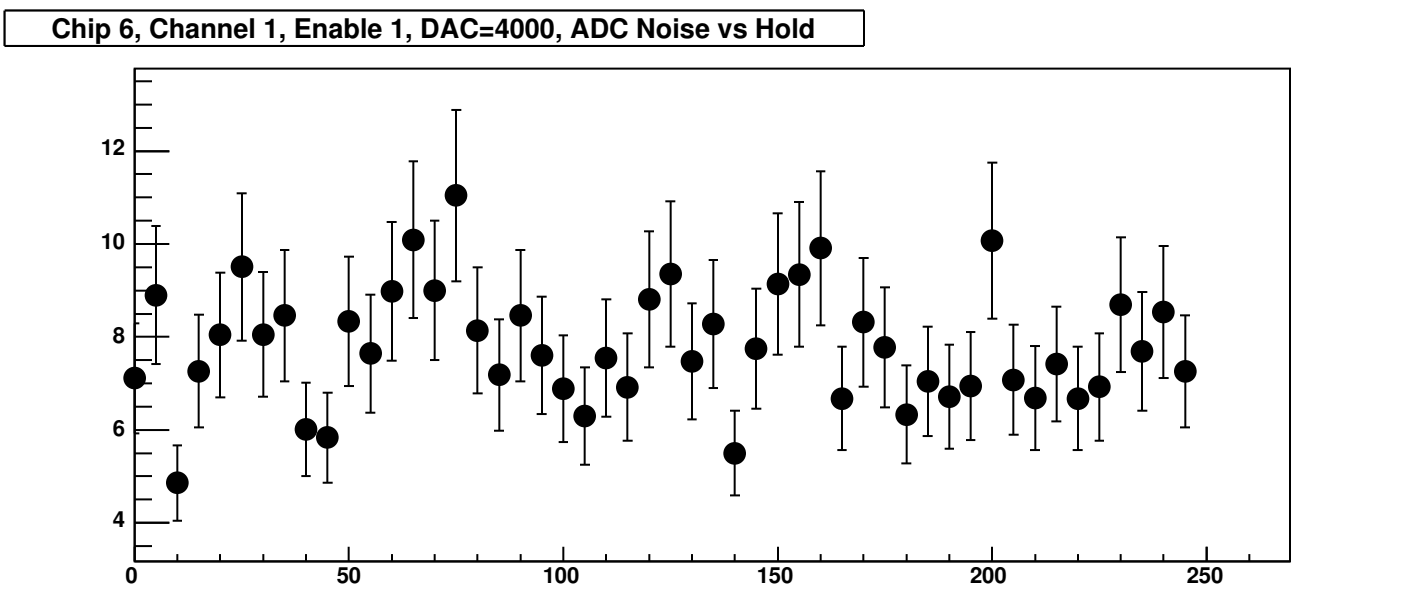
Chip 6, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold

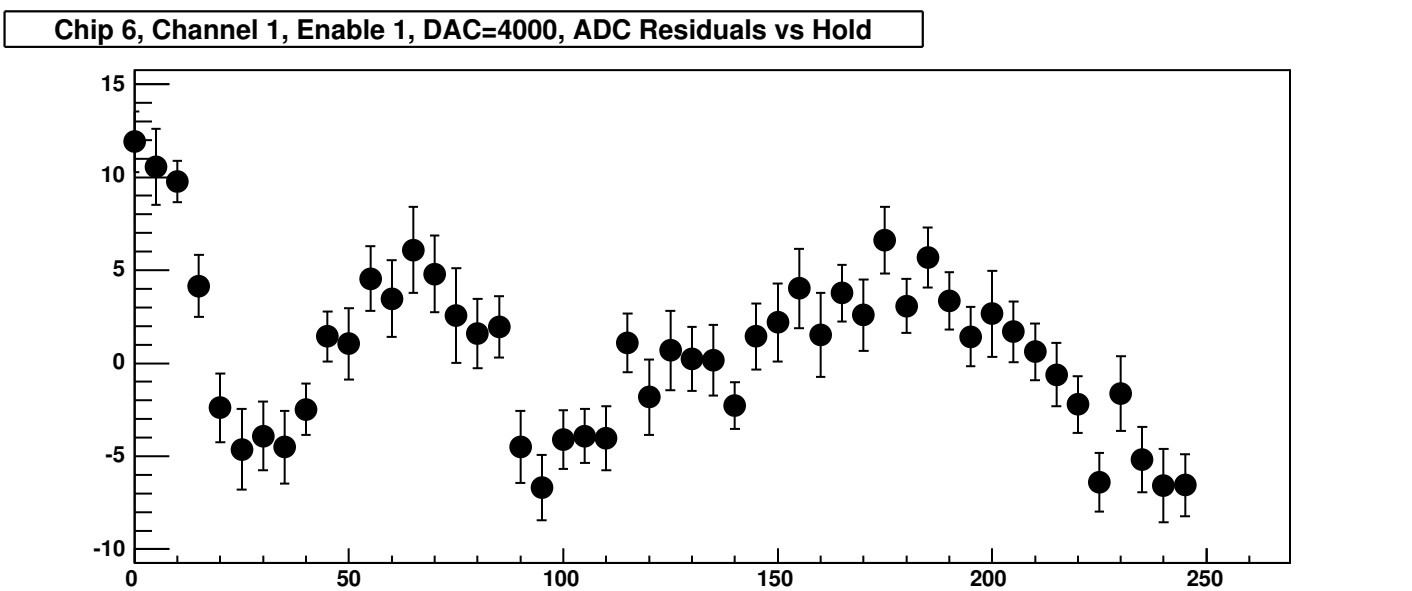


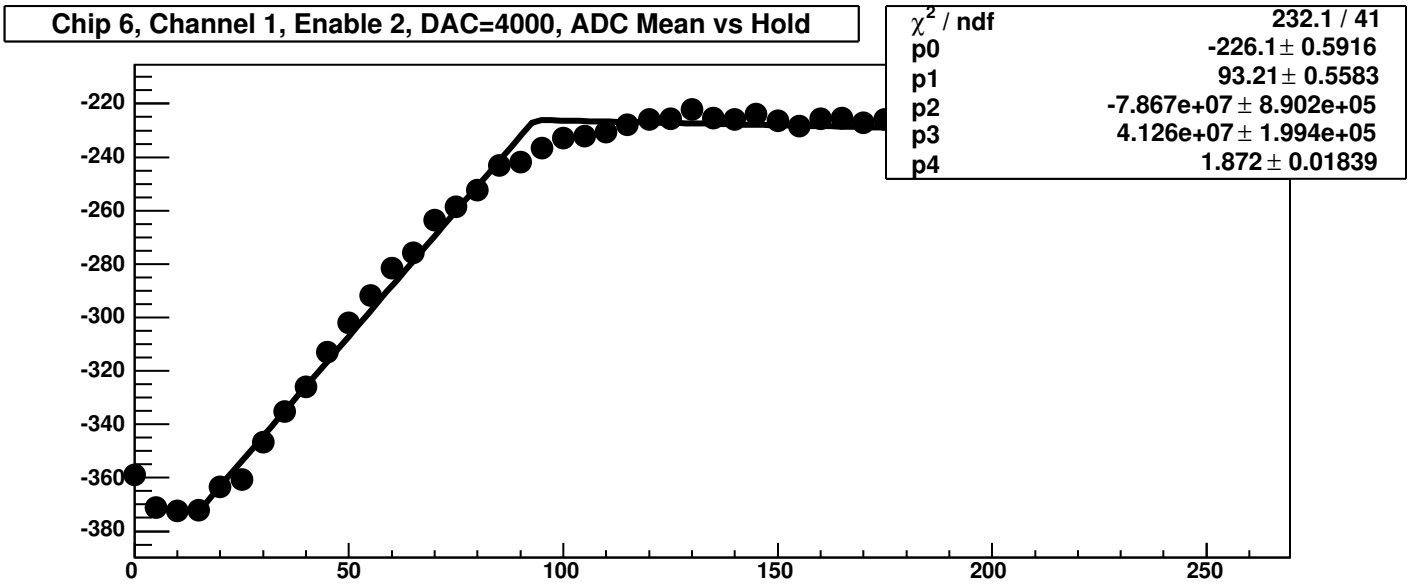
Chip 6, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold

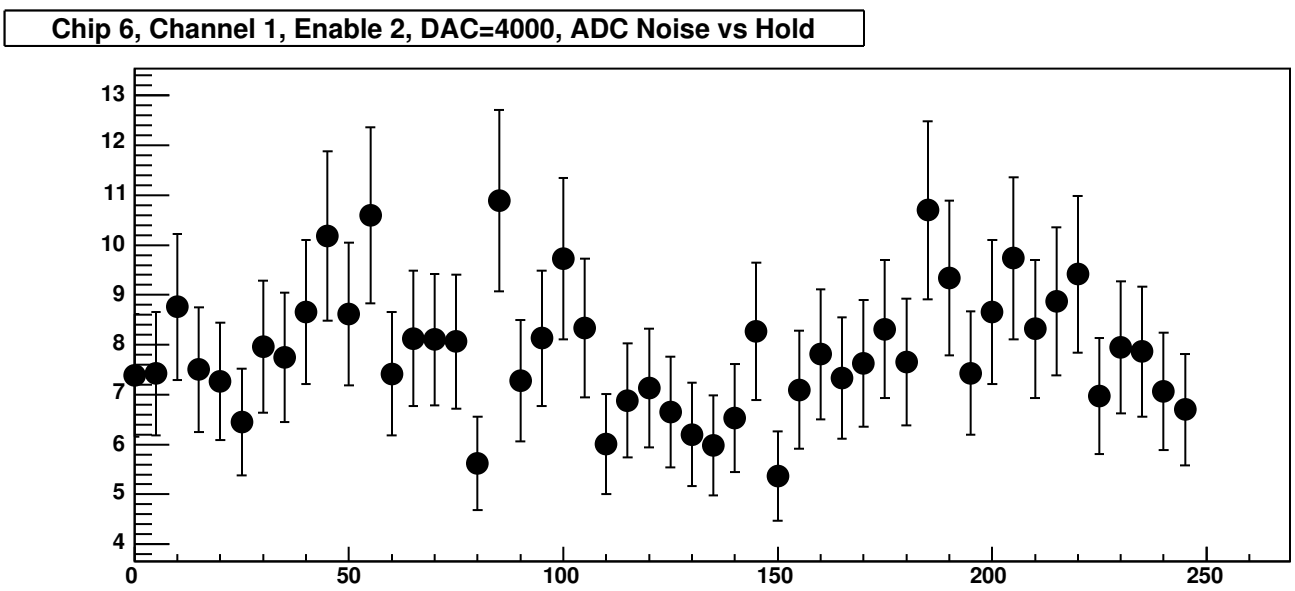


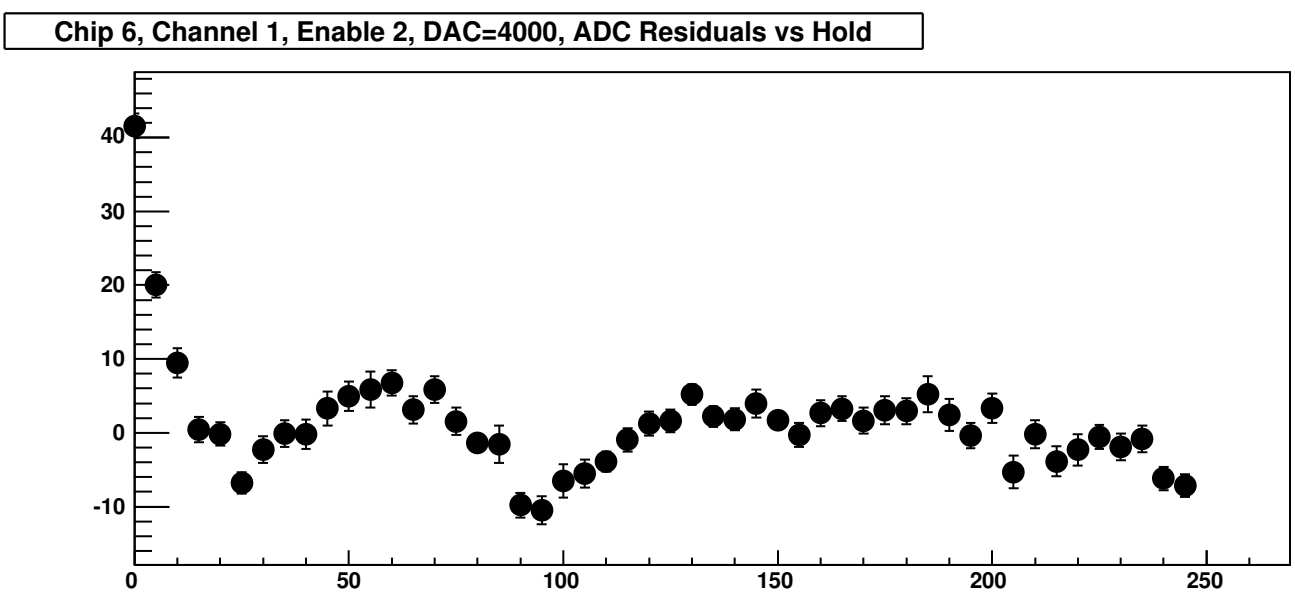

Chip 6, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 187.6 / 41
p0 -219.6 ± 0.6114
p1 96.52 ± 0.6785
p2 -4.141e+07 ± 7.204e+05
p3 2.571e+07 ± 3.226e+05
p4 1.552 ± 0.018
-220
-240
-260
-280
-300
-320
-340
-360
0
50
100
150
200
250

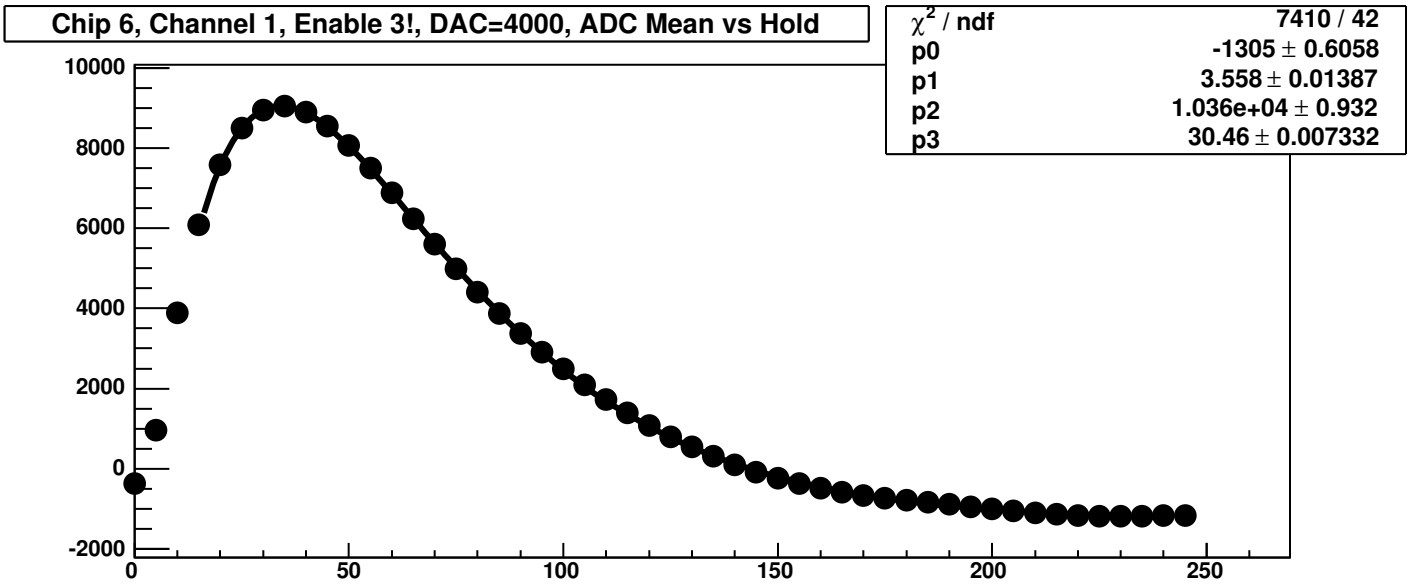

Chip 6, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

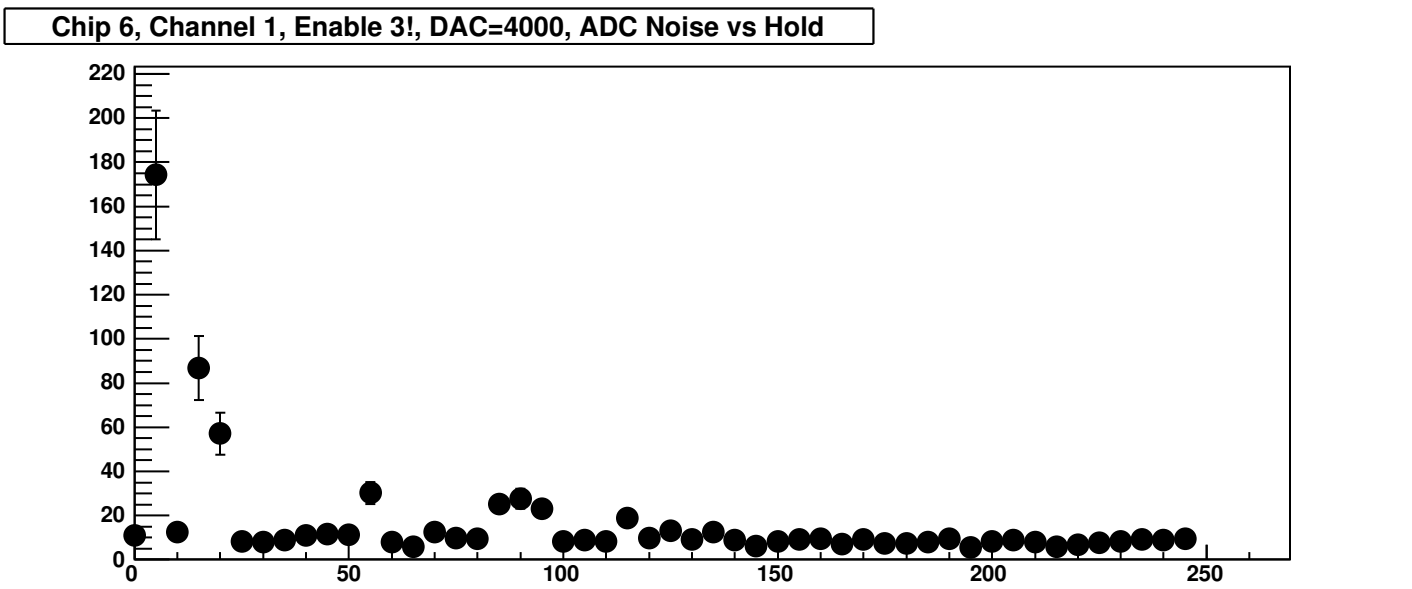

Chip 6, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
0
50
100
150
200
250

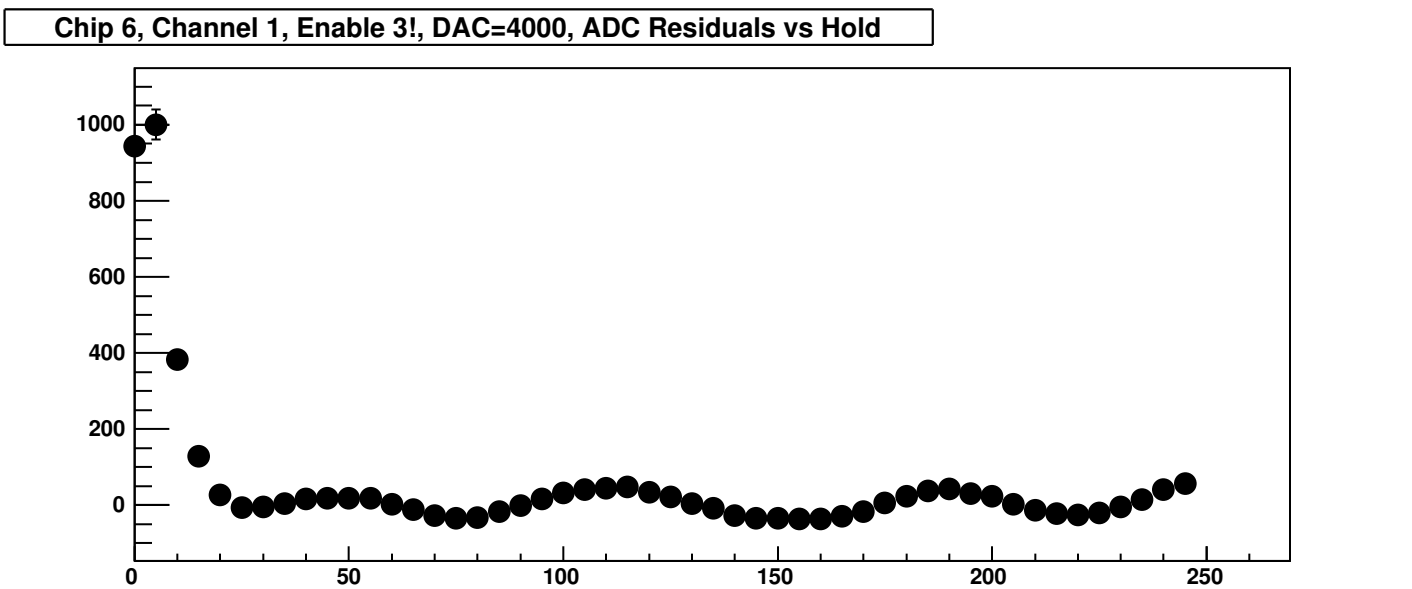

Chip 6, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 232.1 / 41
p0 -226.1 ± 0.5916
p1 93.21 ± 0.5583
p2 -7.867e+07 ± 8.902e+05
p3 4.126e+07 ± 1.994e+05
p4 1.872 ± 0.01839
-220
-240
-260
-280
-300
-320
-340
-360
-380
0
50
100
150
200
250


Chip 6, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

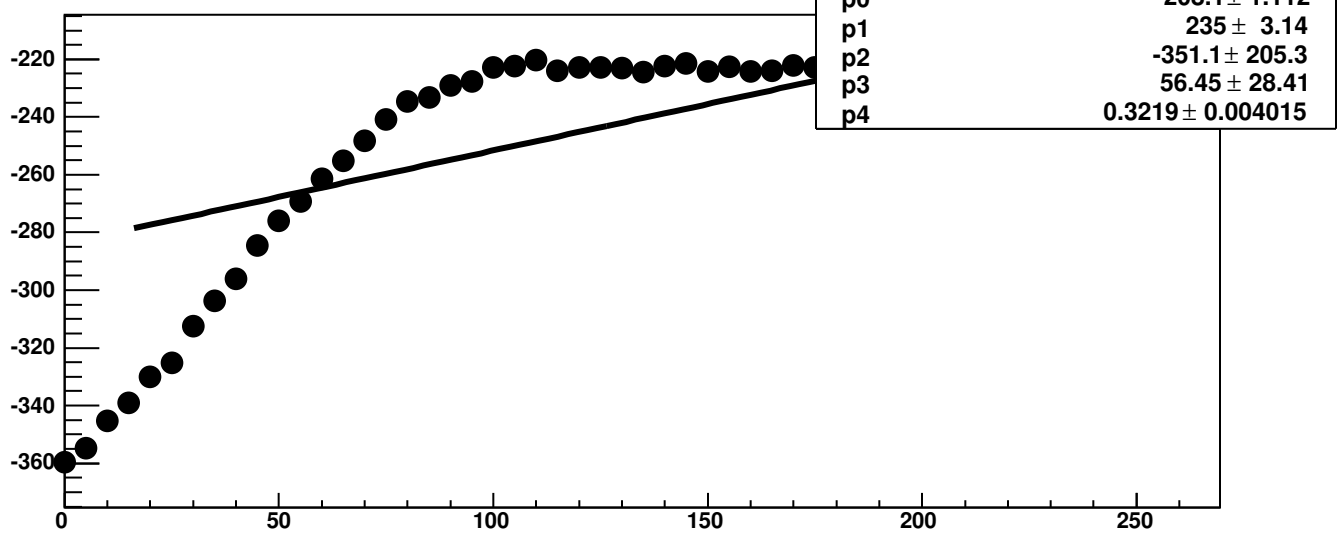

Chip 6, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250


Chip 6, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 7410 / 42
p0 -1305 ± 0.6058
p1 3.558 ± 0.01387
p2 1.036e+04 ± 0.932
p3 30.46 ± 0.007332
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

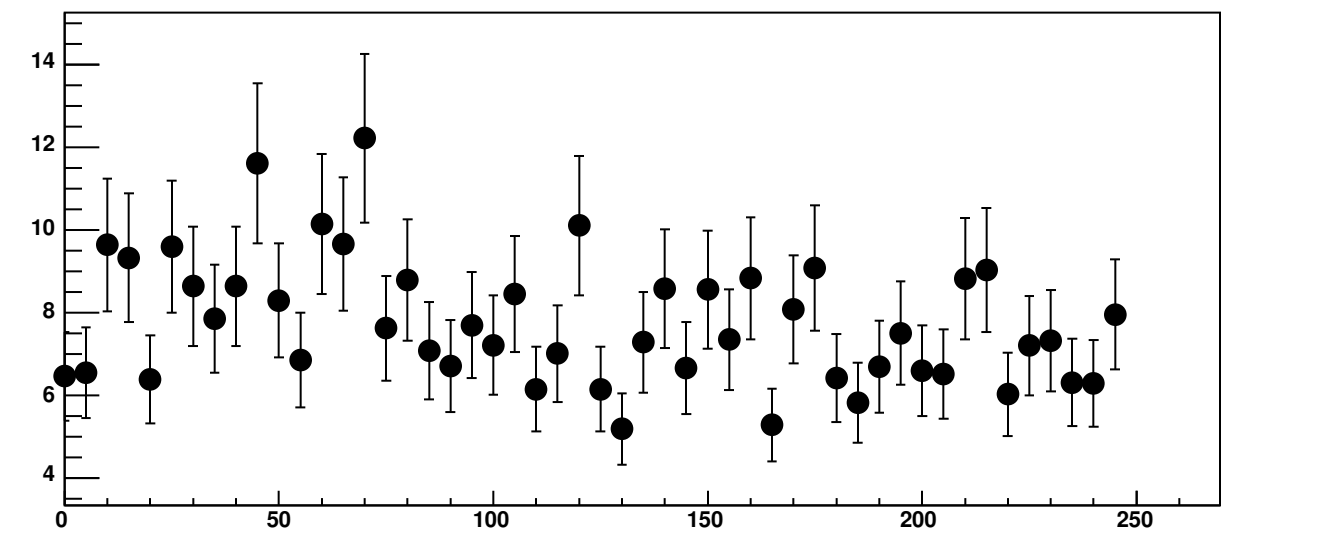

Chip 6, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 6, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold
1000
800
600
400
200
0
0
50
100
150
200
250

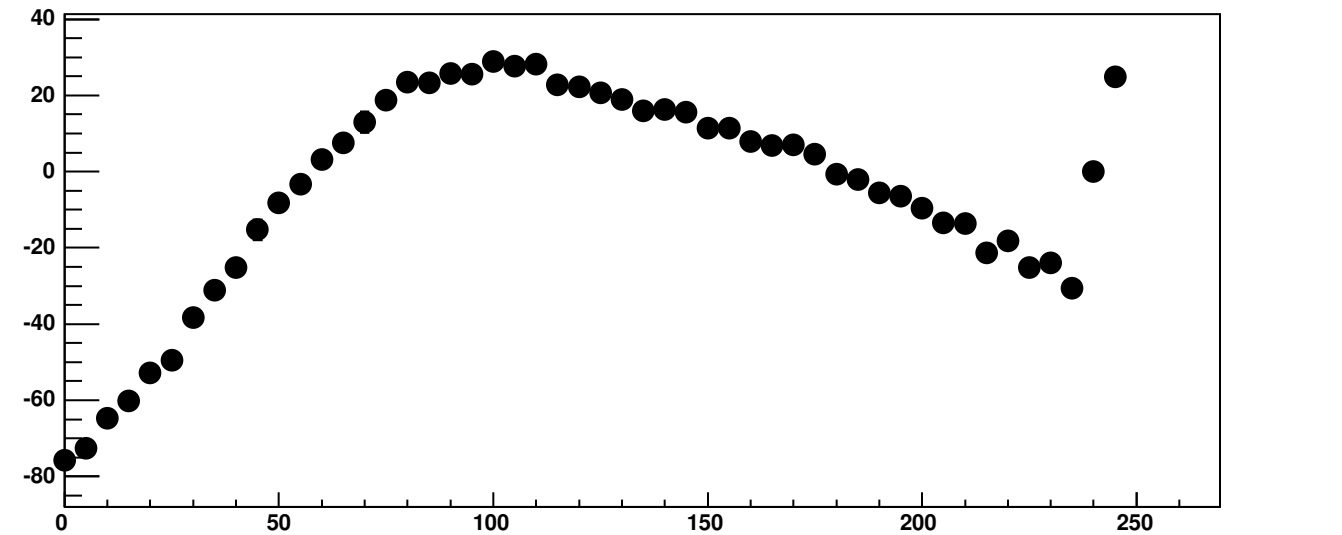
Chip 6, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold



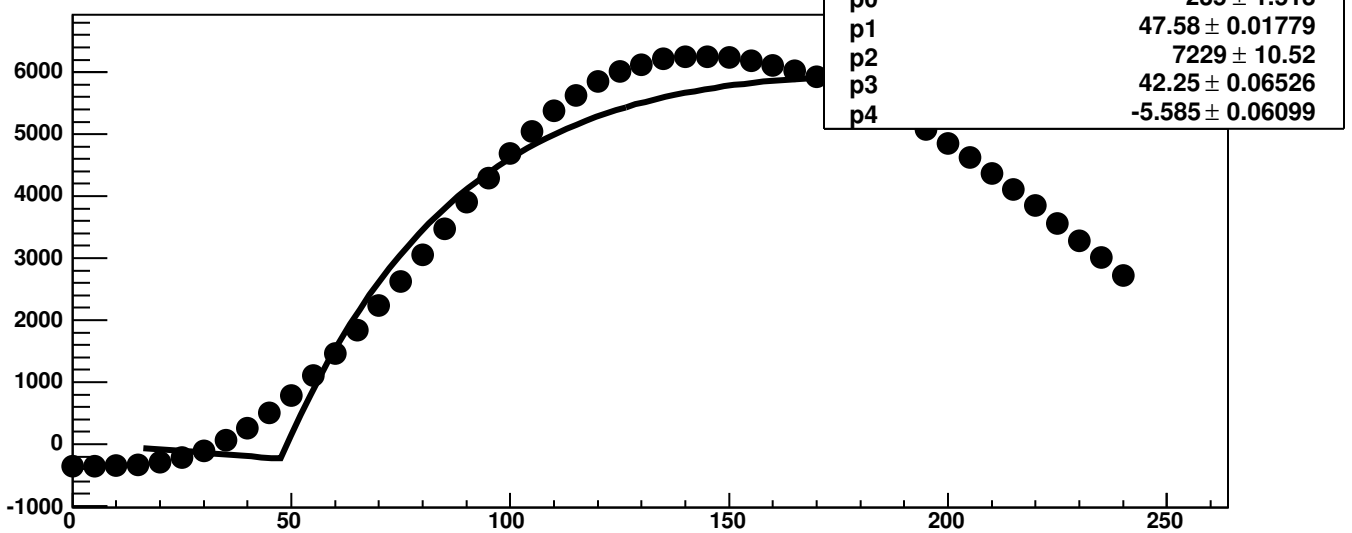
Chip 6, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



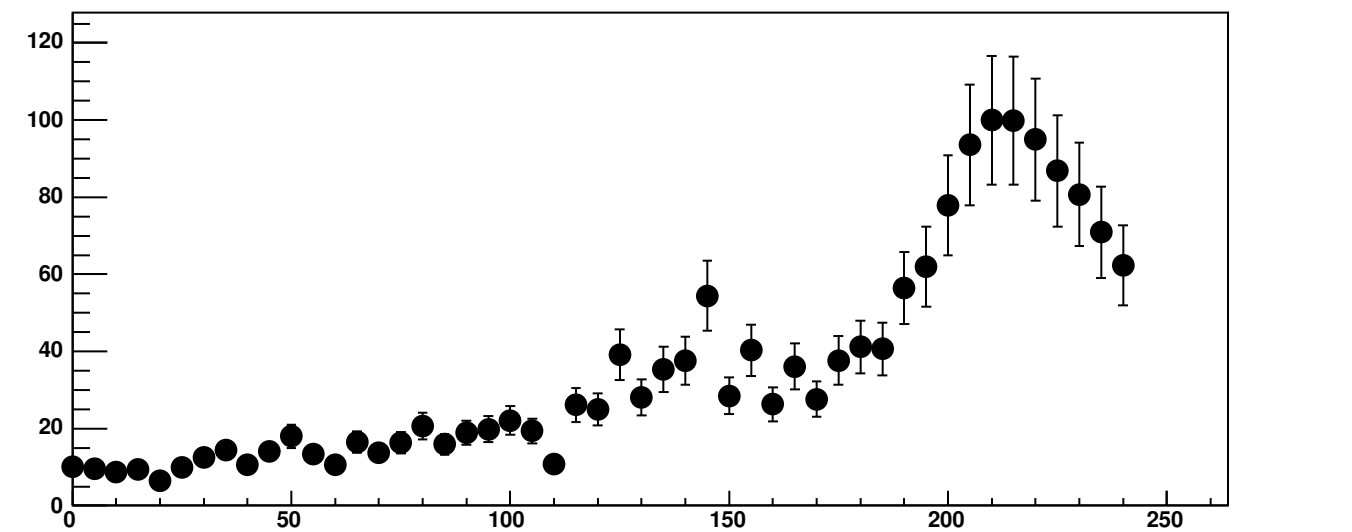
Chip 6, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold



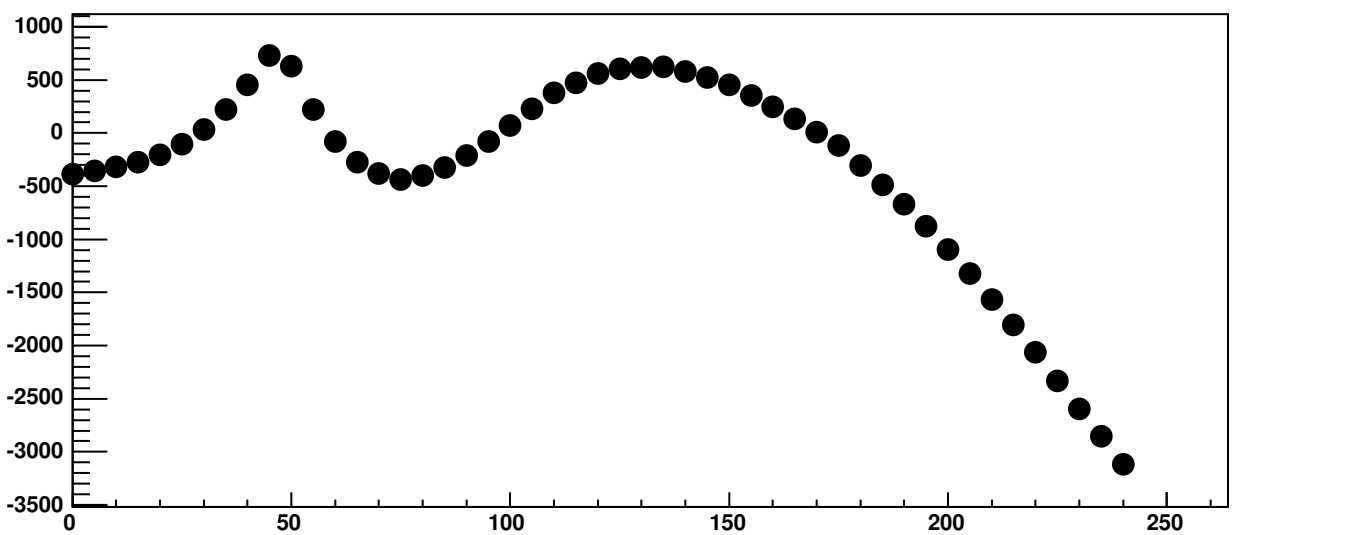
Chip 6, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold



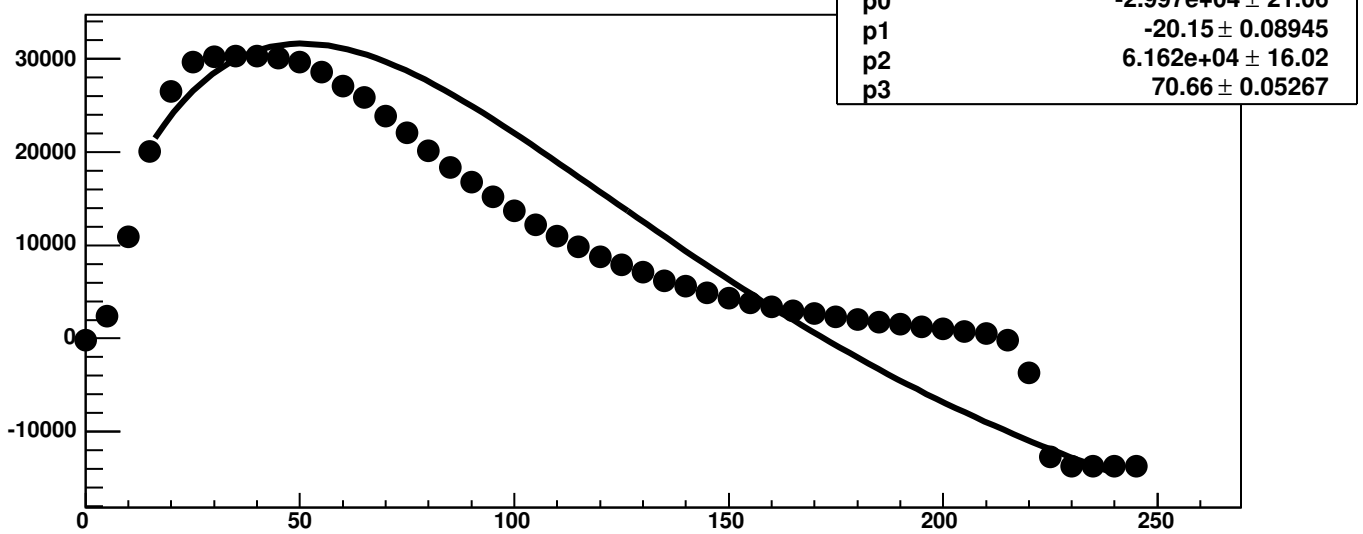
Chip 6, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



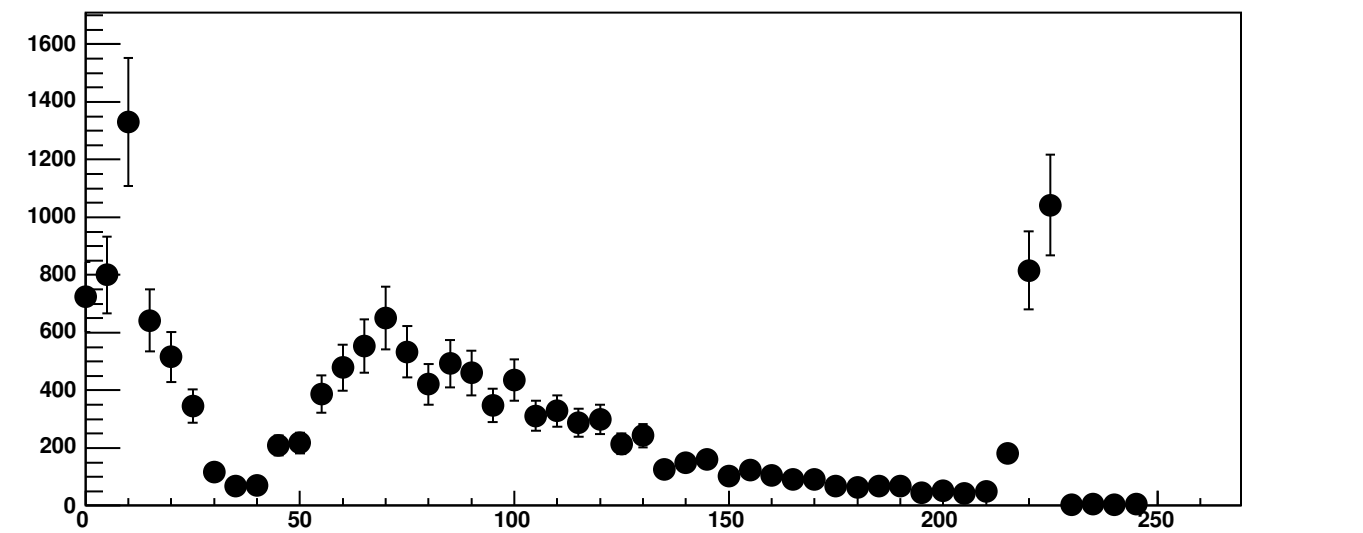
Chip 6, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold



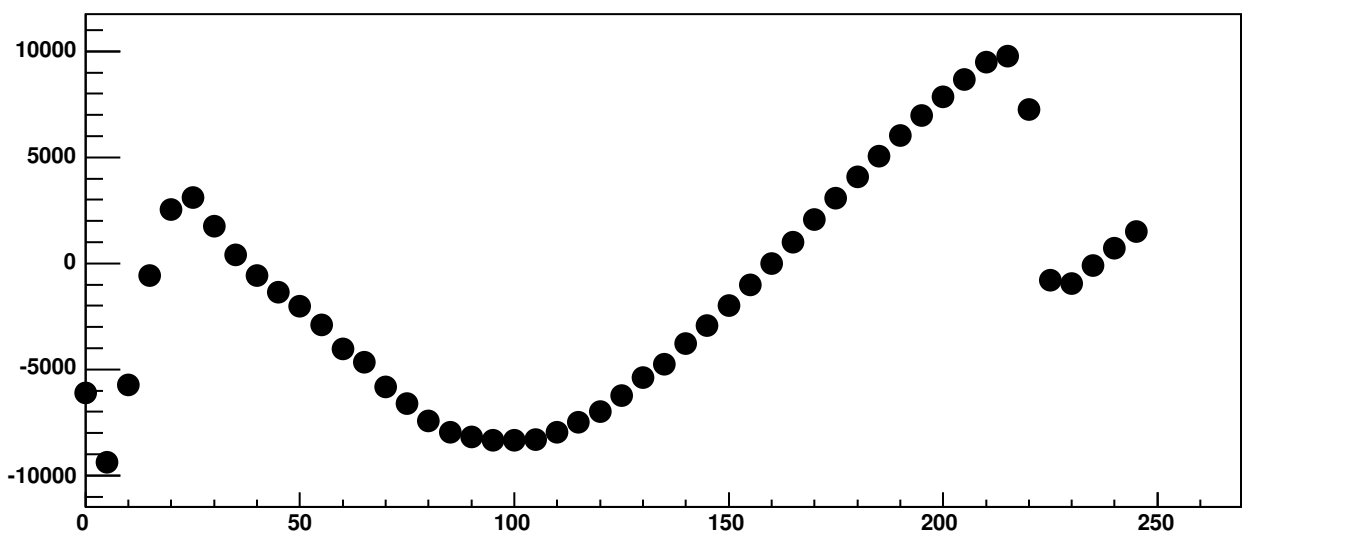
Chip 6, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold



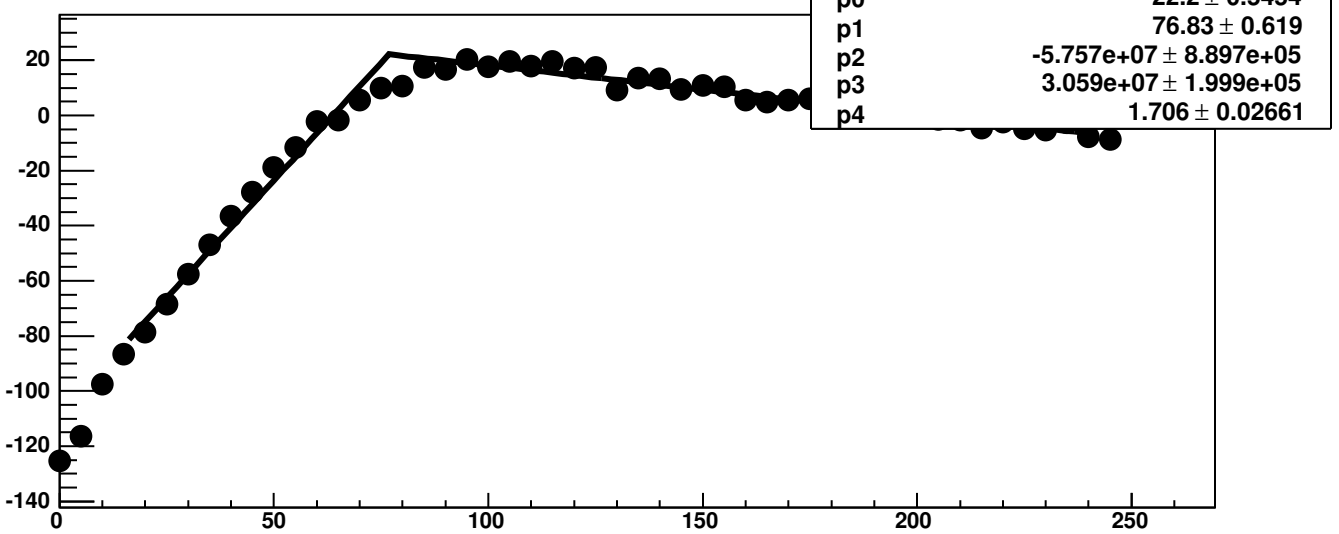
Chip 6, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold



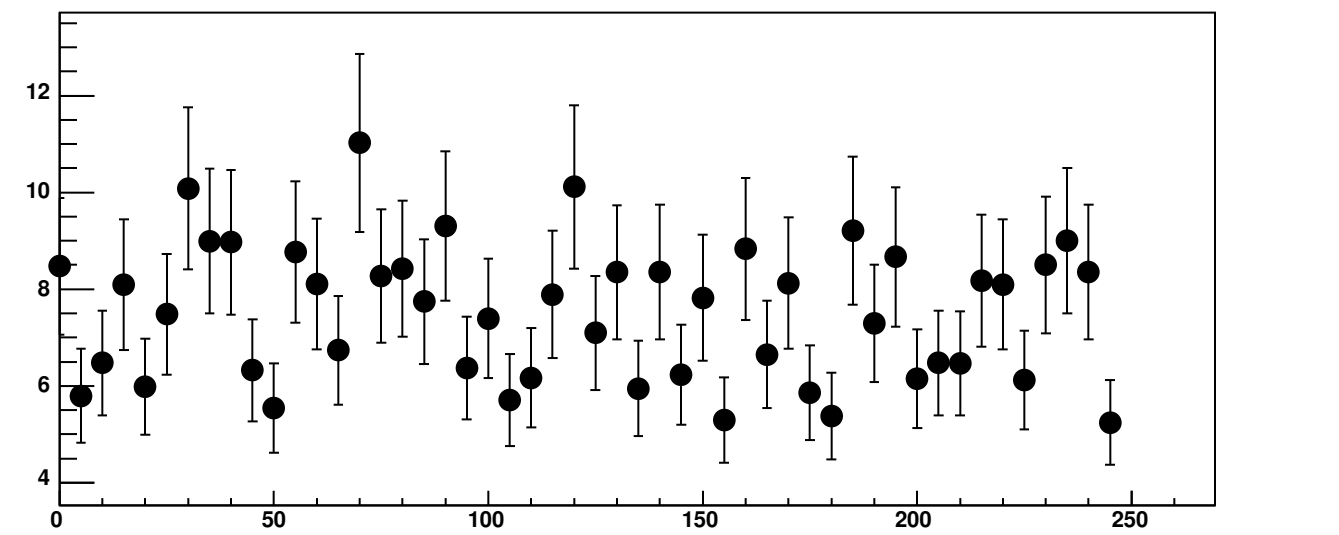
Chip 6, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold



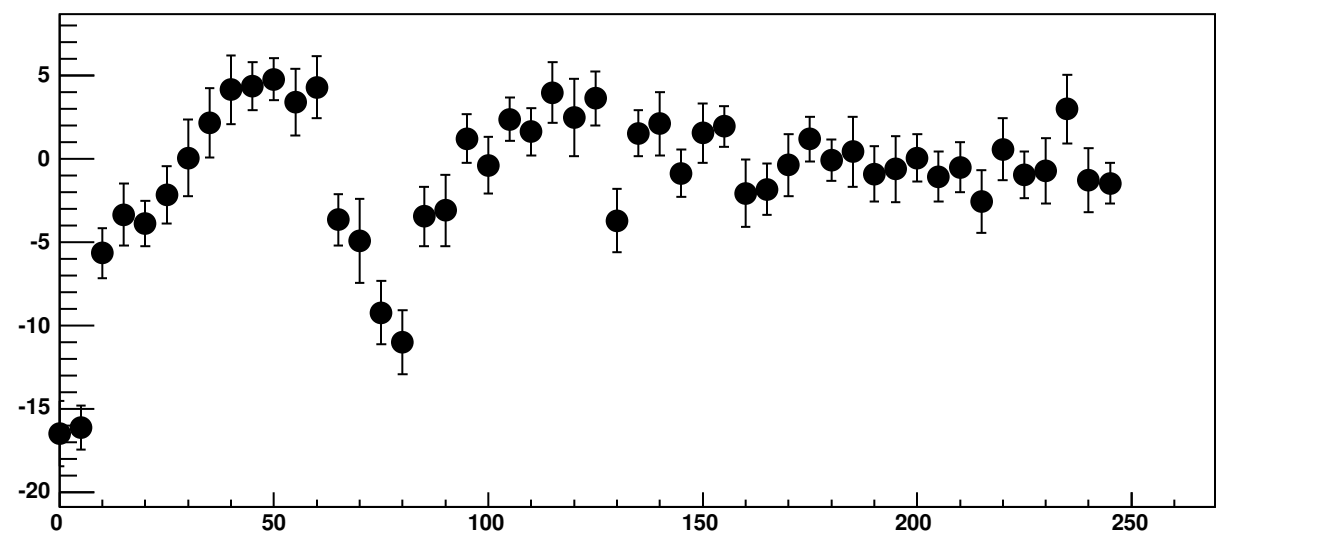
Chip 6, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold



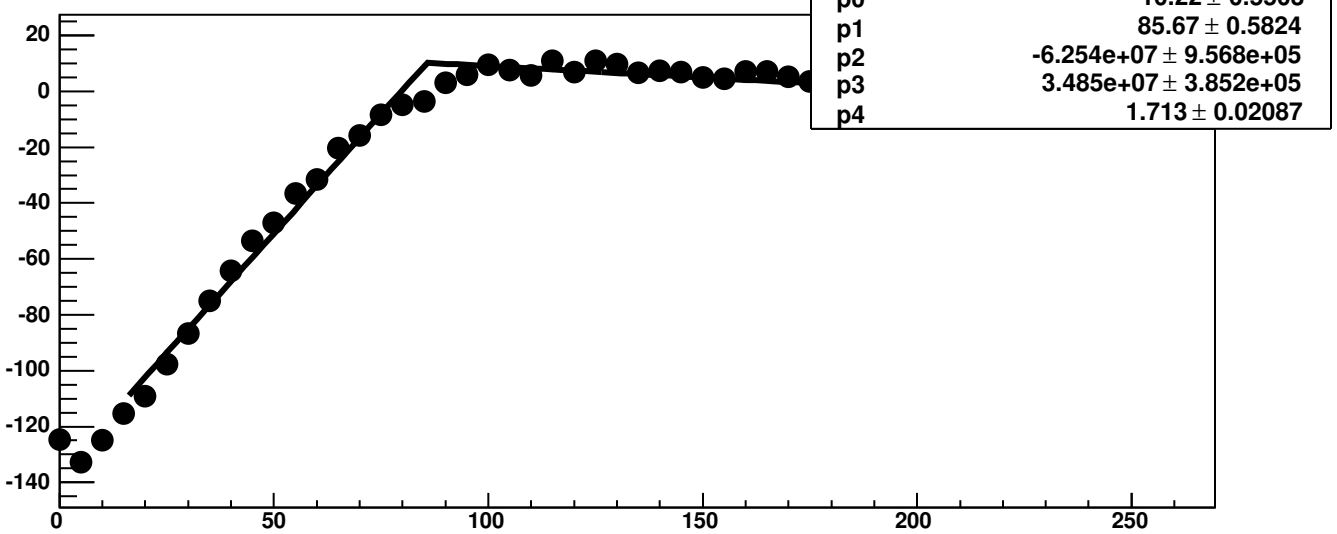
Chip 6, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



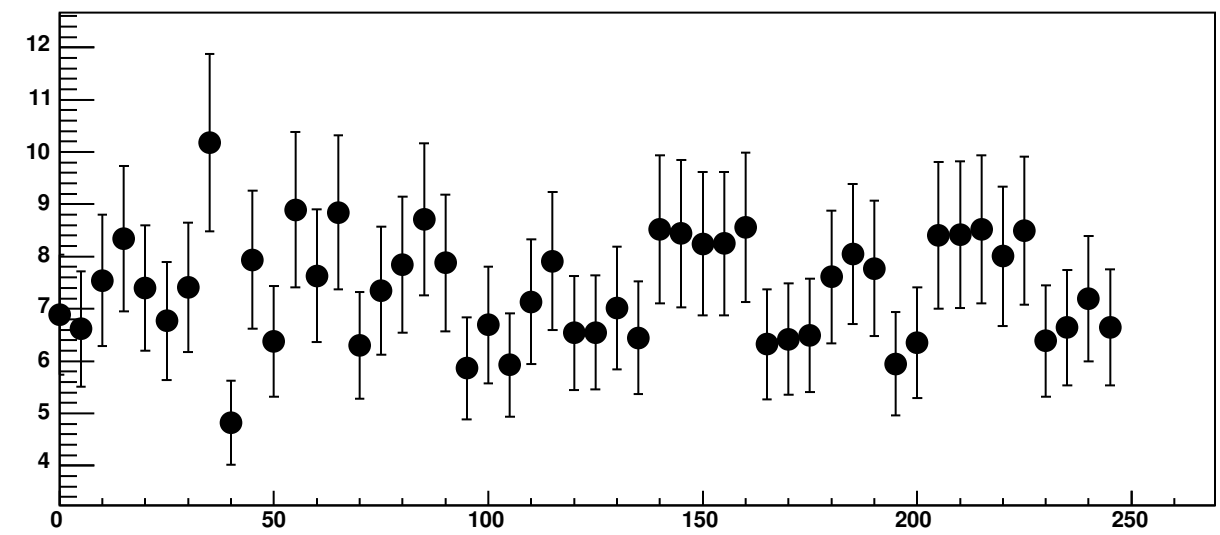
Chip 6, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold



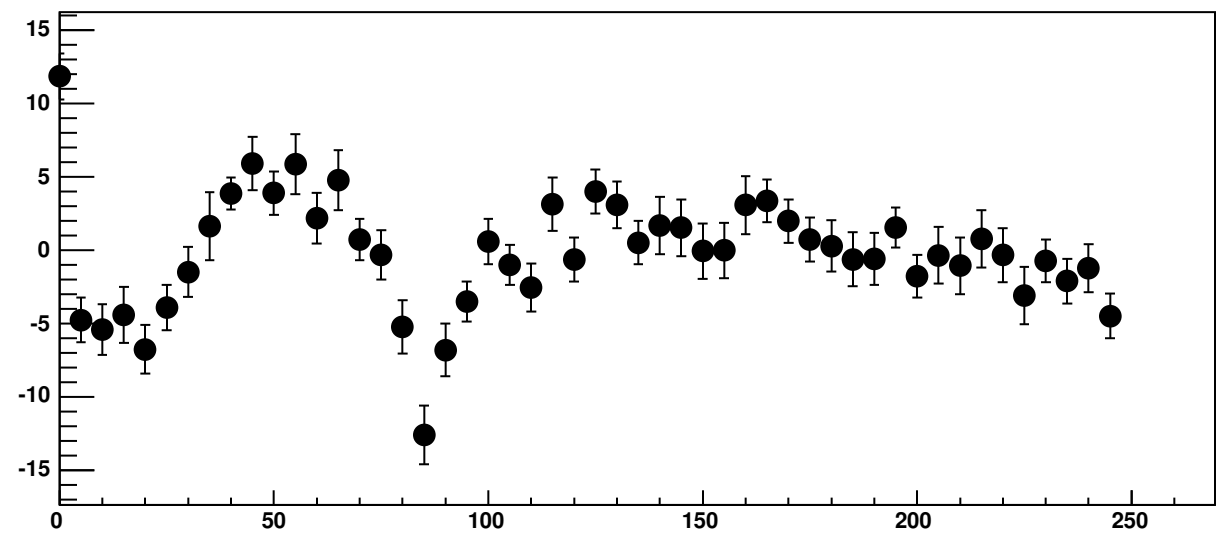
Chip 6, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold



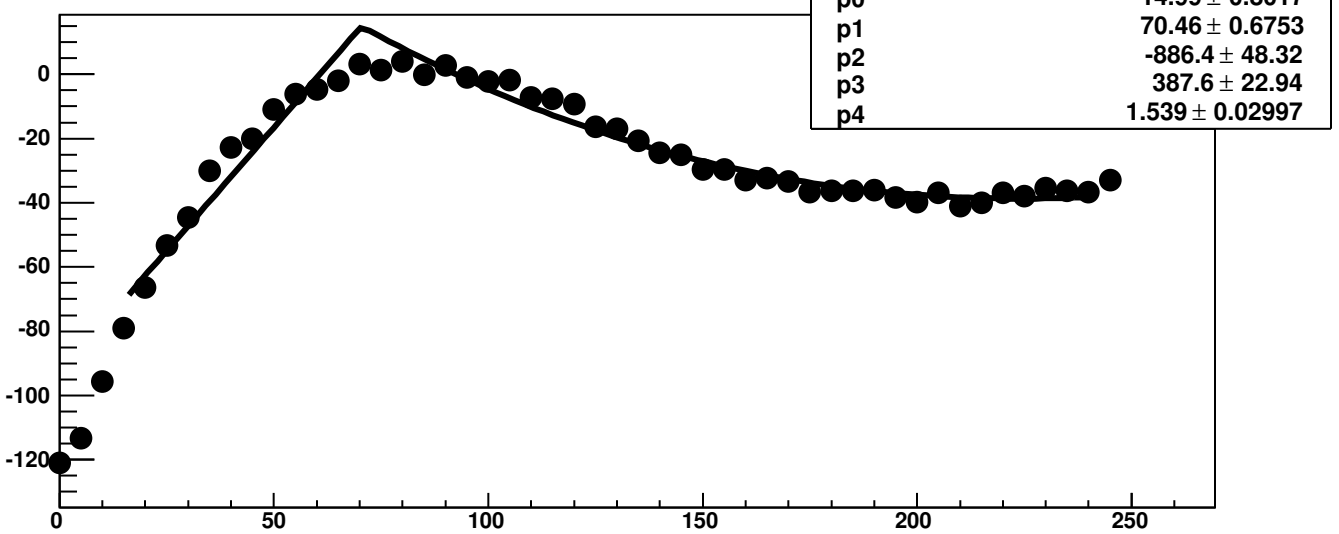
Chip 6, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



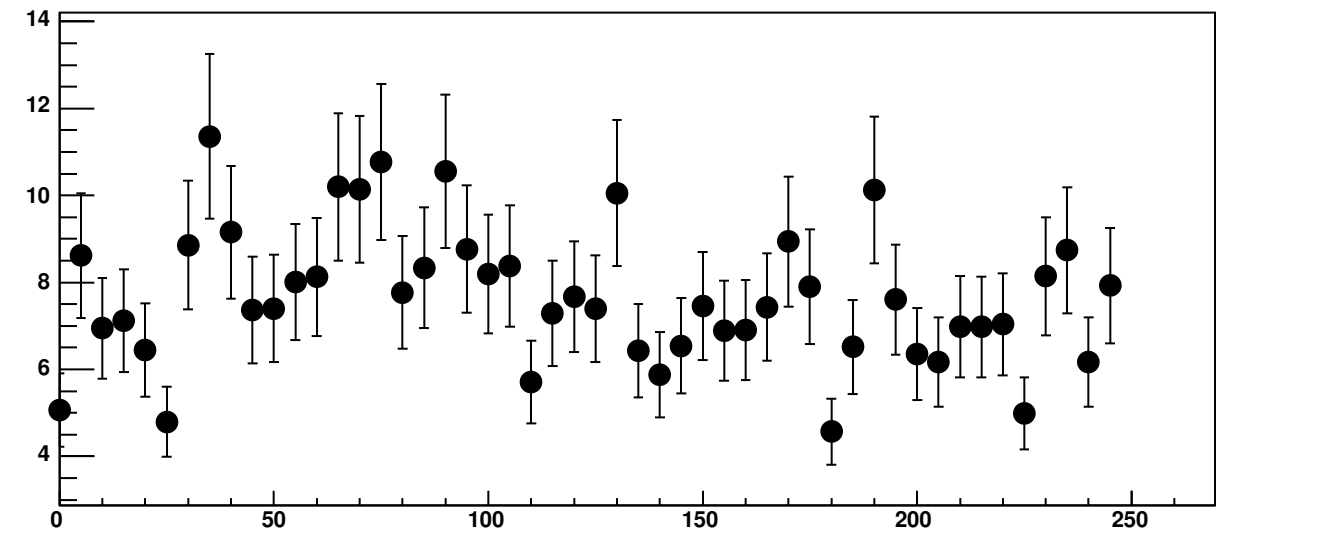
Chip 6, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold



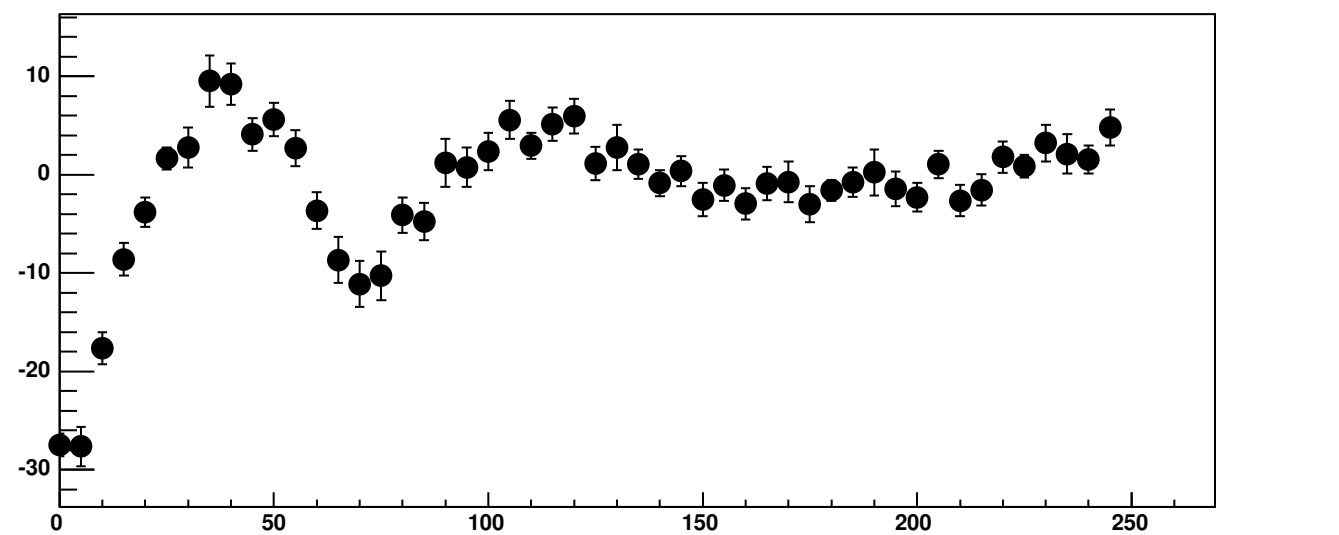
Chip 6, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold

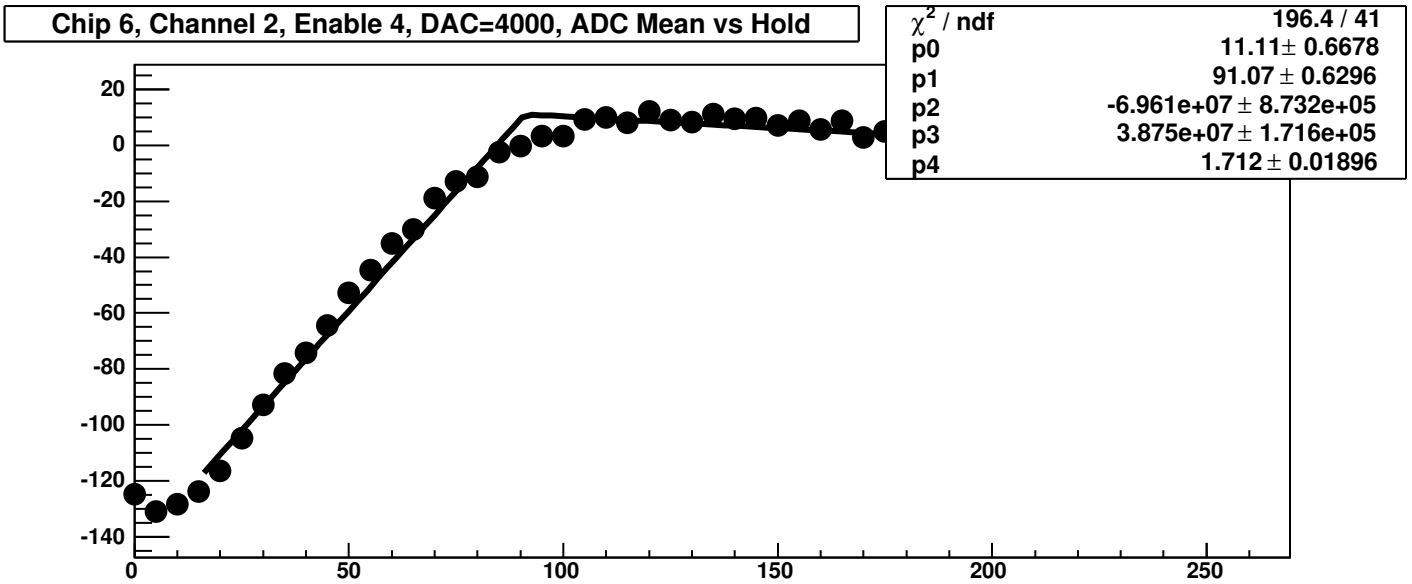


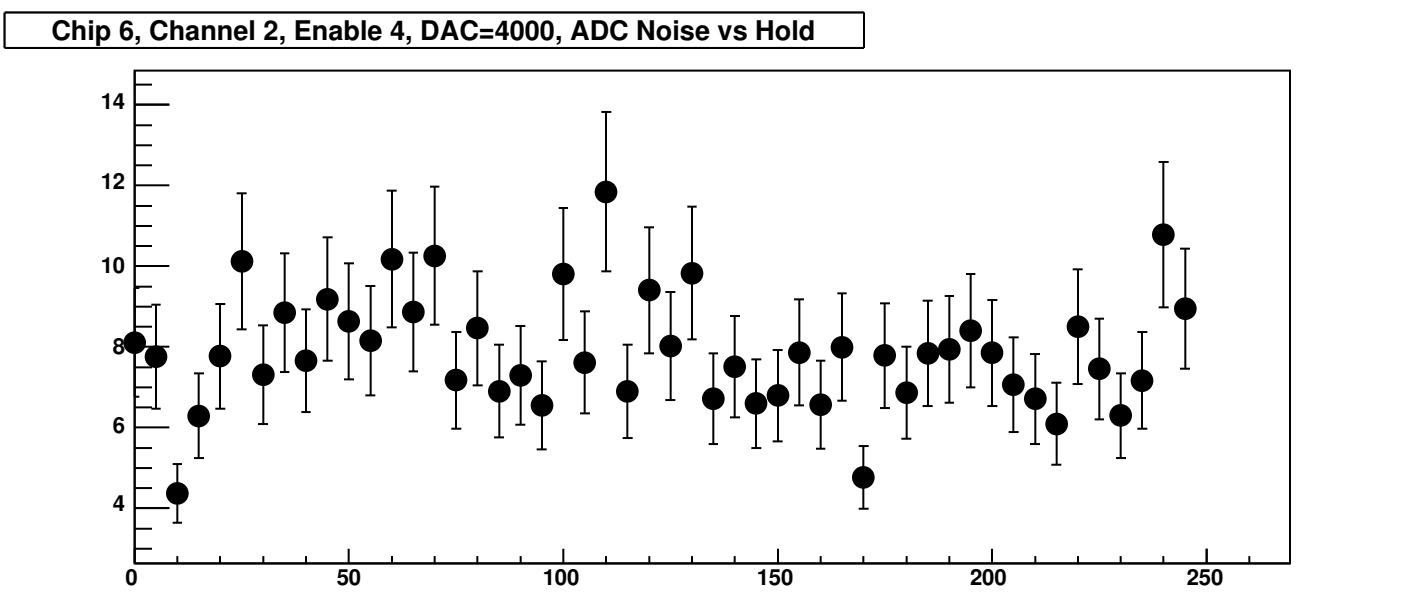
Chip 6, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold

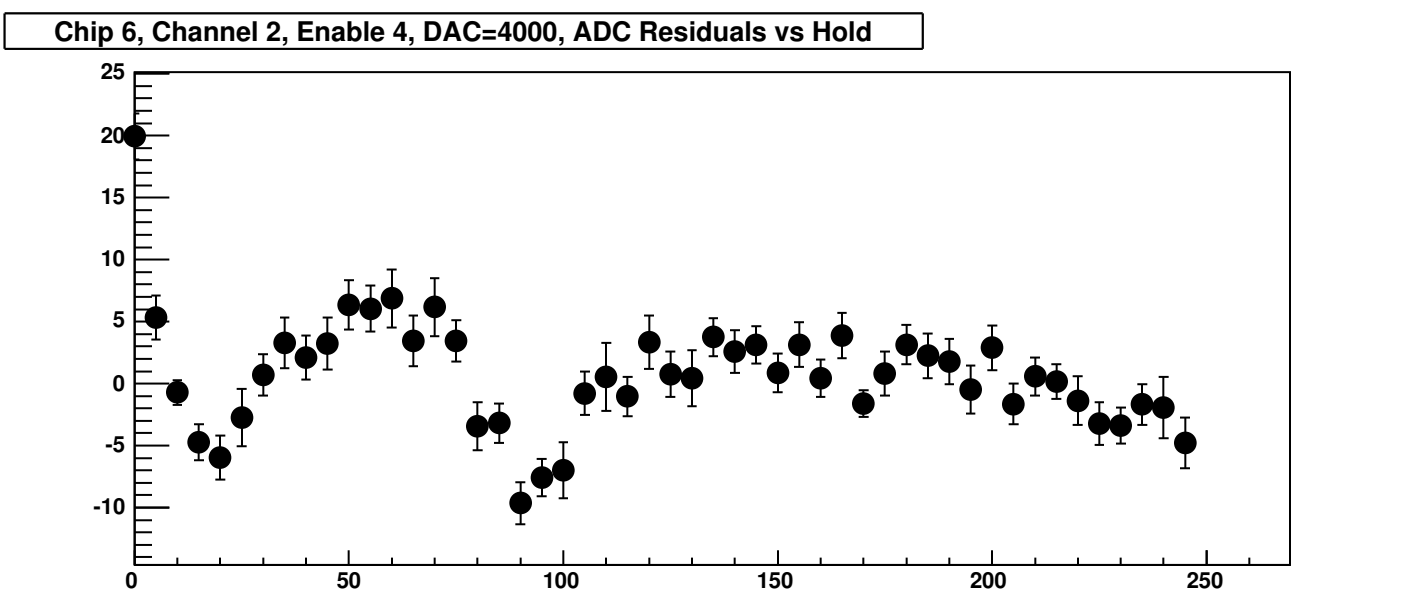


Chip 6, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold

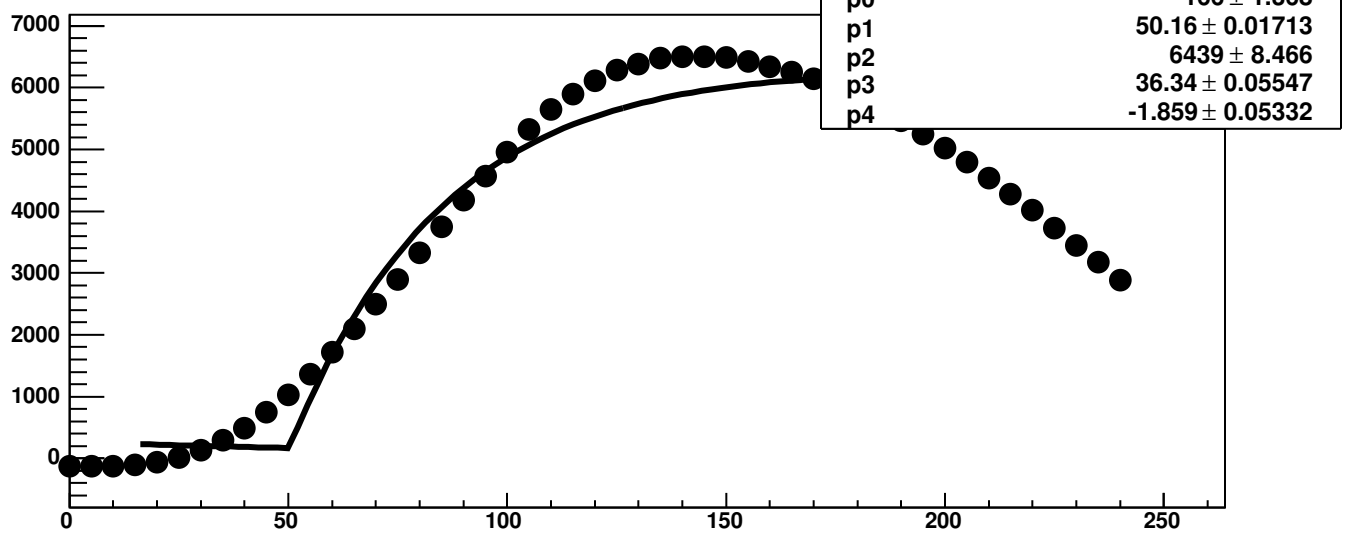



Chip 6, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 196.4 / 41
p0 11.11 ± 0.6678
p1 91.07 ± 0.6296
p2 -6.961e+07 ± 8.732e+05
p3 3.875e+07 ± 1.716e+05
p4 1.712 ± 0.01896
20
0
-20
-40
-60
-80
-100
-120
-140
0
50
100
150
200
250

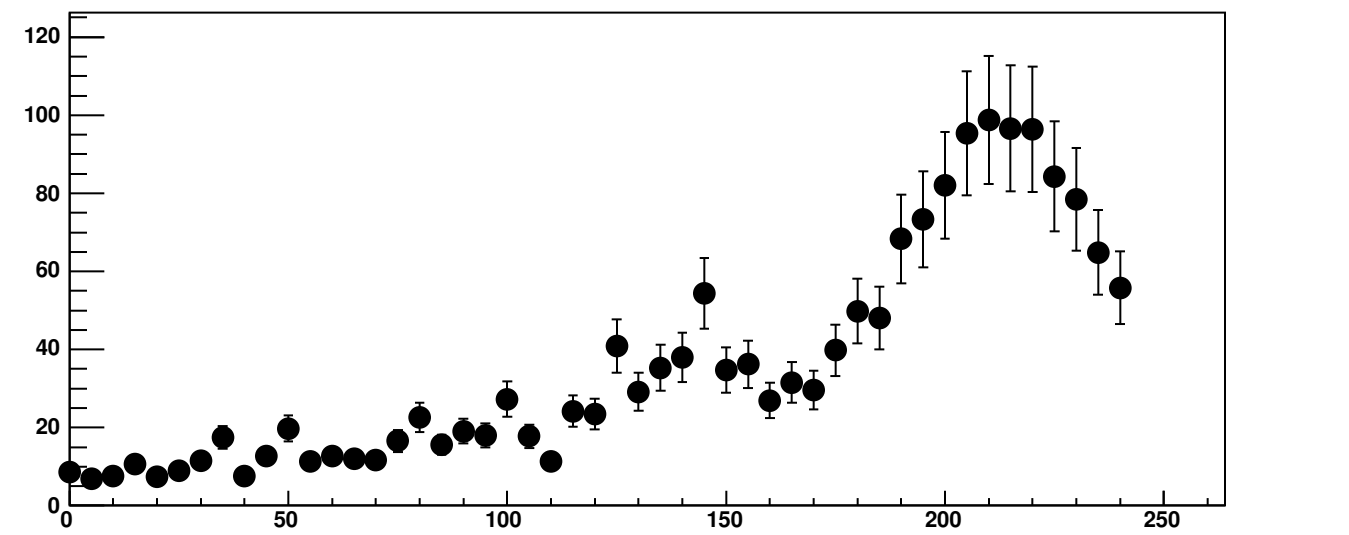

Chip 6, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 6, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
0
50
100
150
200
250

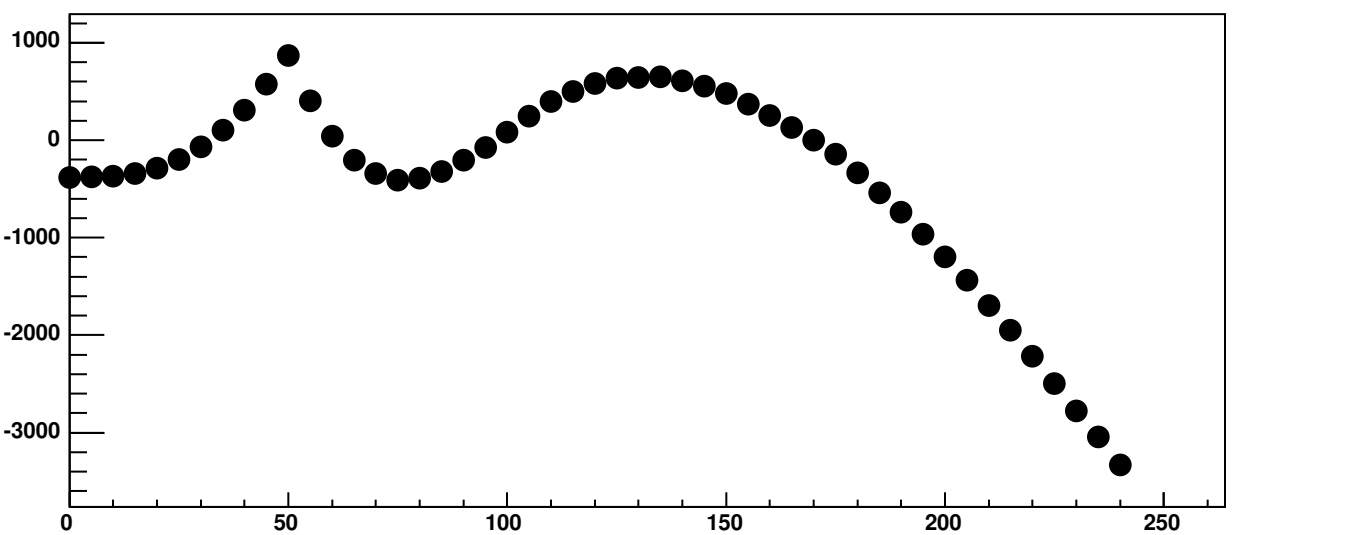
Chip 6, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

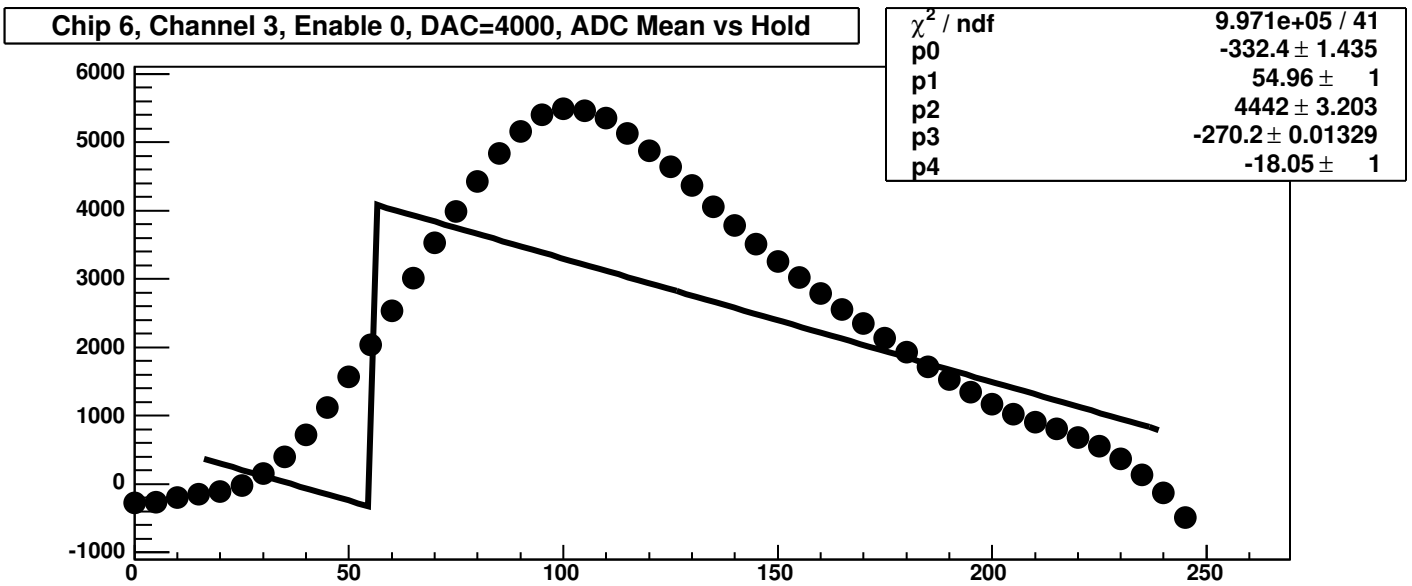


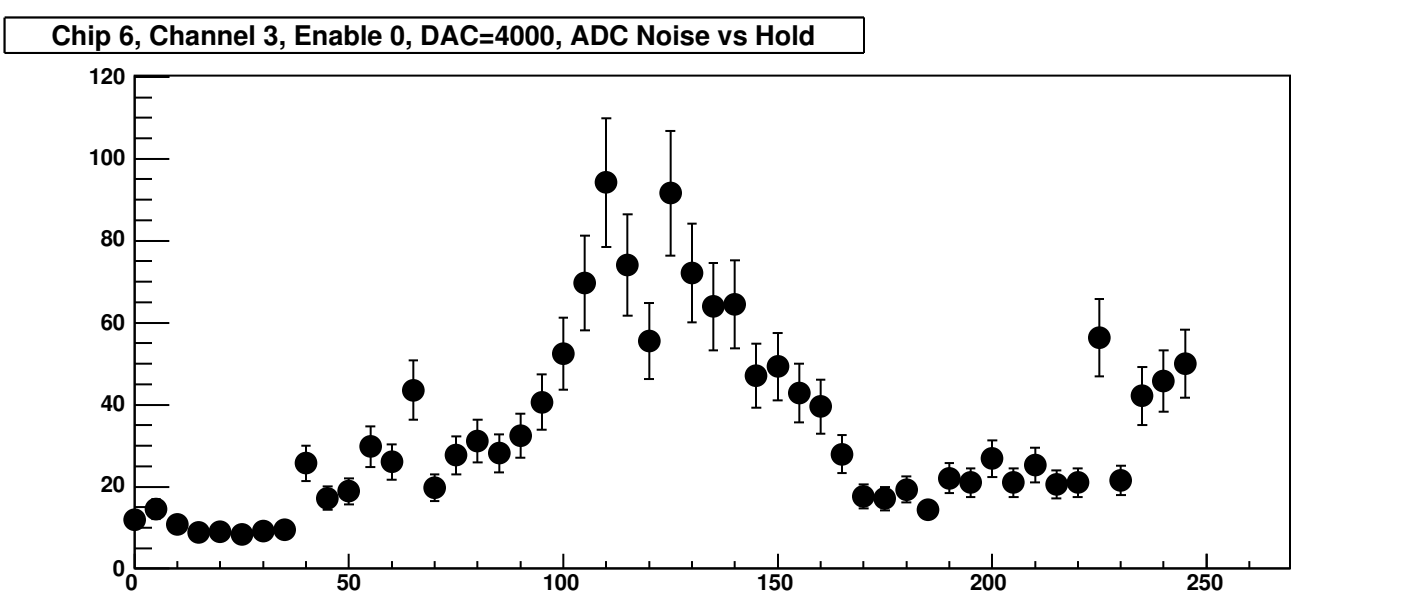
Chip 6, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold

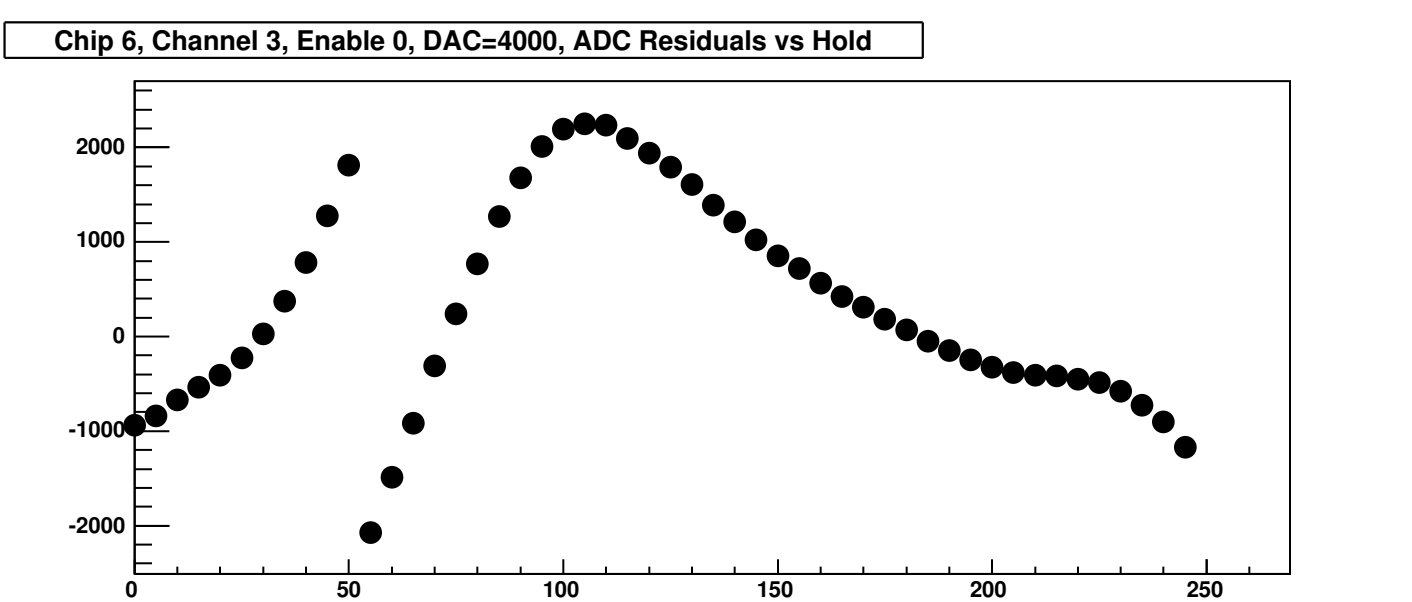


Chip 6, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold

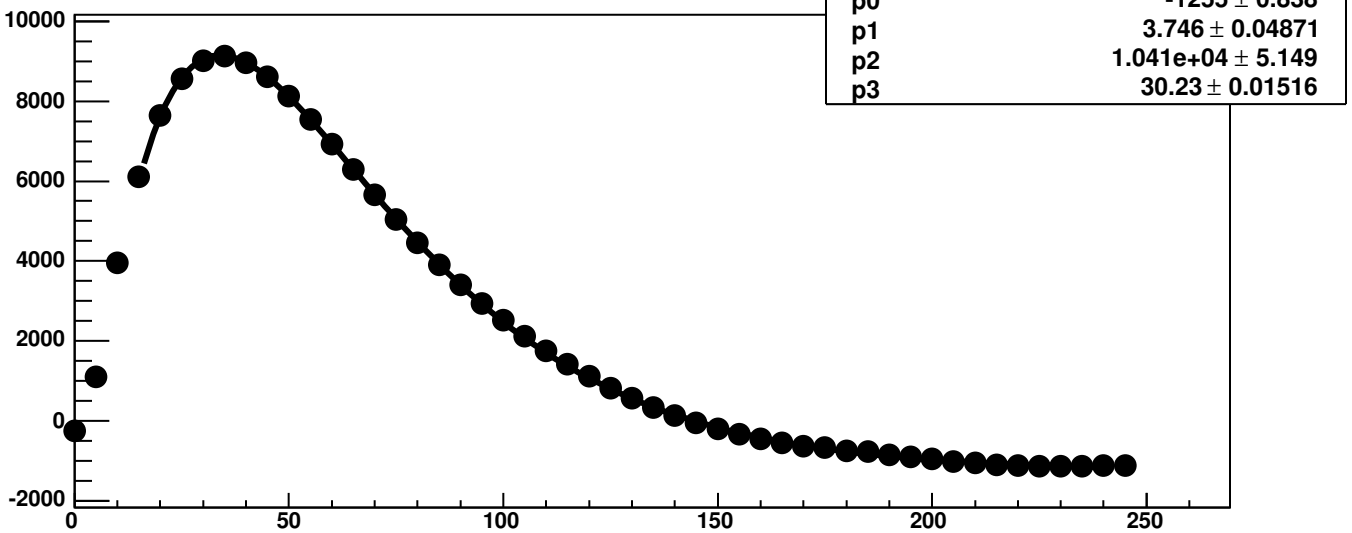



Chip 6, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 9.971e+05 / 41
p0 -332.4 ± 1.435
p1 54.96 ± 1
p2 4442 ± 3.203
p3 -270.2 ± 0.01329
p4 -18.05 ± 1
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

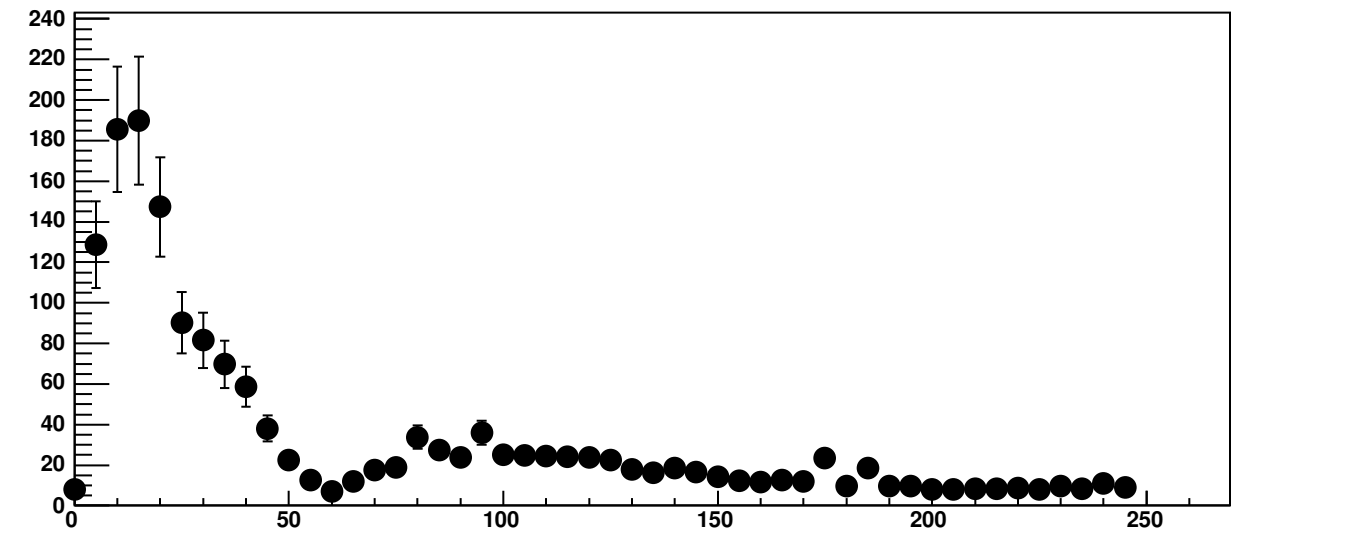

Chip 6, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 6, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
0
50
100
150
200
250

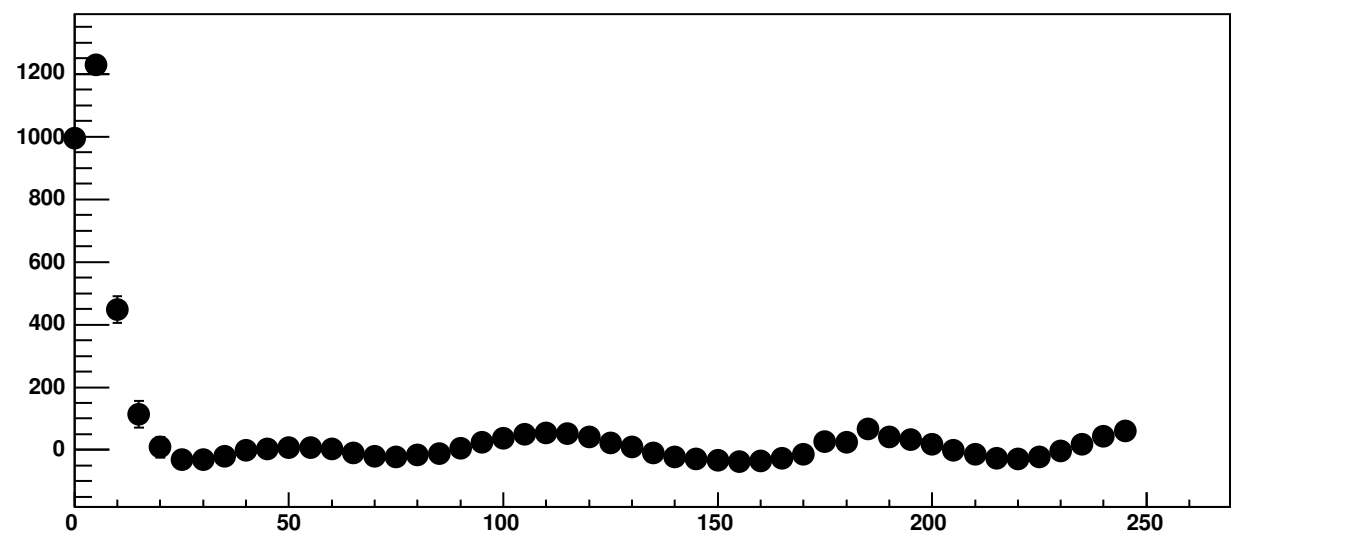
Chip 6, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold

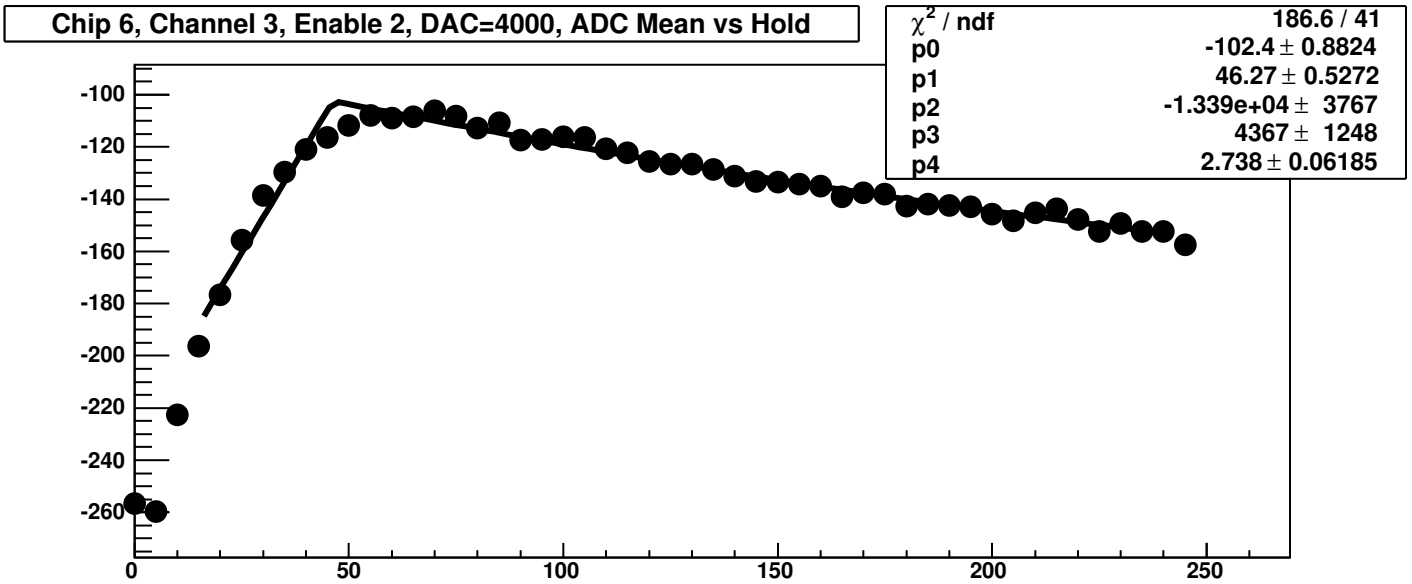


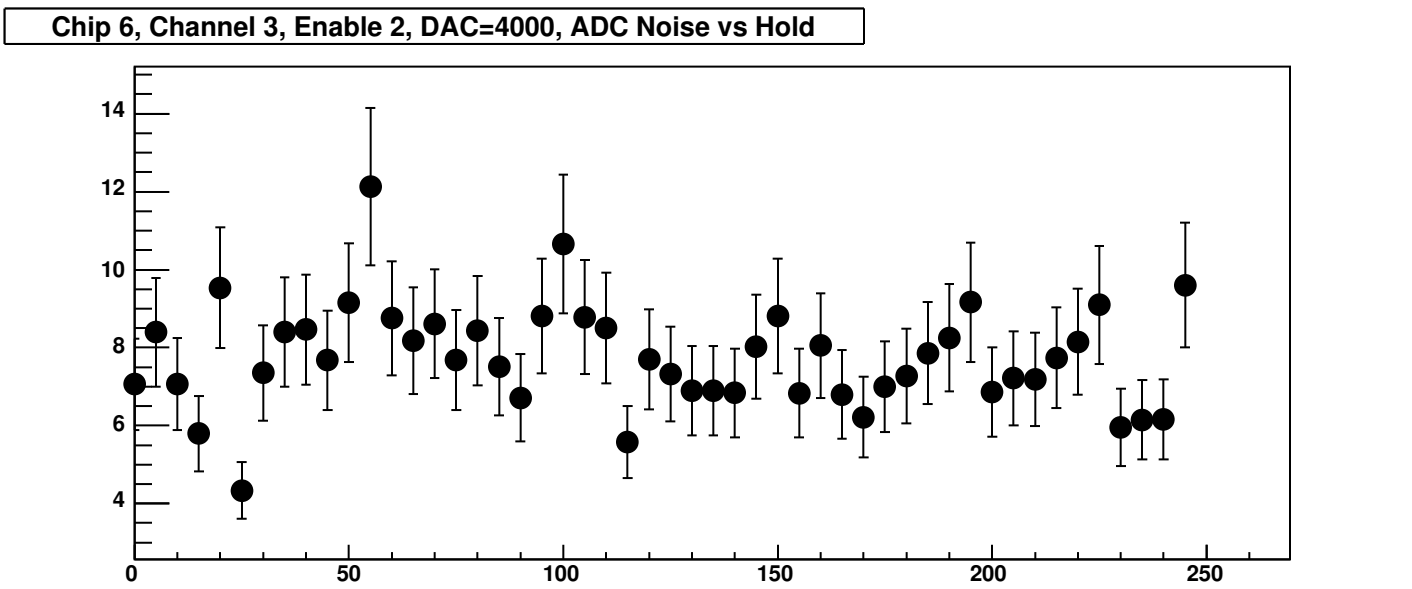
Chip 6, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold

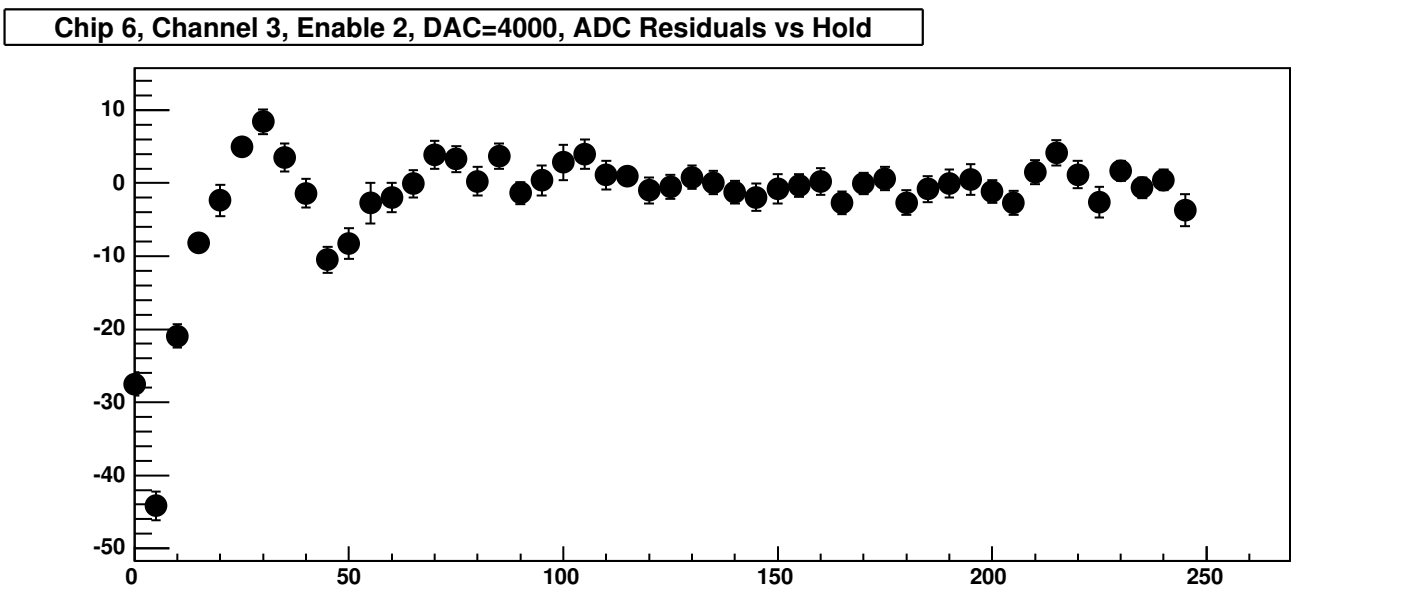


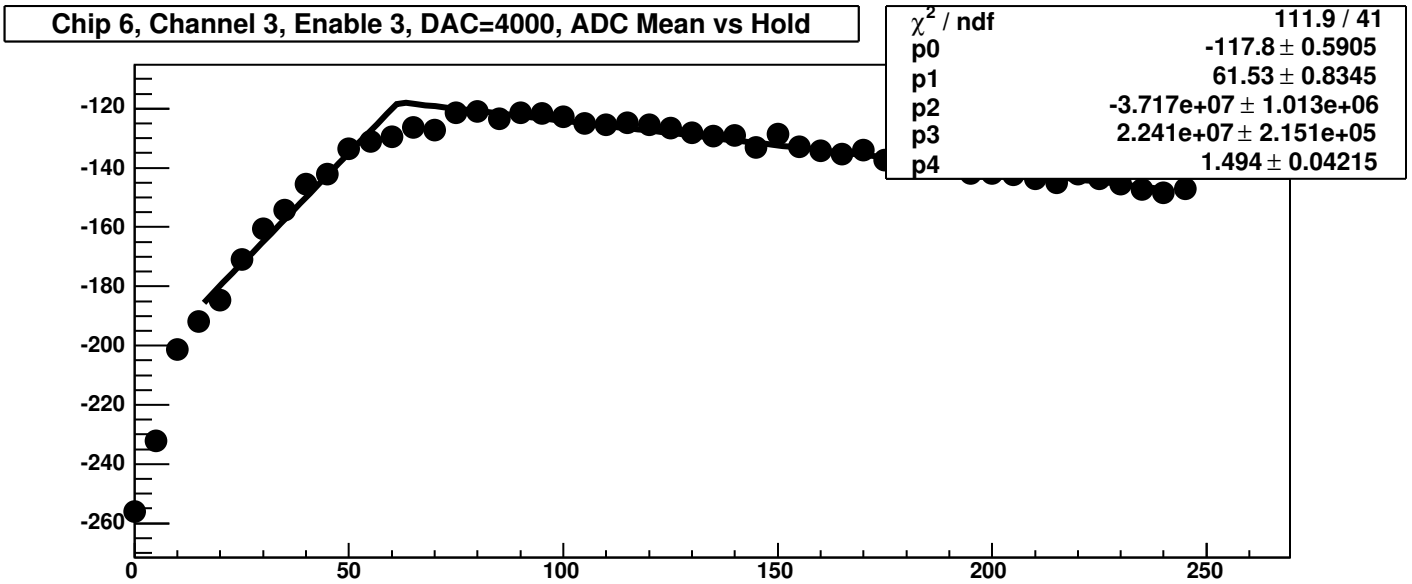
Chip 6, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold

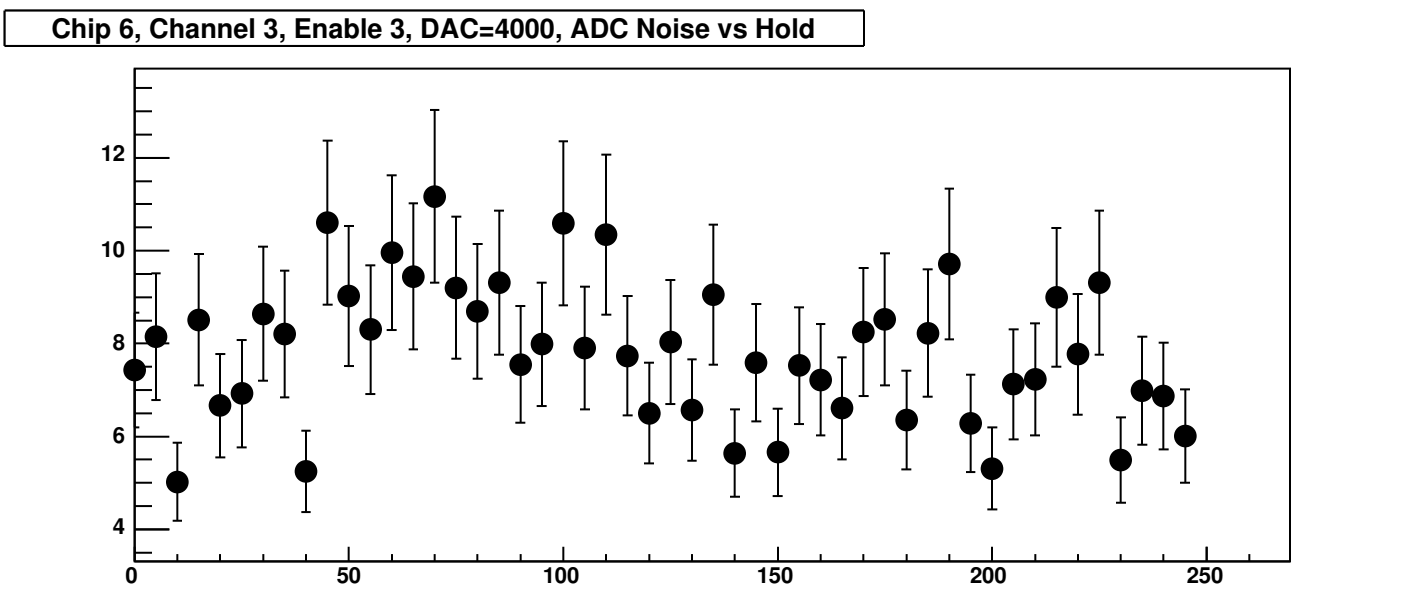


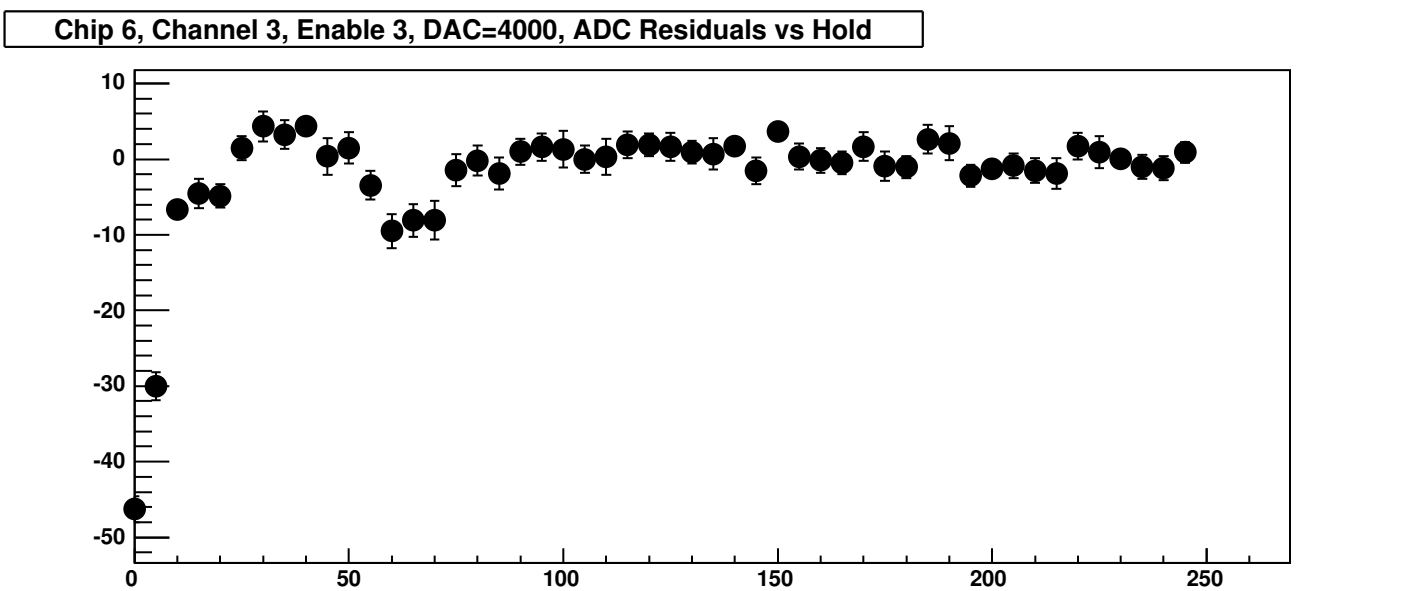

Chip 6, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 186.6 / 41
p0 -102.4 ± 0.8824
p1 46.27 ± 0.5272
p2 -1.339e+04 ± 3767
p3 4367 ± 1248
p4 2.738 ± 0.06185
-100
-120
-140
-160
-180
-200
-220
-240
-260
0
50
100
150
200
250


Chip 6, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

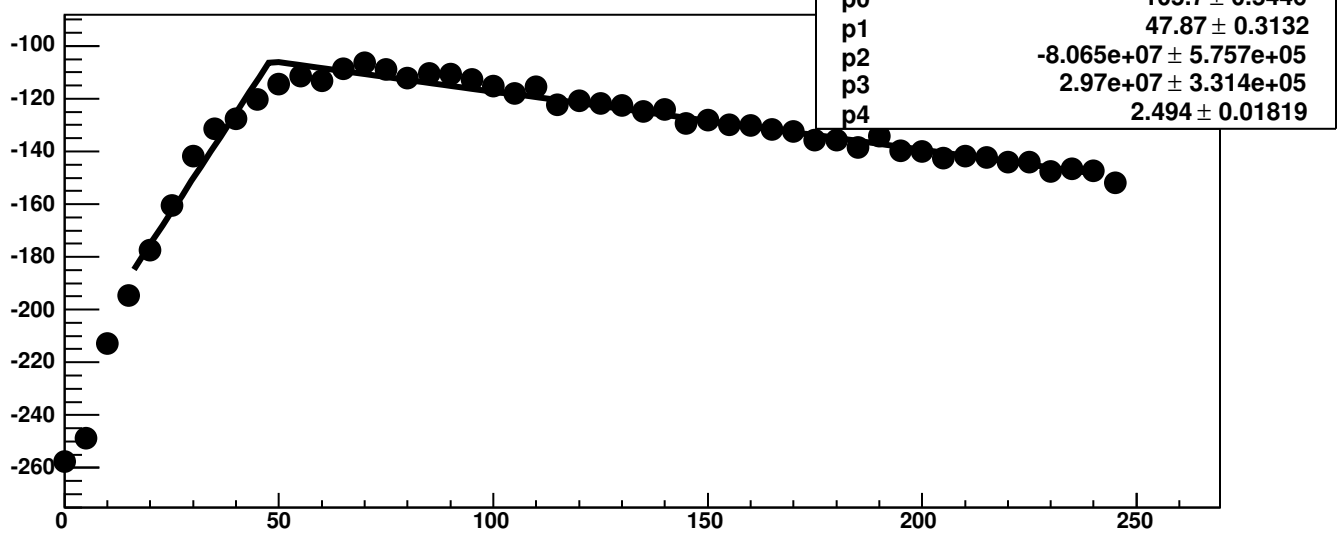

Chip 6, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold
10
0
-10
-20
-30
-40
-50
0
50
100
150
200
250


Chip 6, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 111.9 / 41
p0 -117.8 ± 0.5905
p1 61.53 ± 0.8345
p2 -3.717e+07 ± 1.013e+06
p3 2.241e+07 ± 2.151e+05
p4 1.494 ± 0.04215
-120
-140
-160
-180
-200
-220
-240
-260
0
50
100
150
200
250

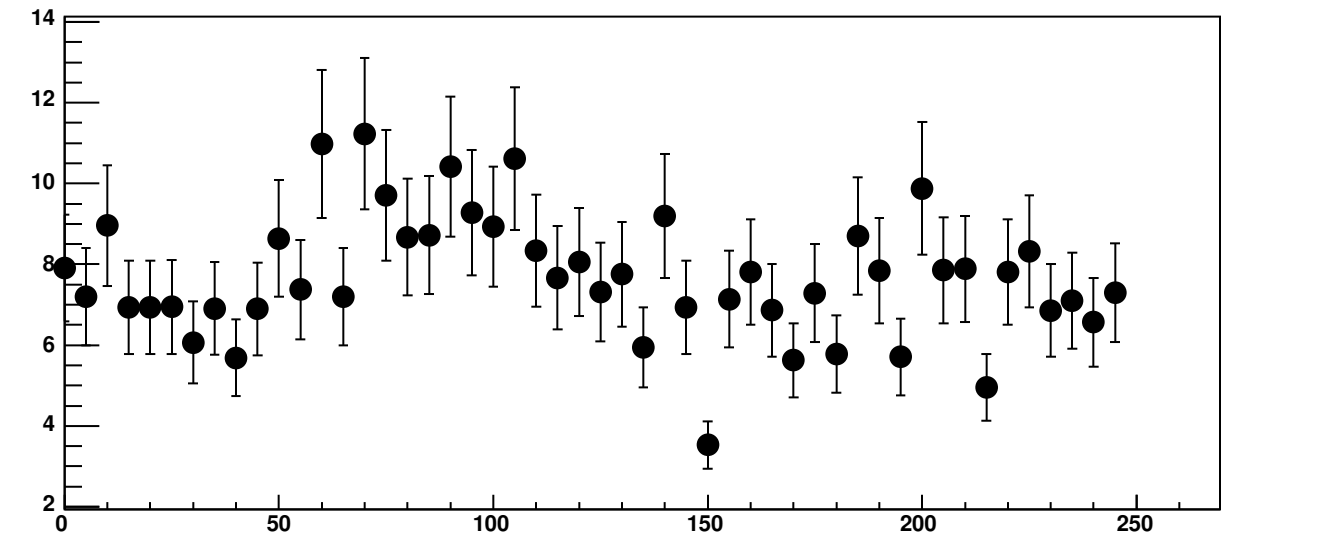

Chip 6, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 6, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold
10
0
-10
-20
-30
-40
-50
0
50
100
150
200
250

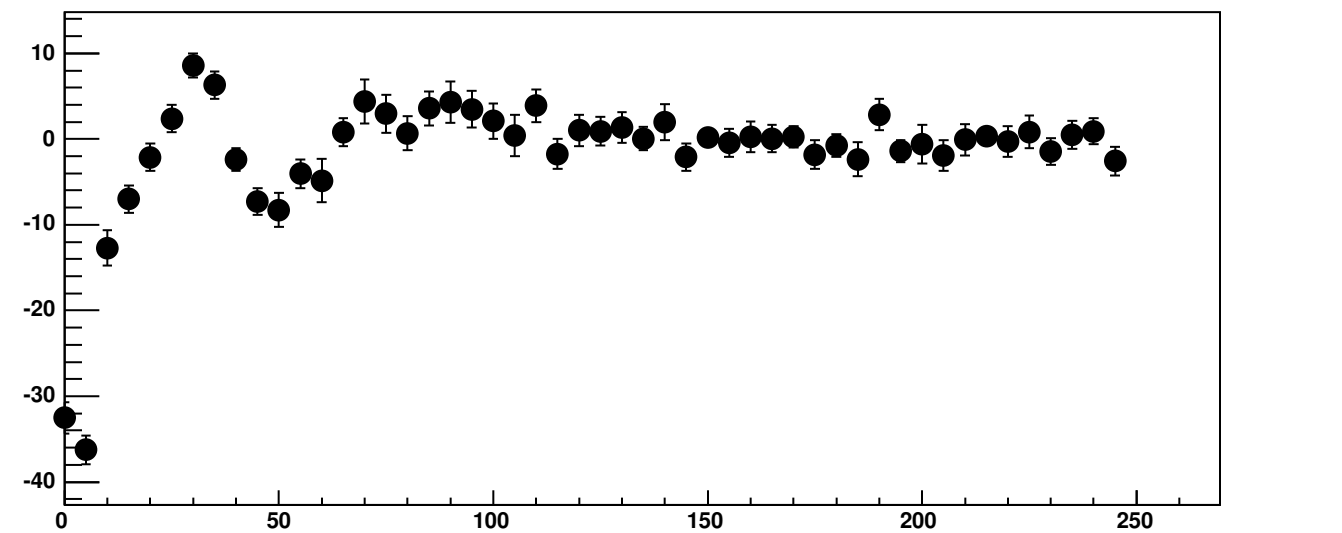
Chip 6, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold



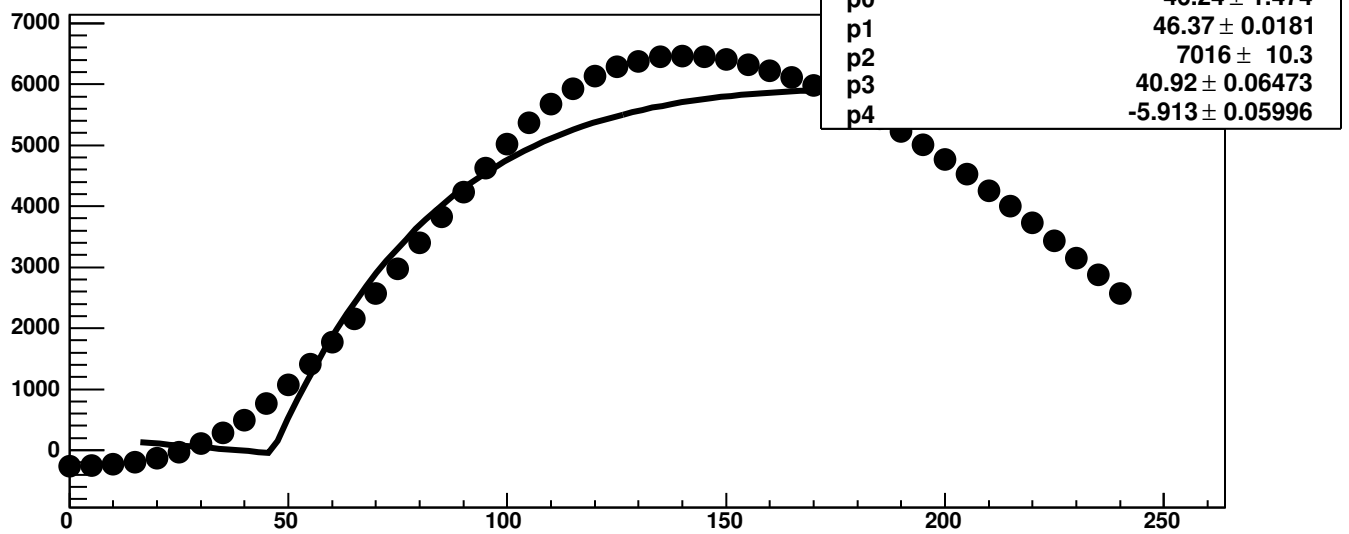
Chip 6, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold



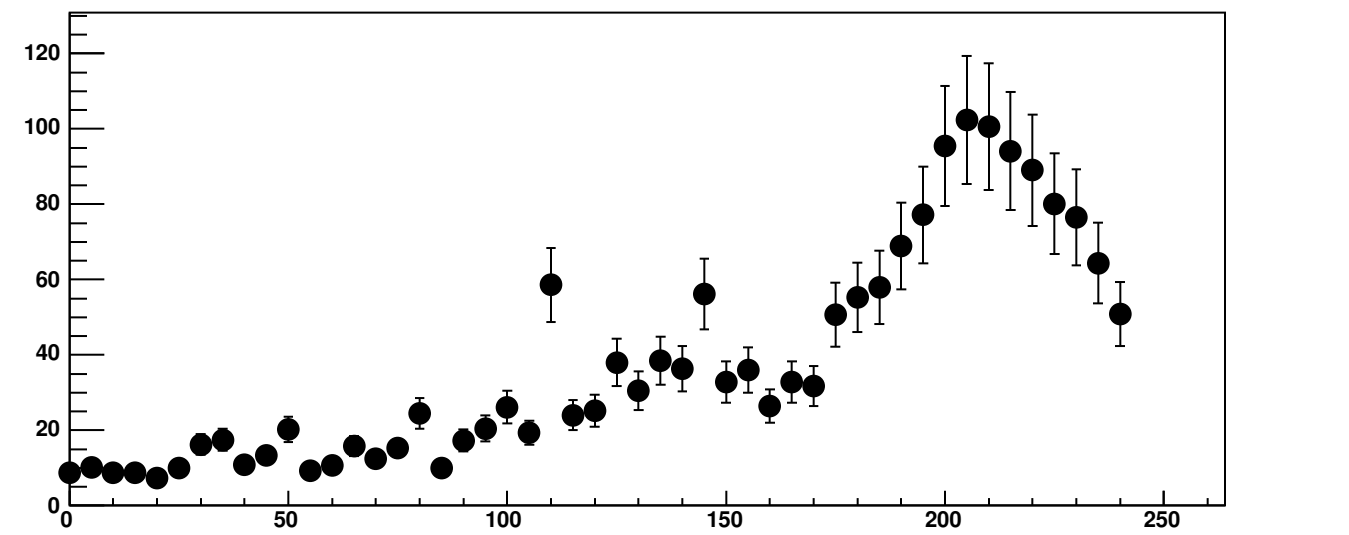
Chip 6, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold



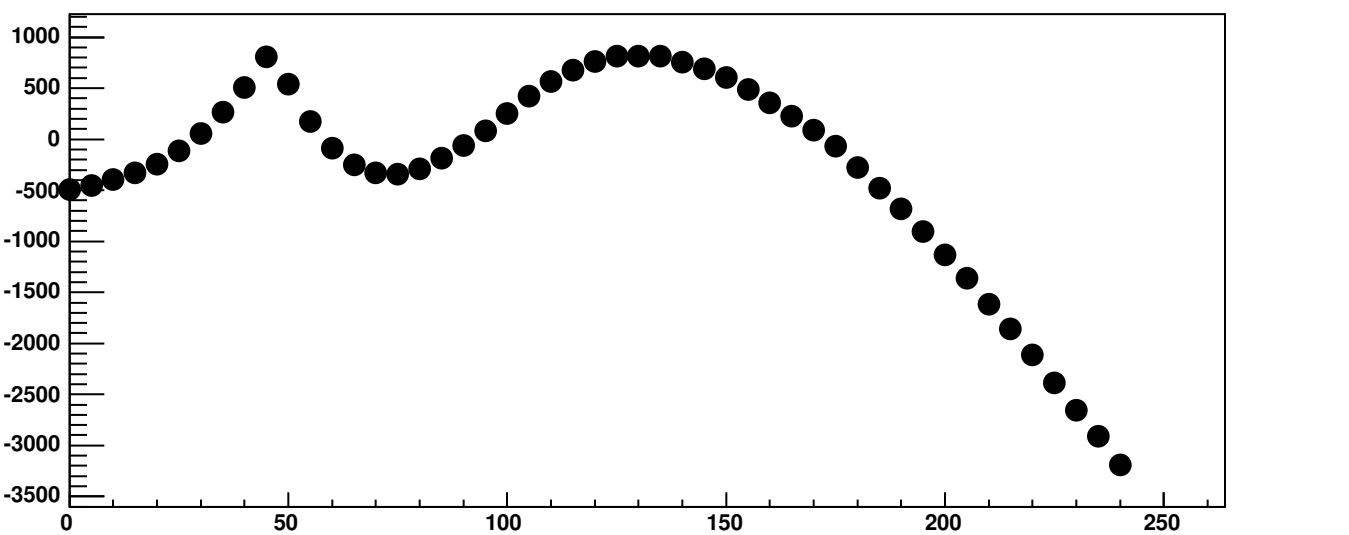
Chip 6, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold



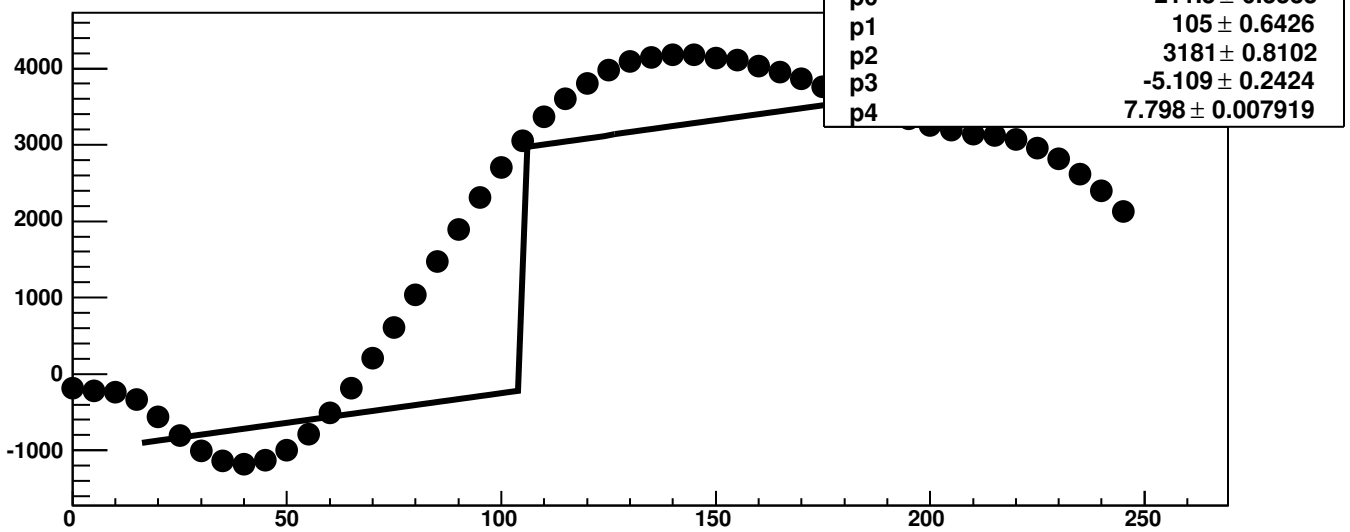
Chip 6, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



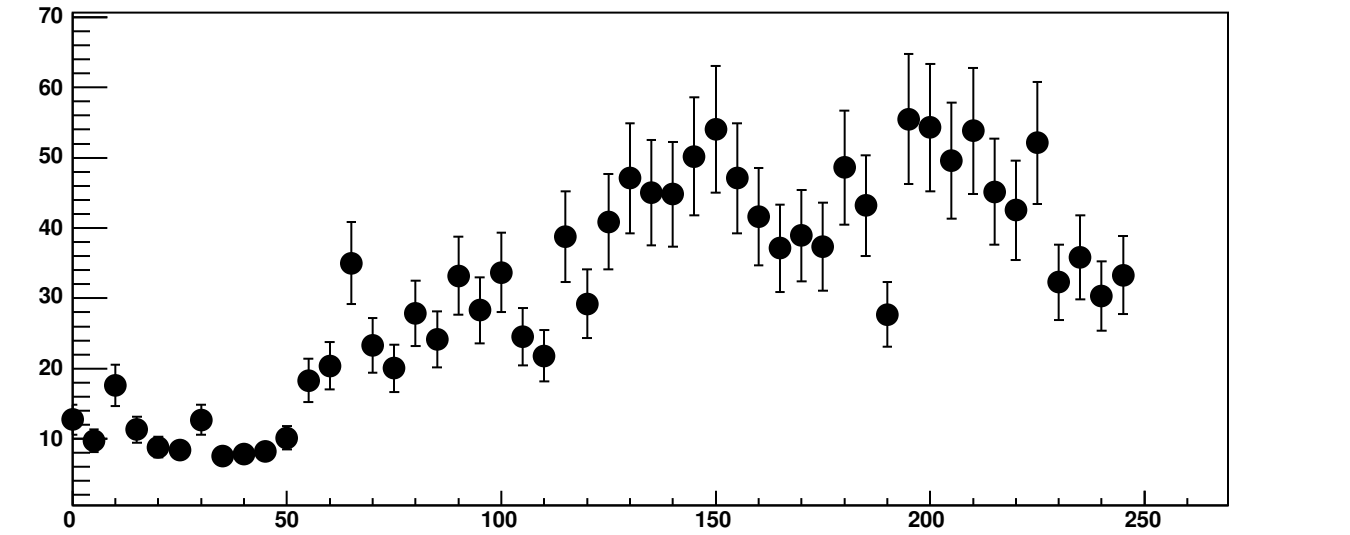
Chip 6, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold



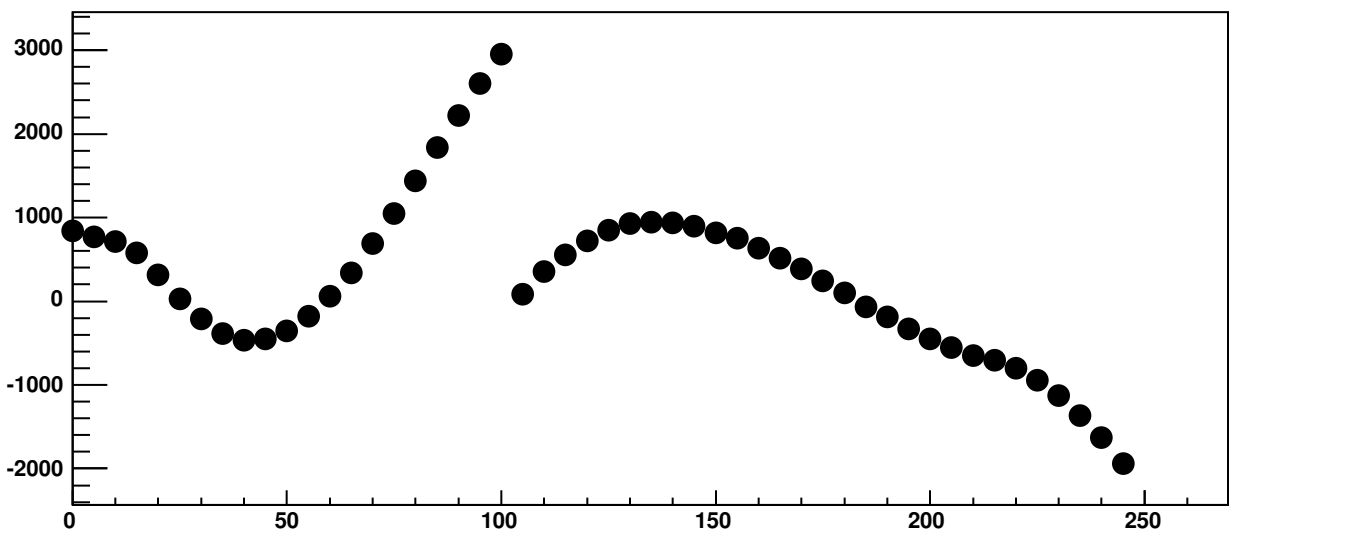
Chip 6, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold



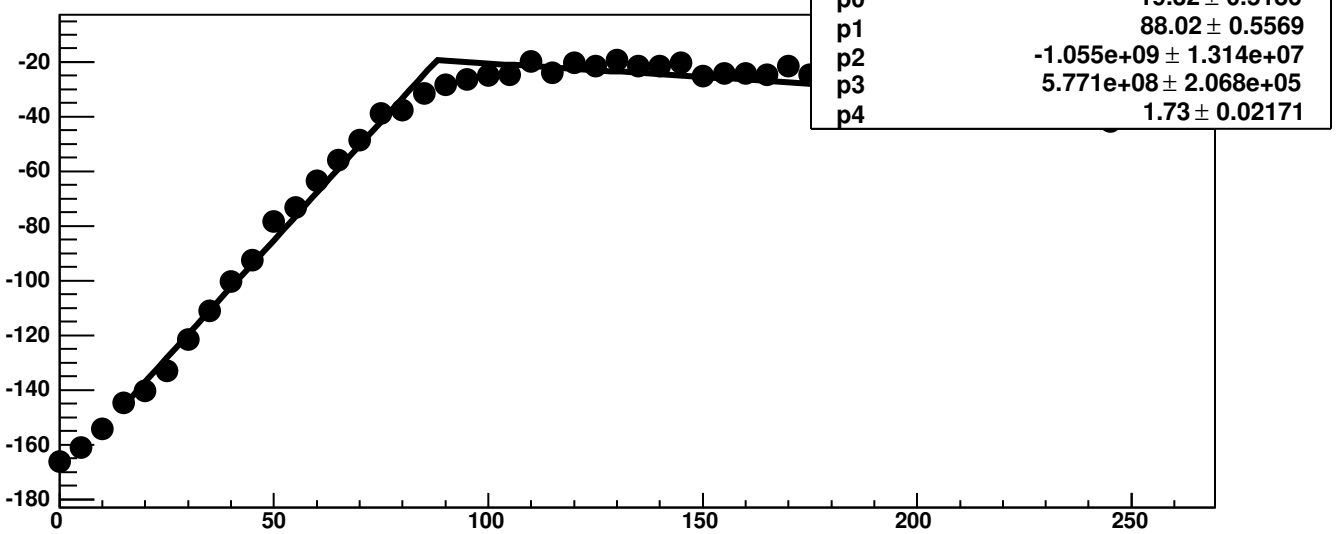
Chip 6, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



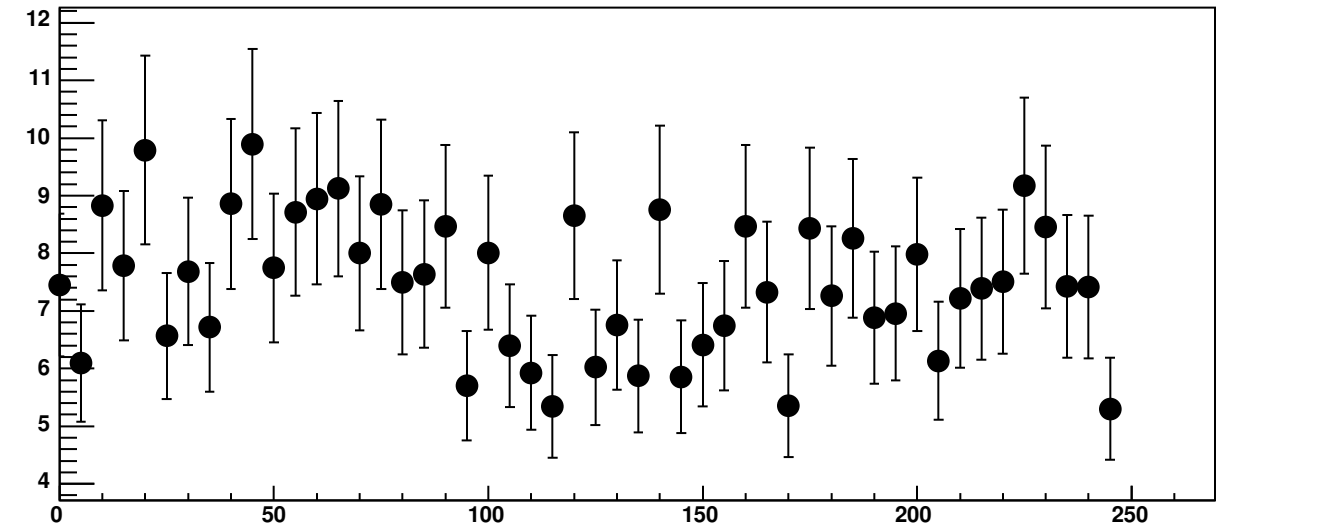
Chip 6, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold



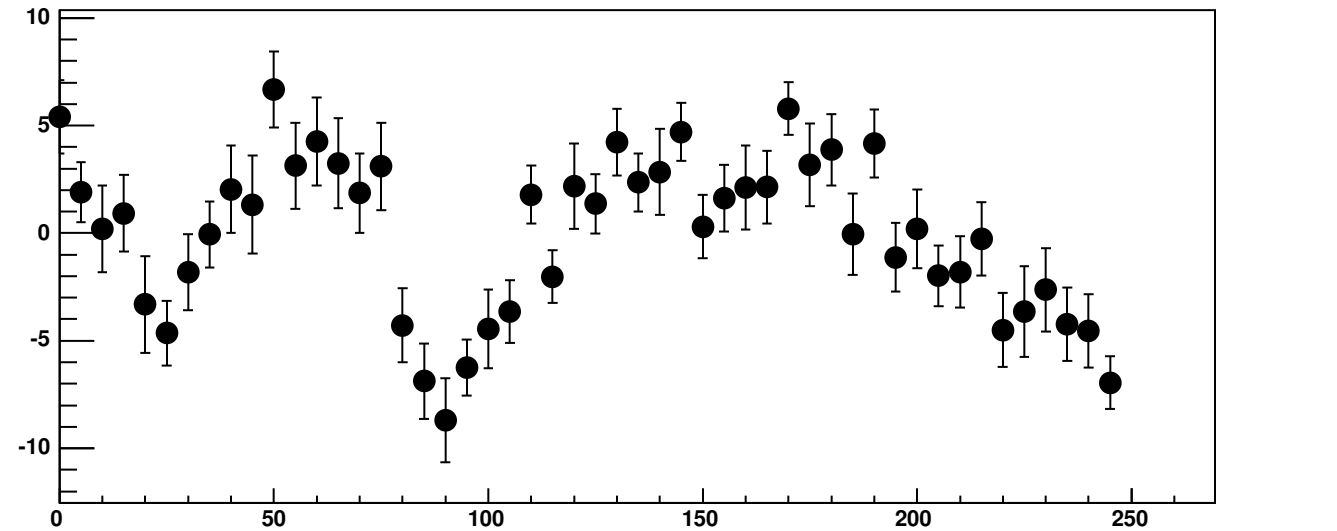
Chip 6, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold



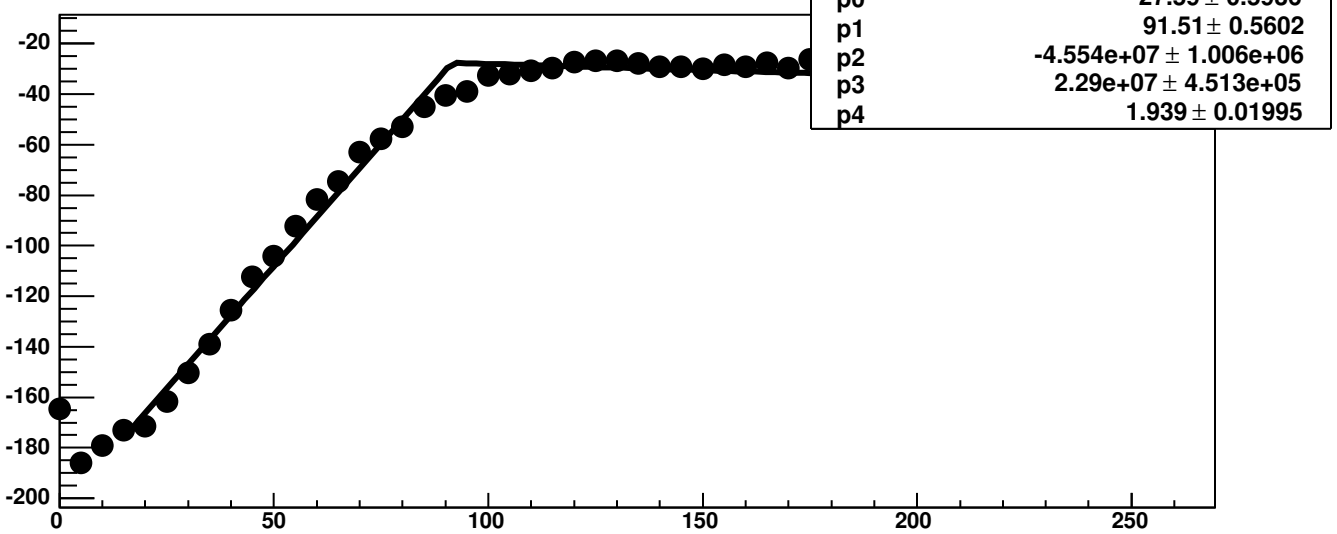
Chip 6, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold



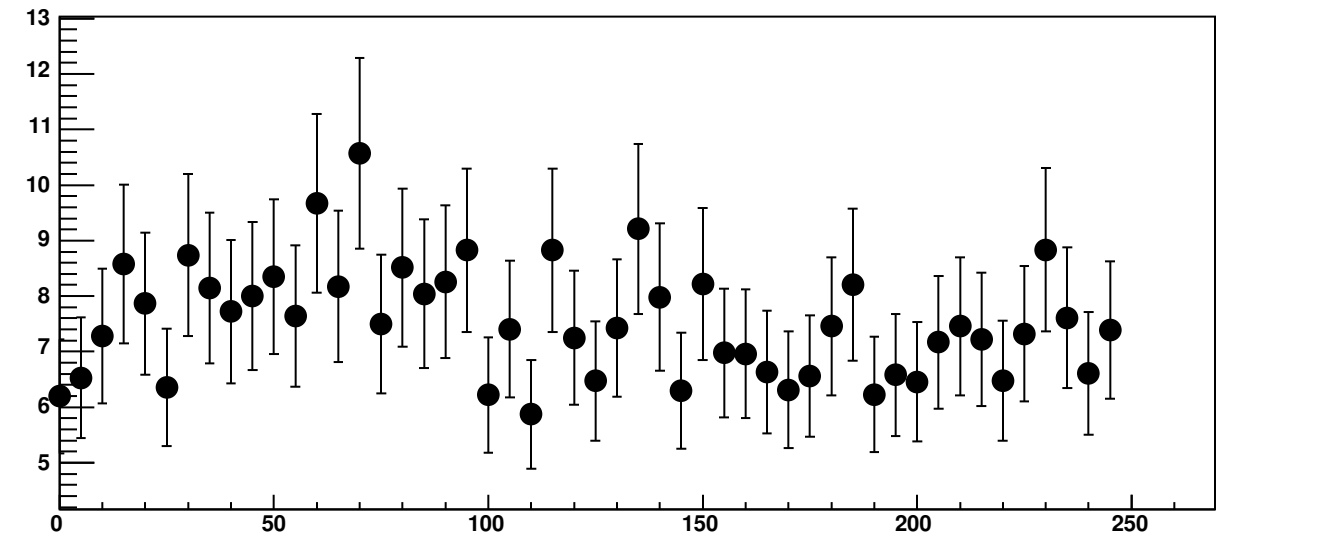
Chip 6, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold



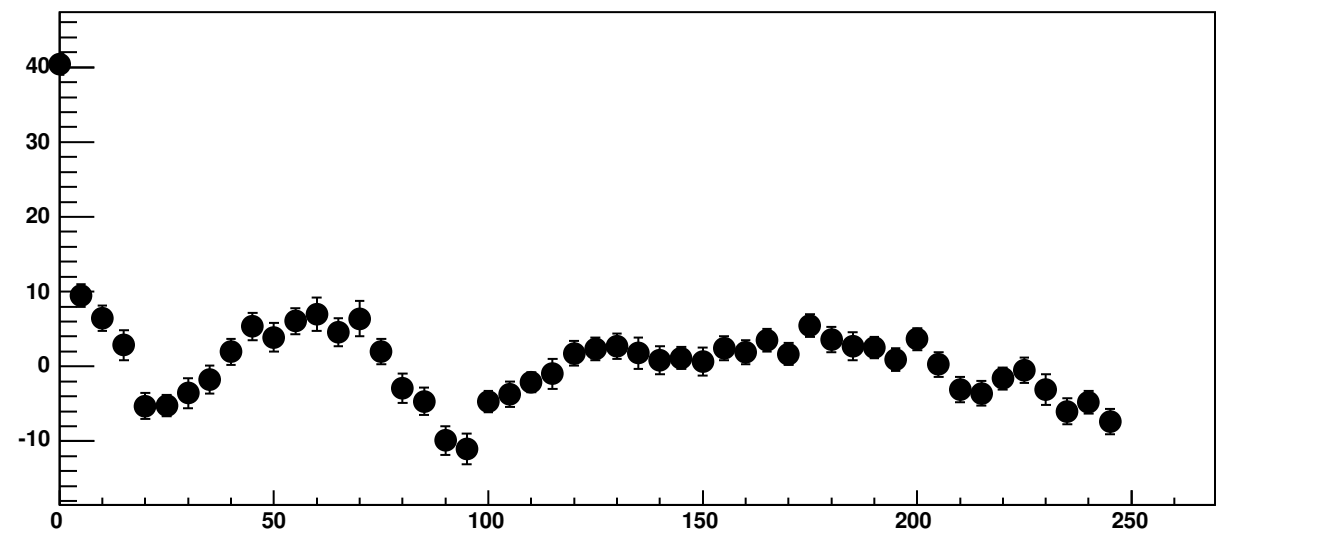
Chip 6, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold



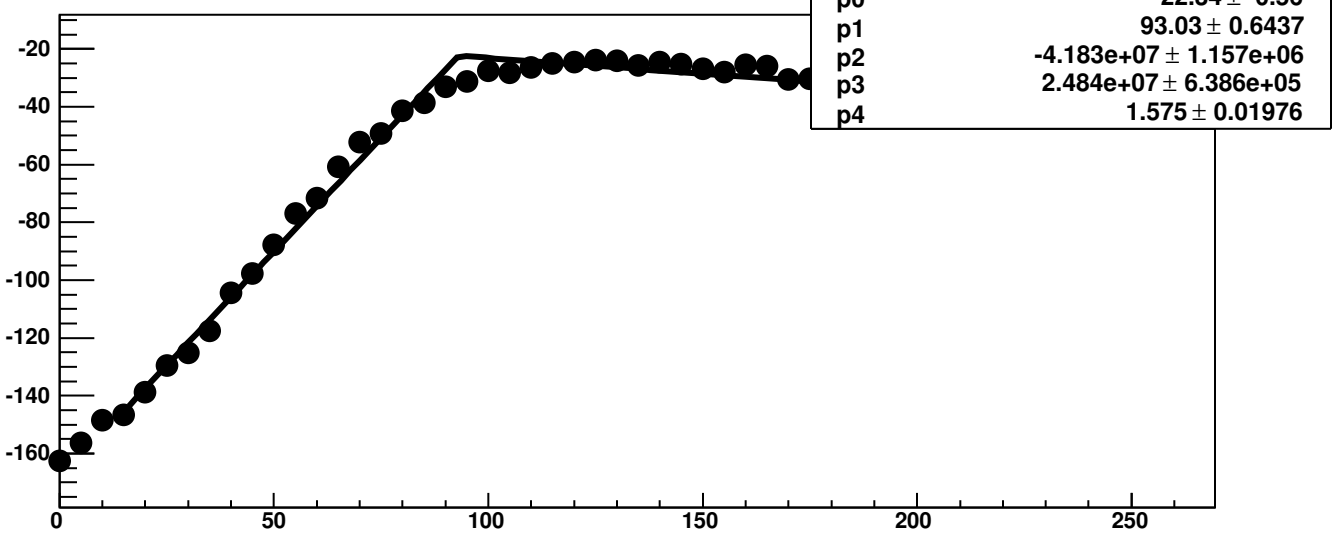
Chip 6, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



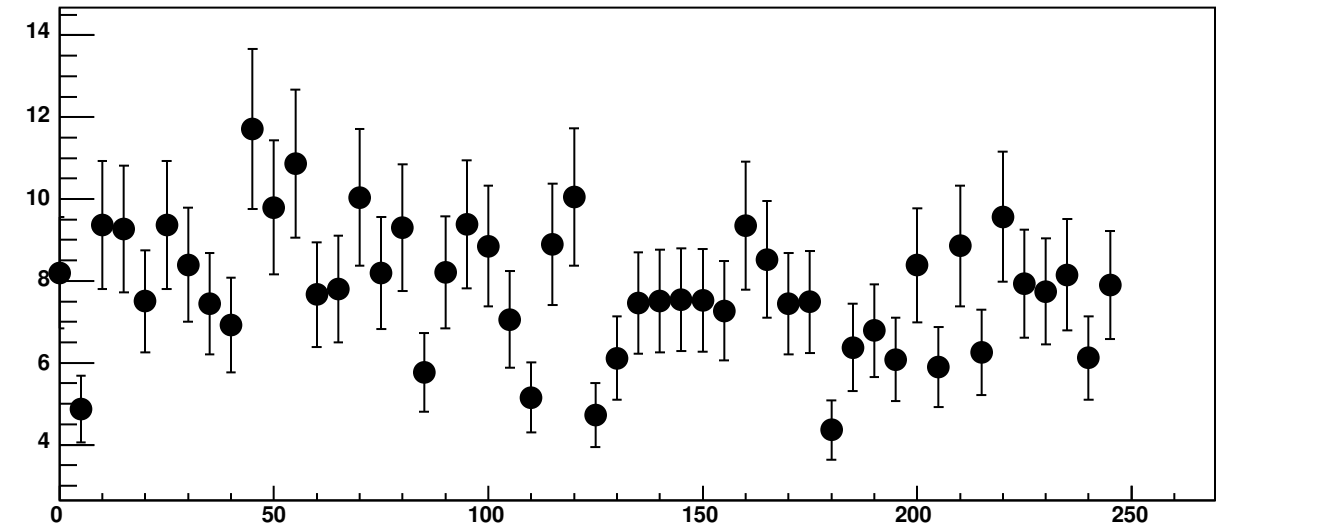
Chip 6, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold



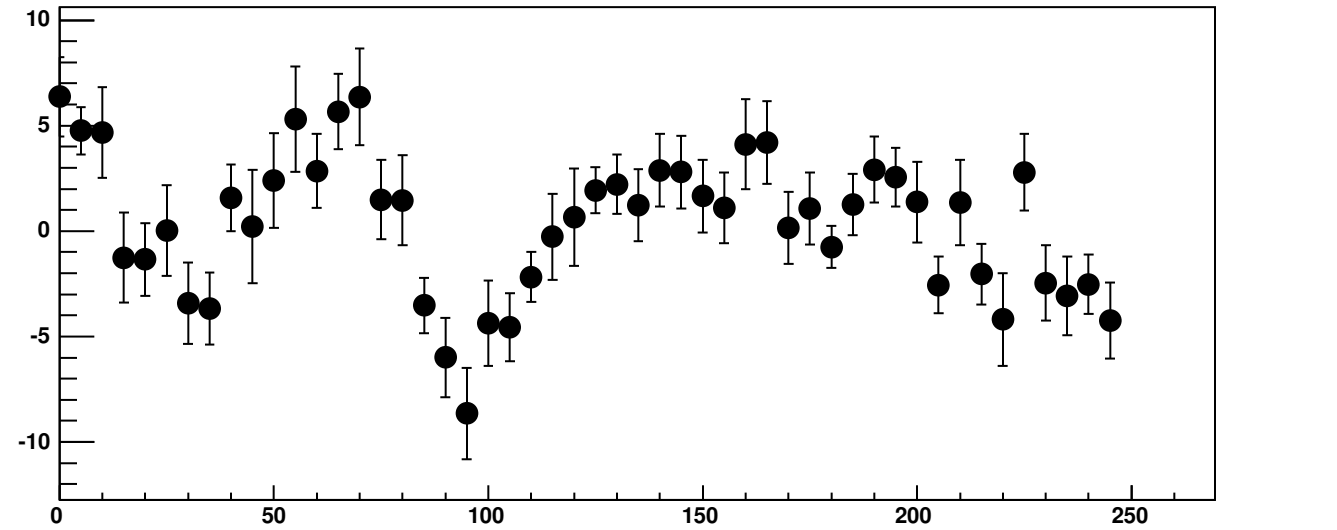
Chip 6, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold



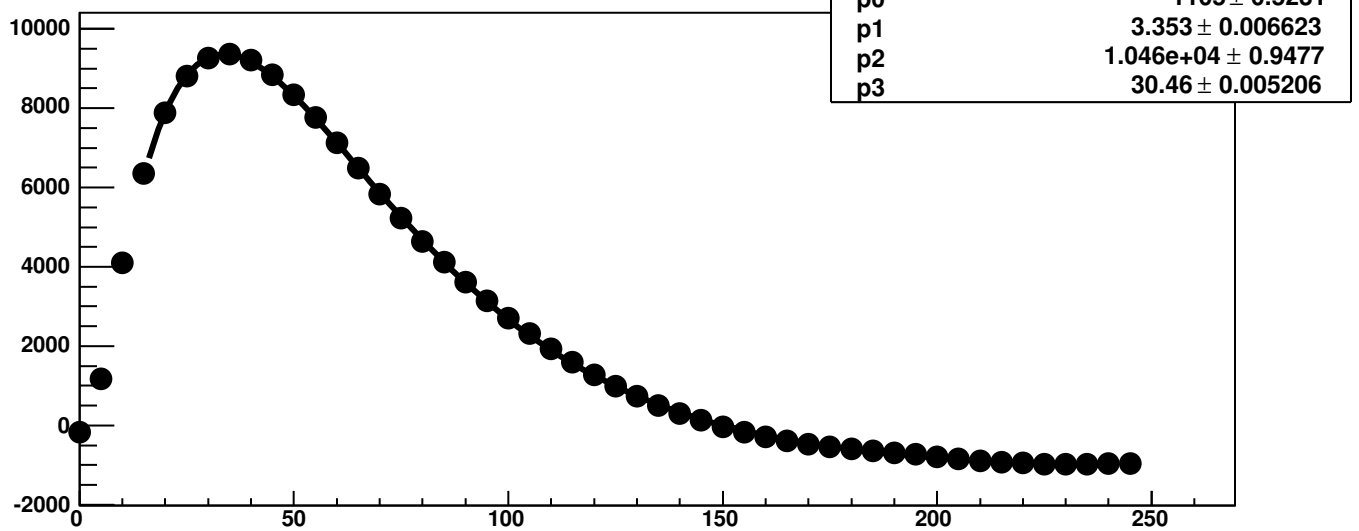
Chip 6, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



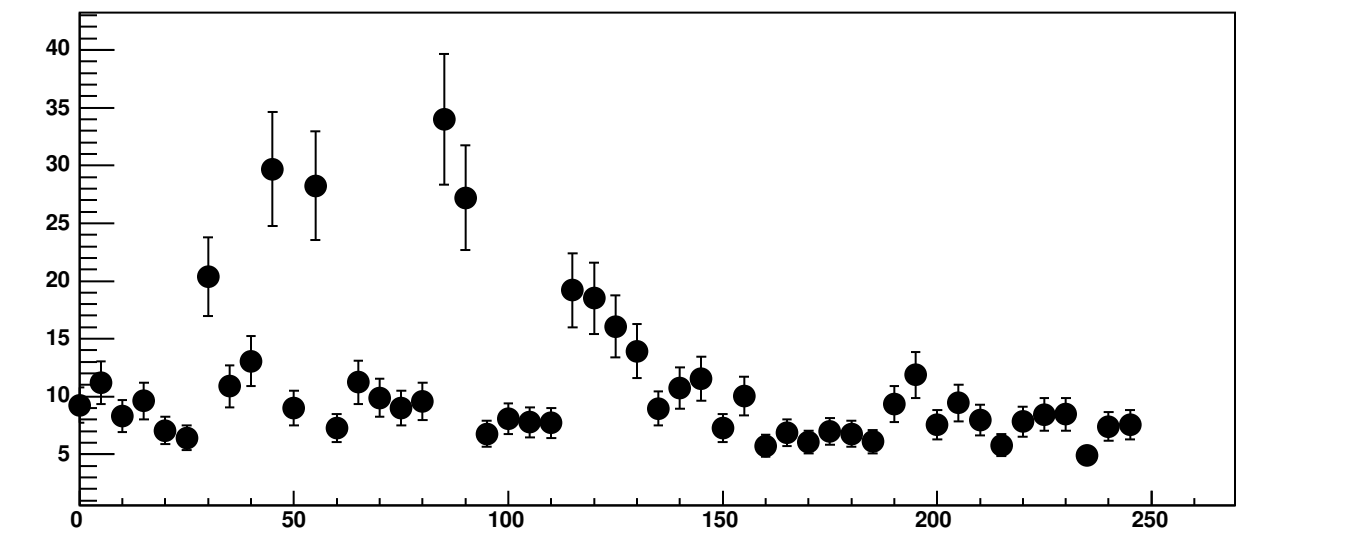
Chip 6, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold



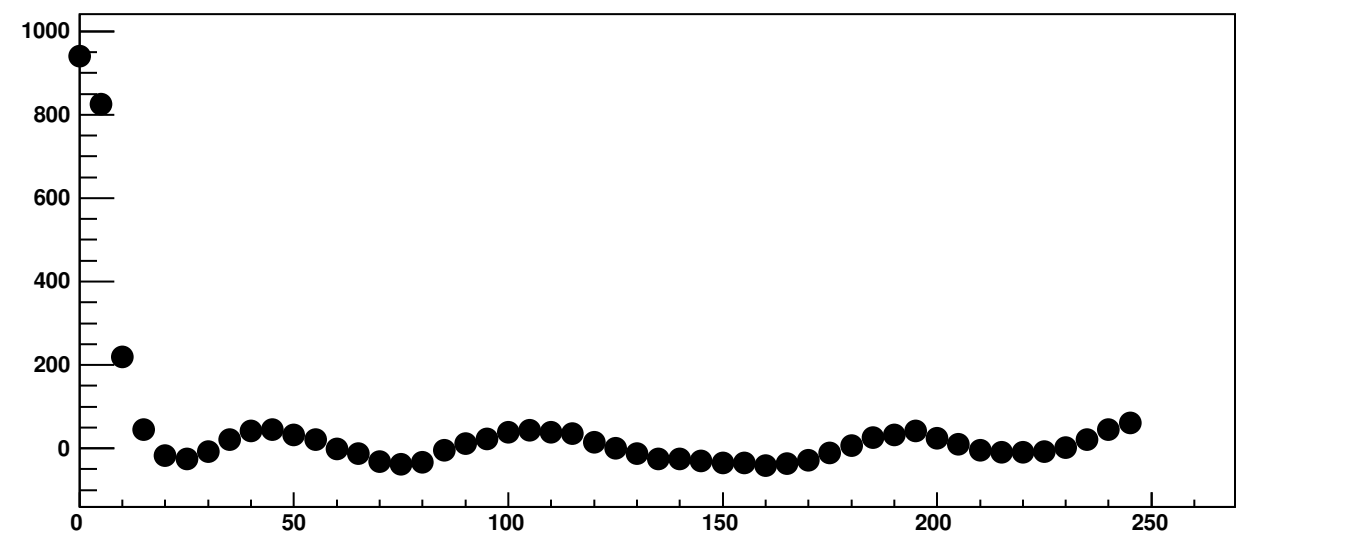
Chip 6, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold

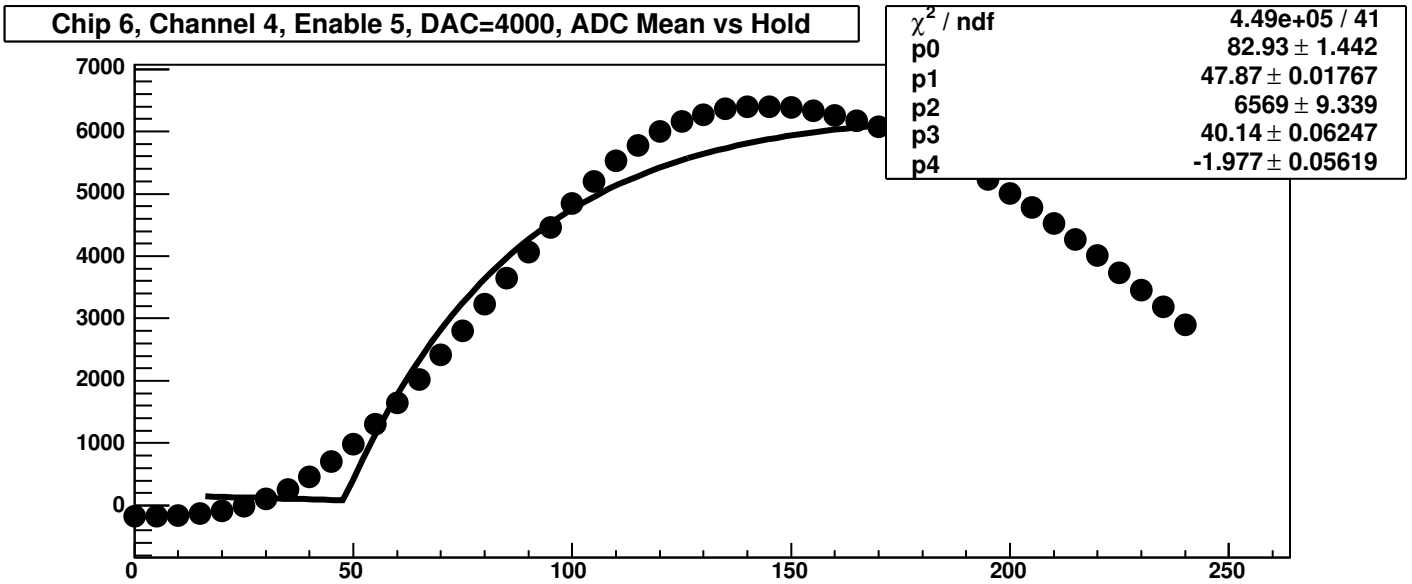


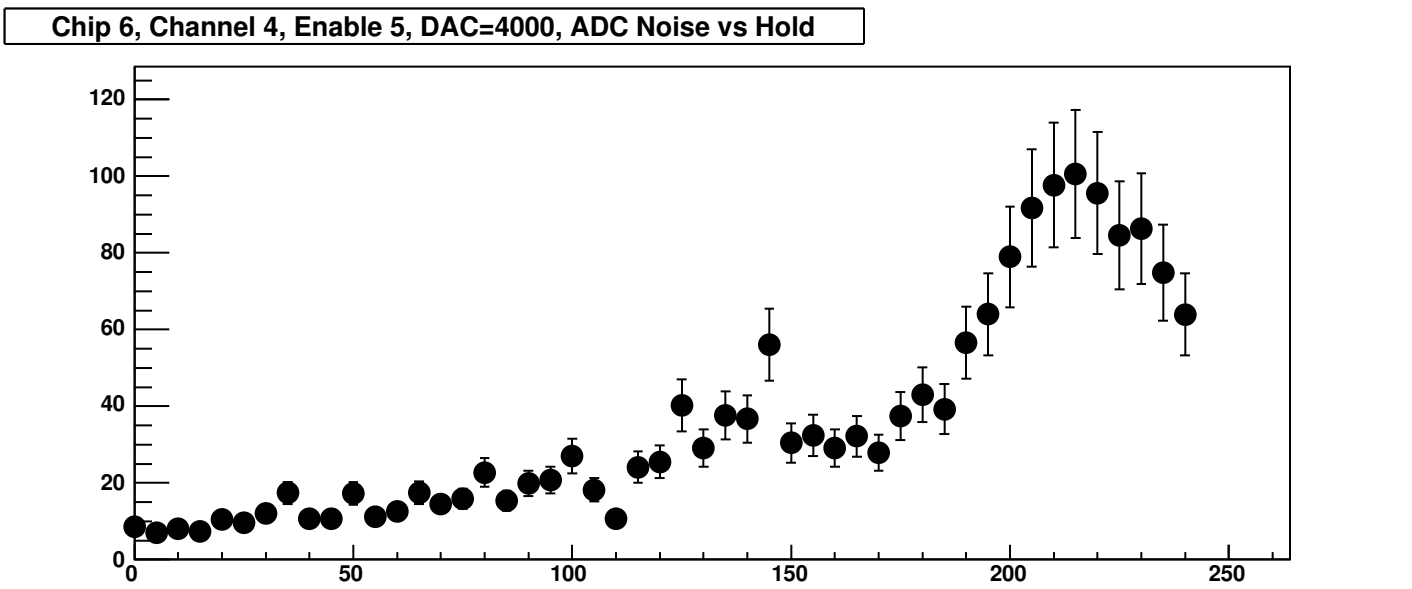
Chip 6, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold

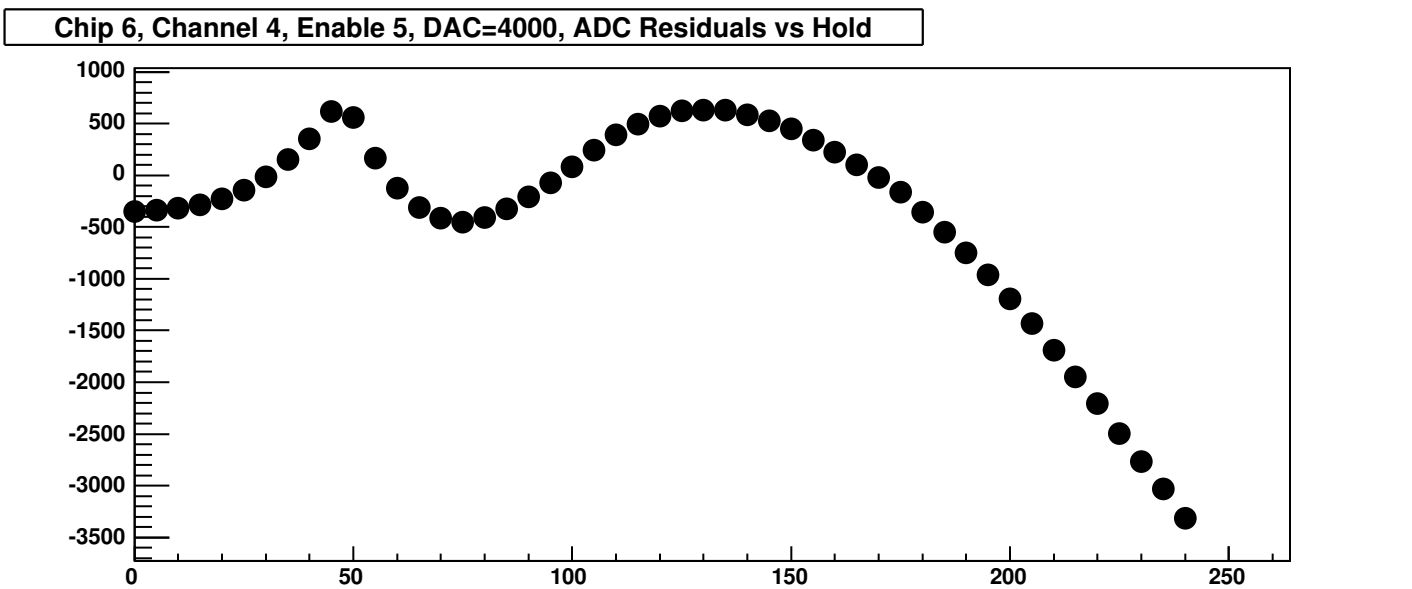


Chip 6, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold

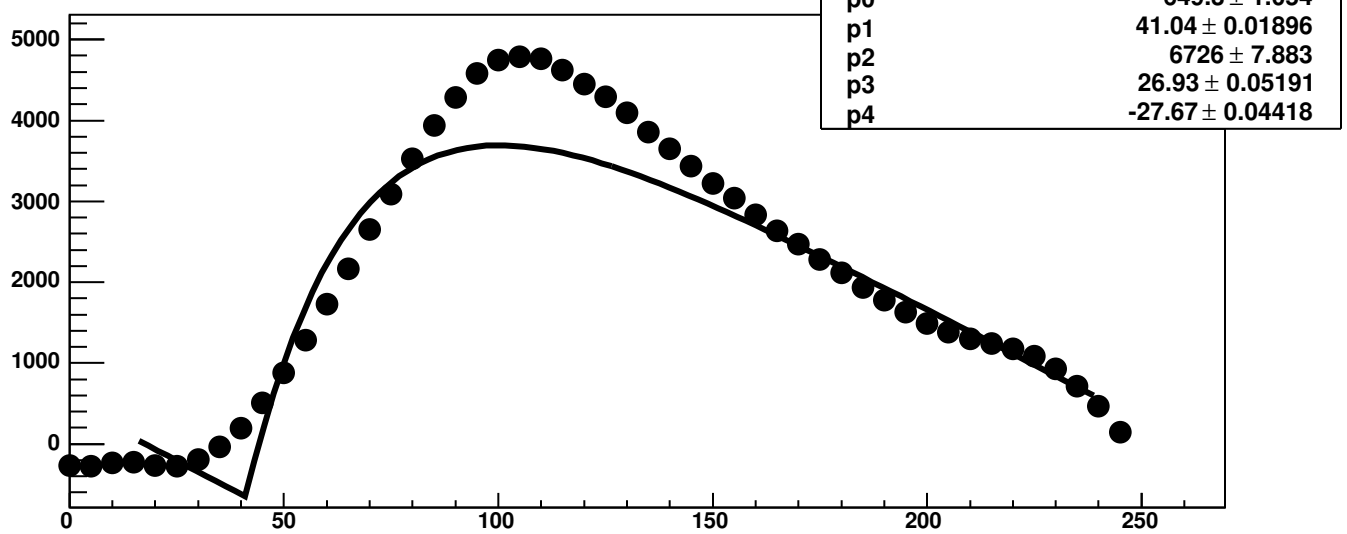



Chip 6, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.49e+05 / 41
p0 82.93 ± 1.442
p1 47.87 ± 0.01767
p2 6569 ± 9.339
p3 40.14 ± 0.06247
p4 -1.977 ± 0.05619
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

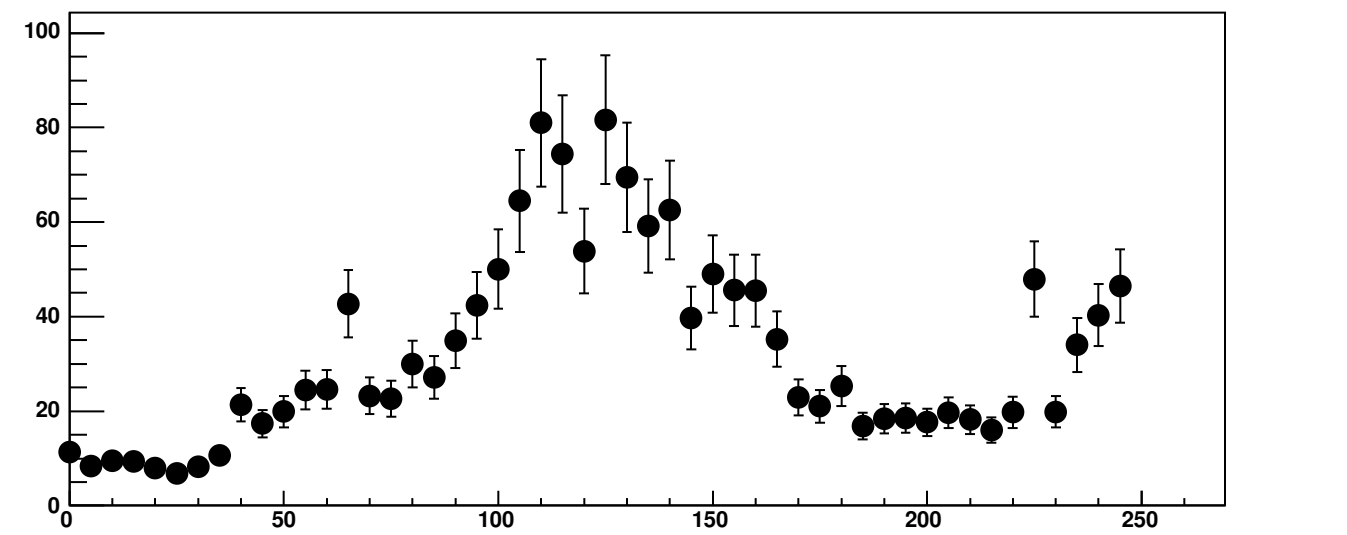

Chip 6, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 6, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
0
50
100
150
200
250

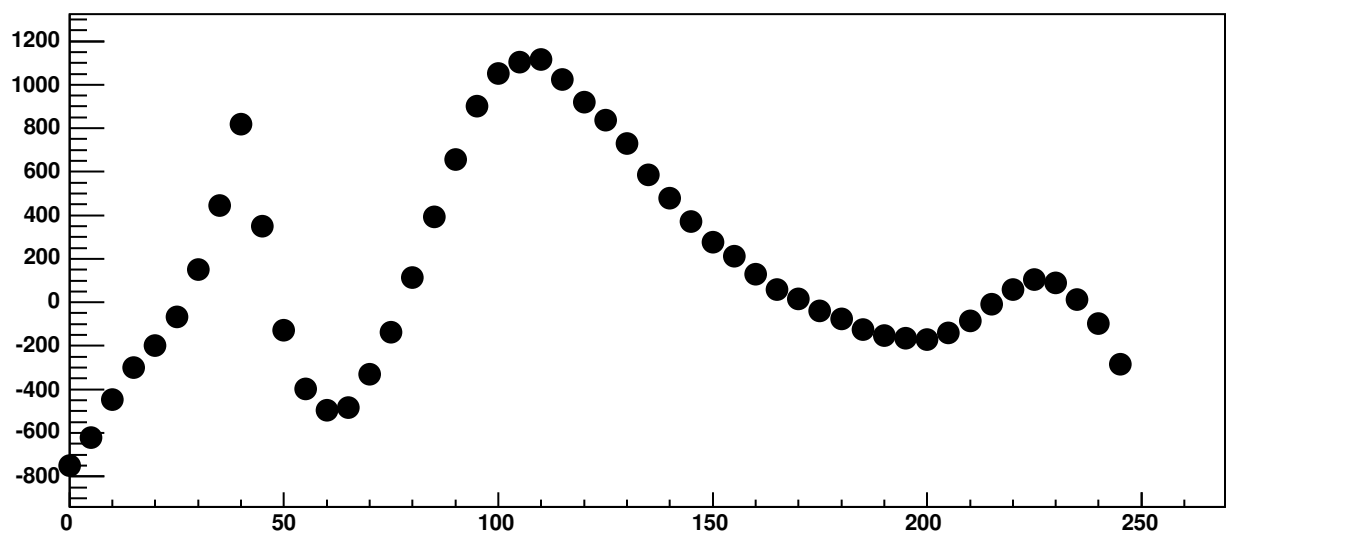
Chip 6, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold



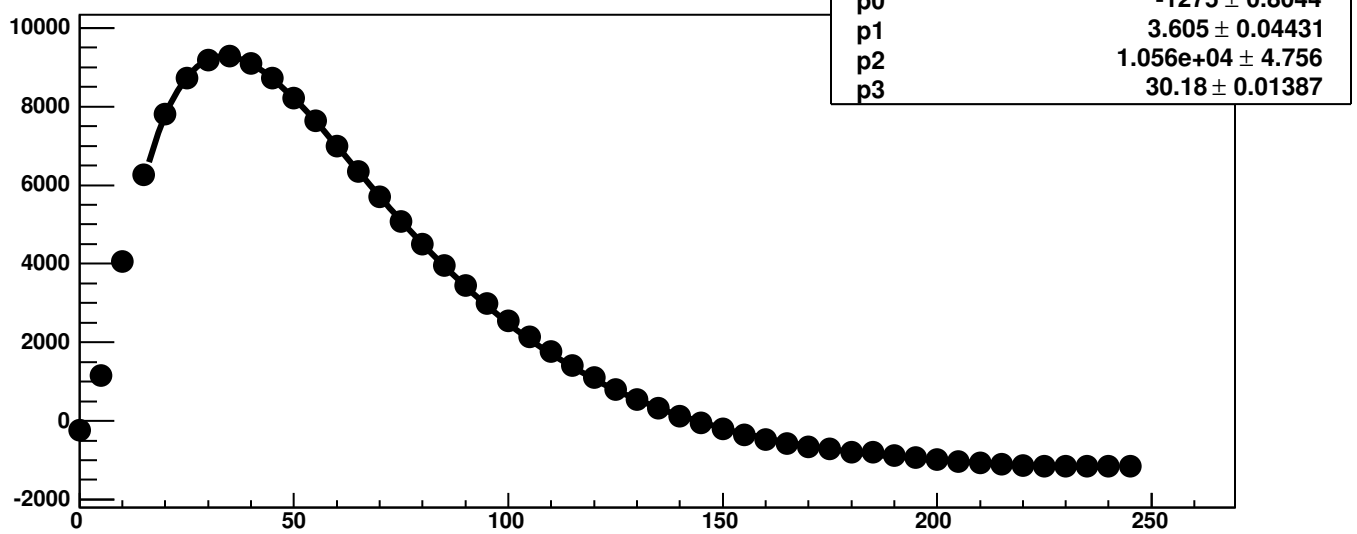
Chip 6, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



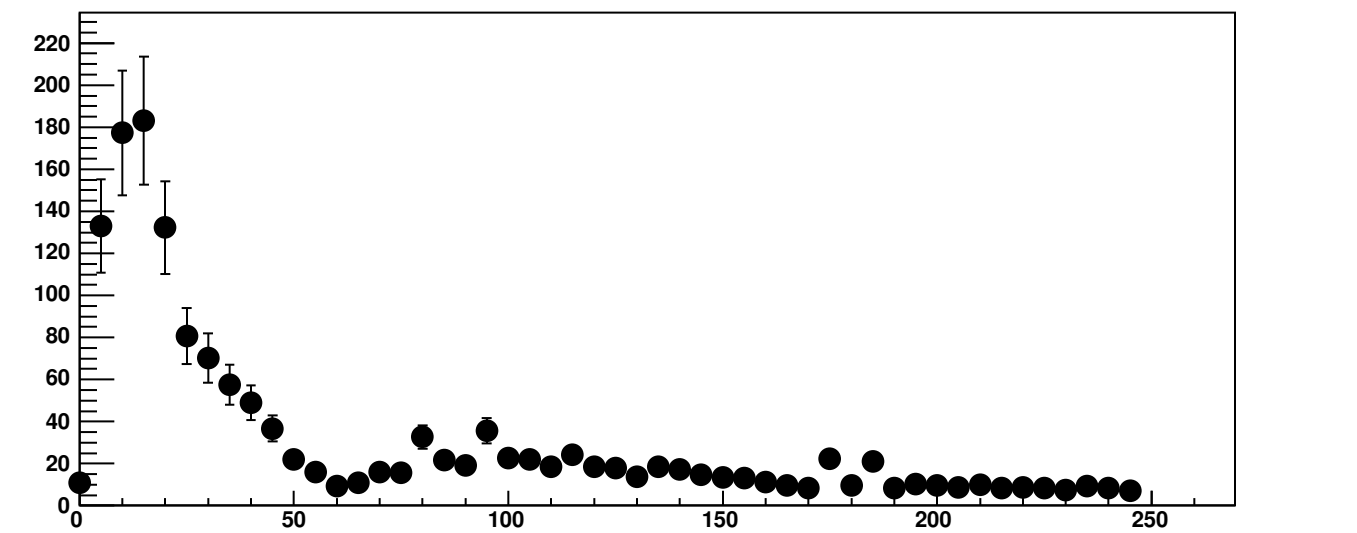
Chip 6, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold



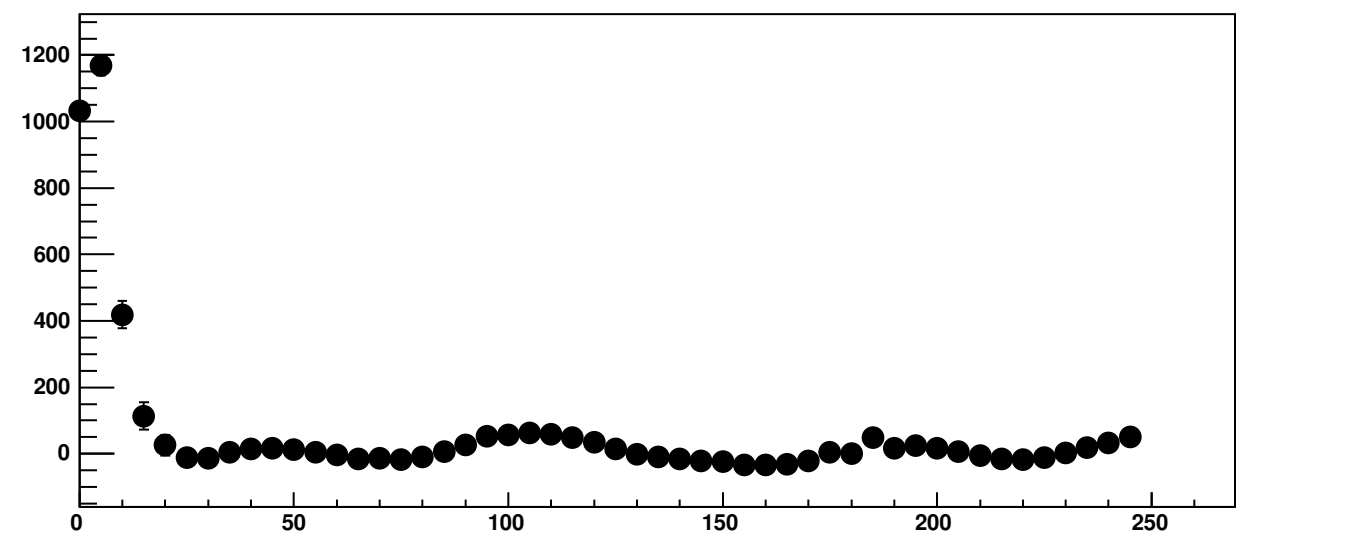
Chip 6, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold

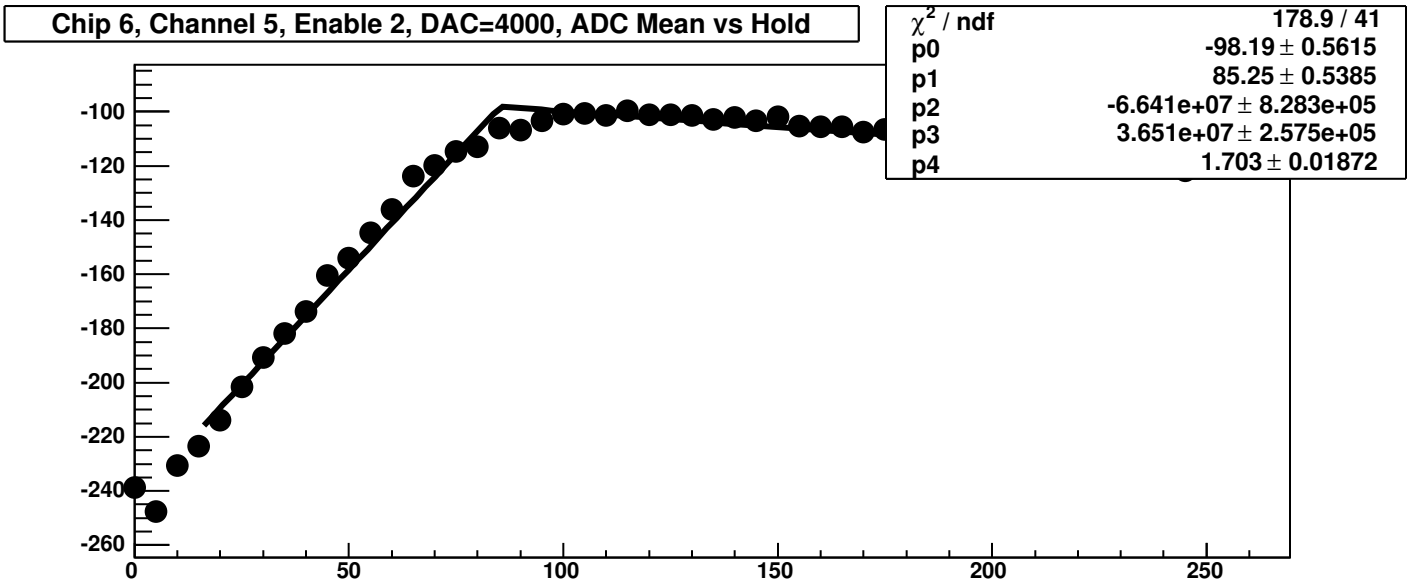


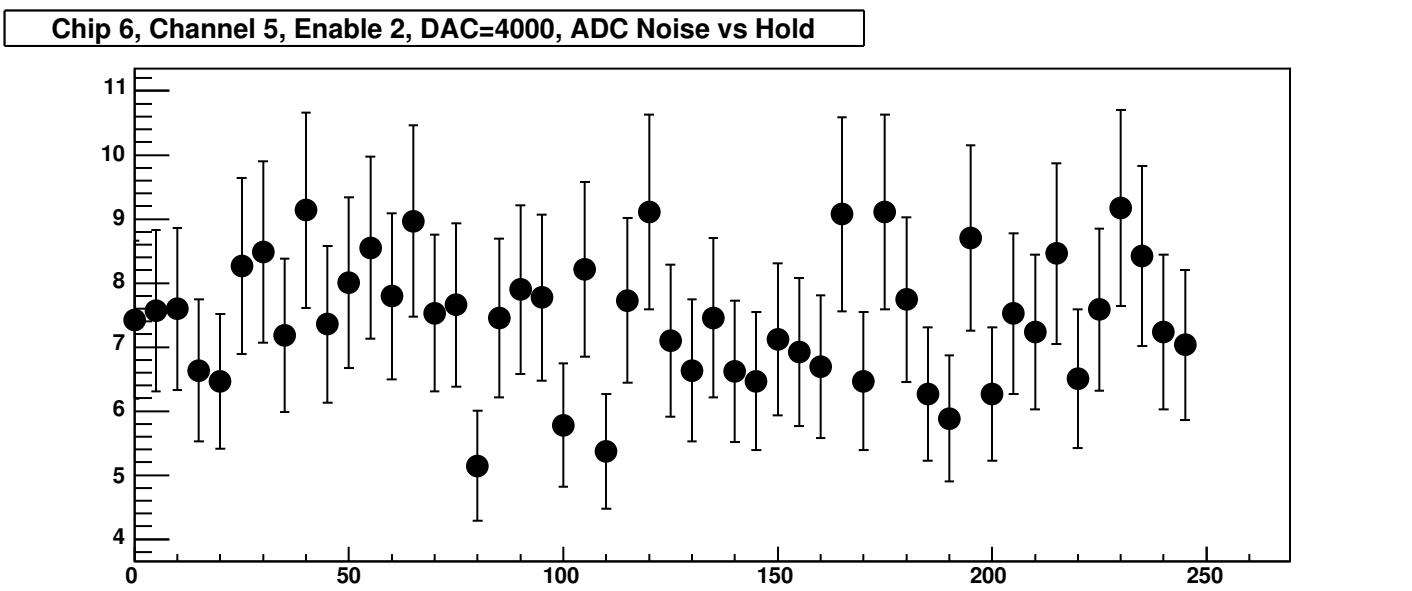
Chip 6, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold

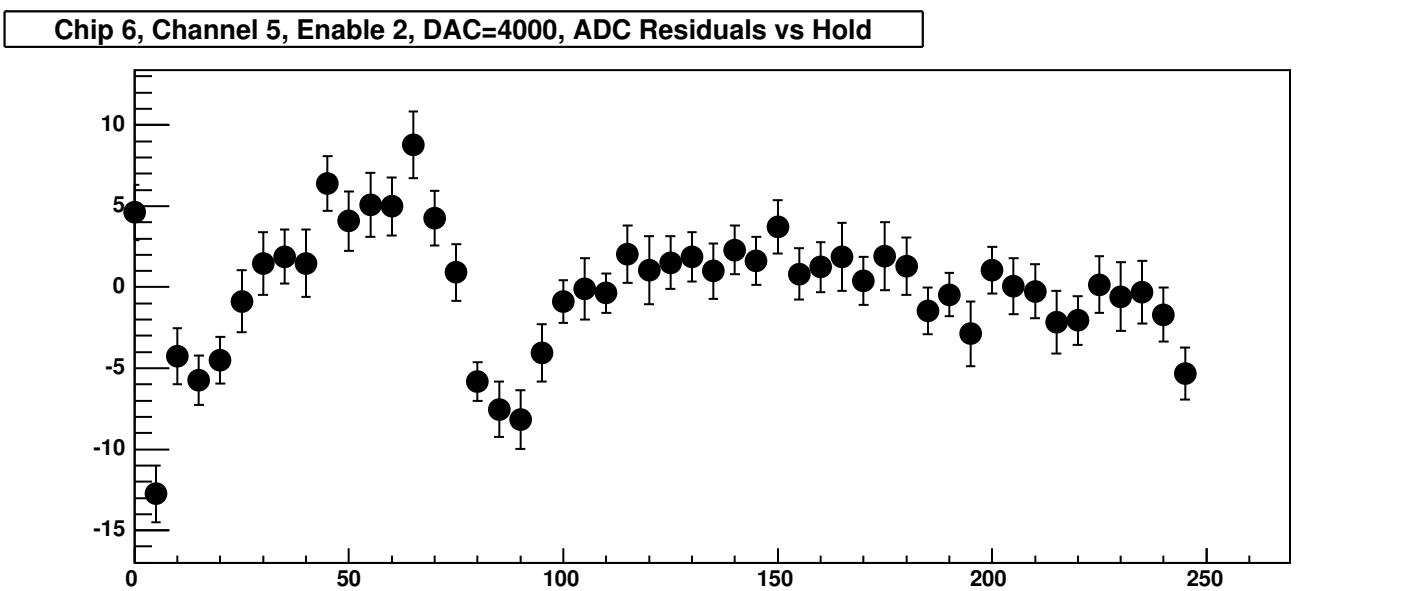


Chip 6, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold

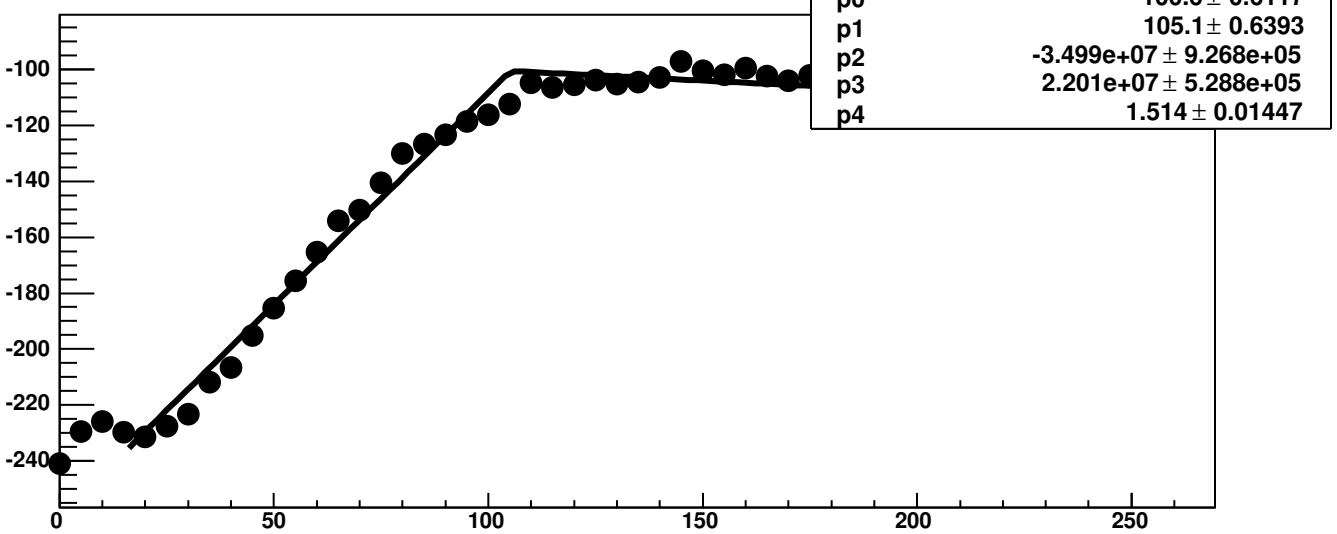



Chip 6, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 178.9 / 41
p0 -98.19 ± 0.5615
p1 85.25 ± 0.5385
p2 -6.641e+07 ± 8.283e+05
p3 3.651e+07 ± 2.575e+05
p4 1.703 ± 0.01872
-100
-120
-140
-160
-180
-200
-220
-240
-260
0
50
100
150
200
250

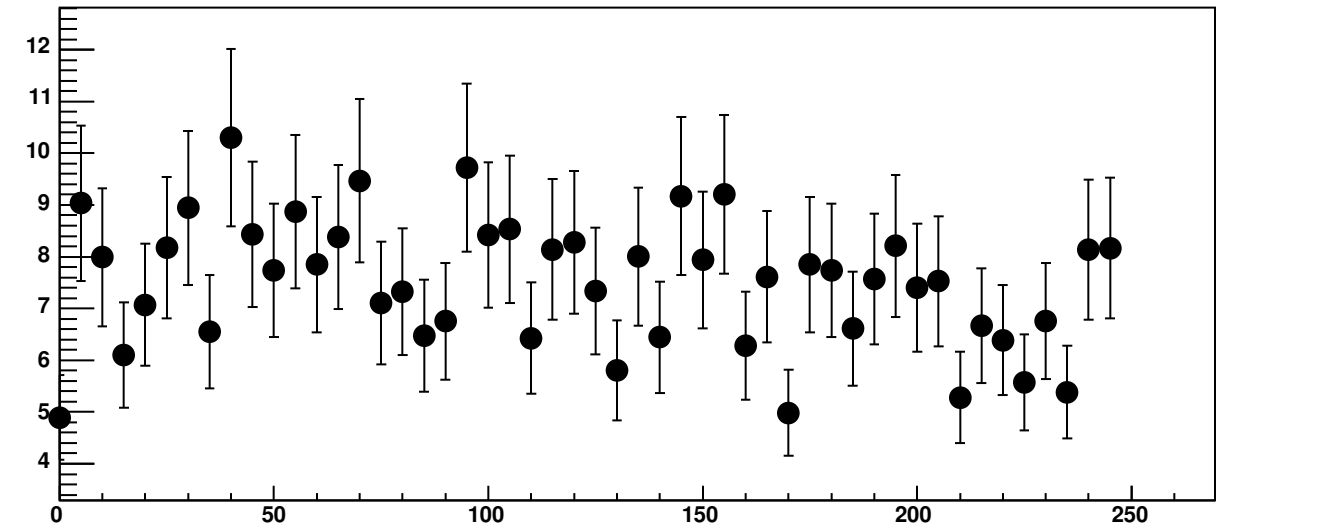

Chip 6, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 6, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
0
50
100
150
200
250

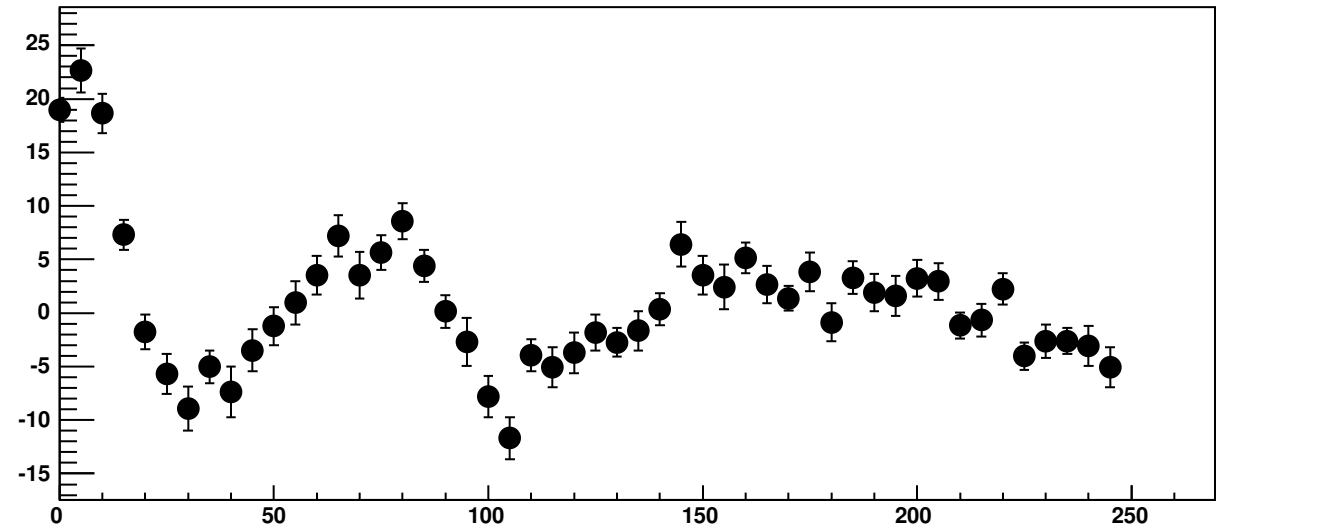
Chip 6, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold



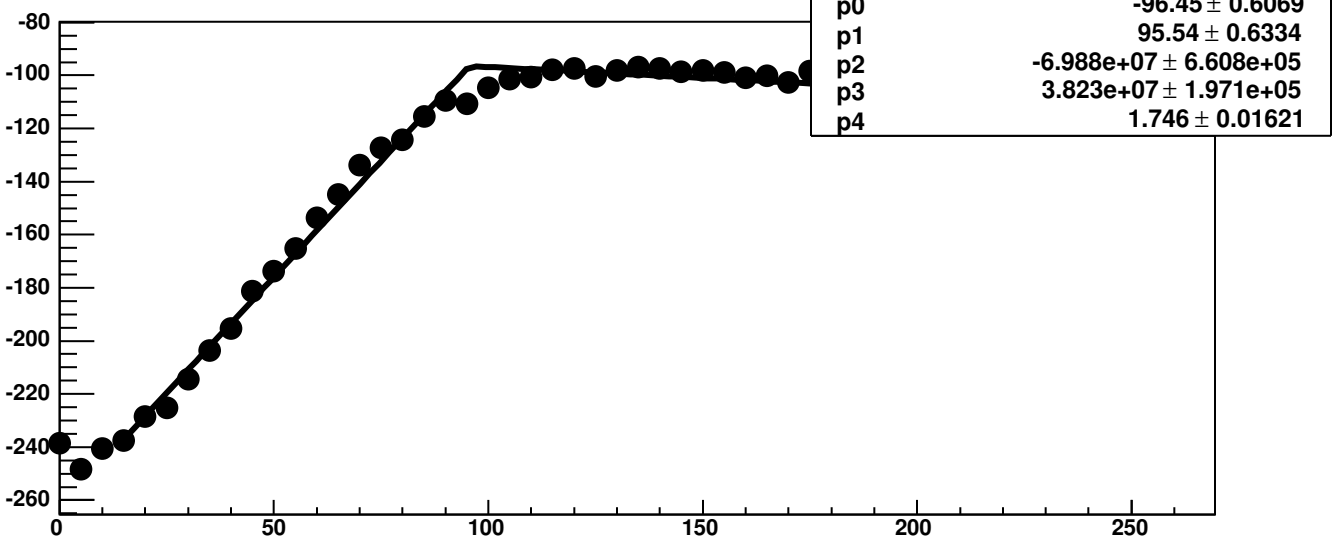
Chip 6, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



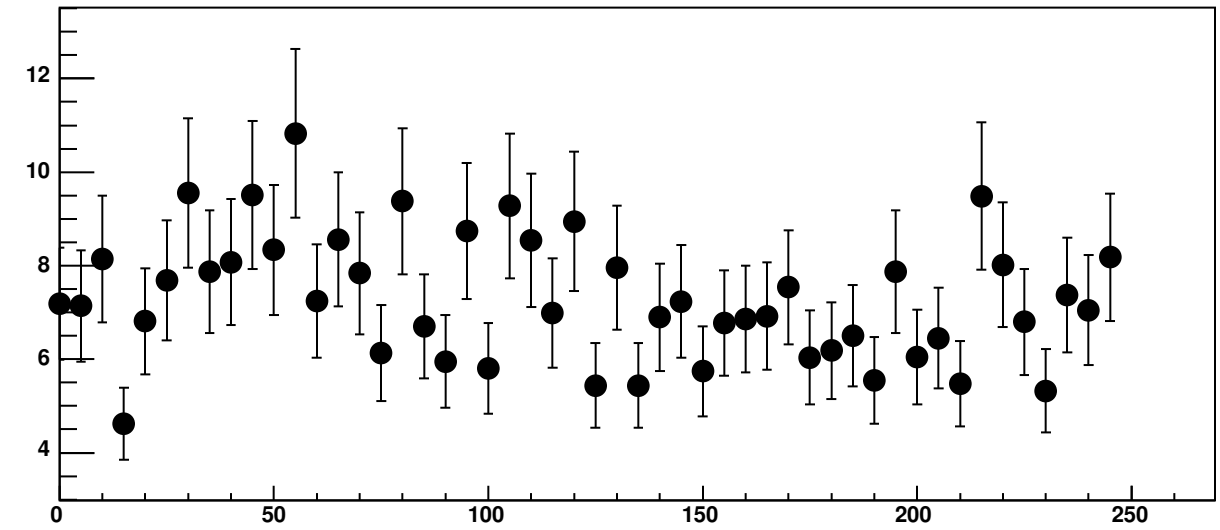
Chip 6, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold



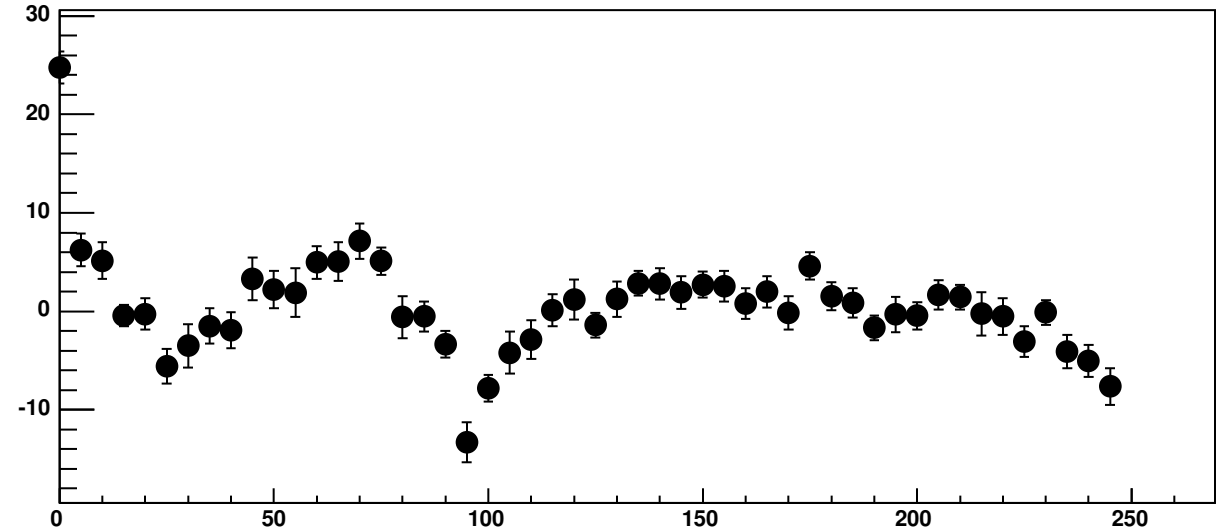
Chip 6, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold



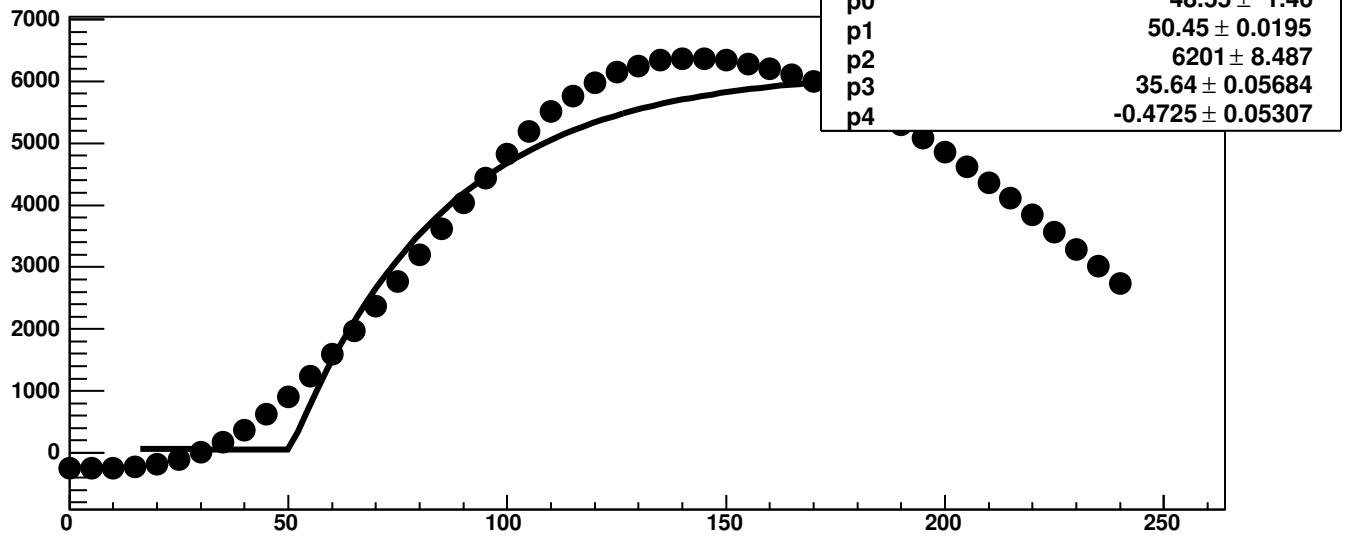
Chip 6, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold



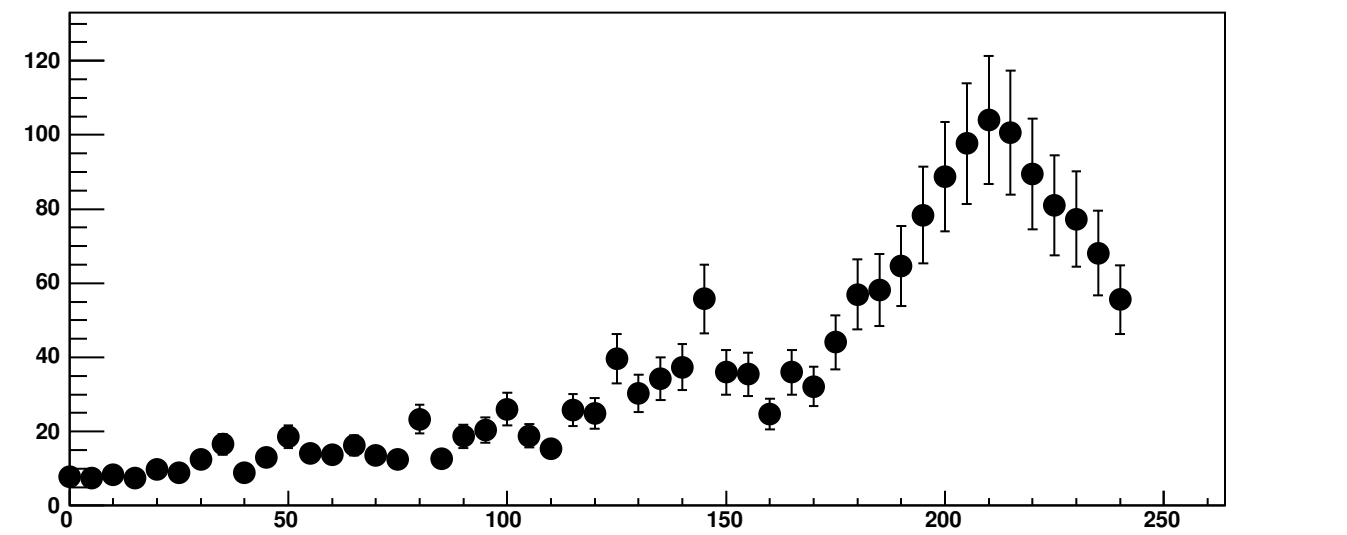
Chip 6, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold



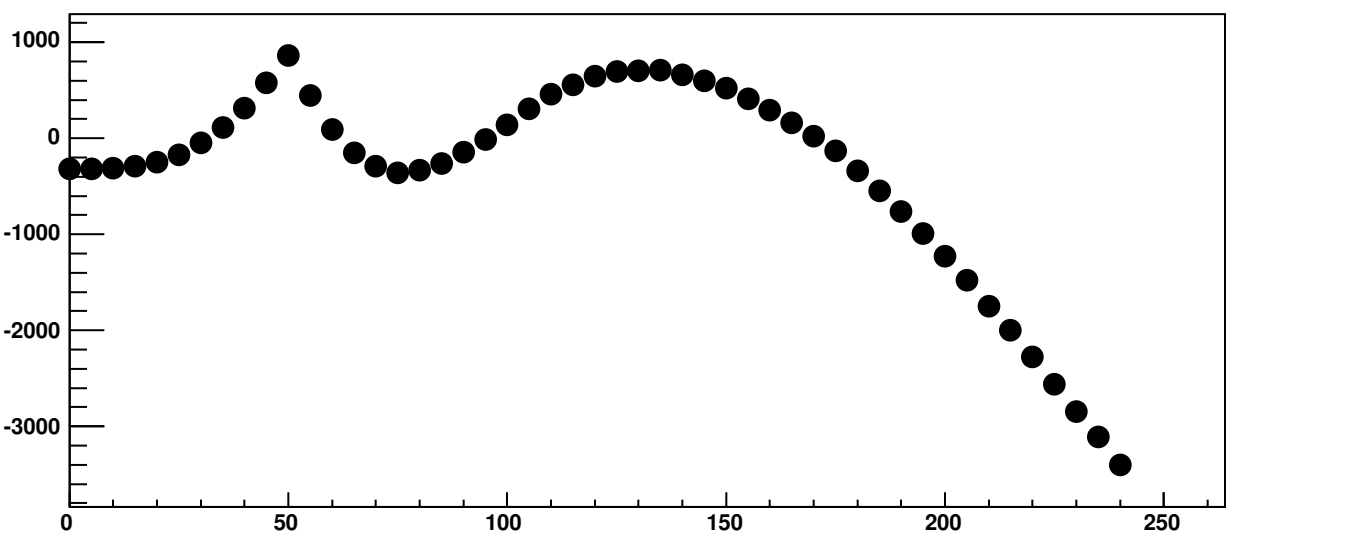
Chip 6, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold



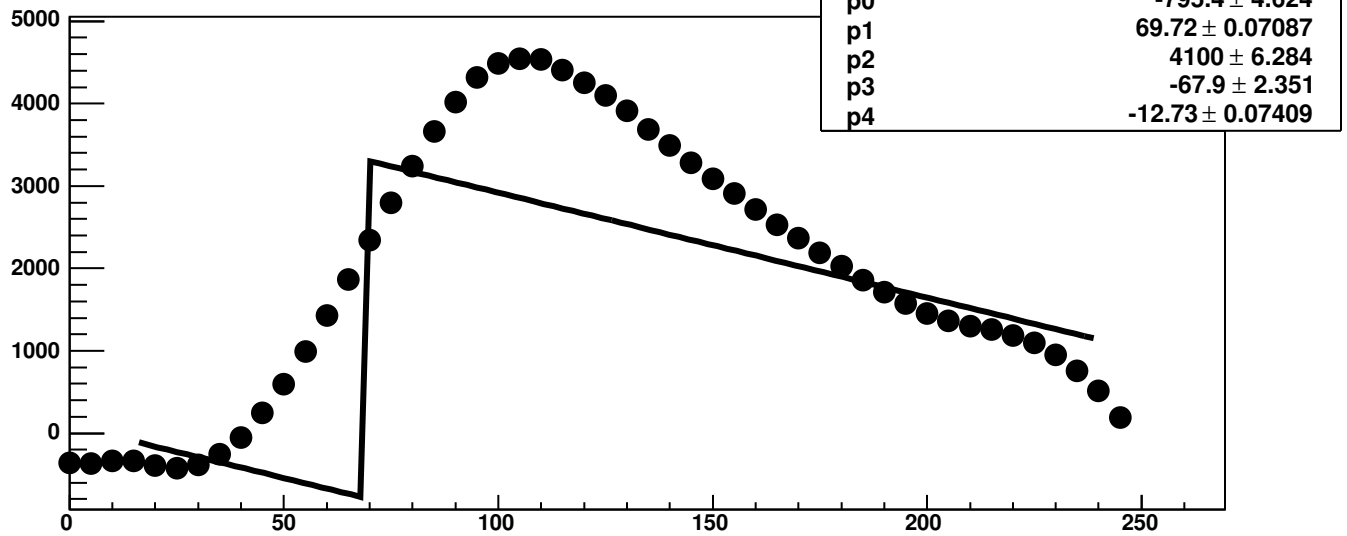
Chip 6, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



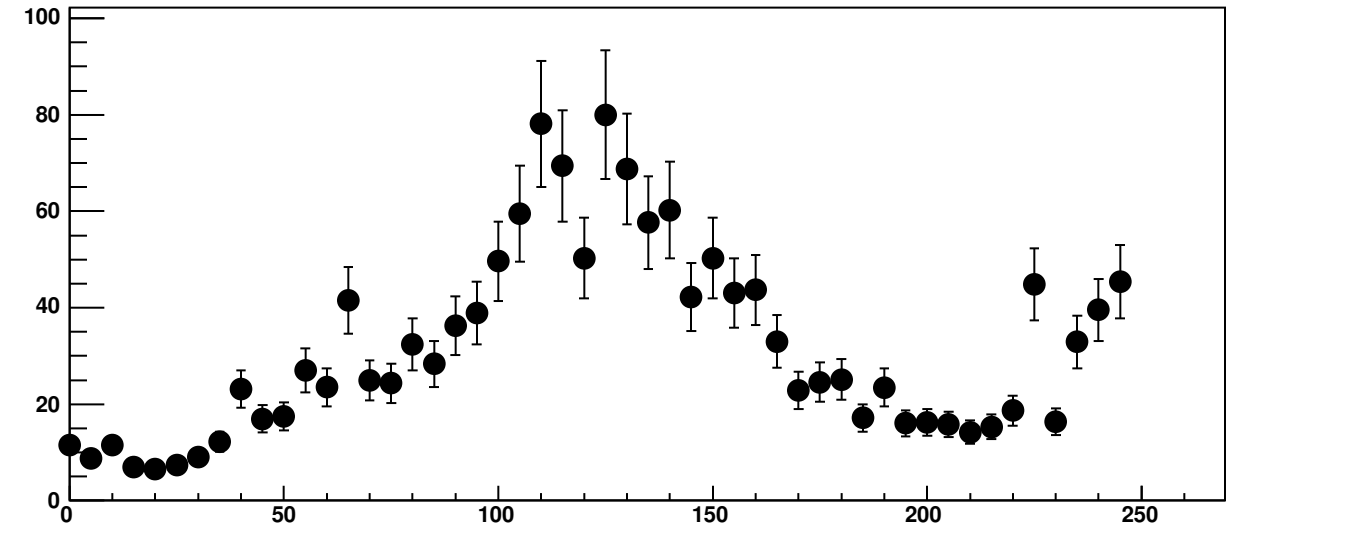
Chip 6, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold



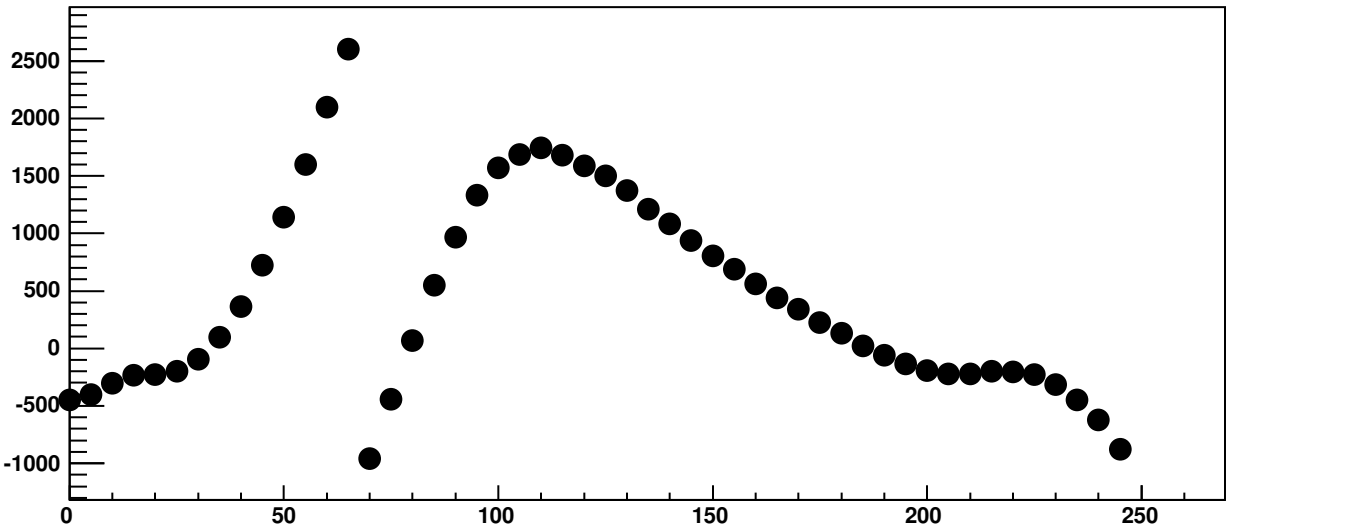
Chip 6, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold



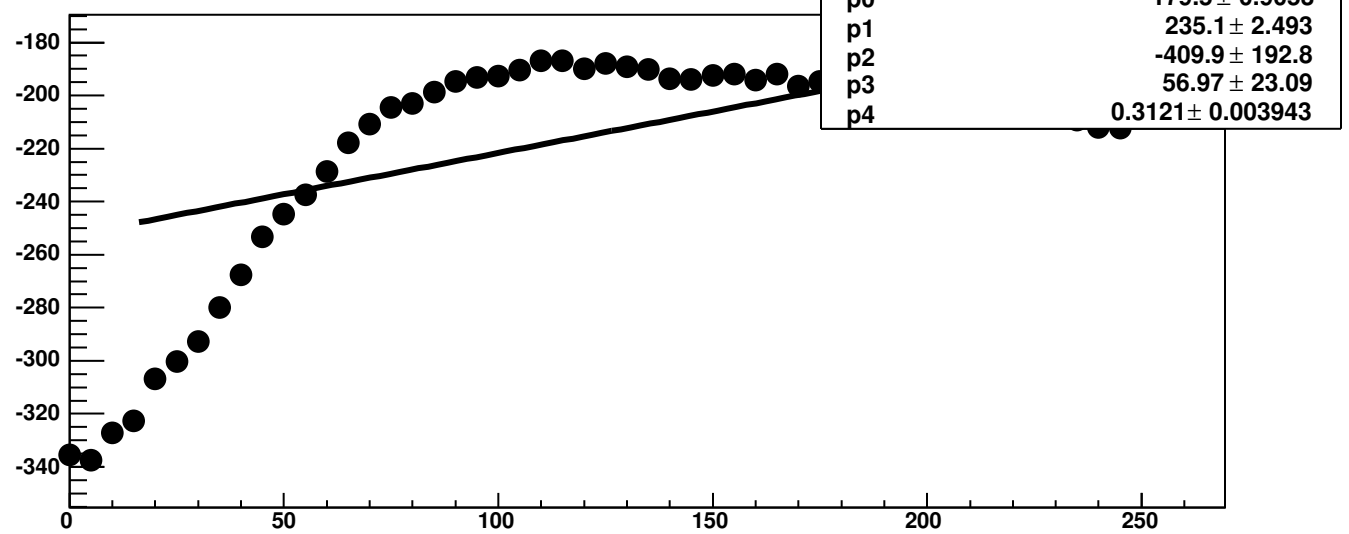
Chip 6, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



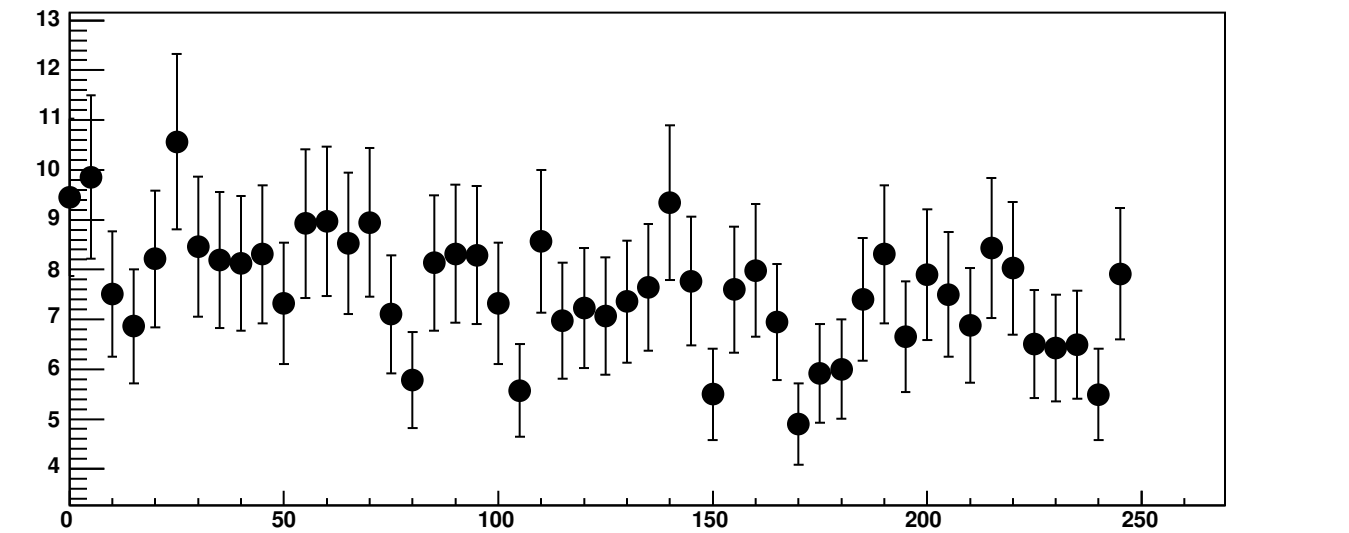
Chip 6, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold



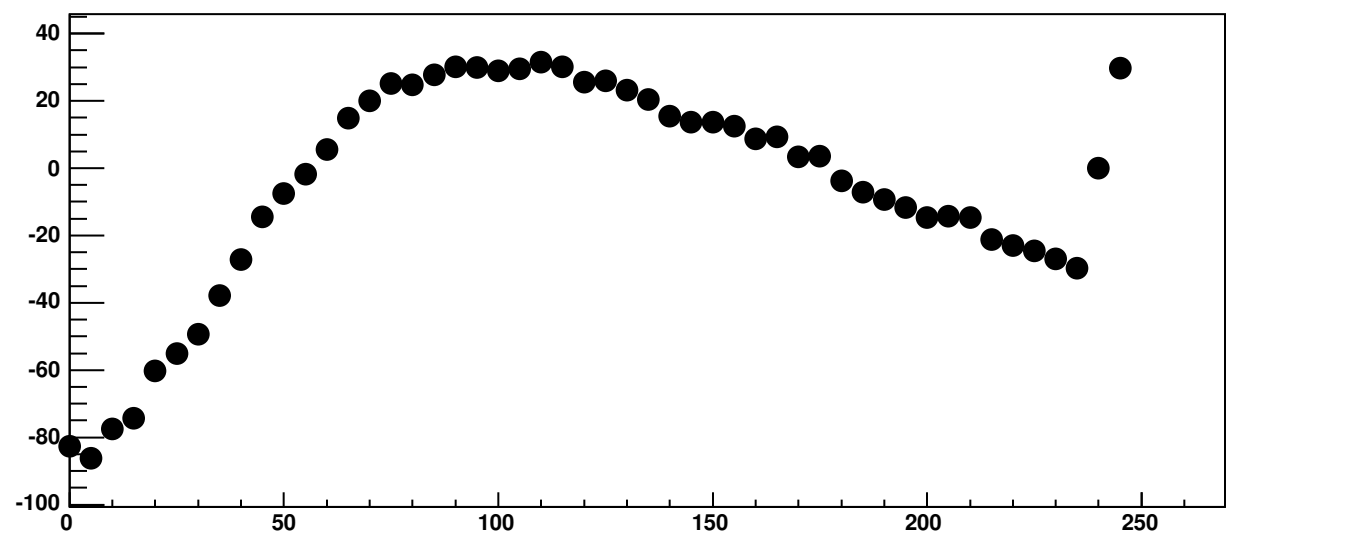
Chip 6, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 6, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold

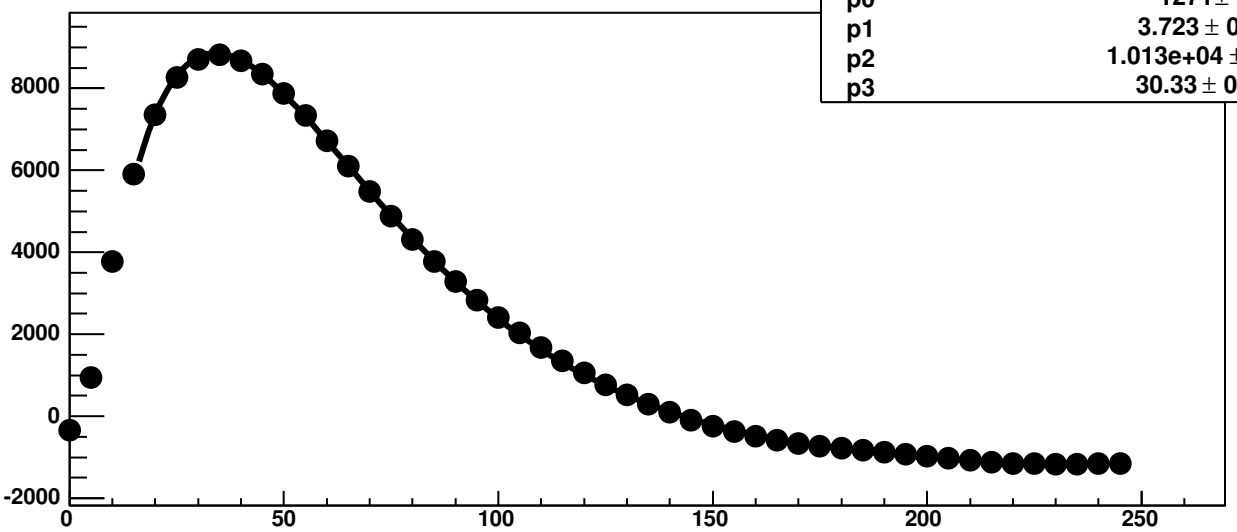


Chip 6, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold

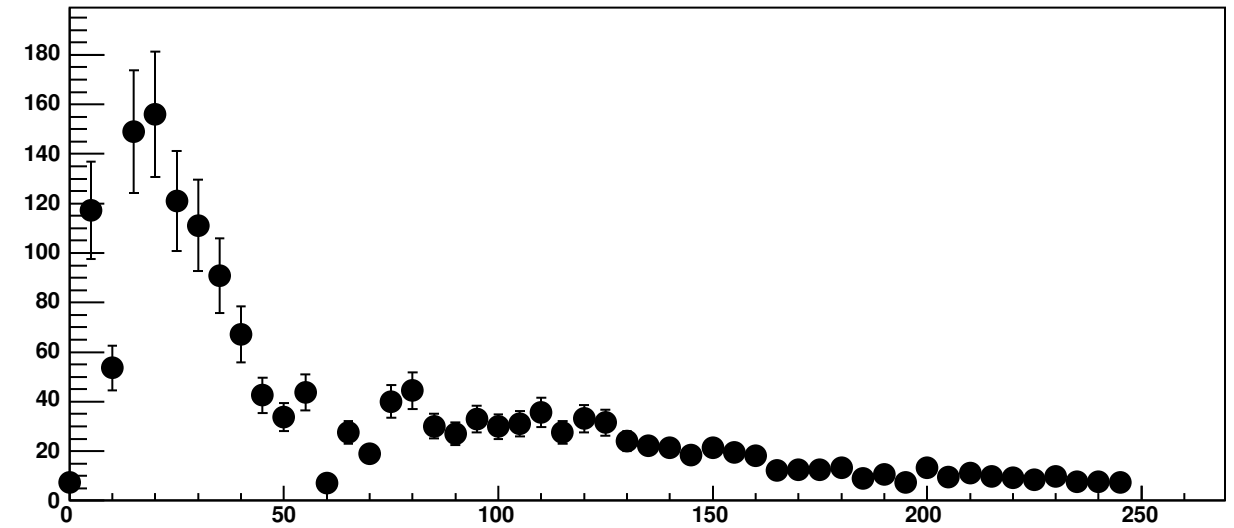


Chip 6, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold

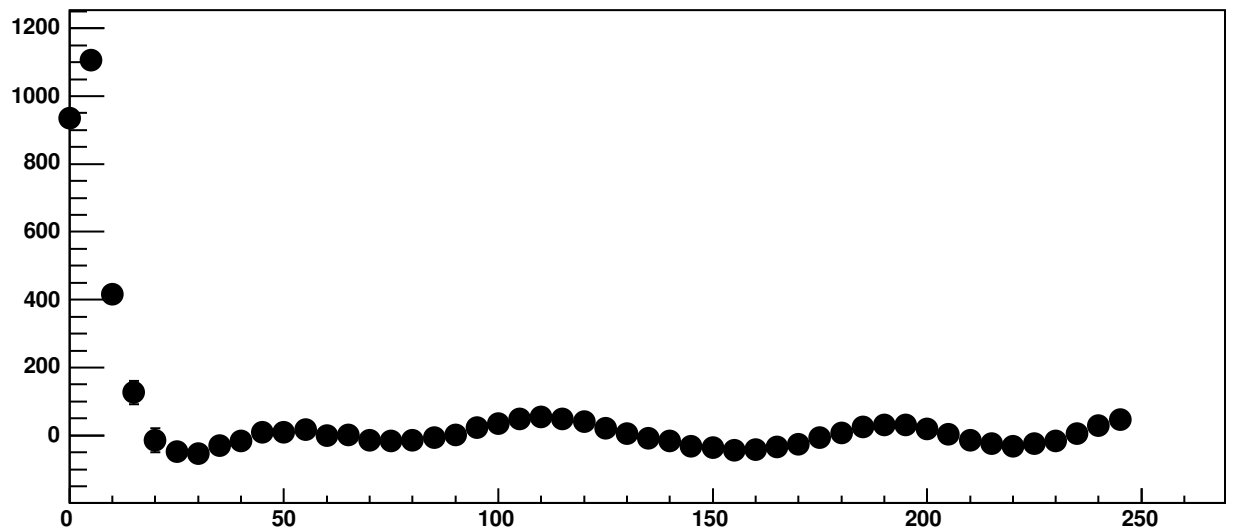
χ^2 / ndf	2368 / 42
p0	-1271 ± 0.8754
p1	3.723 ± 0.05181
p2	1.013e+04 ± 5.517
p3	30.33 ± 0.01739

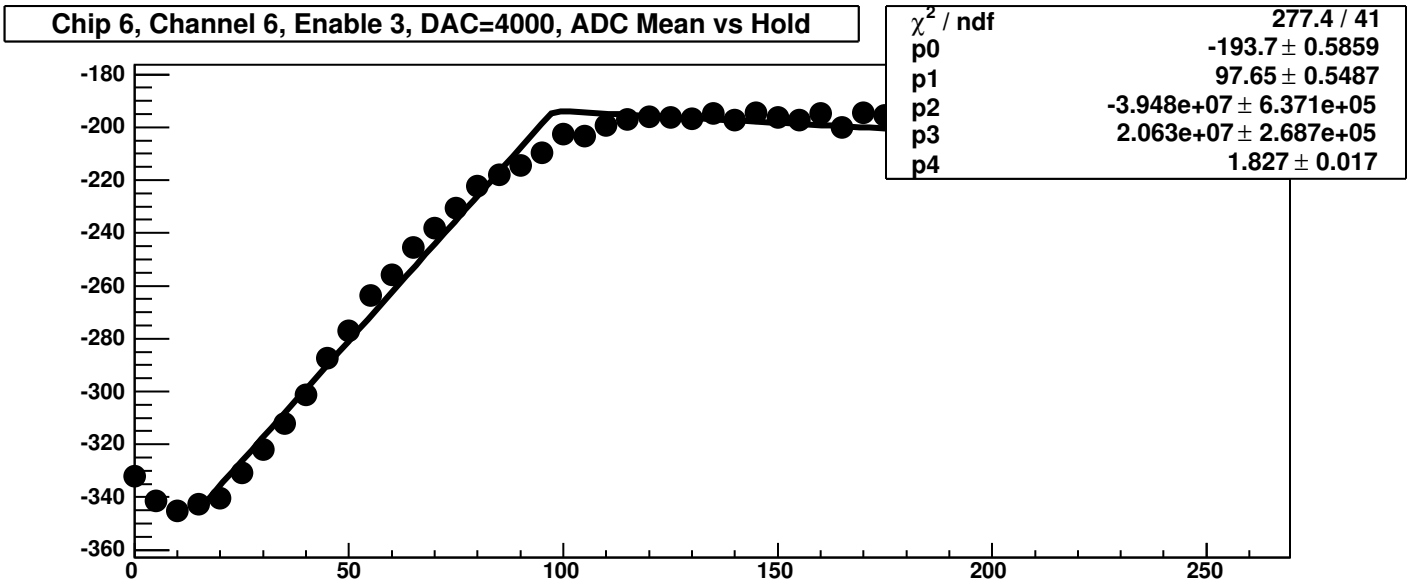


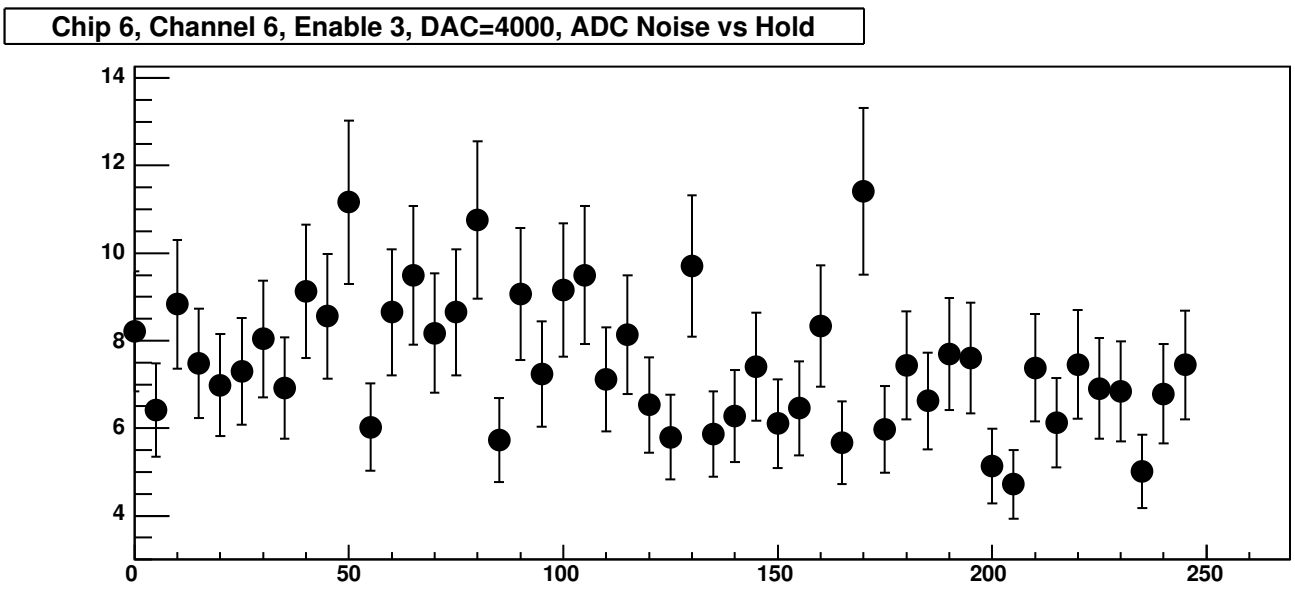
Chip 6, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold

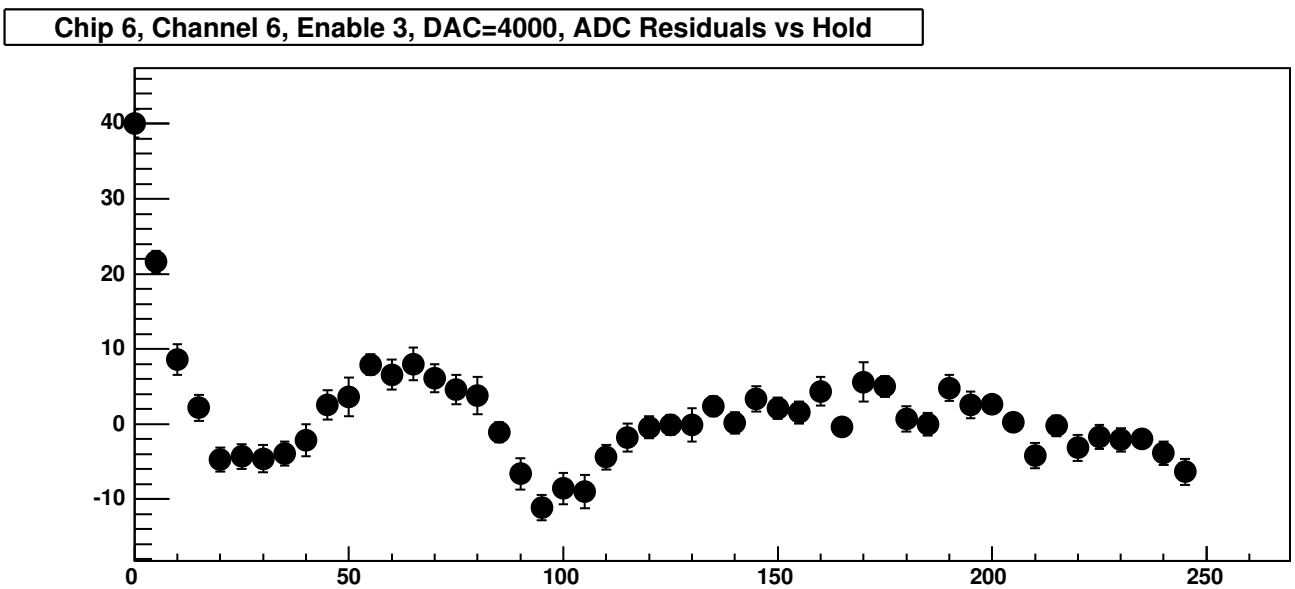


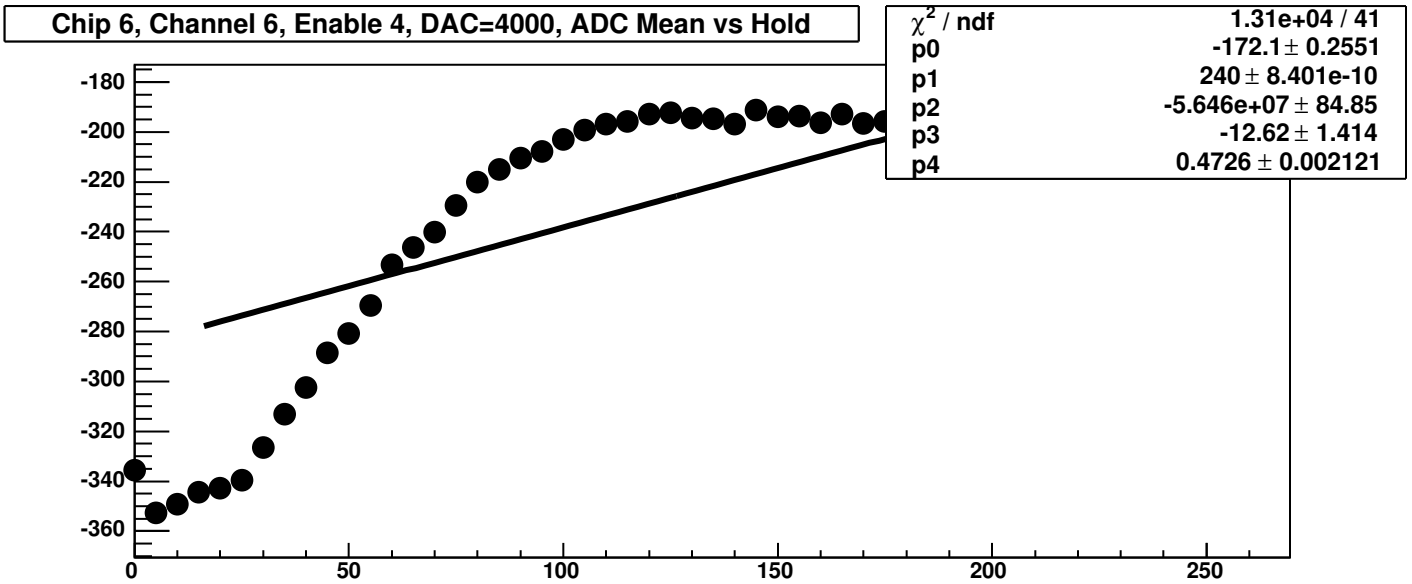
Chip 6, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold

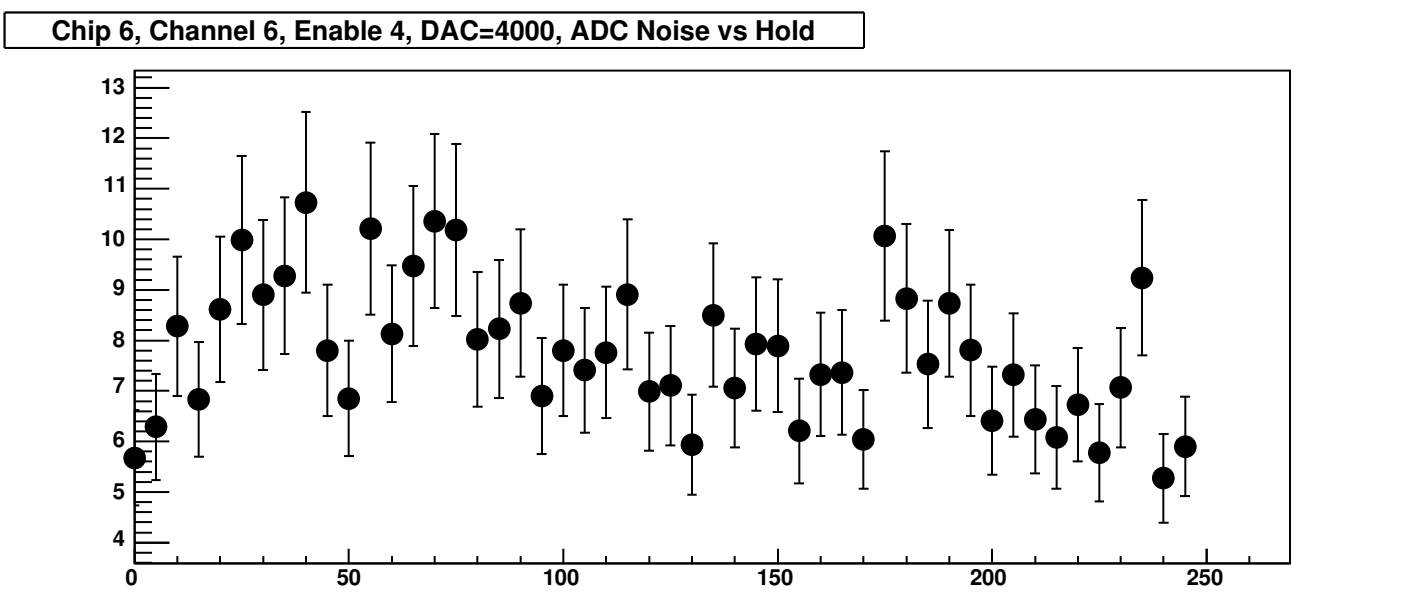


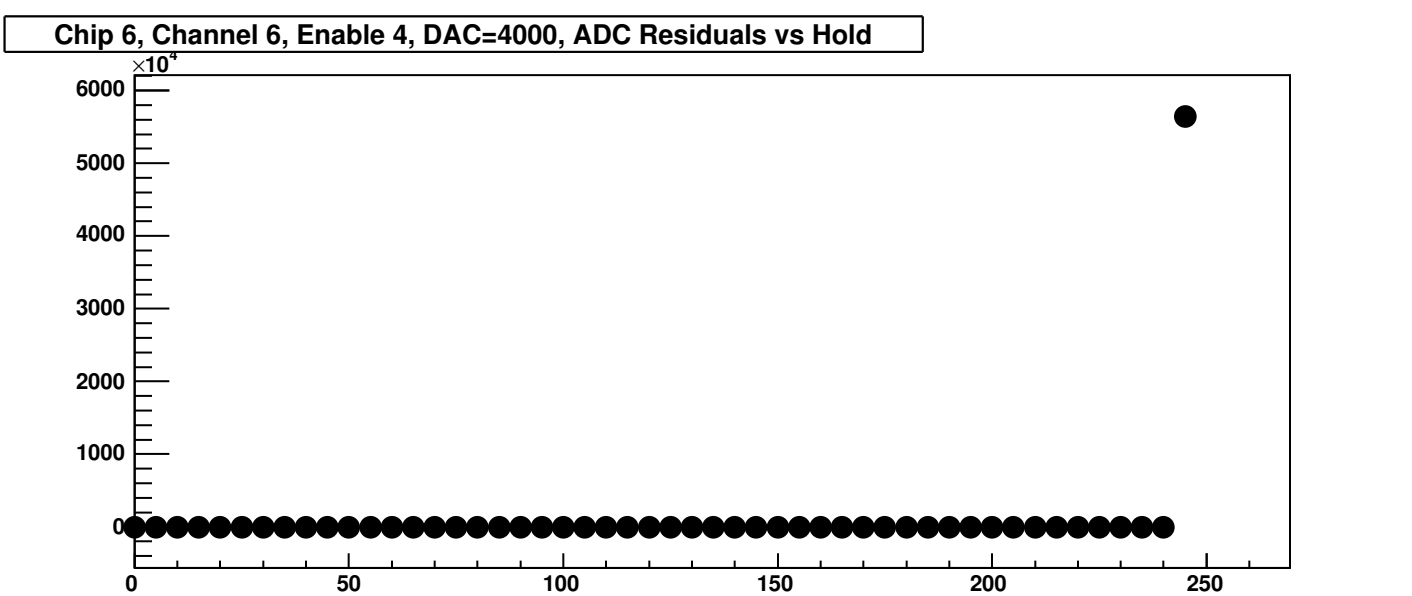

Chip 6, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 277.4 / 41
p0 -193.7 ± 0.5859
p1 97.65 ± 0.5487
p2 -3.948e+07 ± 6.371e+05
p3 2.063e+07 ± 2.687e+05
p4 1.827 ± 0.017
-180
-200
-220
-240
-260
-280
-300
-320
-340
-360
0
50
100
150
200
250


Chip 6, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

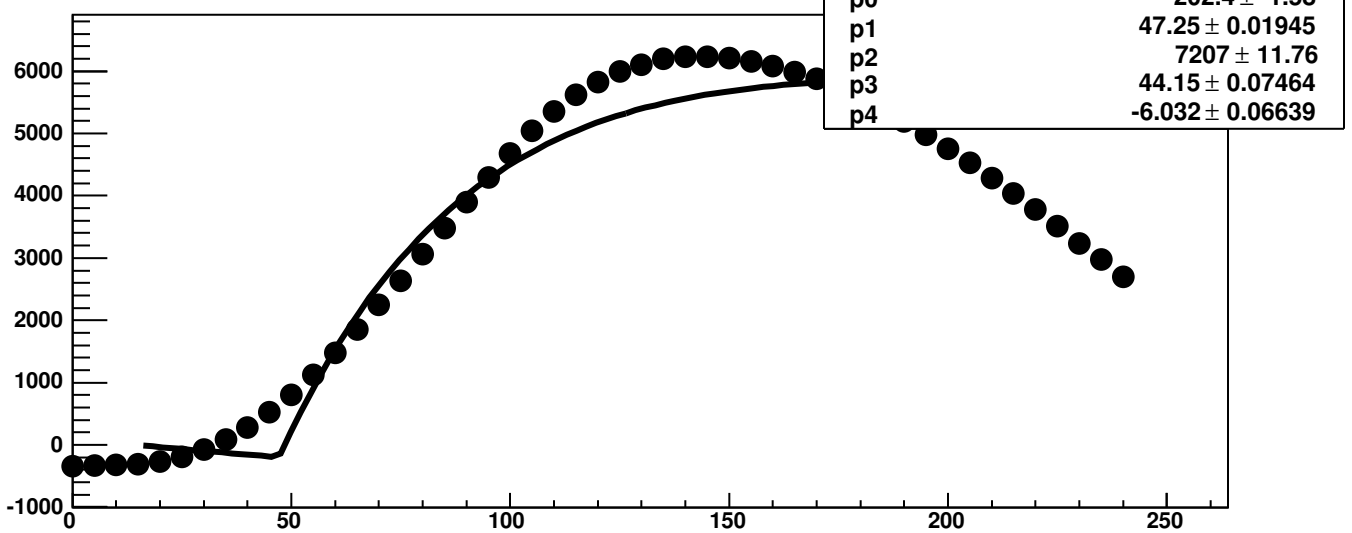

Chip 6, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250


Chip 6, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.31e+04 / 41
p0 -172.1 ± 0.2551
p1 240 ± 8.401e-10
p2 -5.646e+07 ± 84.85
p3 -12.62 ± 1.414
p4 0.4726 ± 0.002121
-180
-200
-220
-240
-260
-280
-300
-320
-340
-360
0
50
100
150
200
250

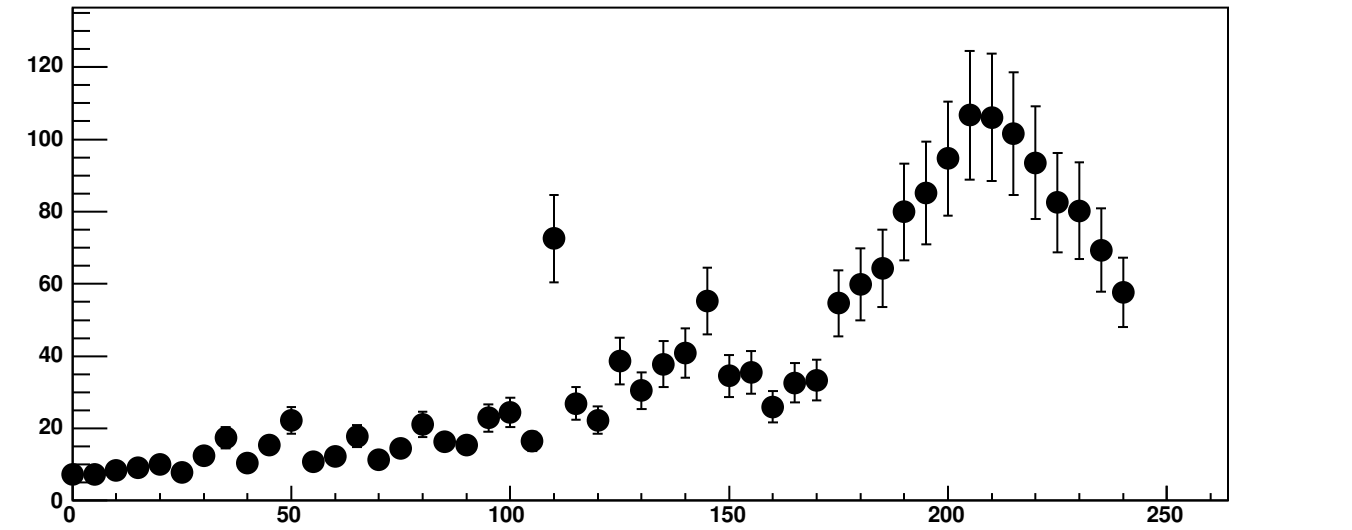

Chip 6, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 6, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold
×10⁴
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

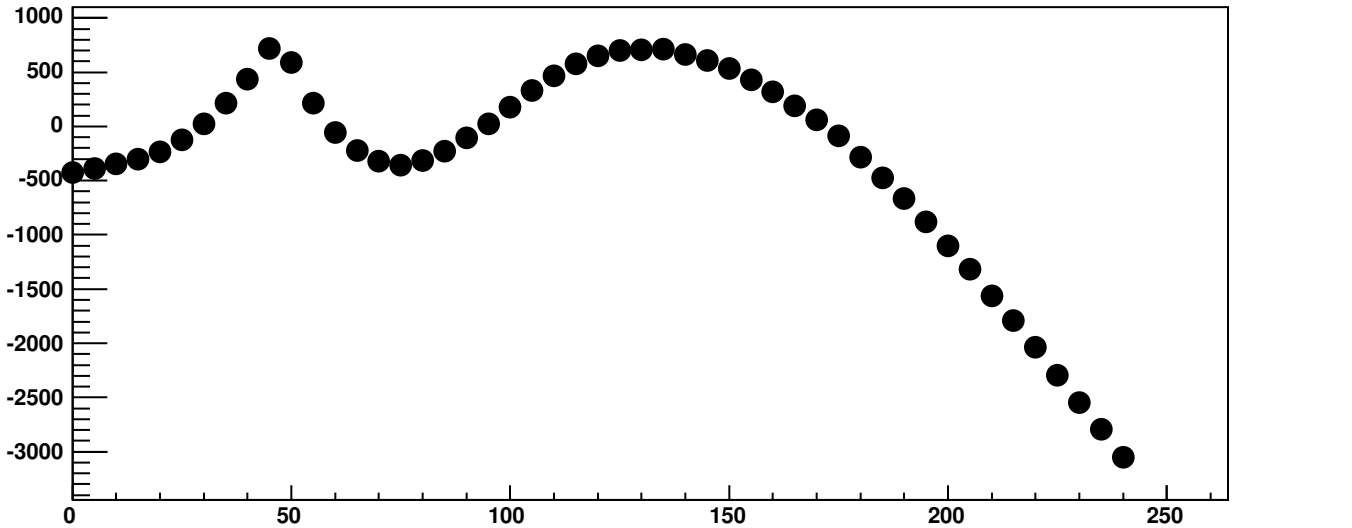
Chip 6, Channel 6, Enable 5, DAC=4000, ADC Mean vs Hold



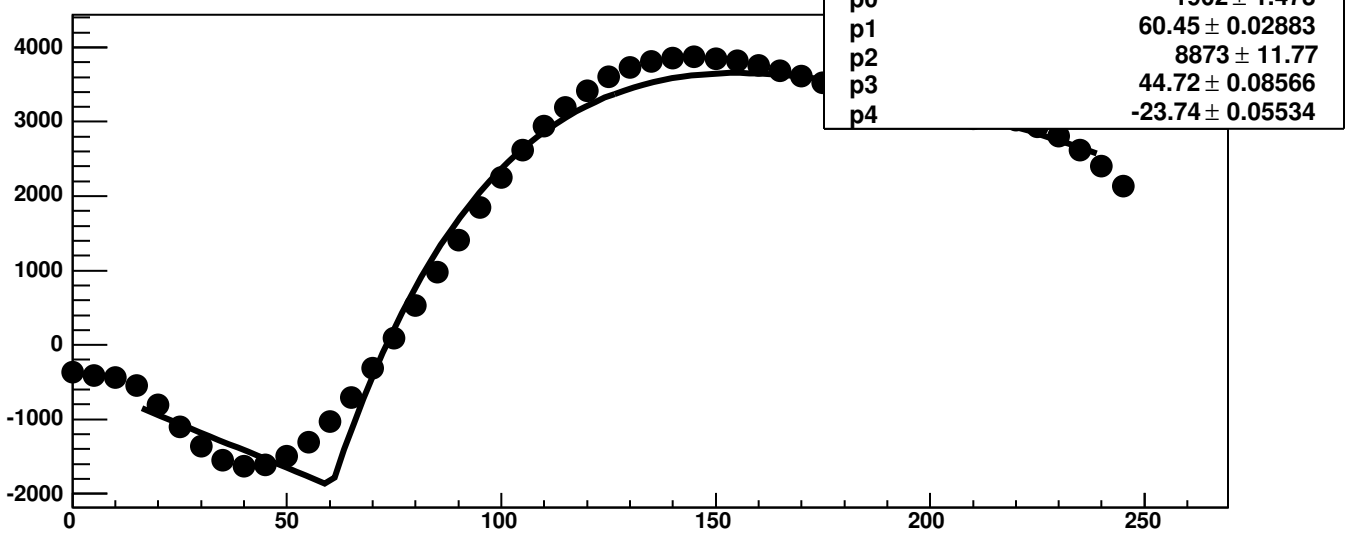
Chip 6, Channel 6, Enable 5, DAC=4000, ADC Noise vs Hold



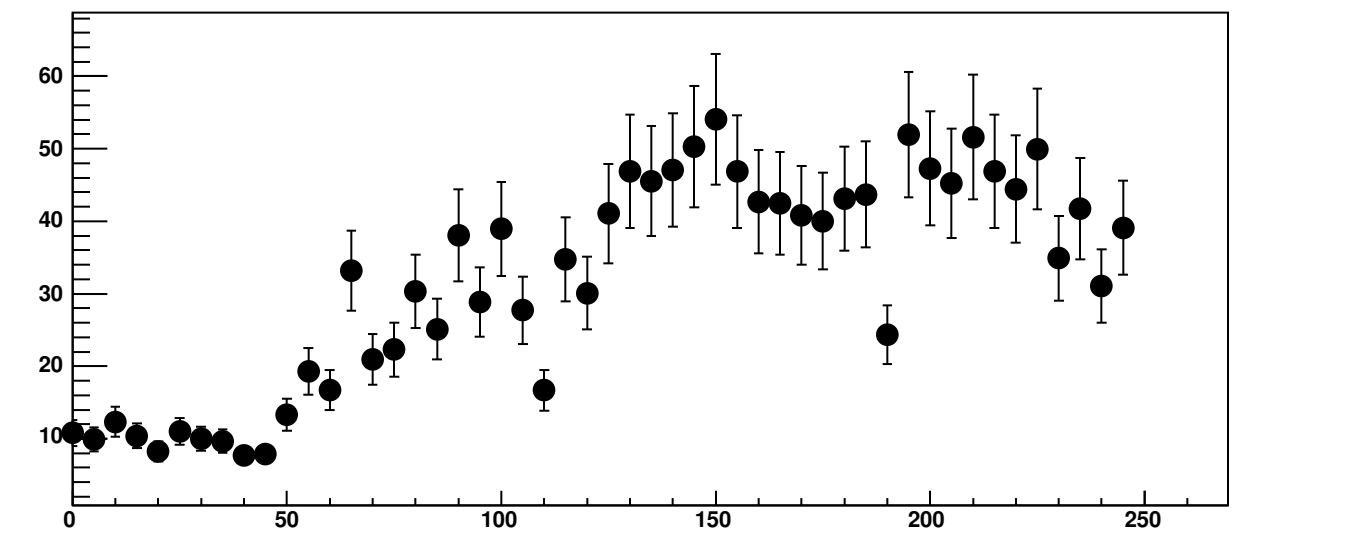
Chip 6, Channel 6, Enable 5, DAC=4000, ADC Residuals vs Hold



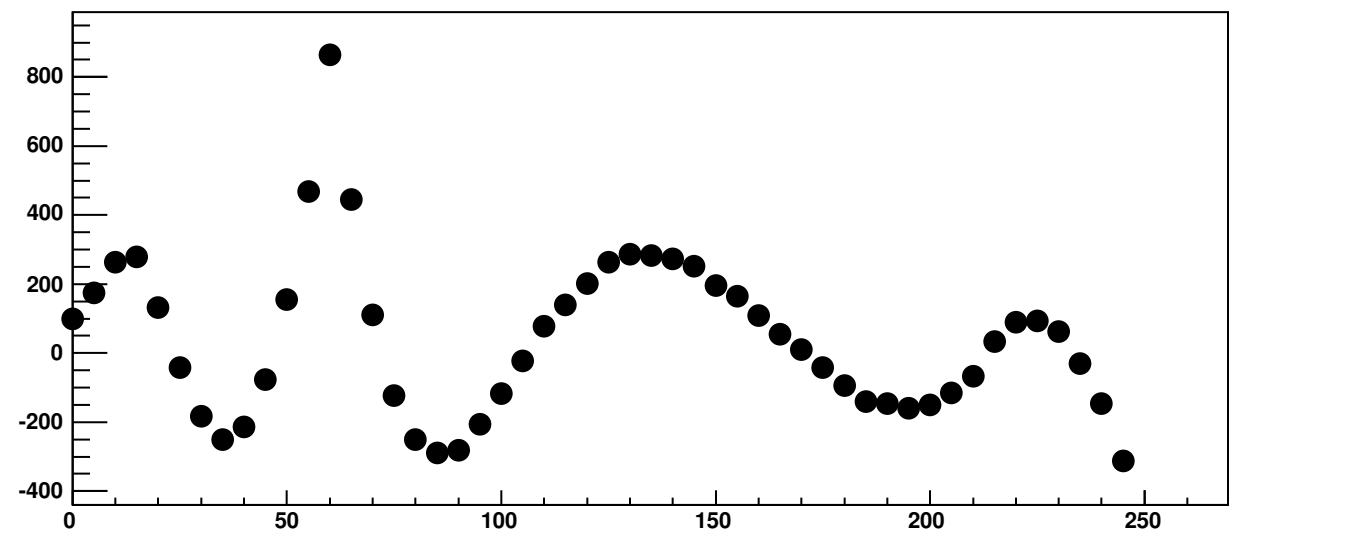
Chip 6, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

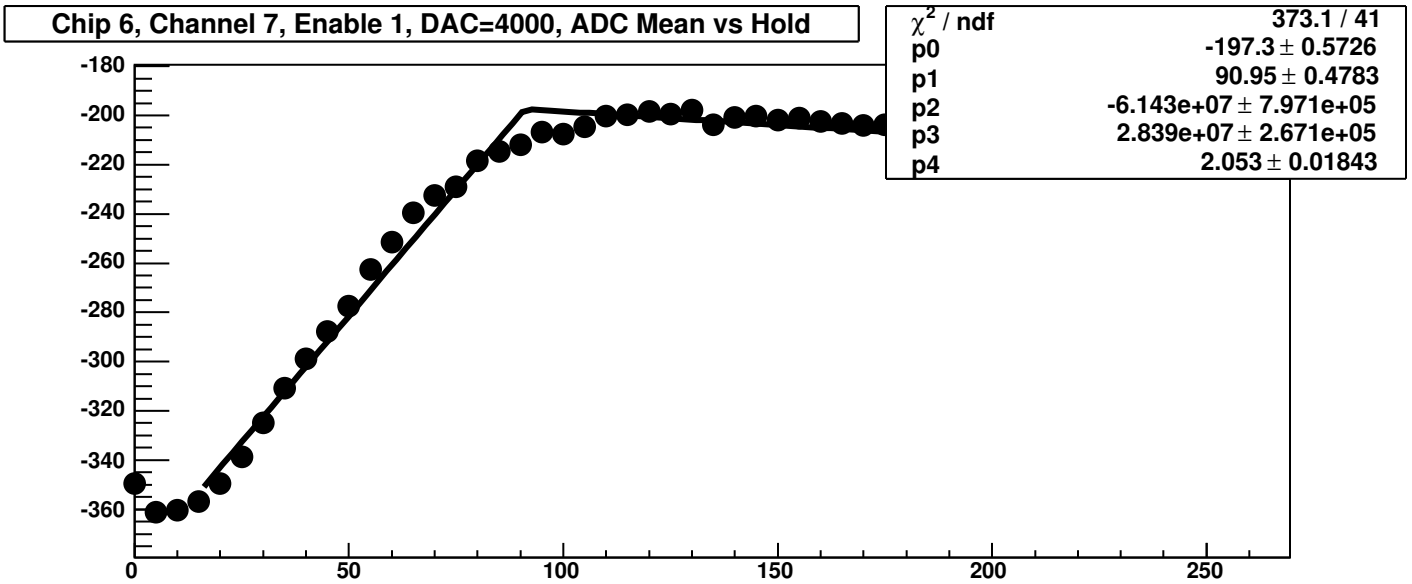


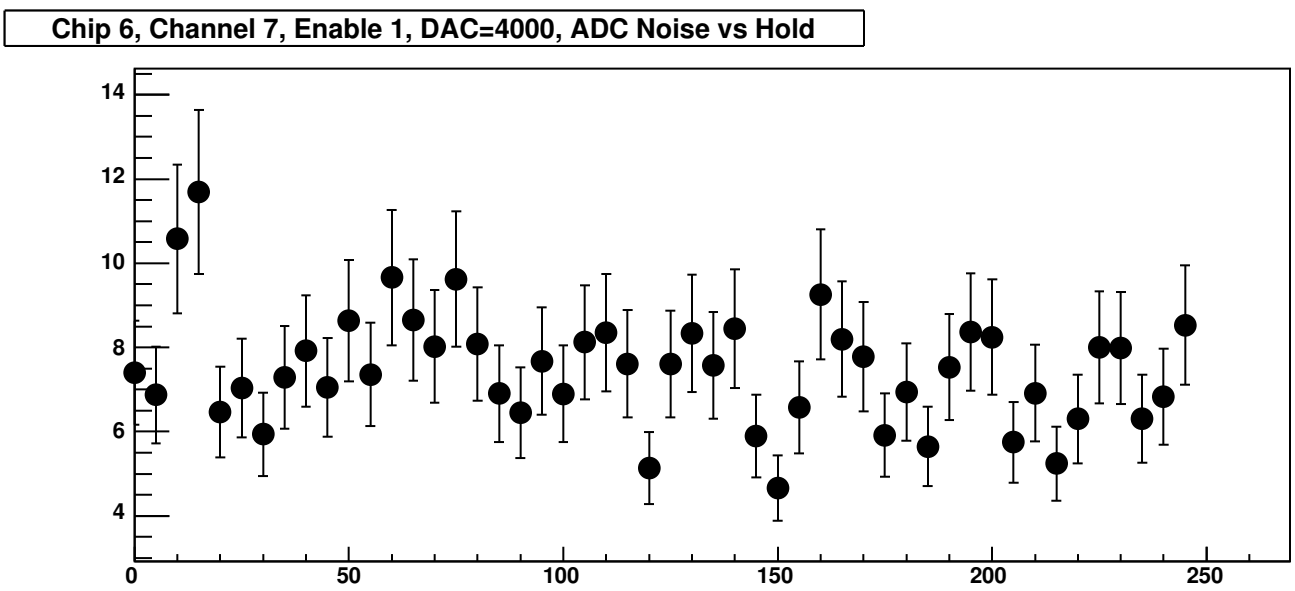
Chip 6, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold

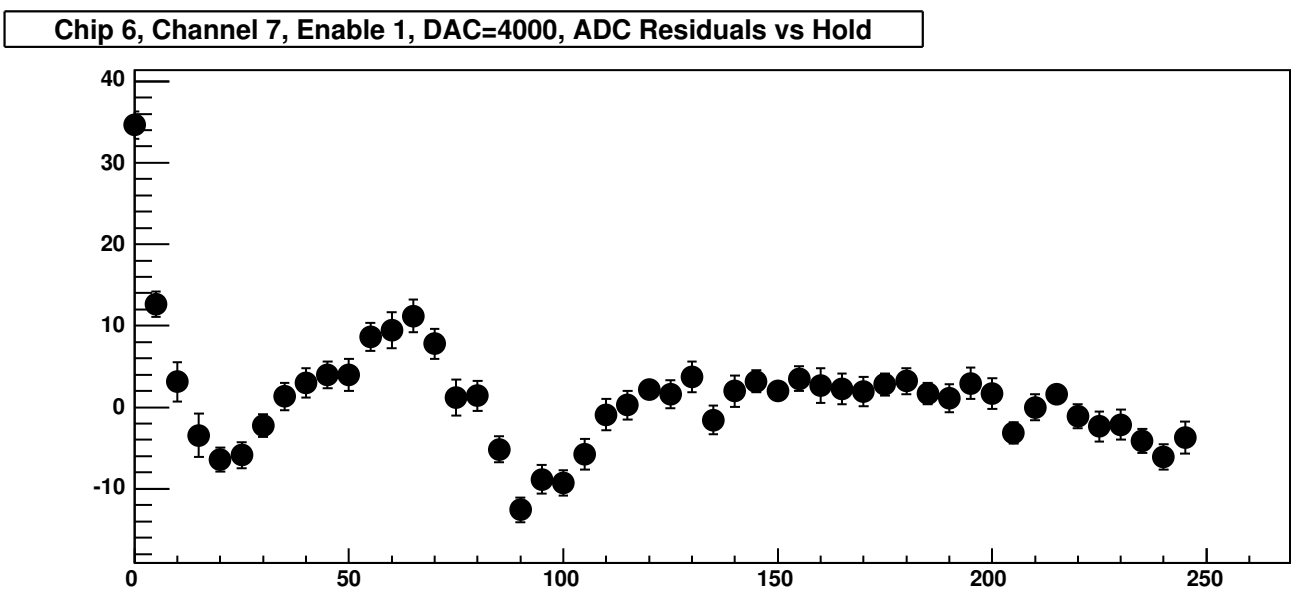


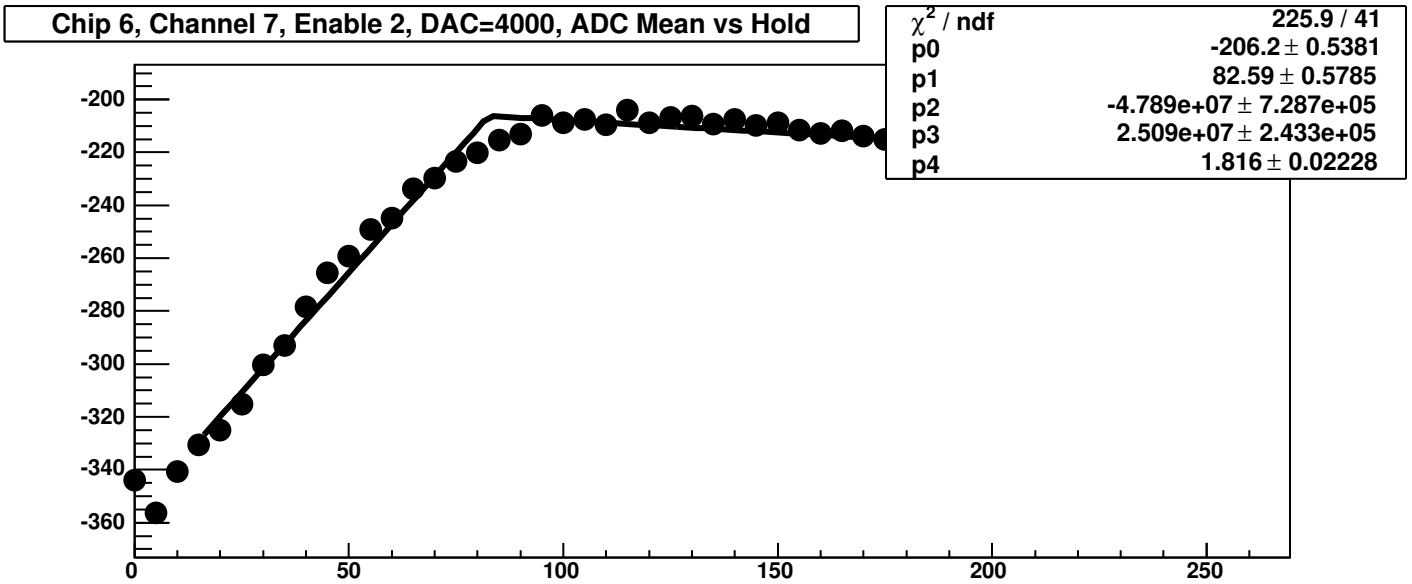
Chip 6, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

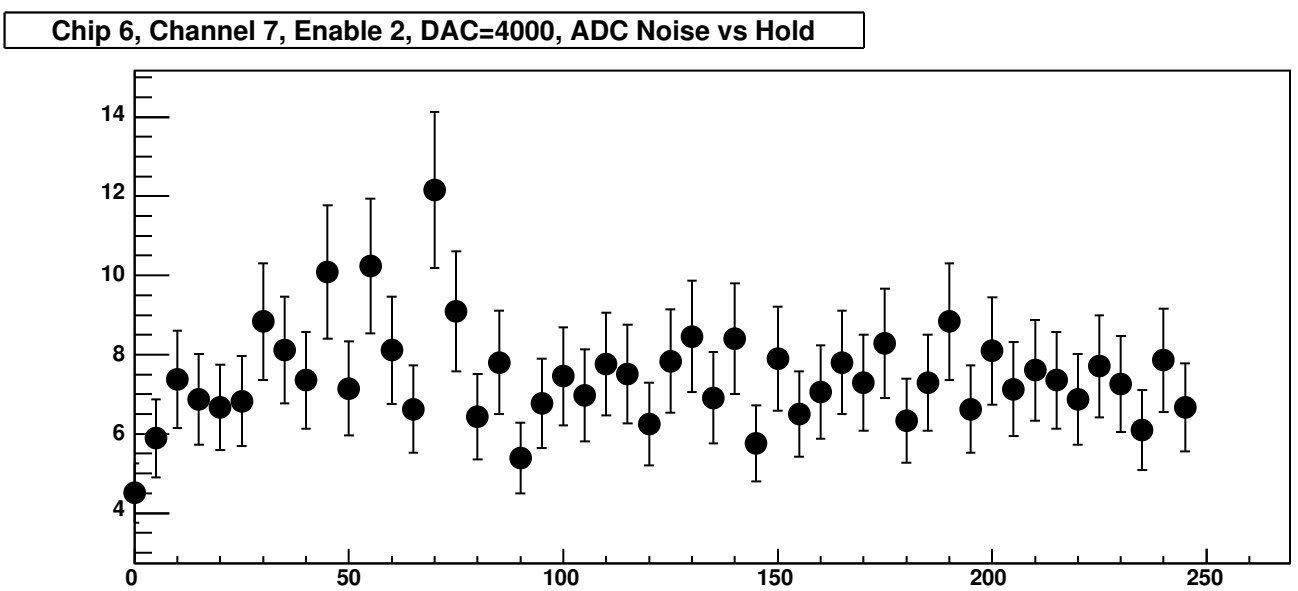


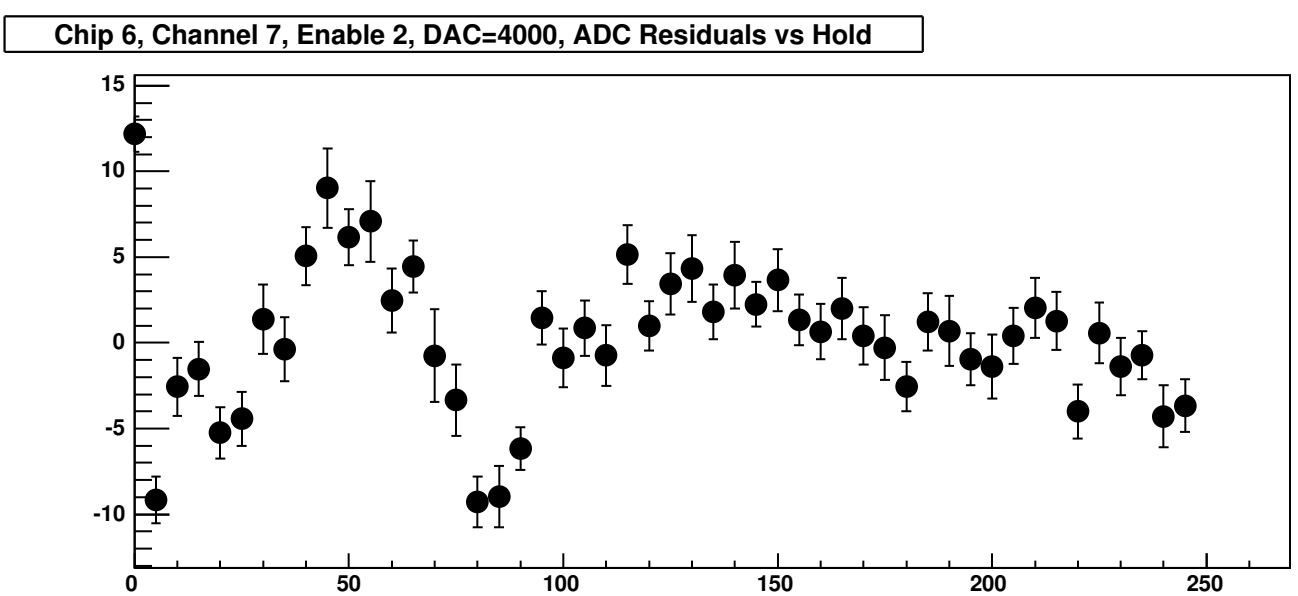

Chip 6, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 373.1 / 41
p0 -197.3 ± 0.5726
p1 90.95 ± 0.4783
p2 -6.143e+07 ± 7.971e+05
p3 2.839e+07 ± 2.671e+05
p4 2.053 ± 0.01843
-180
-200
-220
-240
-260
-280
-300
-320
-340
-360
0
50
100
150
200
250

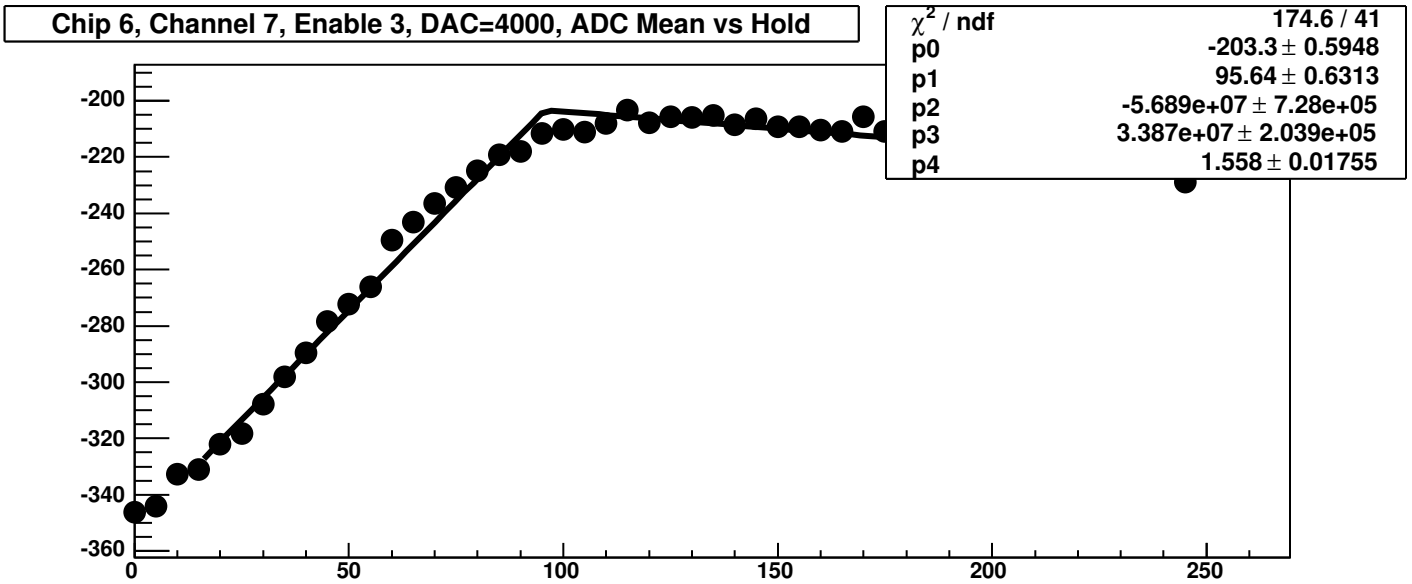

Chip 6, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

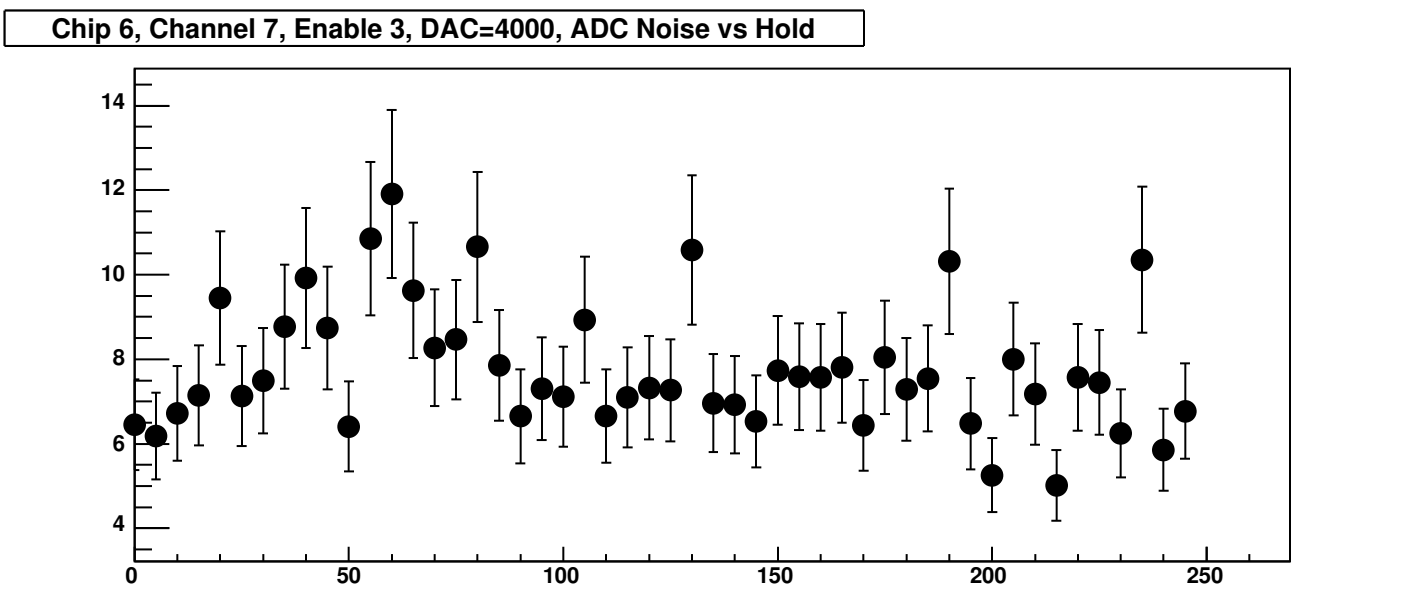

Chip 6, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

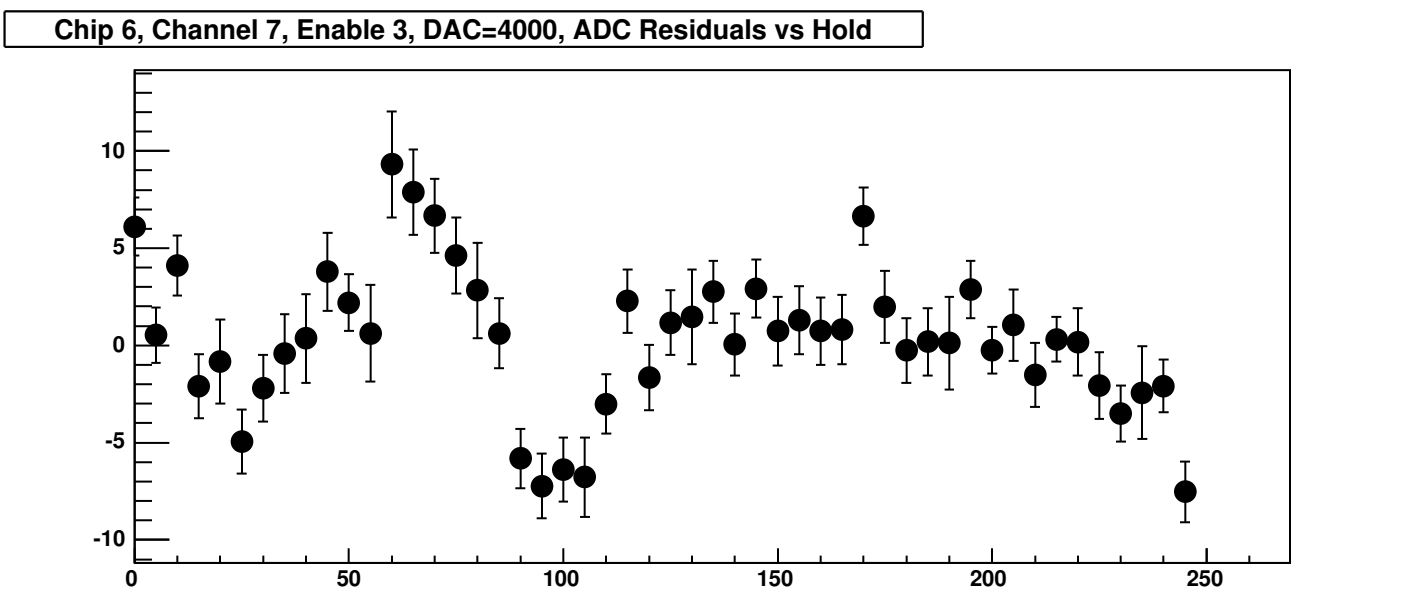

Chip 6, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 225.9 / 41
p0 -206.2 ± 0.5381
p1 82.59 ± 0.5785
p2 -4.789e+07 ± 7.287e+05
p3 2.509e+07 ± 2.433e+05
p4 1.816 ± 0.02228
-200
-220
-240
-260
-280
-300
-320
-340
-360
0
50
100
150
200
250

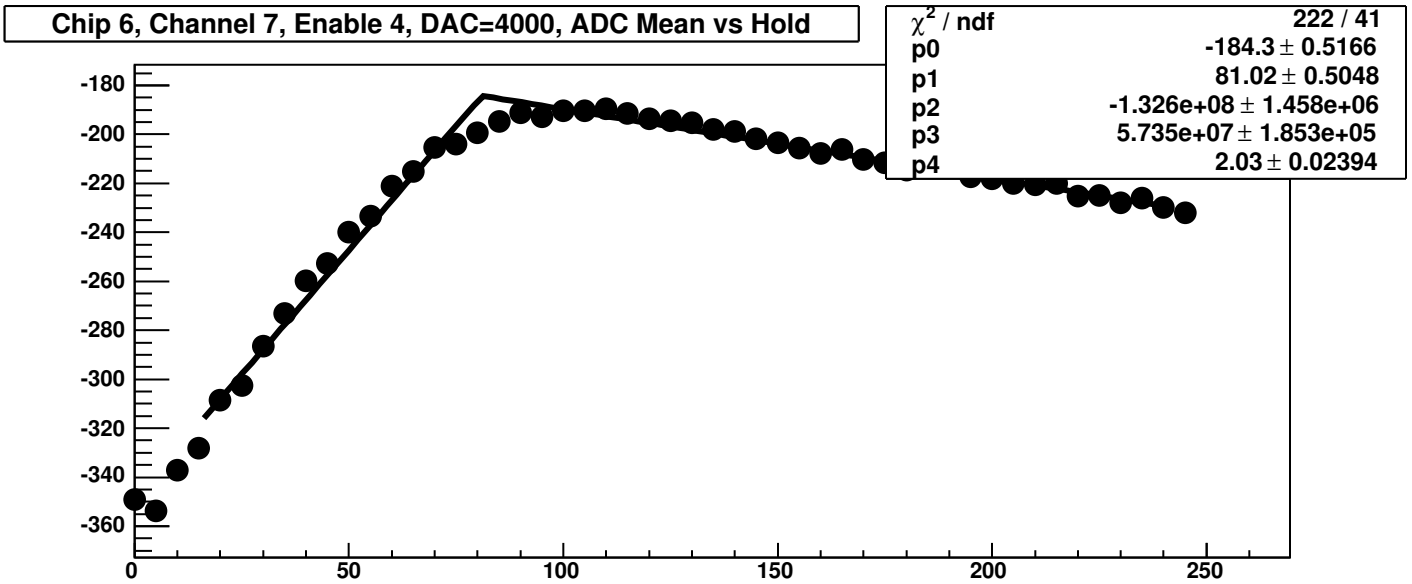

Chip 6, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

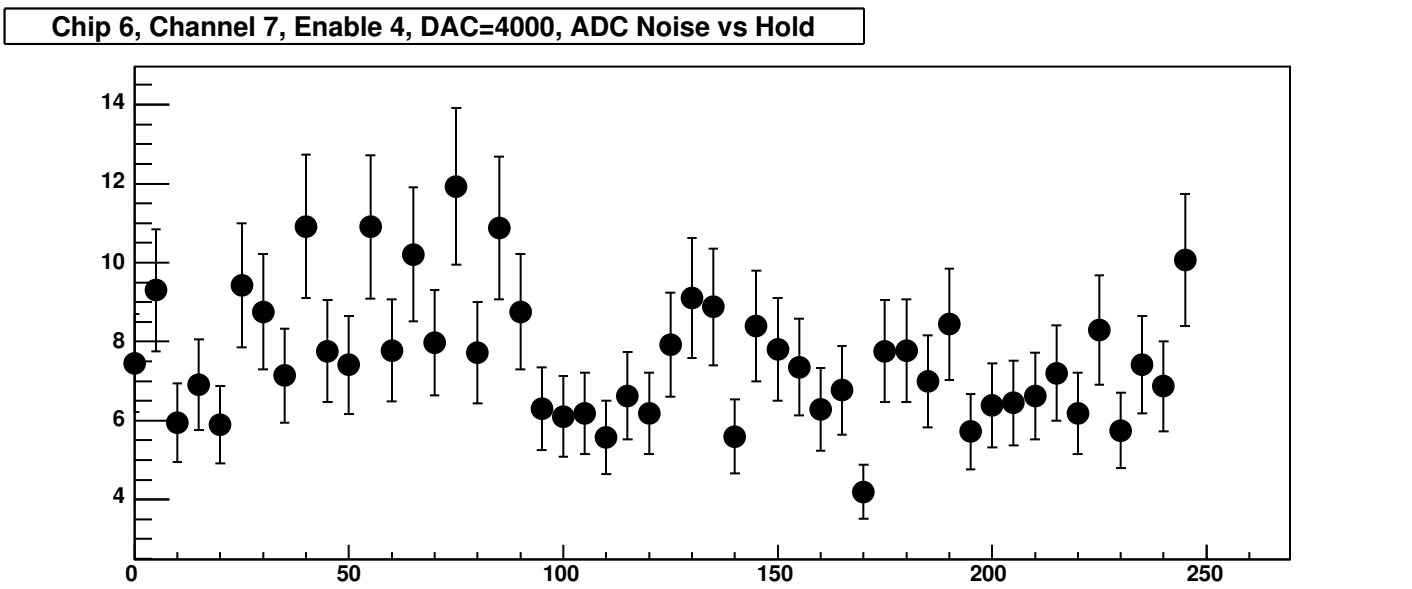

Chip 6, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
0
50
100
150
200
250

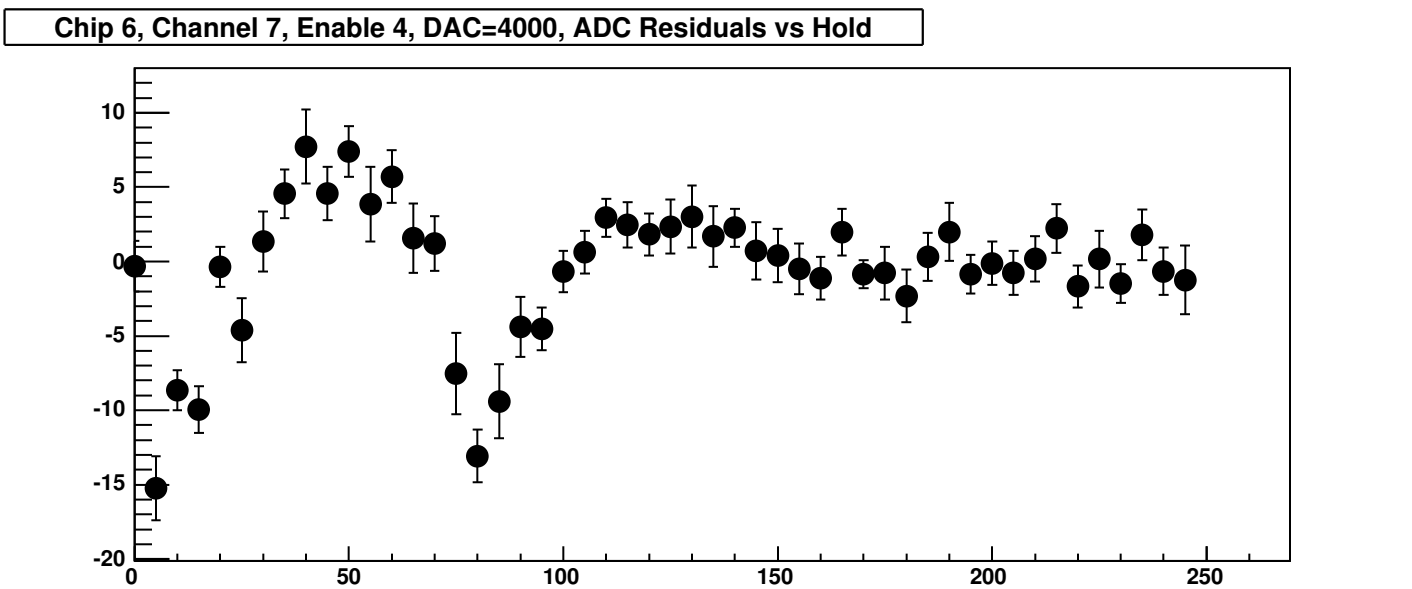

Chip 6, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 174.6 / 41
p0 -203.3 ± 0.5948
p1 95.64 ± 0.6313
p2 -5.689e+07 ± 7.28e+05
p3 3.387e+07 ± 2.039e+05
p4 1.558 ± 0.01755
-200
-220
-240
-260
-280
-300
-320
-340
-360
0
50
100
150
200
250

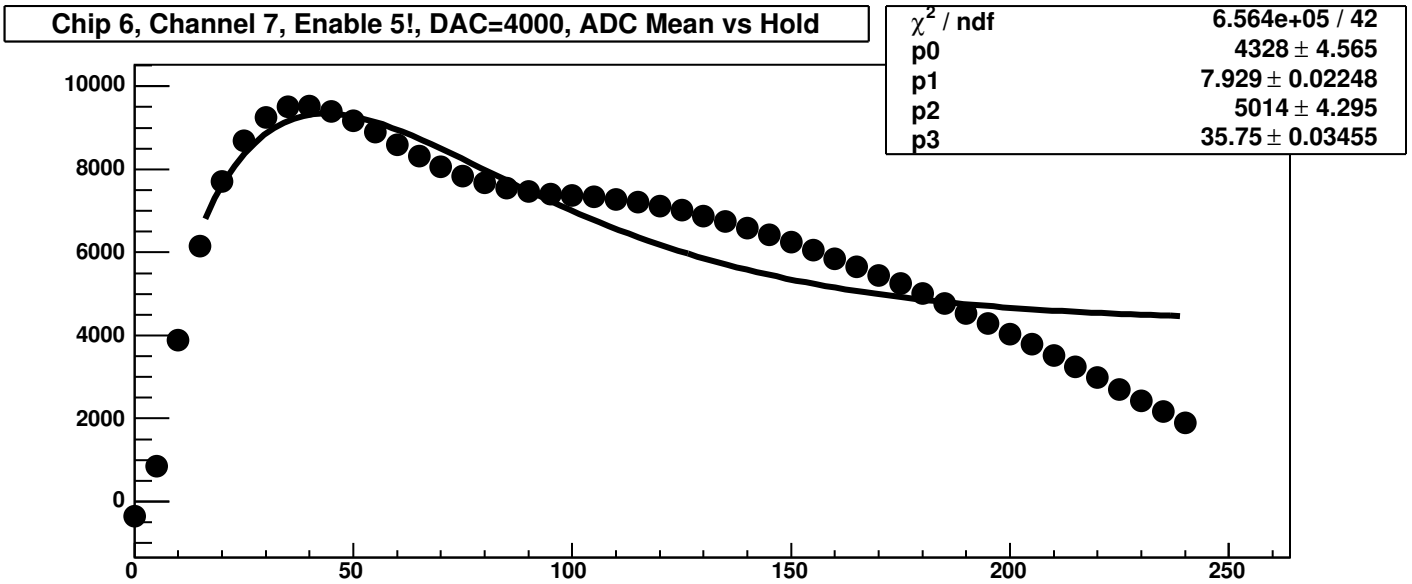

Chip 6, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

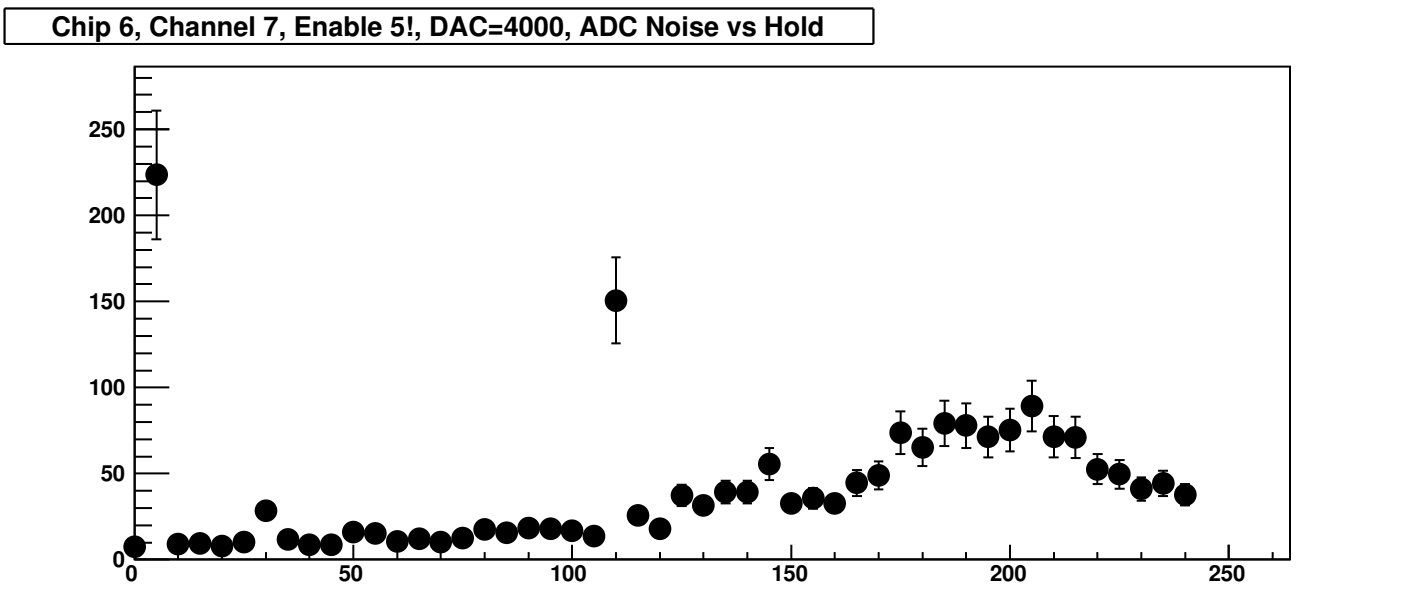

Chip 6, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250

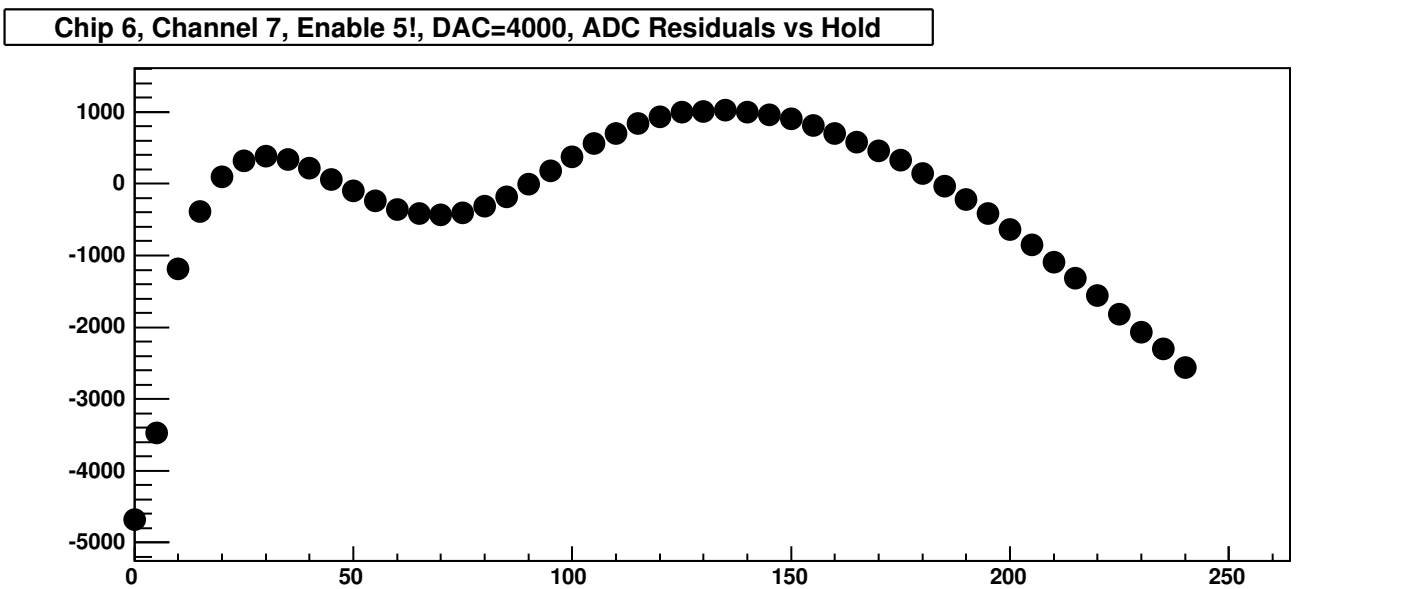

Chip 6, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 222 / 41
p0 -184.3 ± 0.5166
p1 81.02 ± 0.5048
p2 -1.326e+08 ± 1.458e+06
p3 5.735e+07 ± 1.853e+05
p4 2.03 ± 0.02394
-180
-200
-220
-240
-260
-280
-300
-320
-340
-360
0
50
100
150
200
250


Chip 6, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

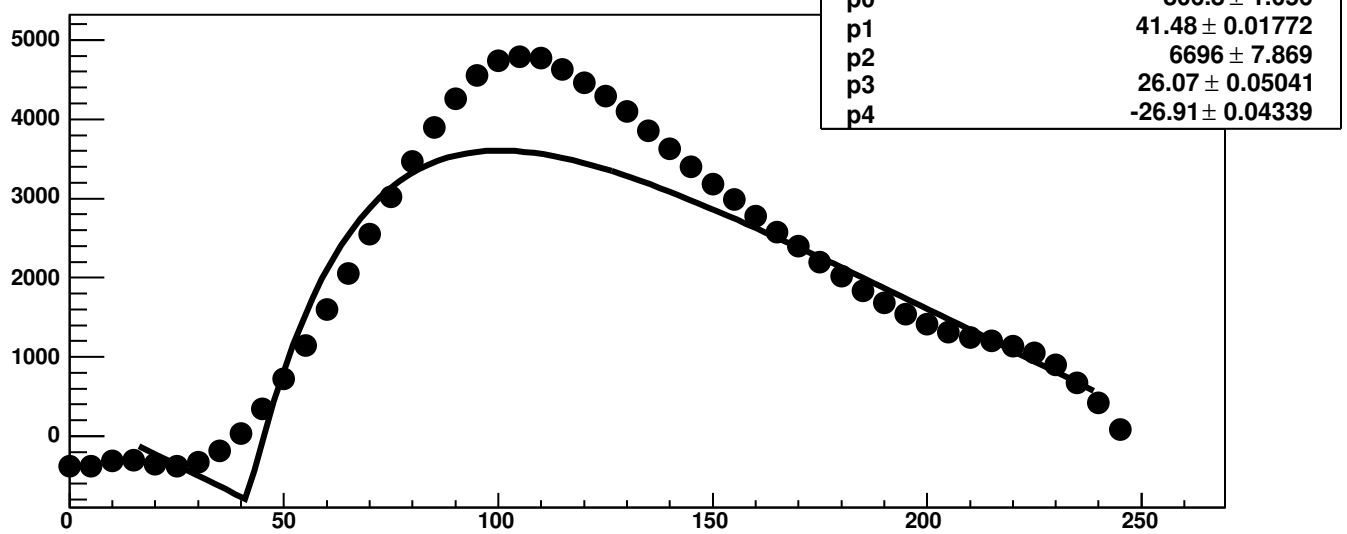

Chip 6, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
-20
0
50
100
150
200
250


Chip 6, Channel 7, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 6.564e+05 / 42
p0 4328 ± 4.565
p1 7.929 ± 0.02248
p2 5014 ± 4.295
p3 35.75 ± 0.03455
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

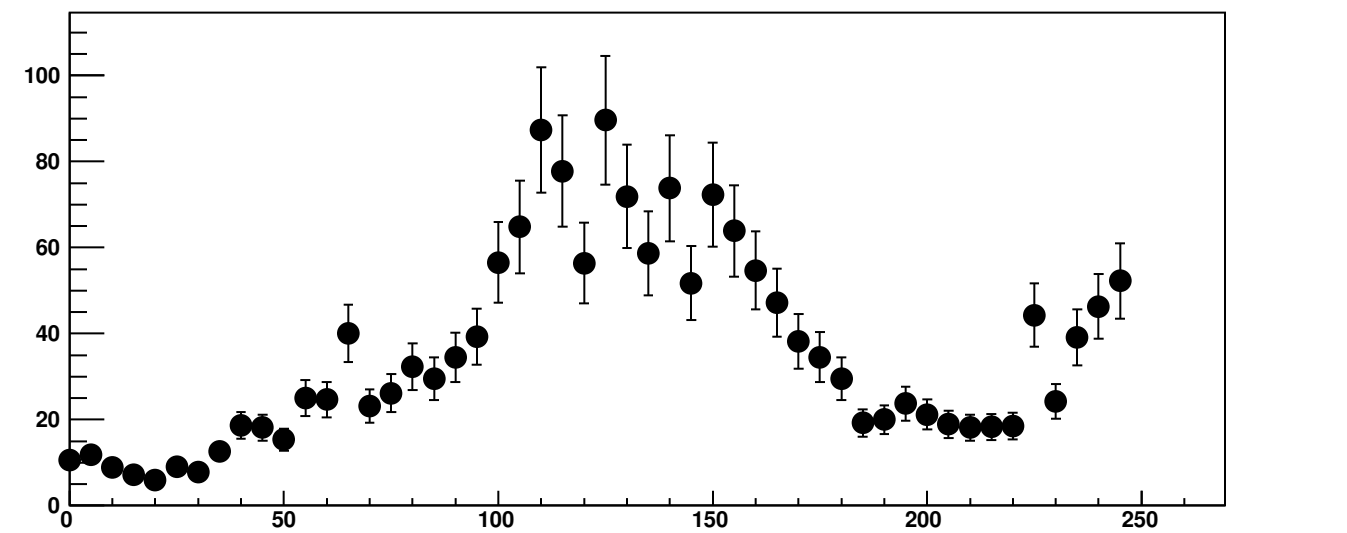

Chip 6, Channel 7, Enable 5!, DAC=4000, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250


Chip 6, Channel 7, Enable 5!, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

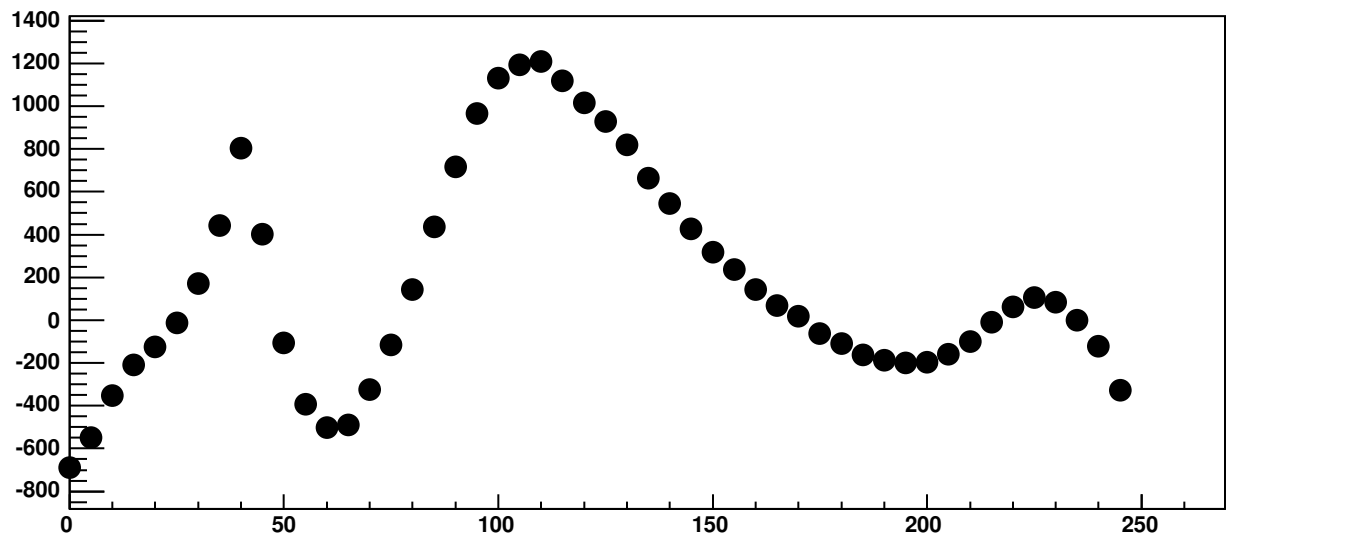
Chip 6, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold



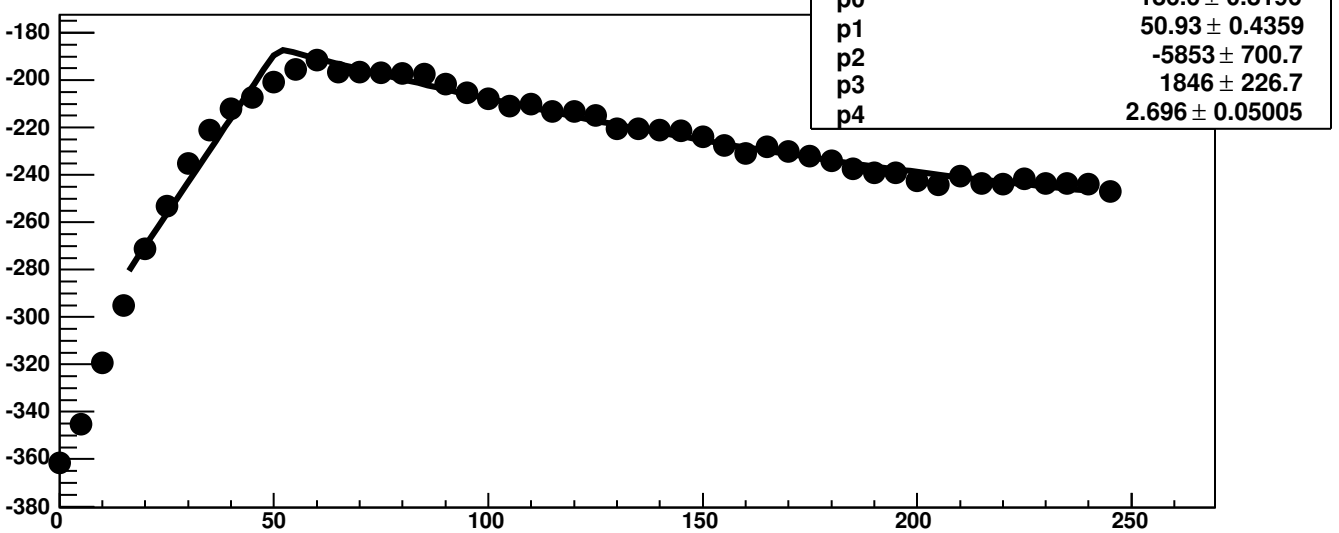
Chip 6, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



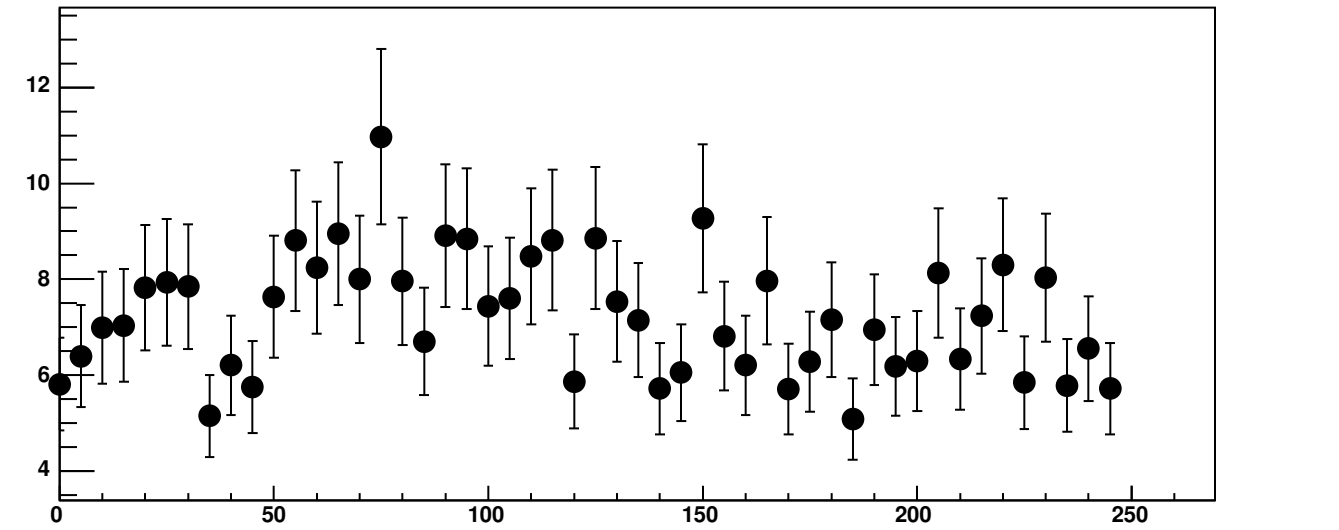
Chip 6, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold



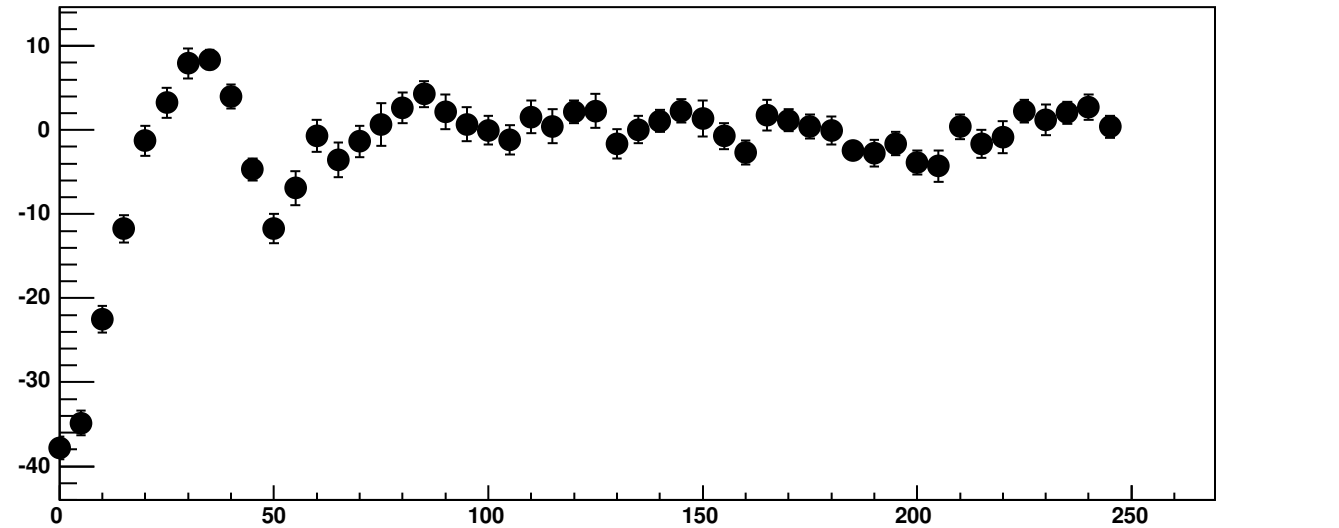
Chip 6, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold



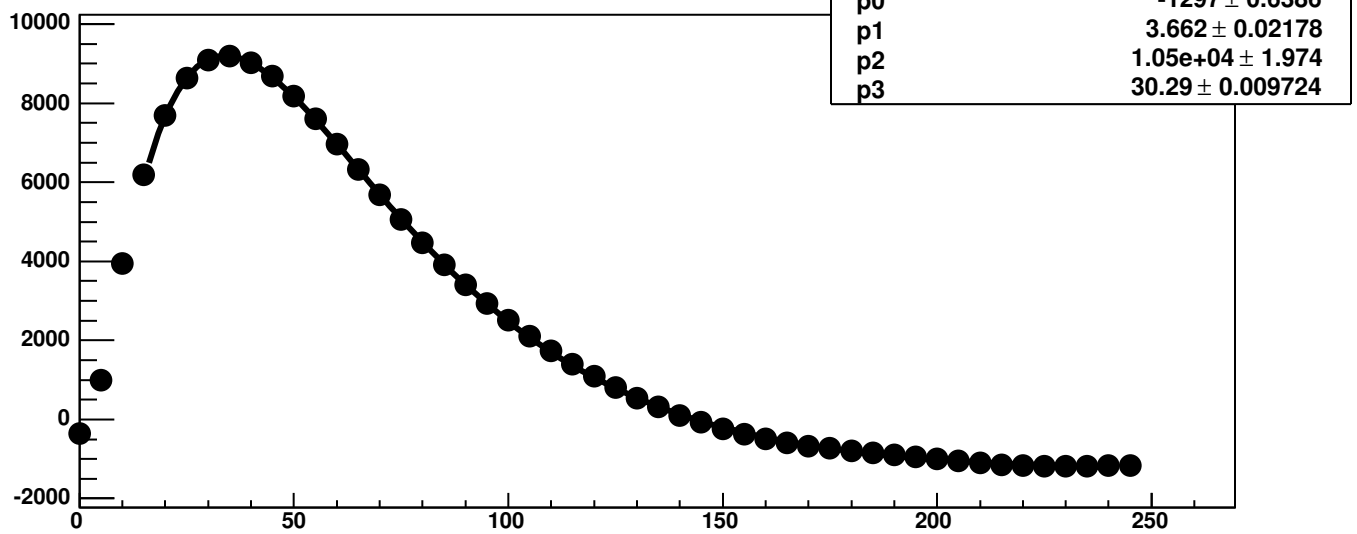
Chip 6, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



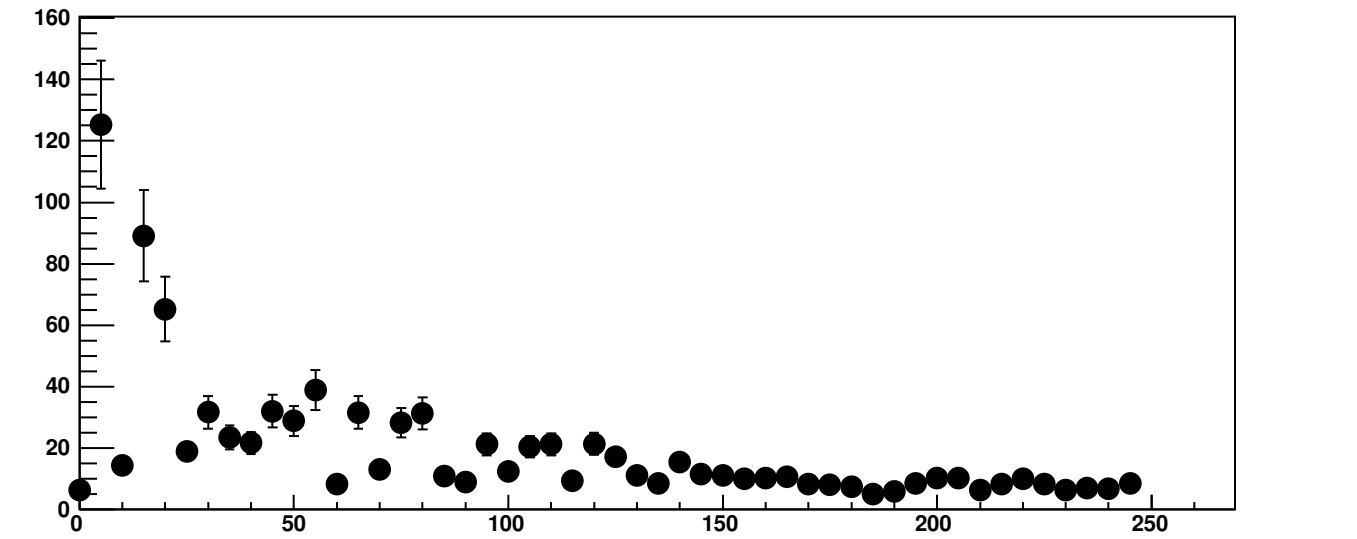
Chip 6, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold



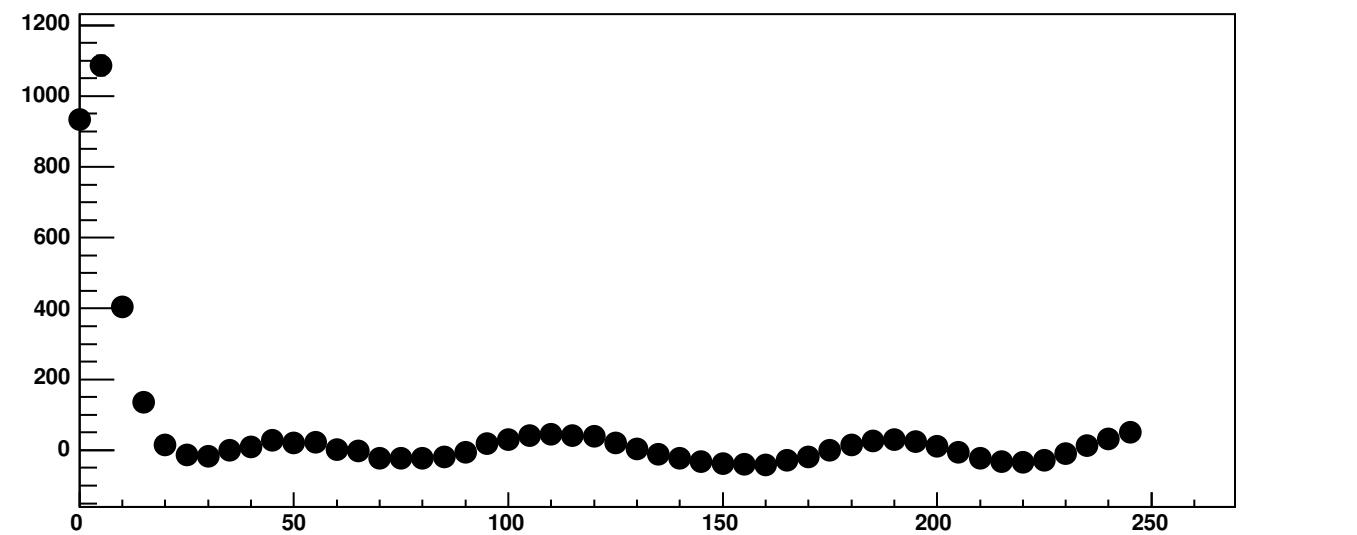
Chip 6, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold



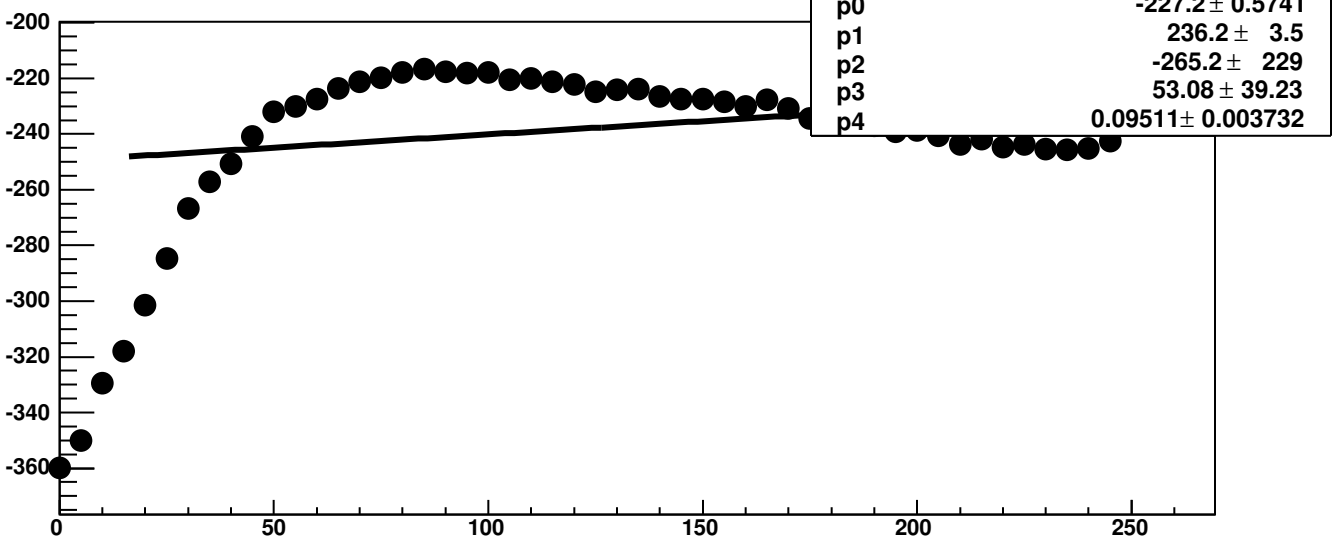
Chip 6, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold



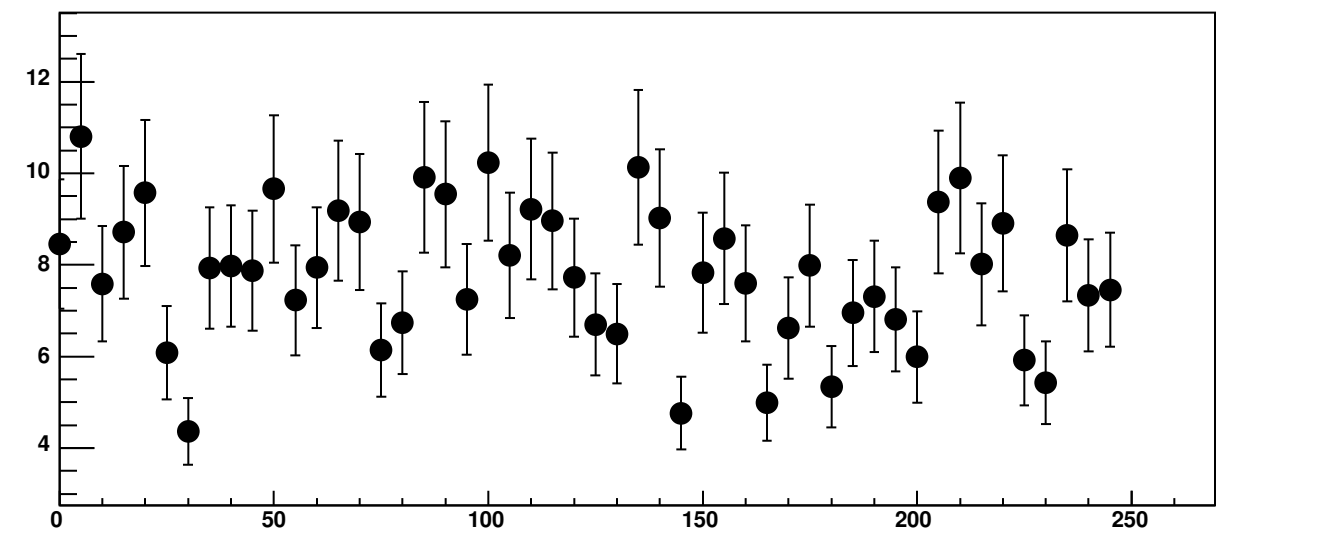
Chip 6, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold



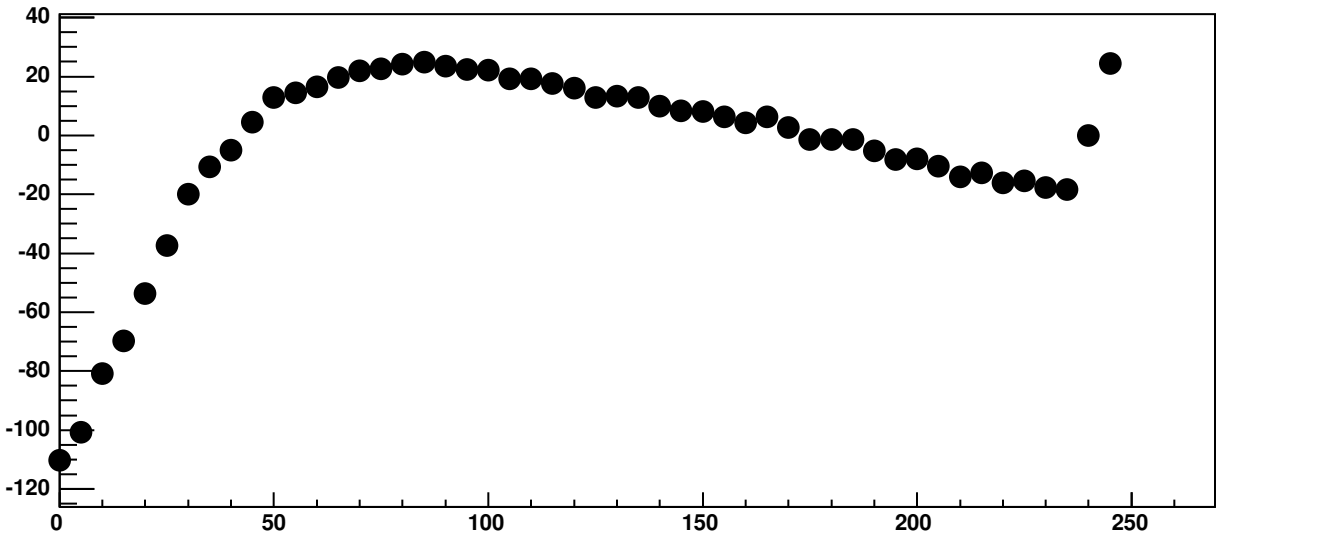
Chip 6, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold



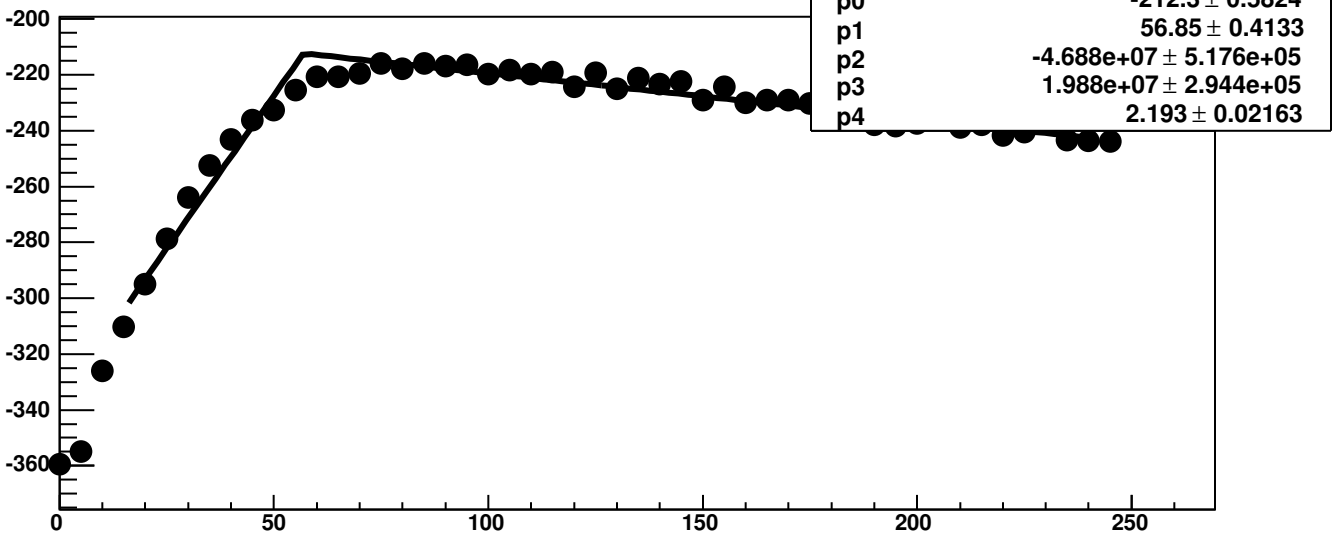
Chip 6, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



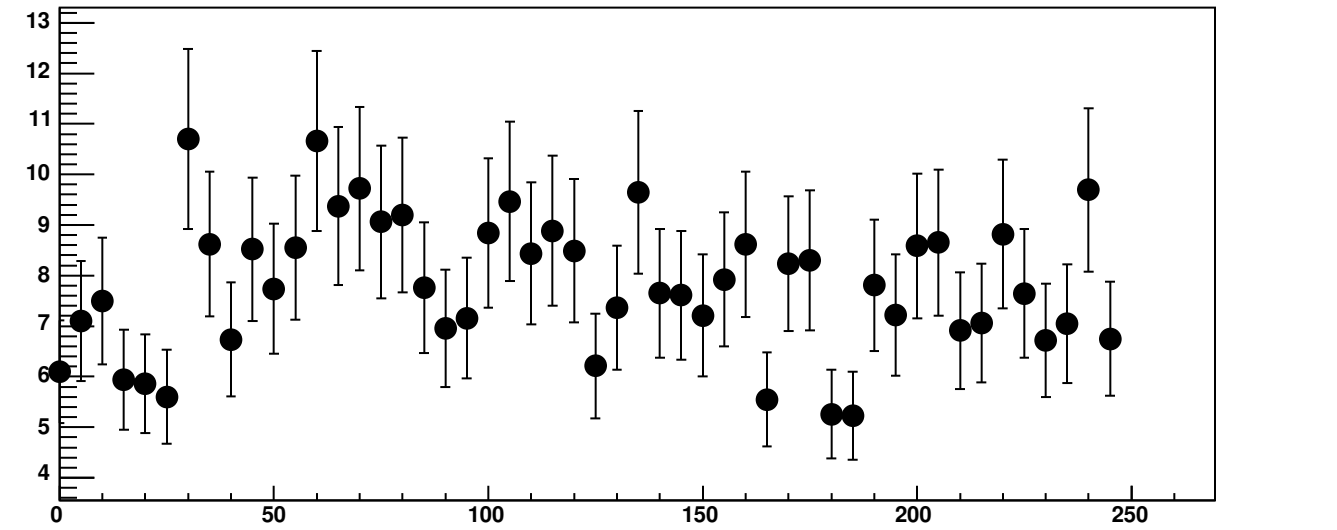
Chip 6, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold



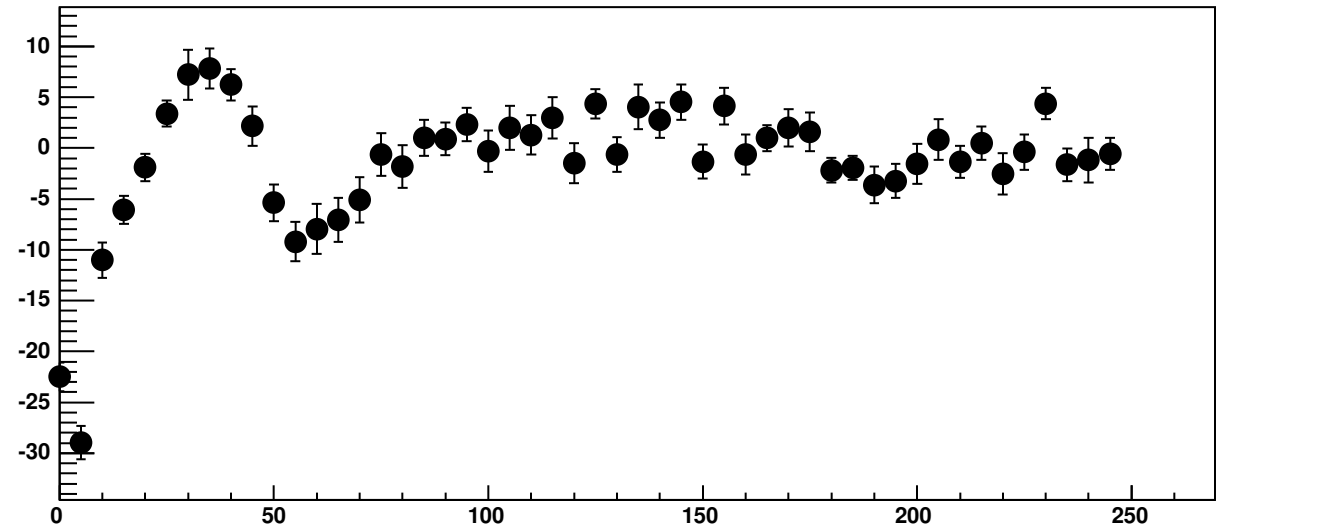
Chip 6, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

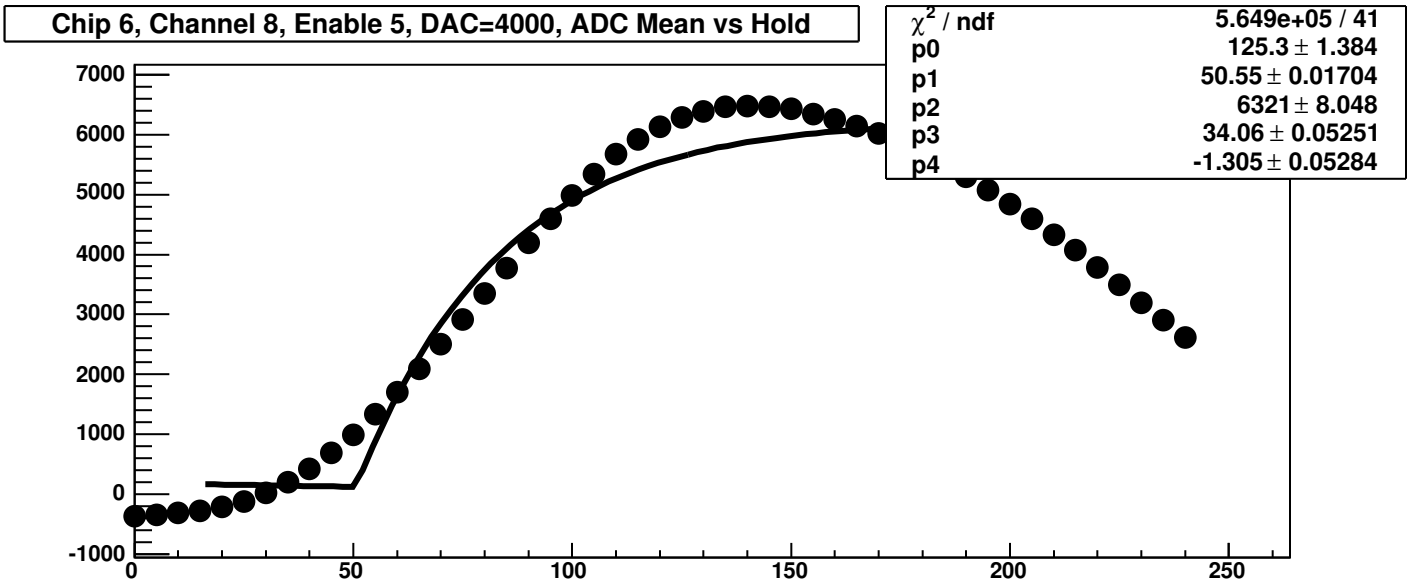


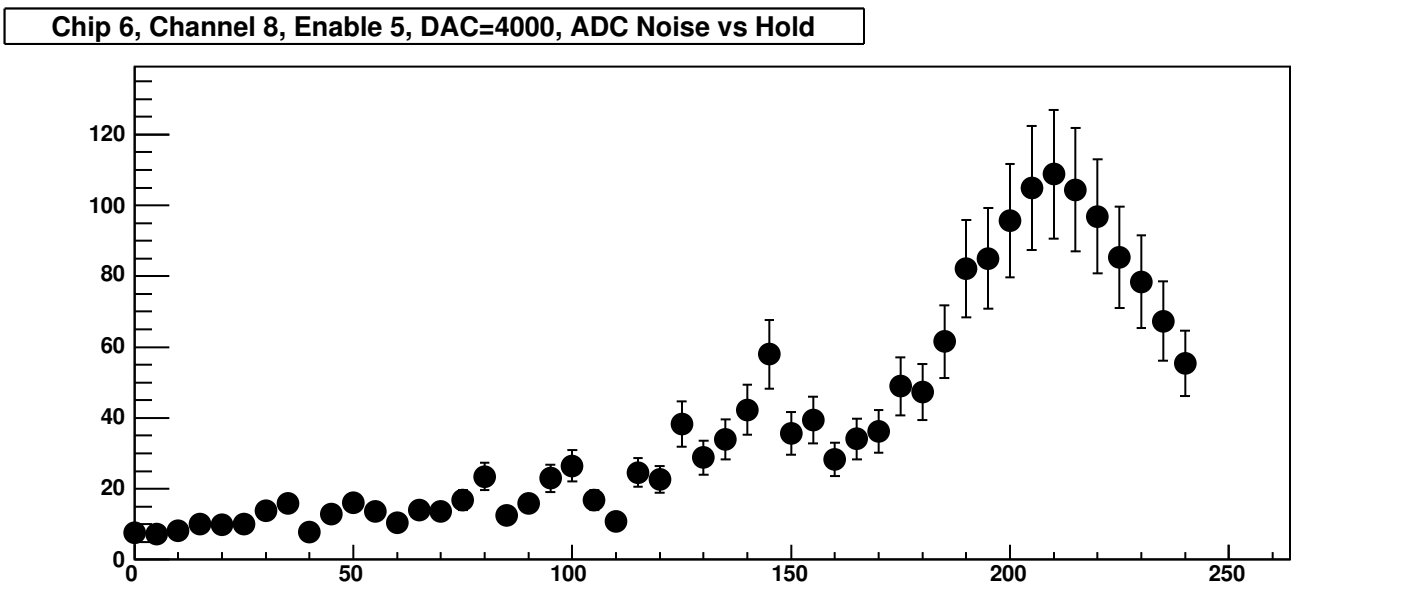
Chip 6, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold

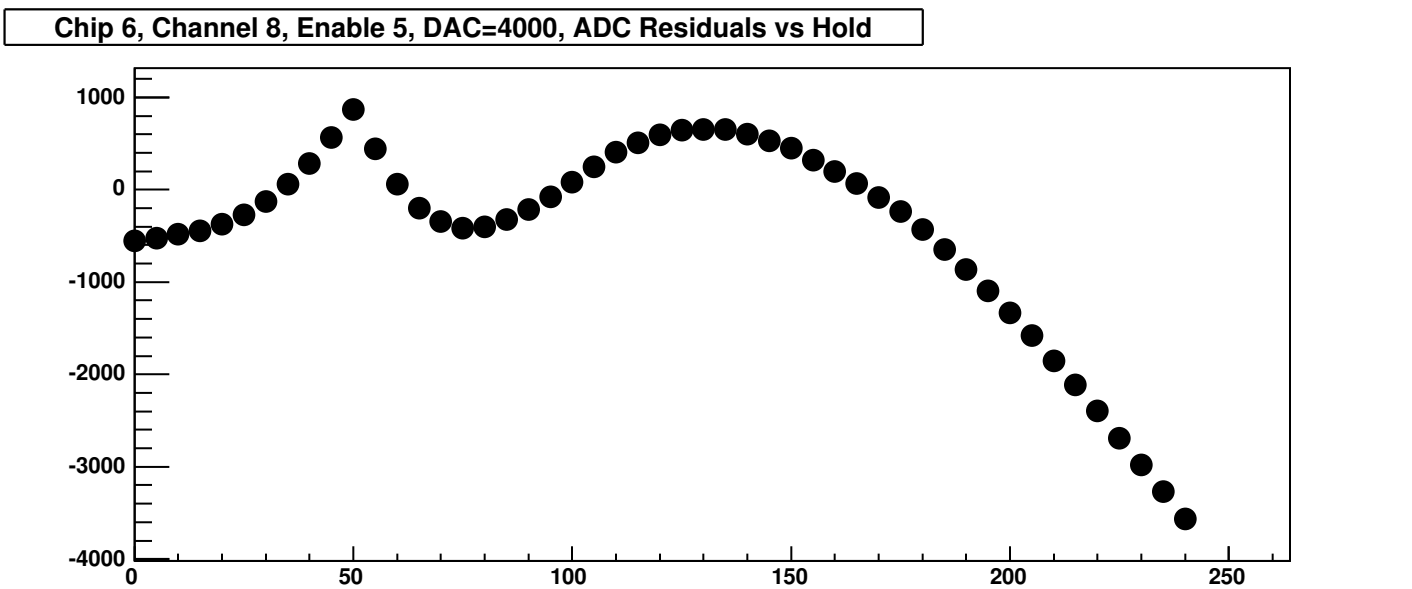


Chip 6, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

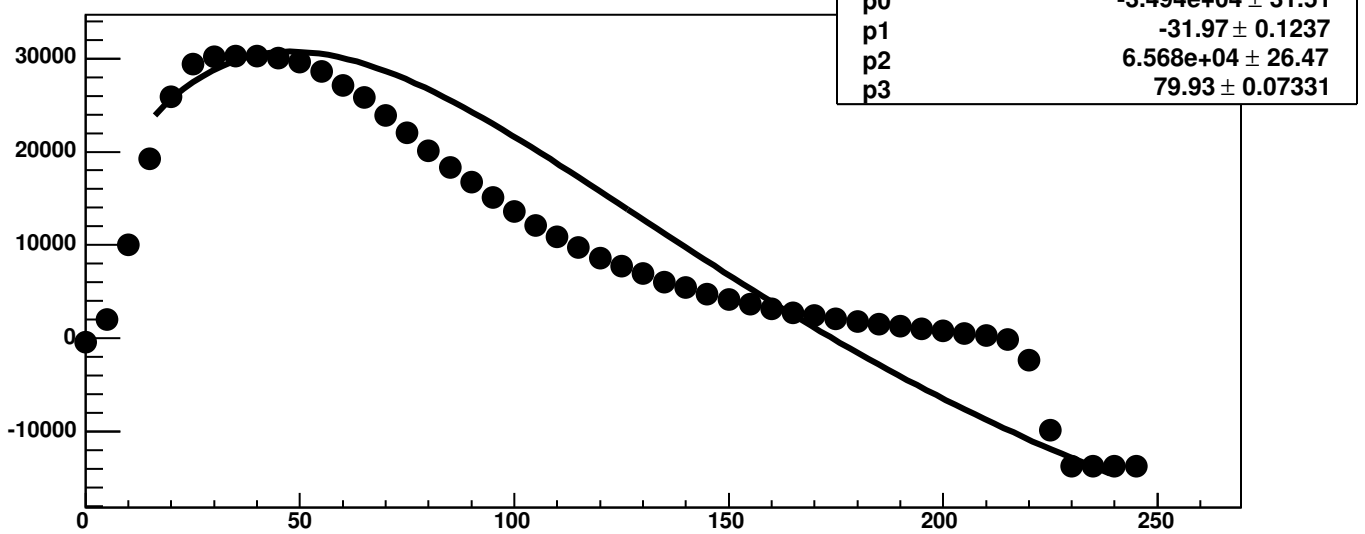



Chip 6, Channel 8, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.649e+05 / 41
p0 125.3 ± 1.384
p1 50.55 ± 0.01704
p2 6321 ± 8.048
p3 34.06 ± 0.05251
p4 -1.305 ± 0.05284
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

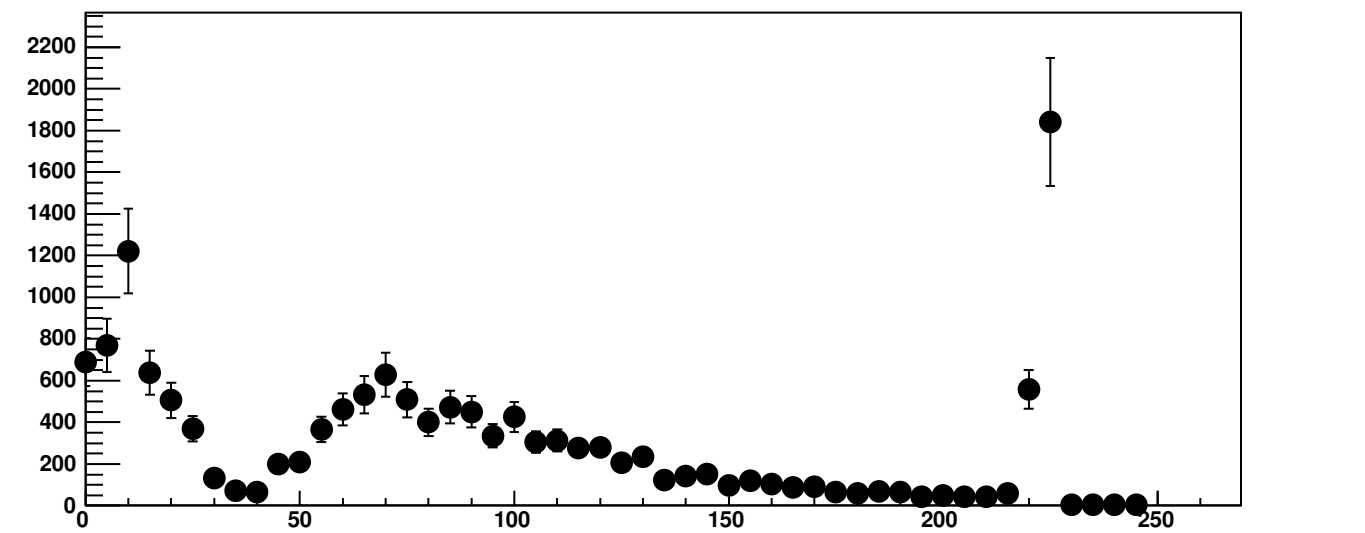

Chip 6, Channel 8, Enable 5, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 6, Channel 8, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

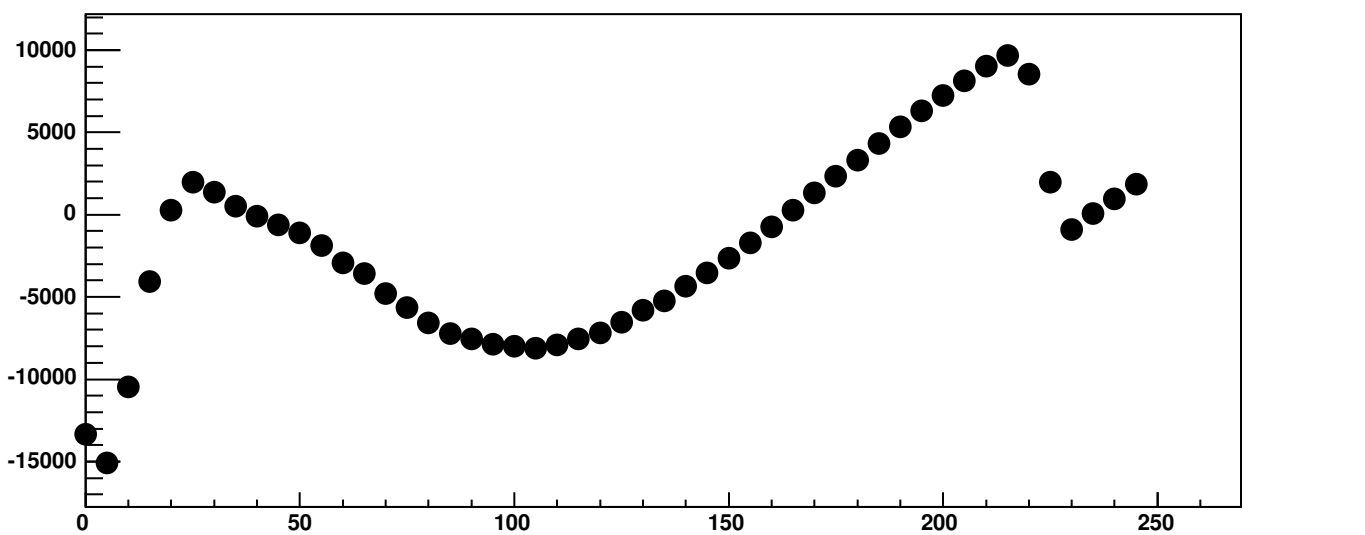
Chip 6, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold

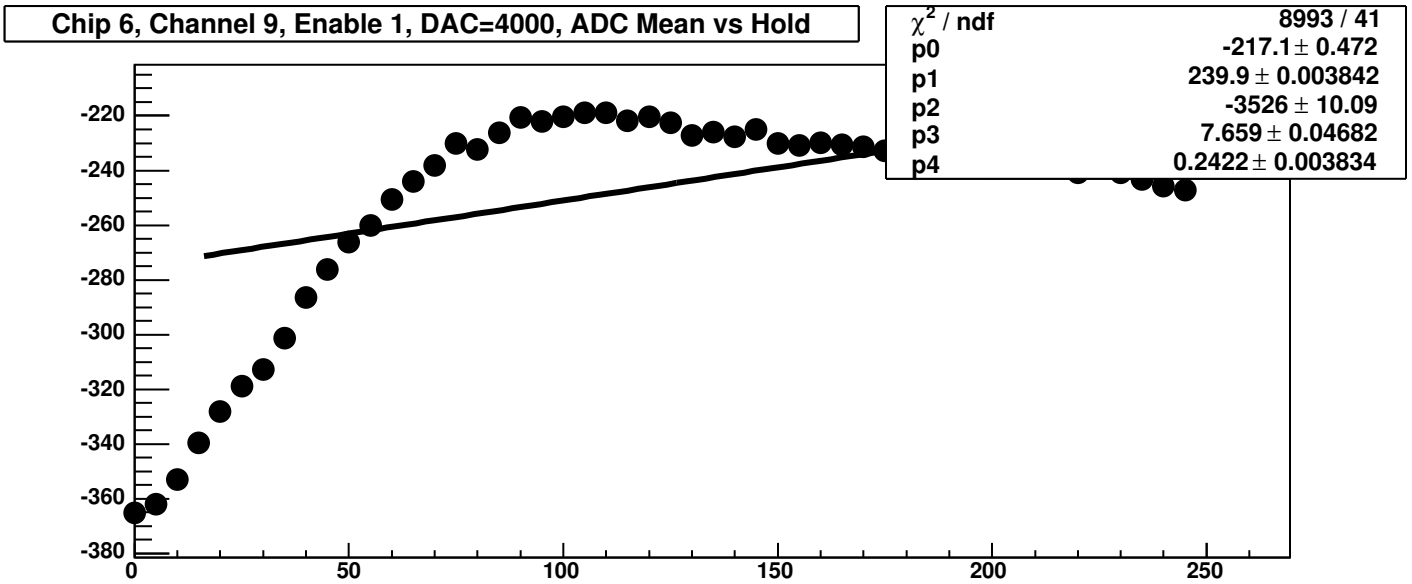


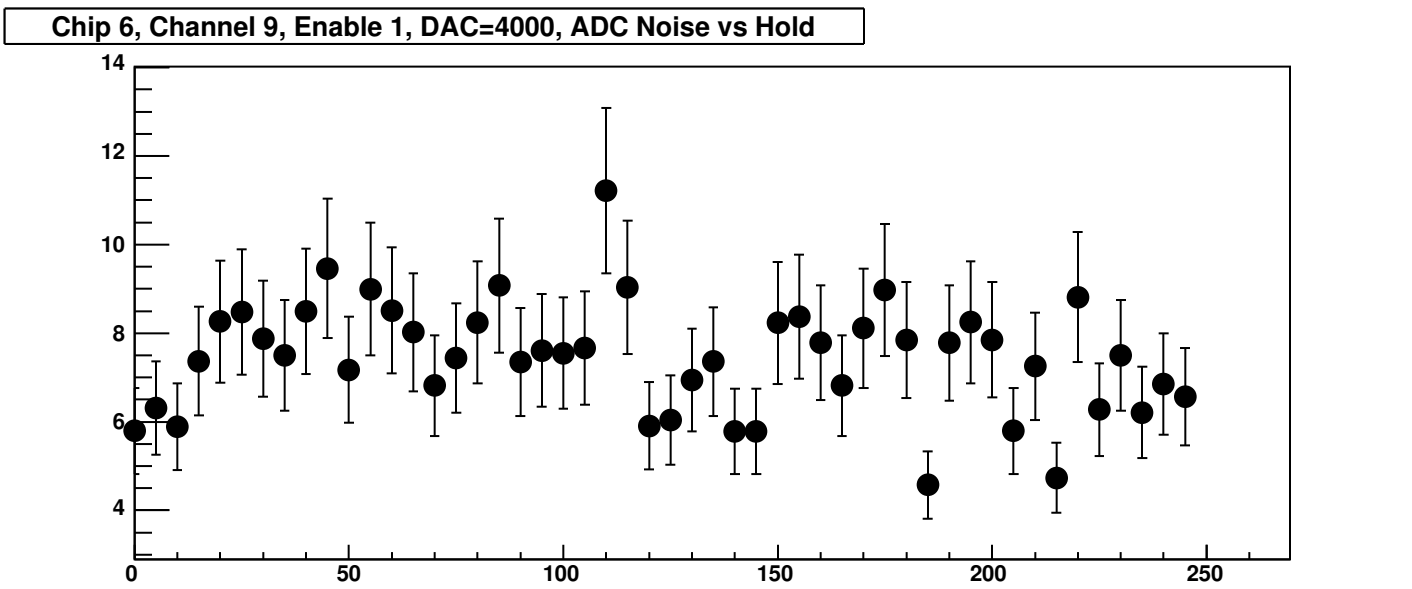
Chip 6, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold

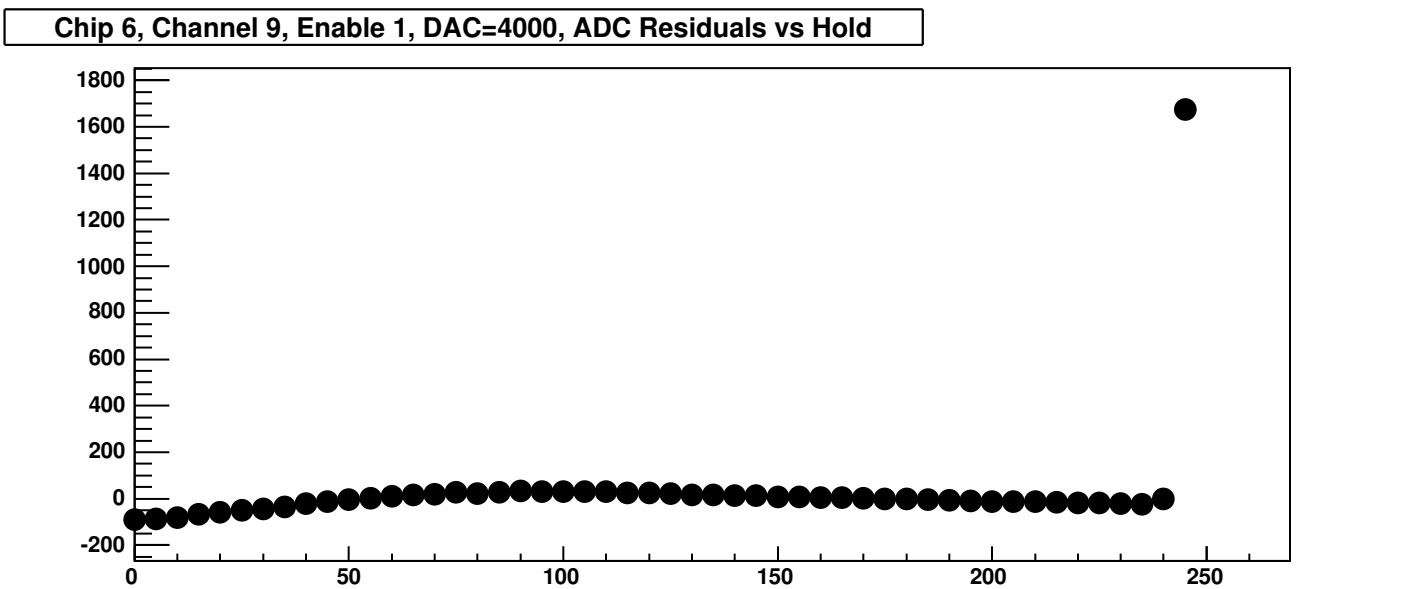


Chip 6, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold

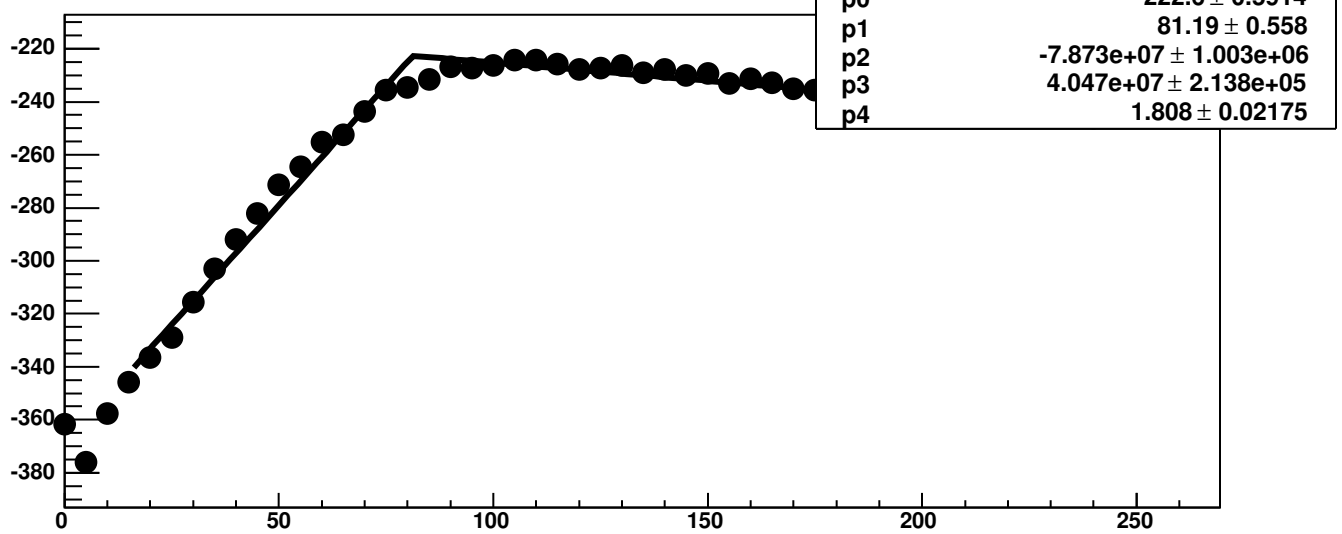



Chip 6, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 8993 / 41
p0 -217.1 ± 0.472
p1 239.9 ± 0.003842
p2 -3526 ± 10.09
p3 7.659 ± 0.04682
p4 0.2422 ± 0.003834
-220
-240
-260
-280
-300
-320
-340
-360
-380
0
50
100
150
200
250

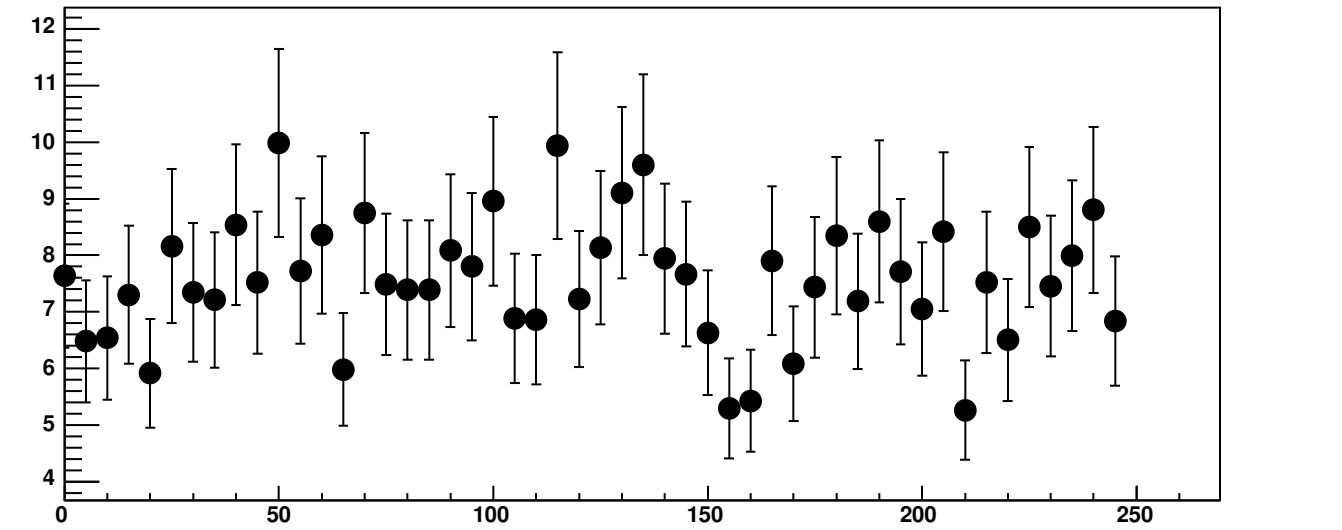

Chip 6, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 6, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

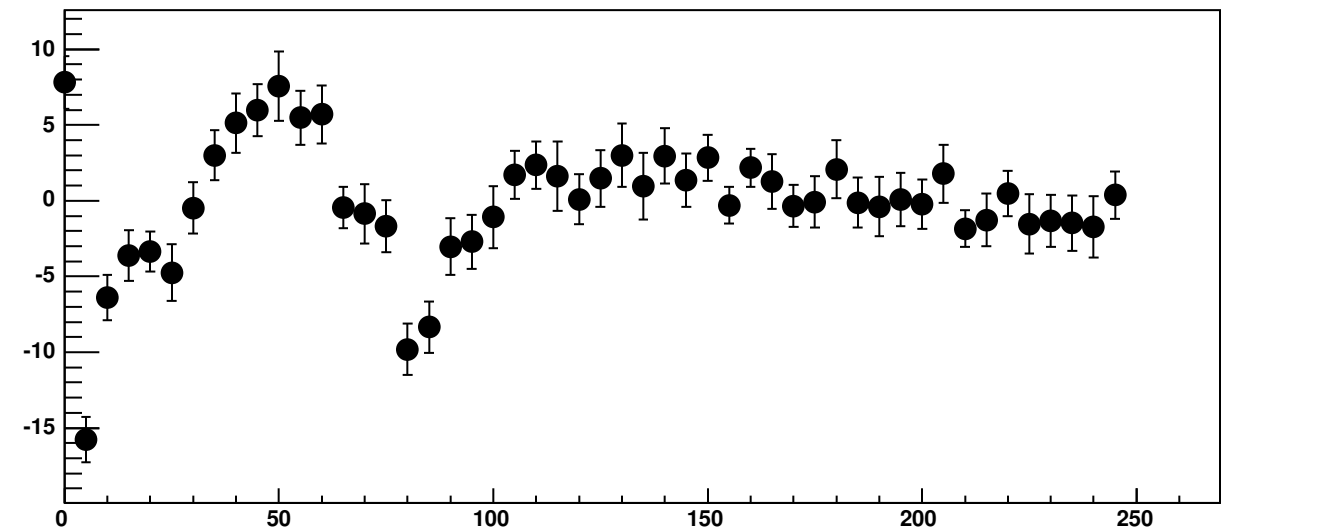
Chip 6, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold



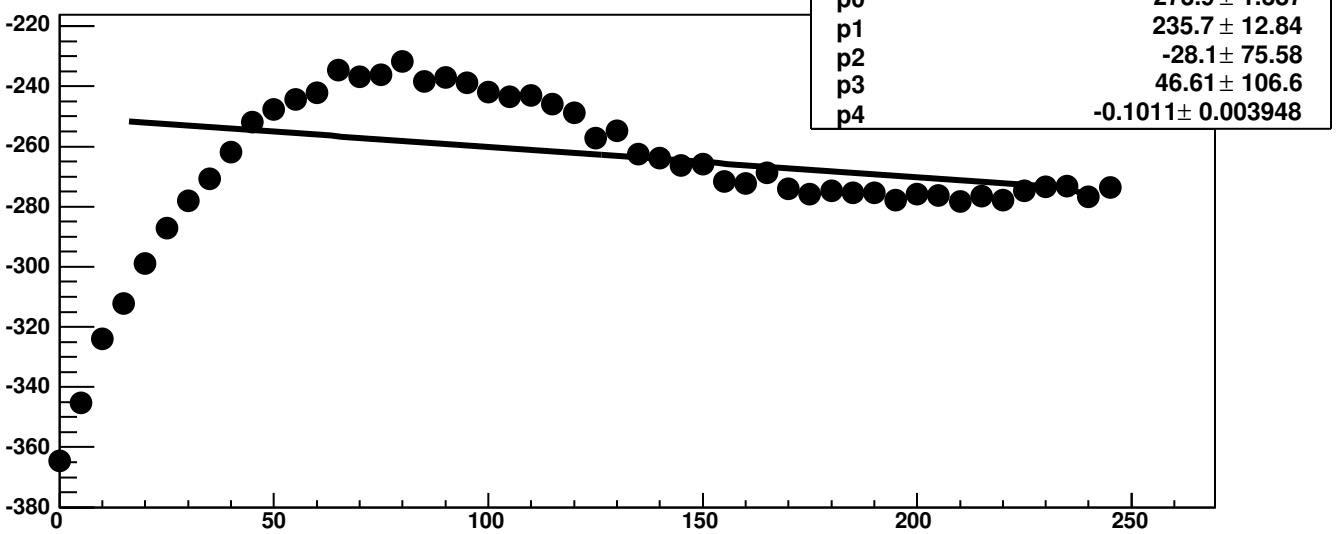
Chip 6, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



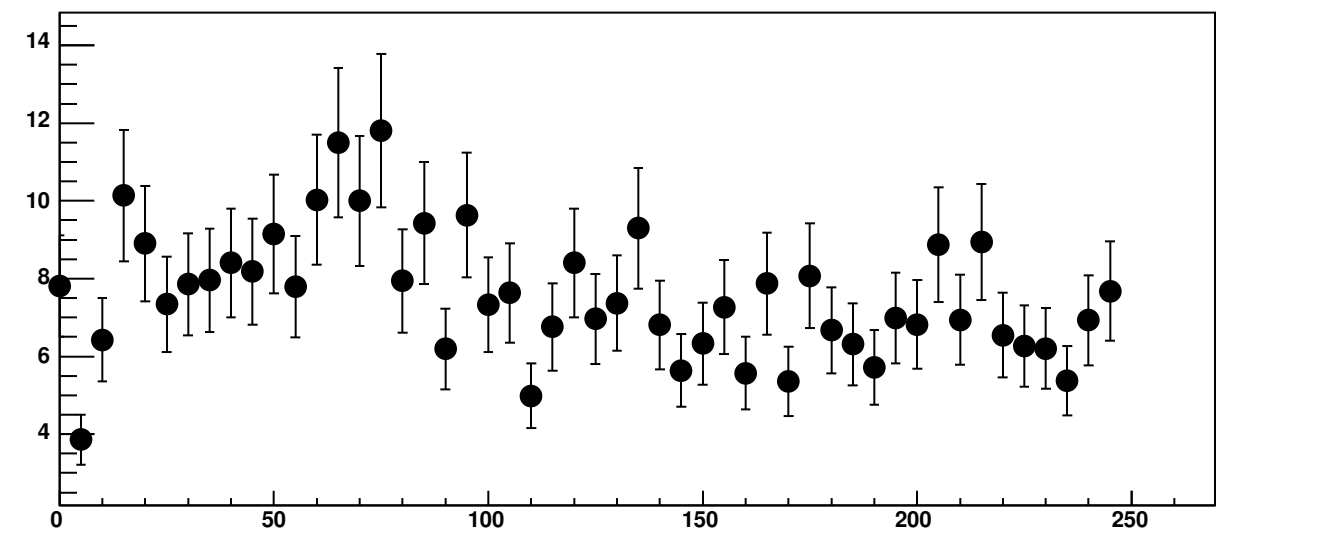
Chip 6, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold



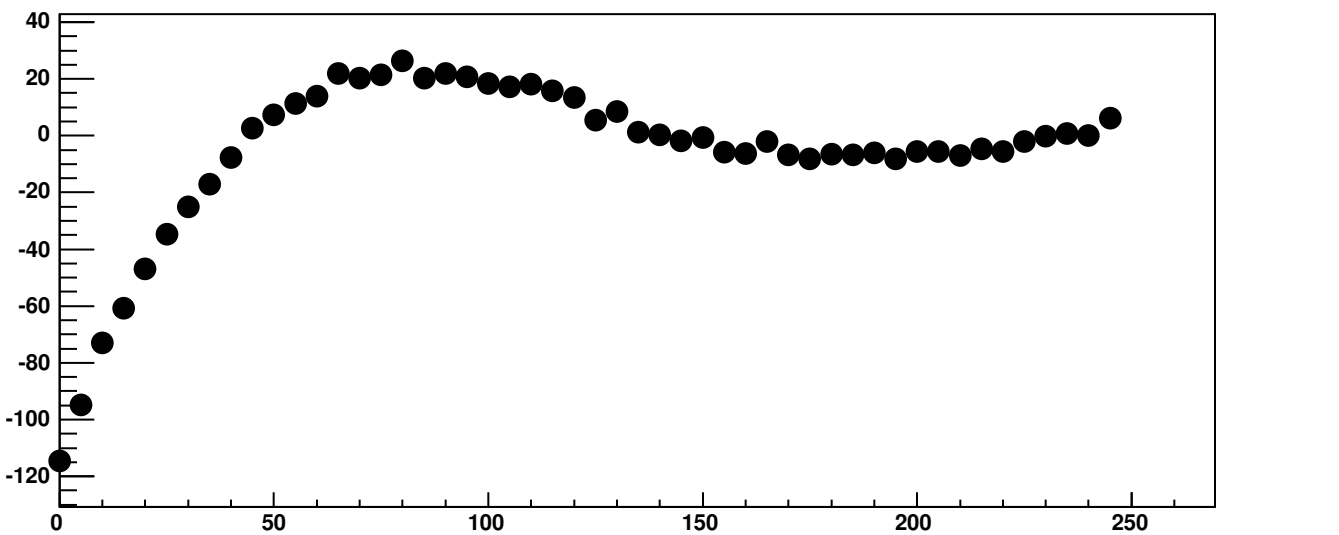
Chip 6, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold



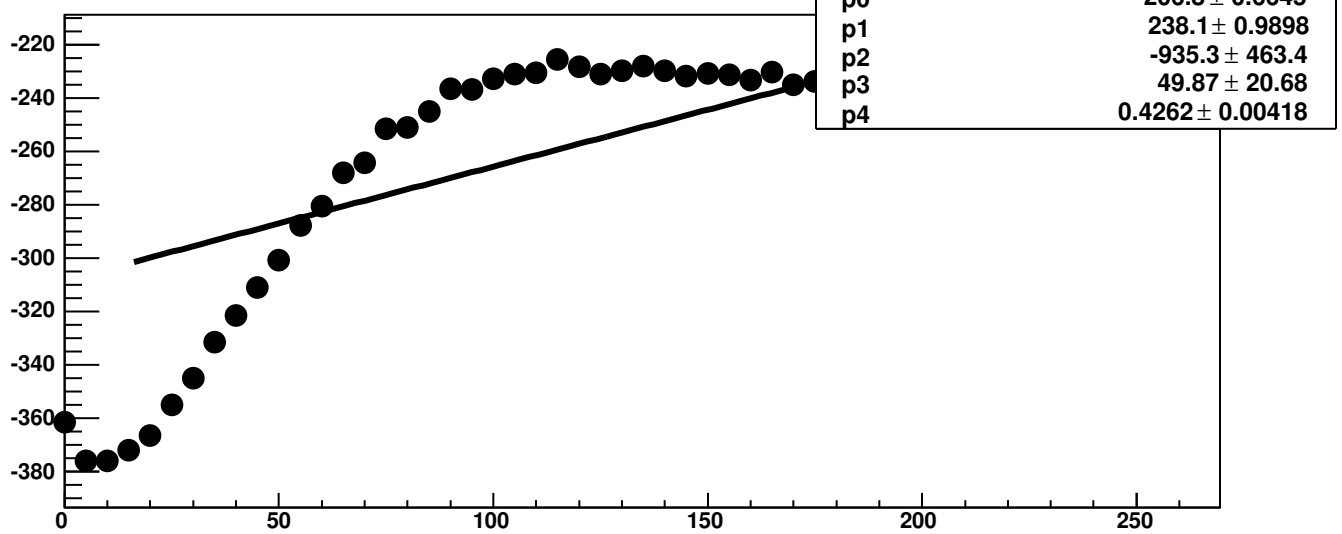
Chip 6, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold



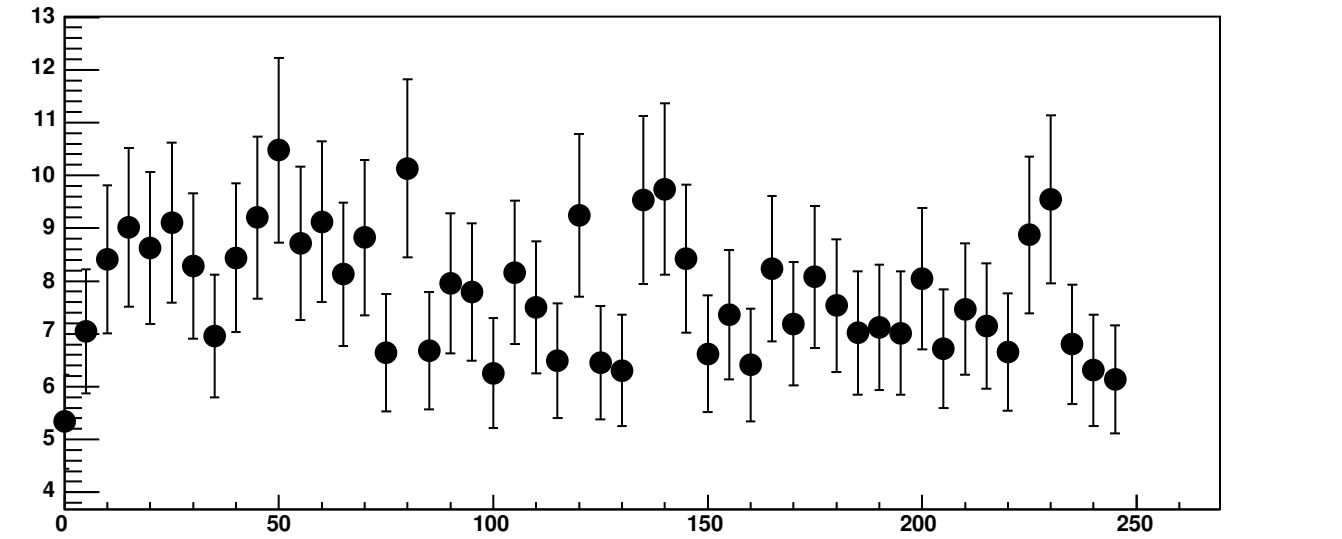
Chip 6, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold



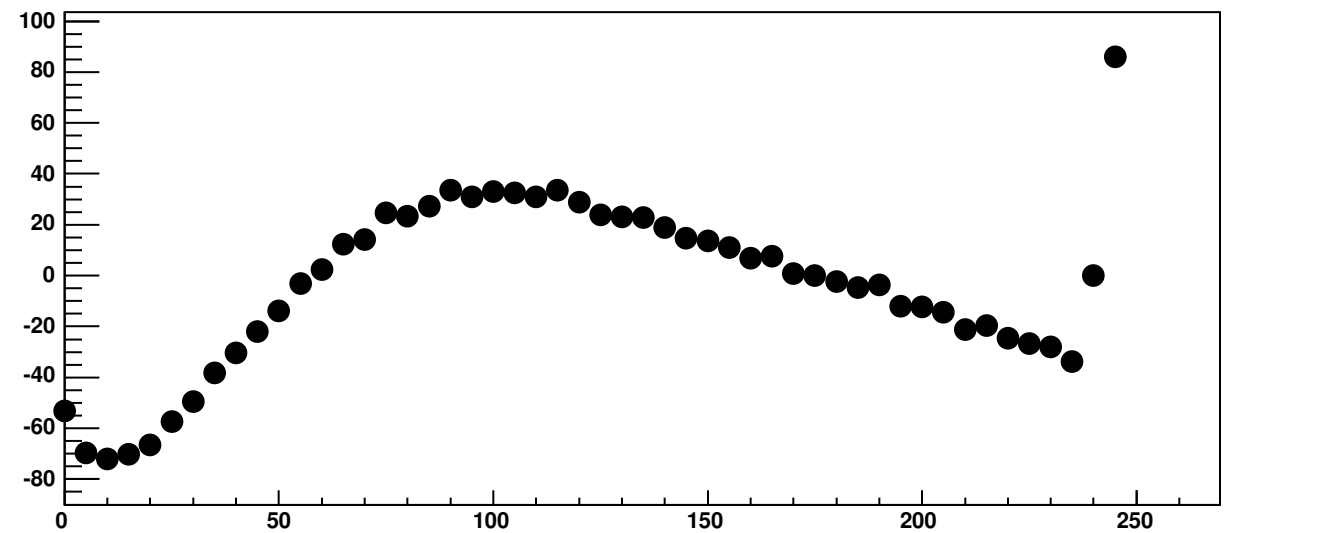
Chip 6, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold



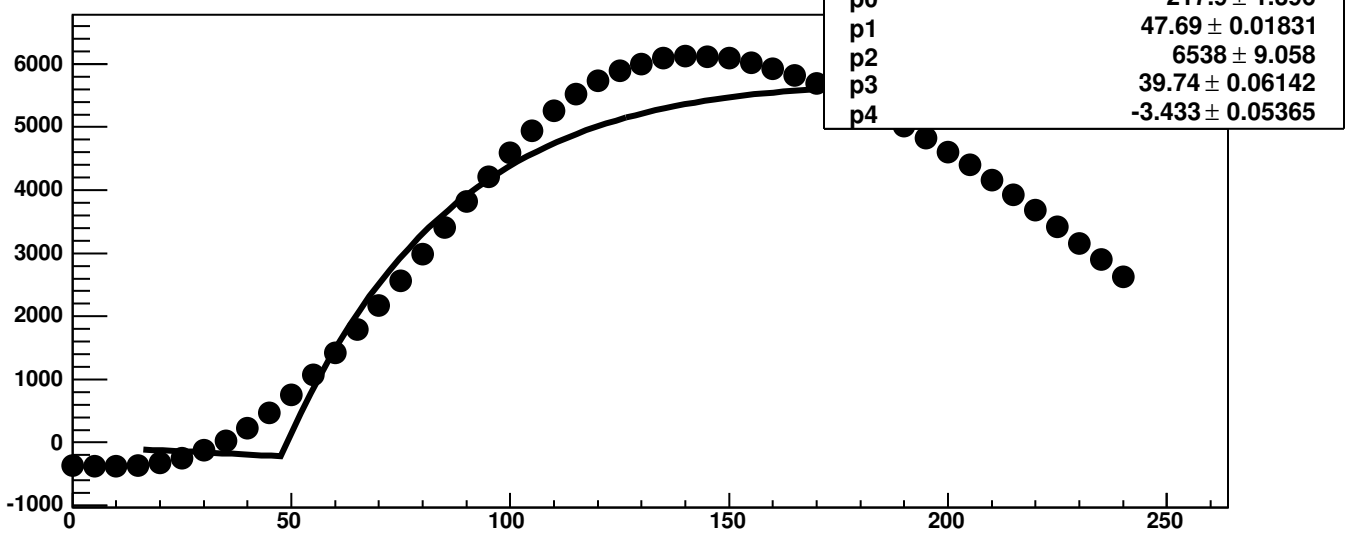
Chip 6, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



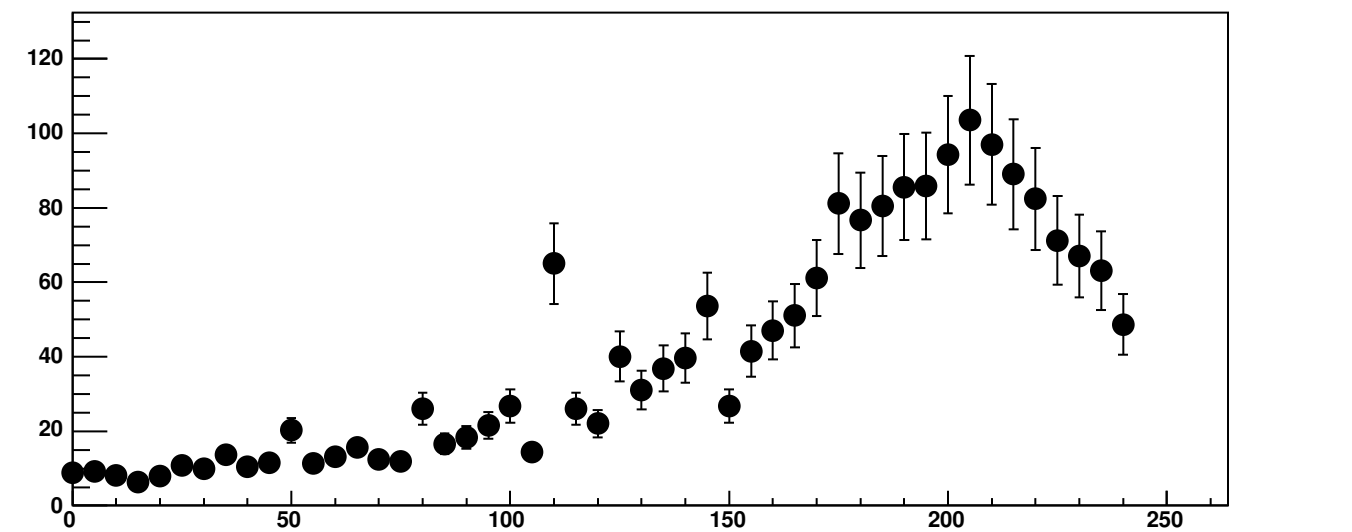
Chip 6, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold



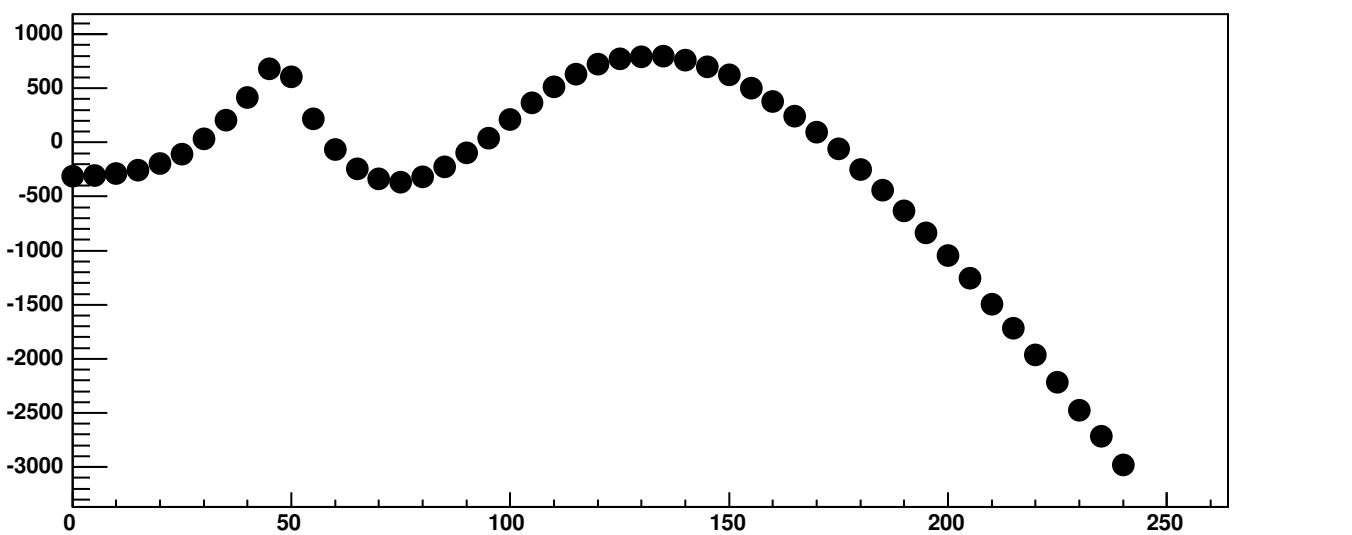
Chip 6, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold



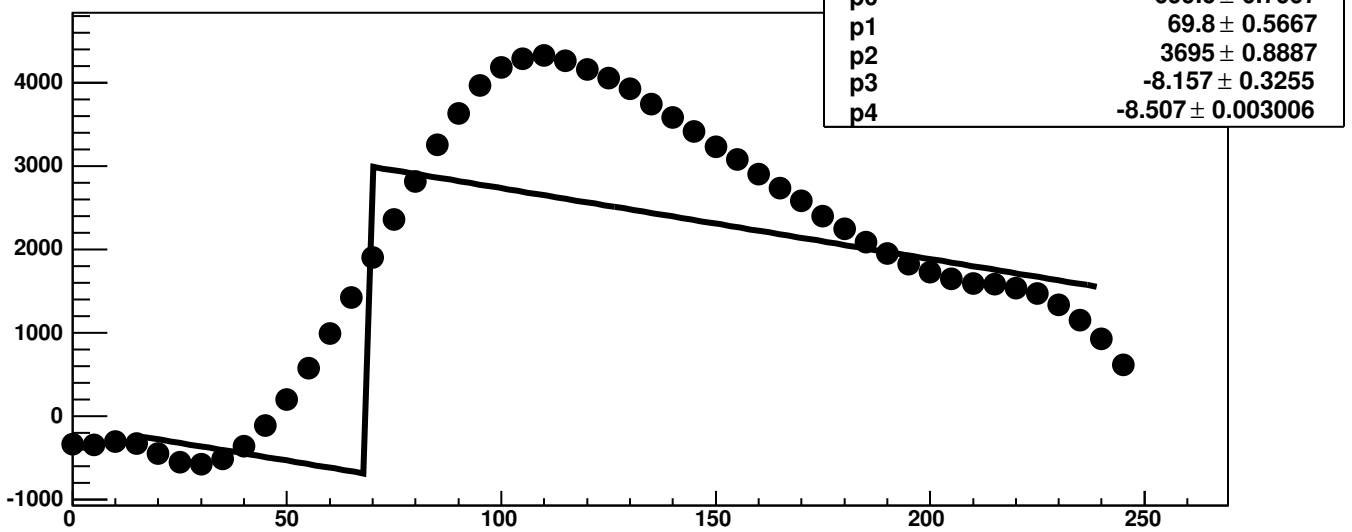
Chip 6, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



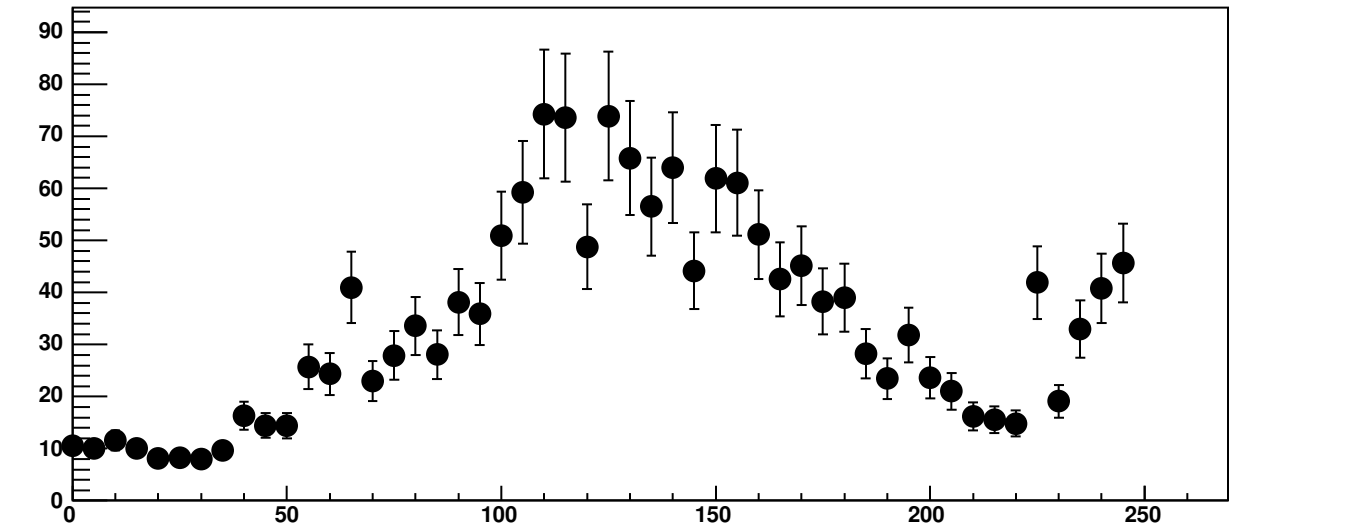
Chip 6, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold



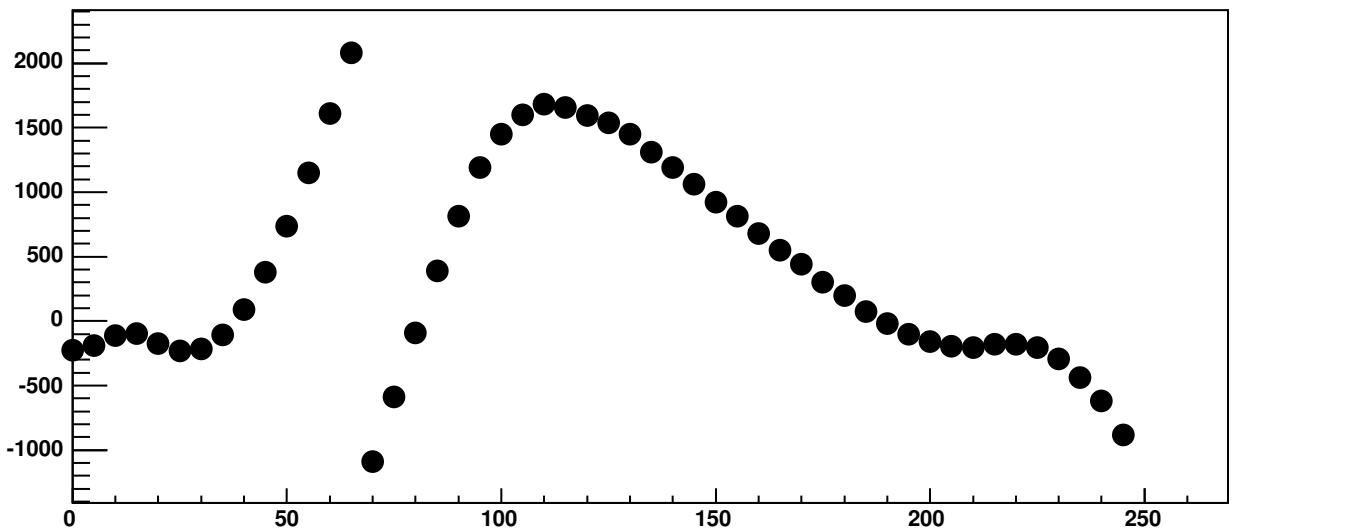
Chip 6, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold



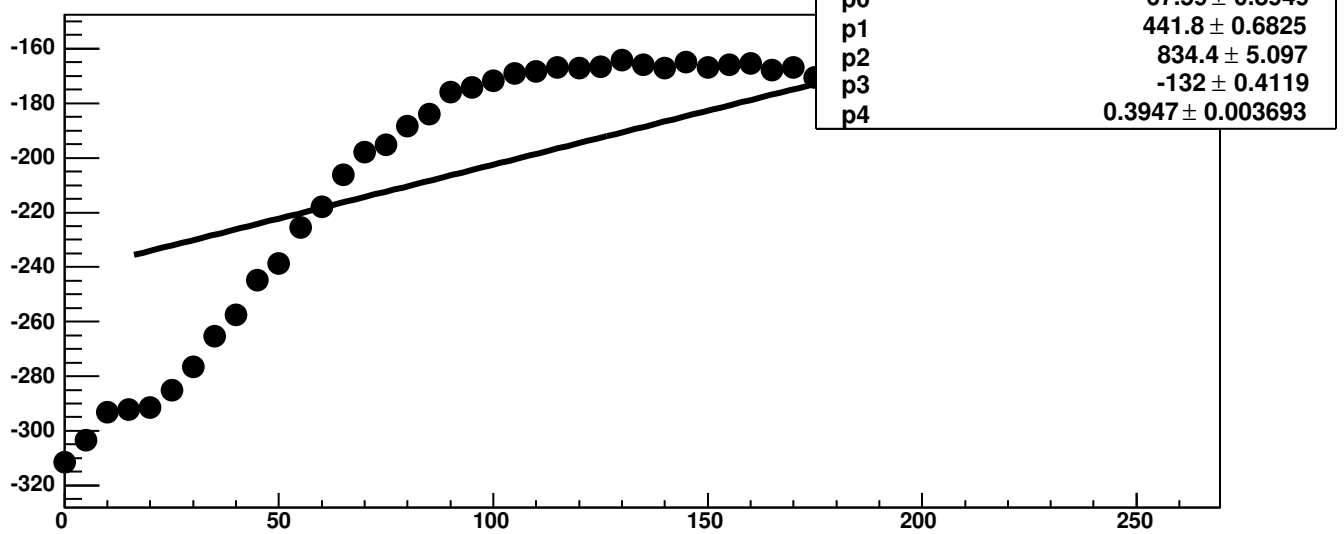
Chip 6, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold



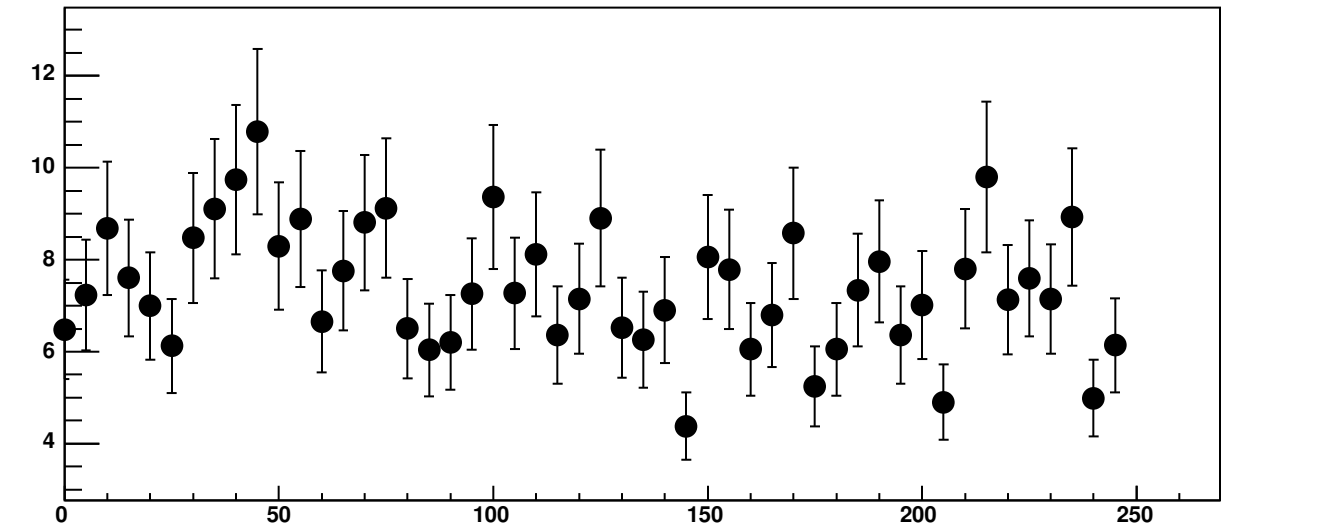
Chip 6, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold



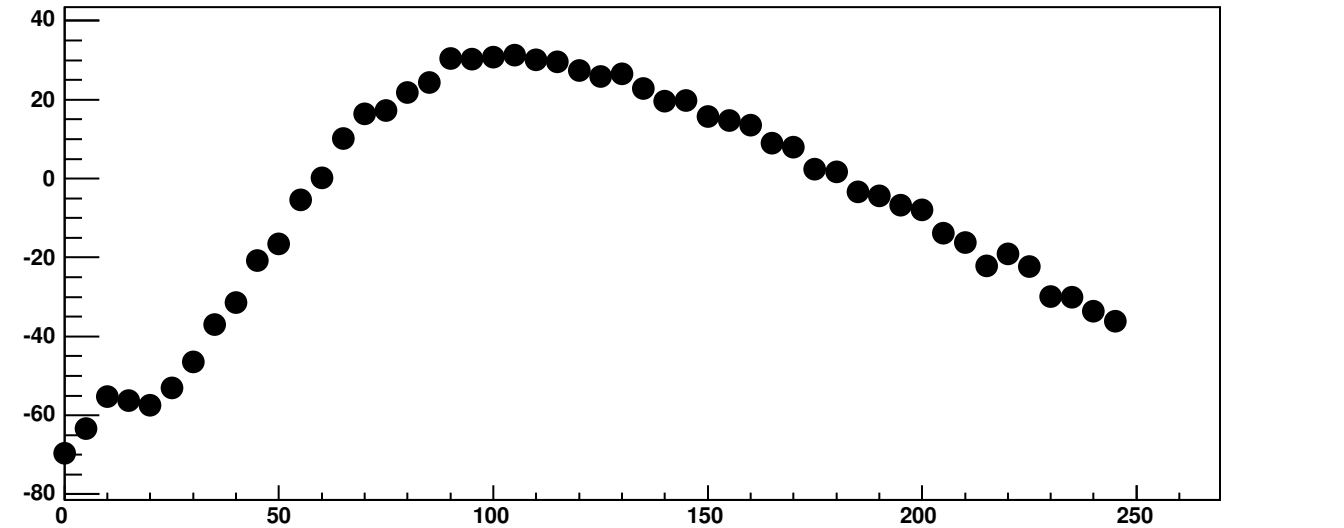
Chip 6, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold



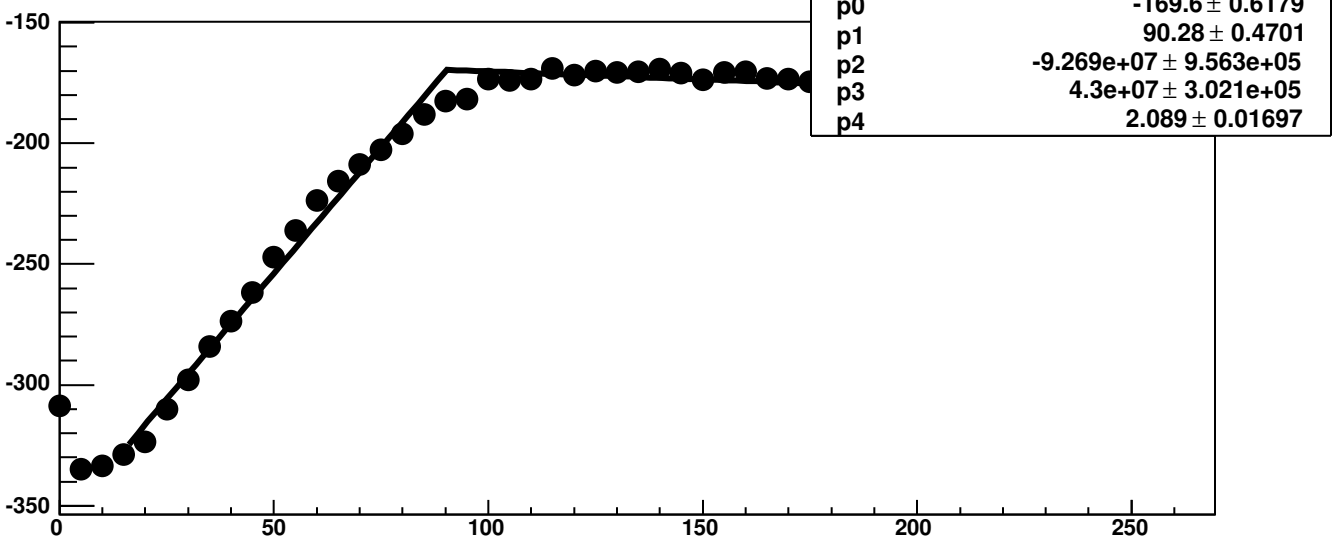
Chip 6, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



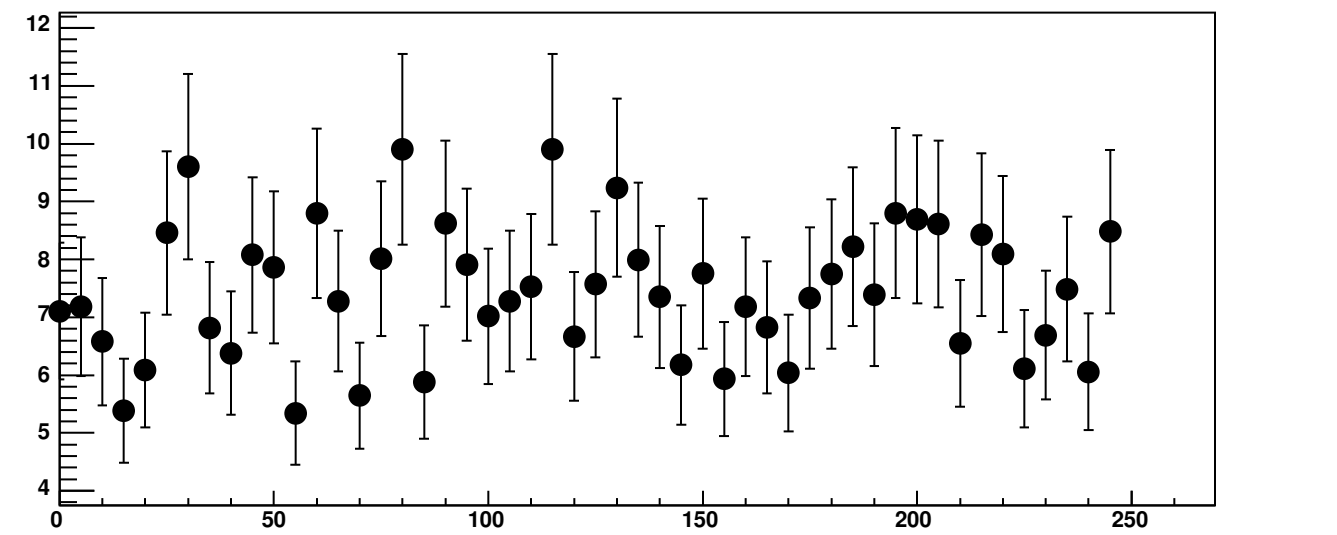
Chip 6, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold



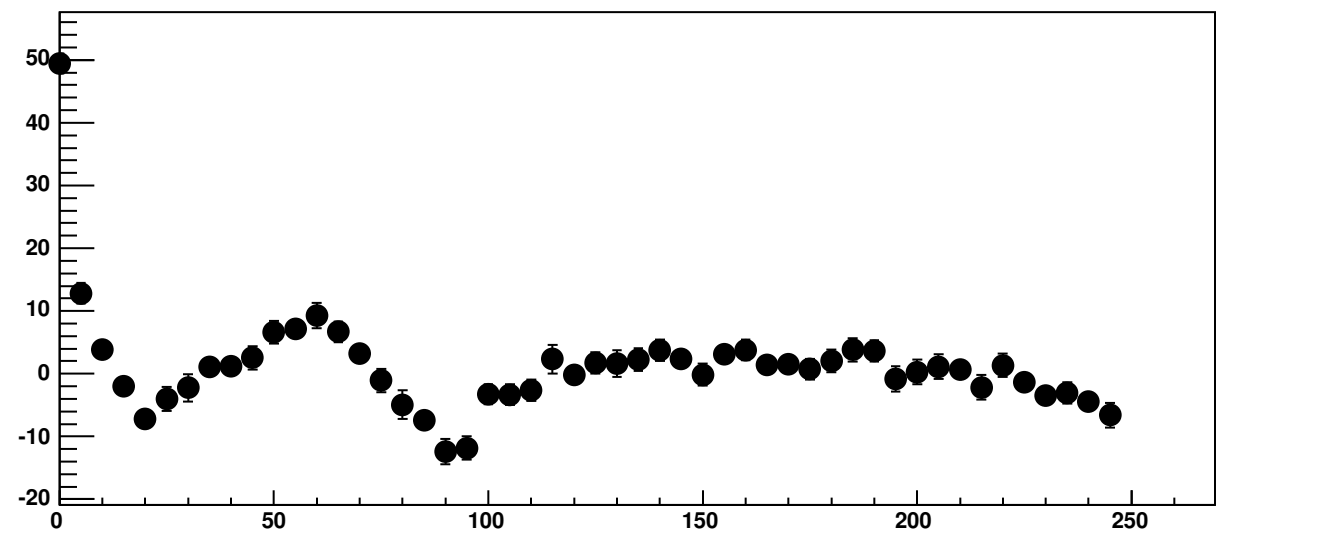
Chip 6, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold



Chip 6, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold

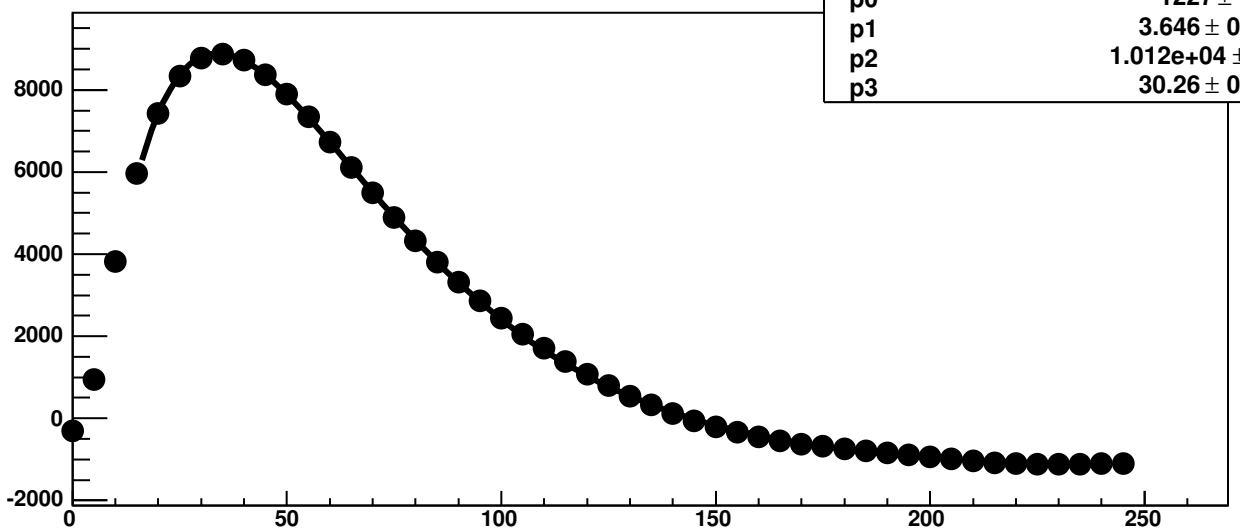


Chip 6, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

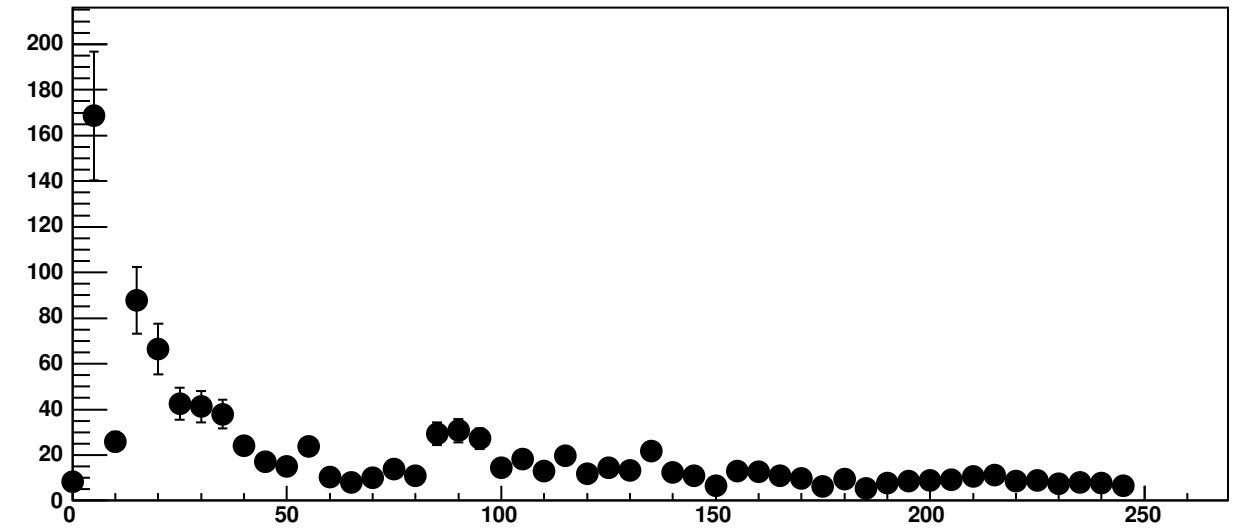


Chip 6, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold

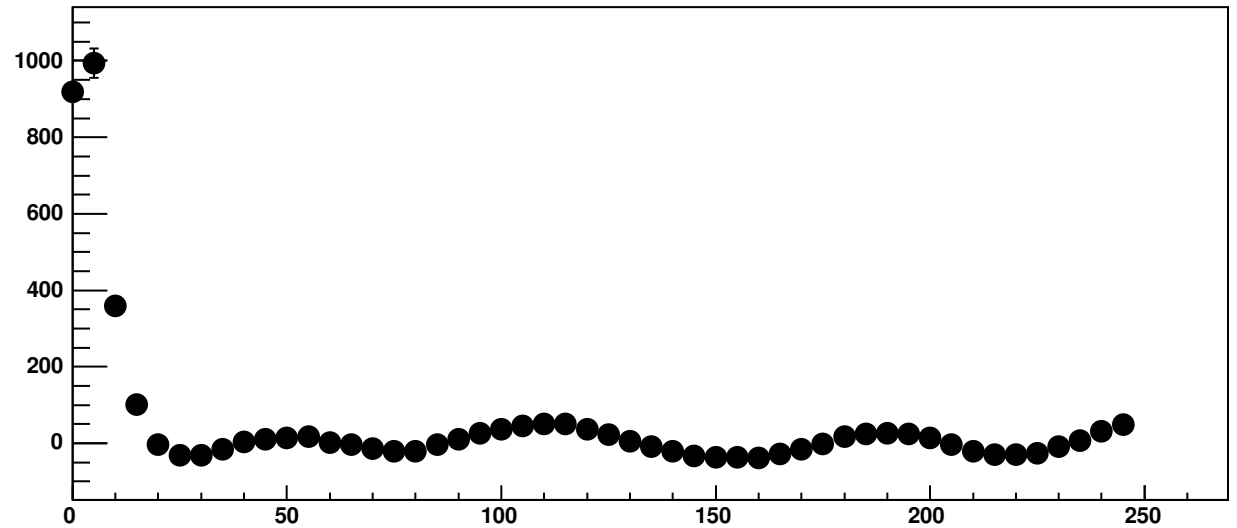
χ^2 / ndf	4315 / 42
p0	-1227 ± 0.7076
p1	3.646 ± 0.02735
p2	1.012e+04 ± 2.603
p3	30.26 ± 0.01073



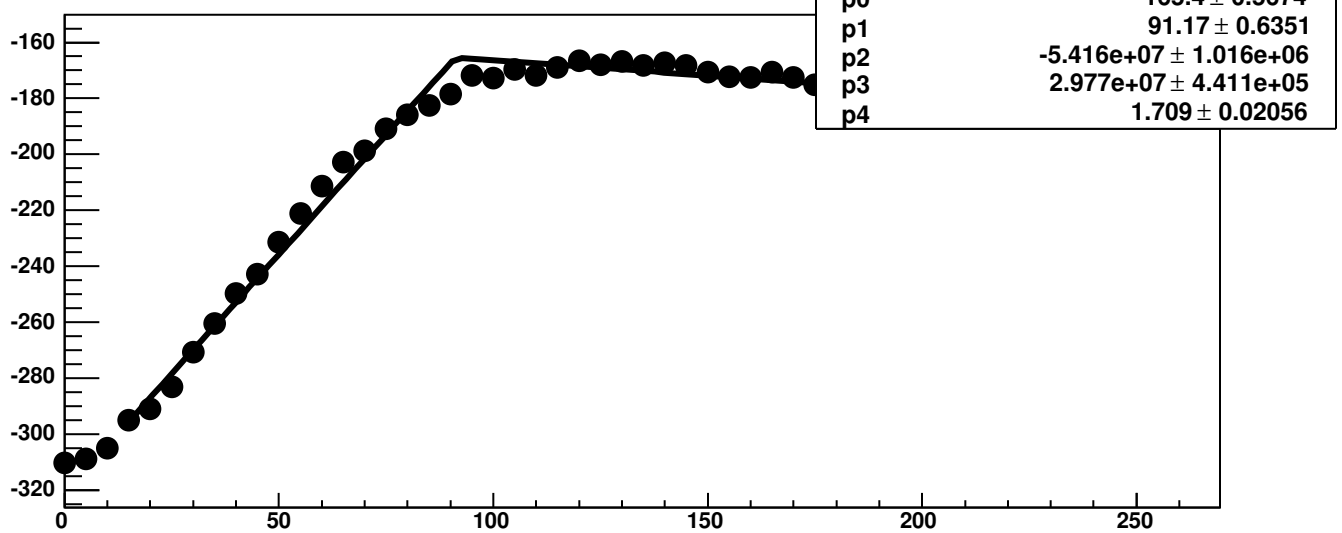
Chip 6, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold



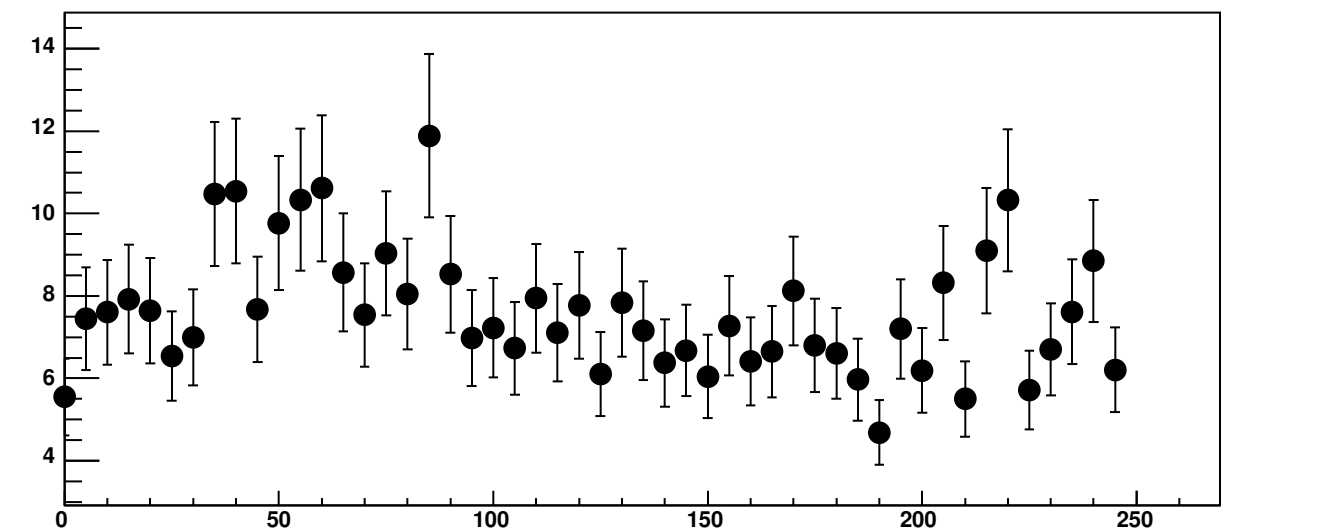
Chip 6, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold



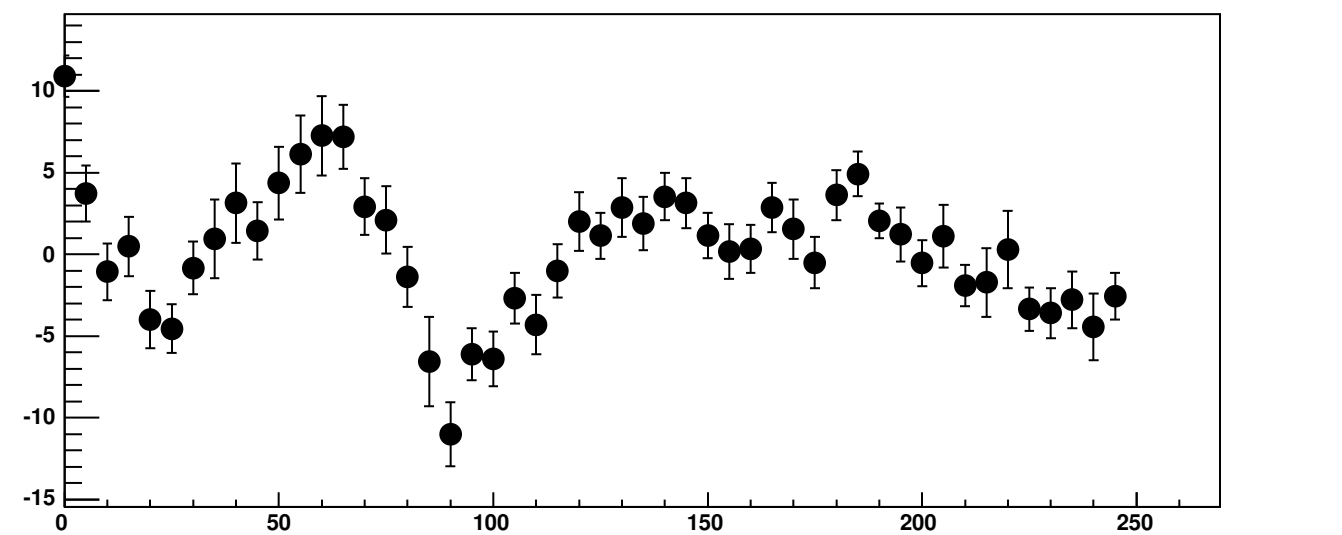
Chip 6, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

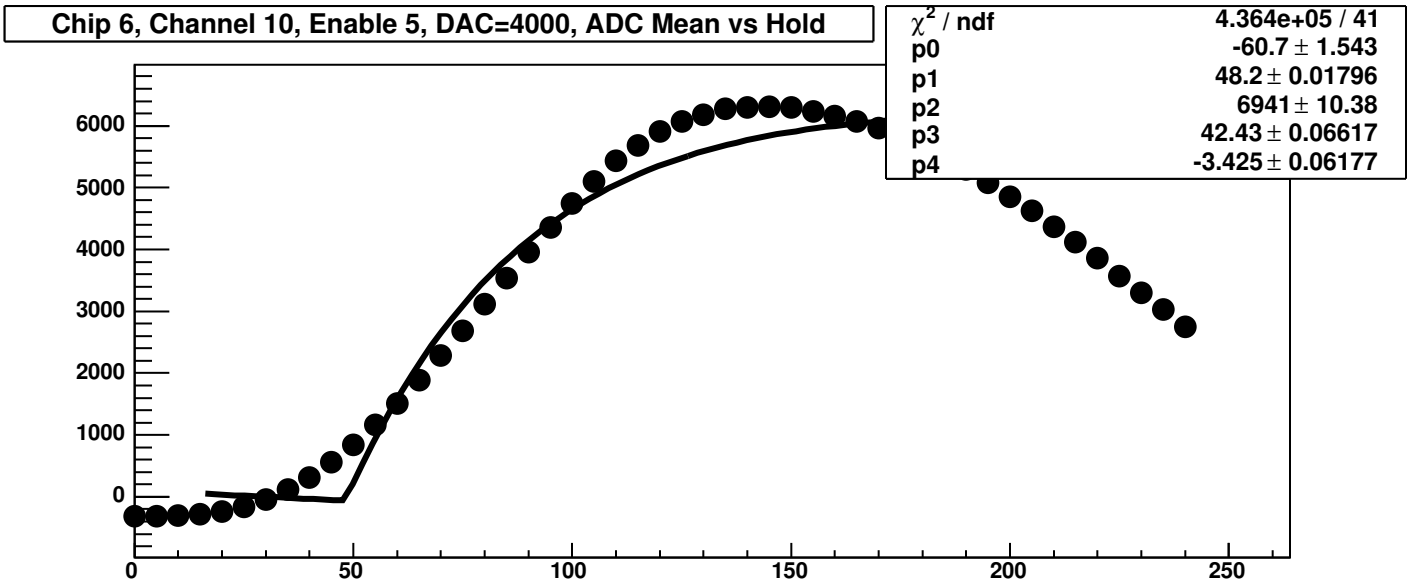


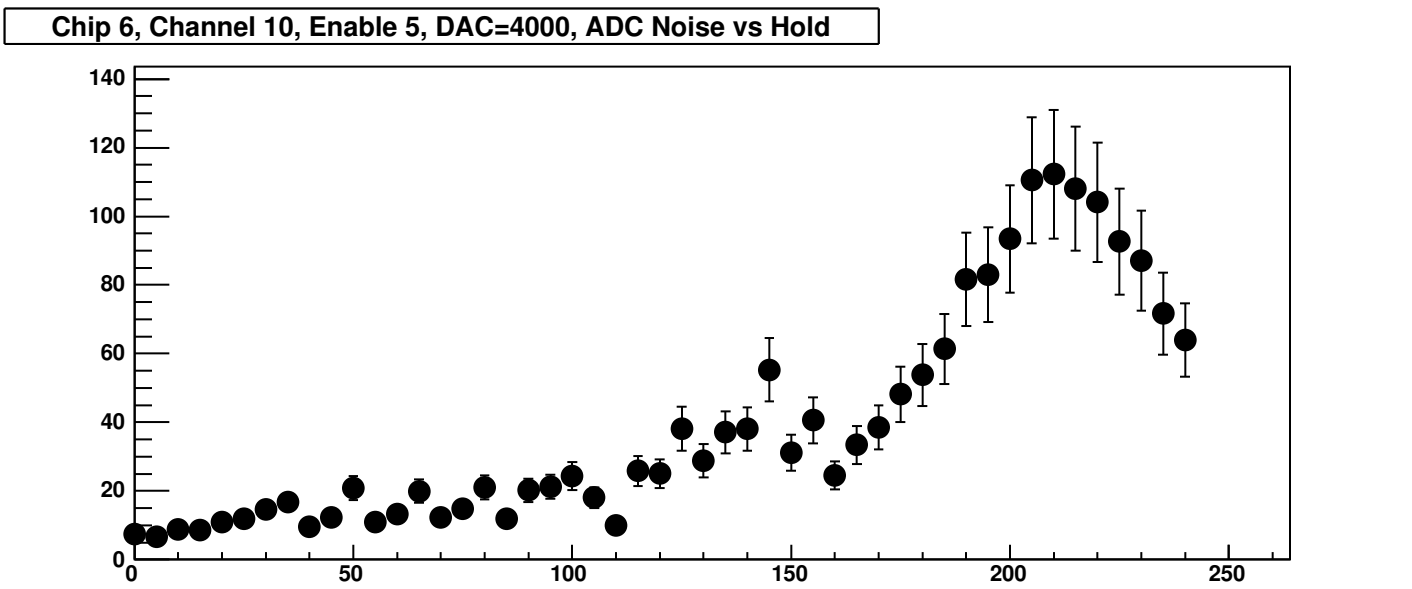
Chip 6, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold

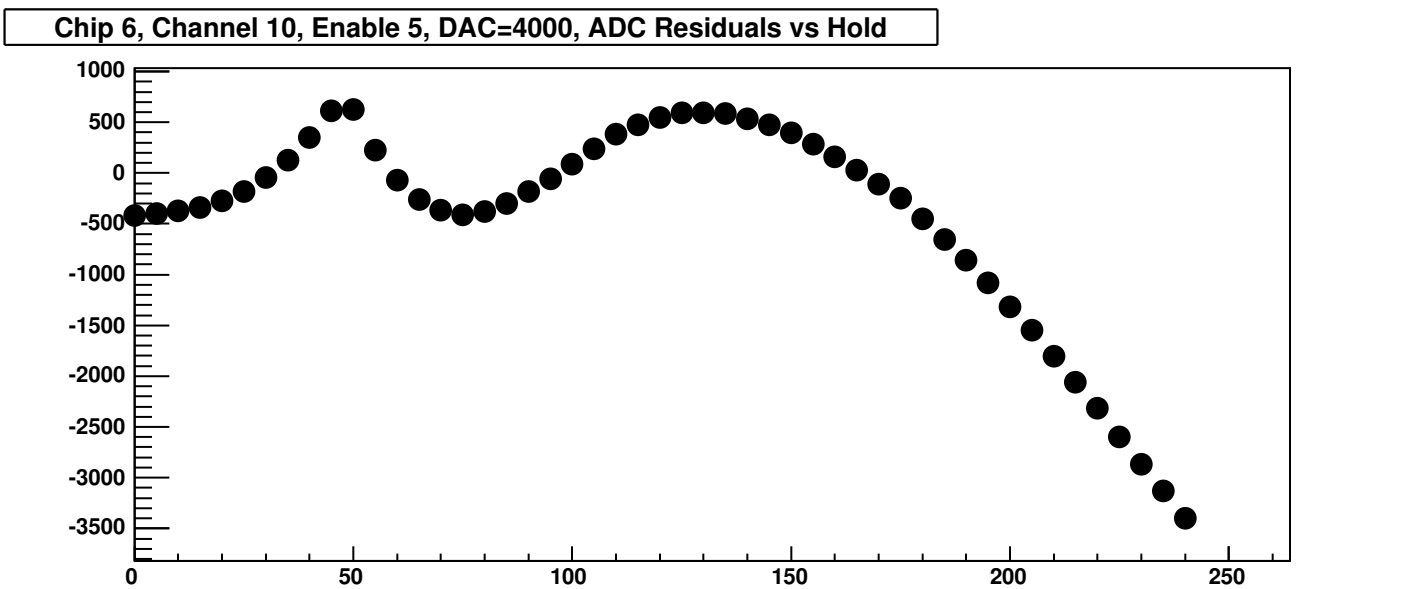


Chip 6, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

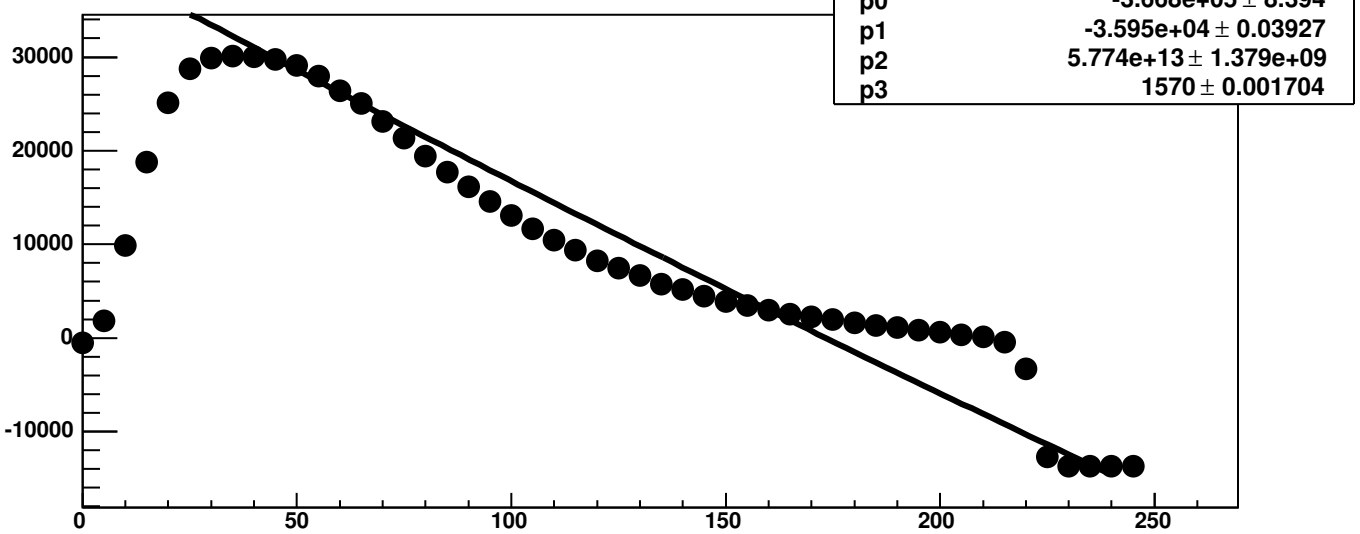



Chip 6, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.364e+05 / 41
p0 -60.7 ± 1.543
p1 48.2 ± 0.01796
p2 6941 ± 10.38
p3 42.43 ± 0.06617
p4 -3.425 ± 0.06177
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

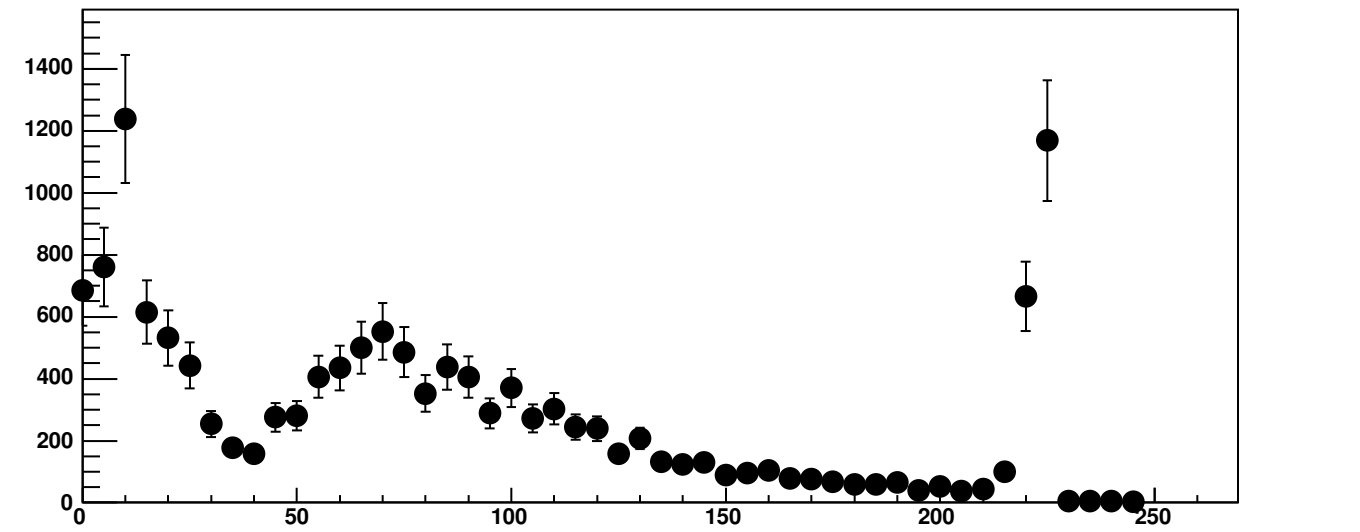

Chip 6, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 6, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
0
50
100
150
200
250

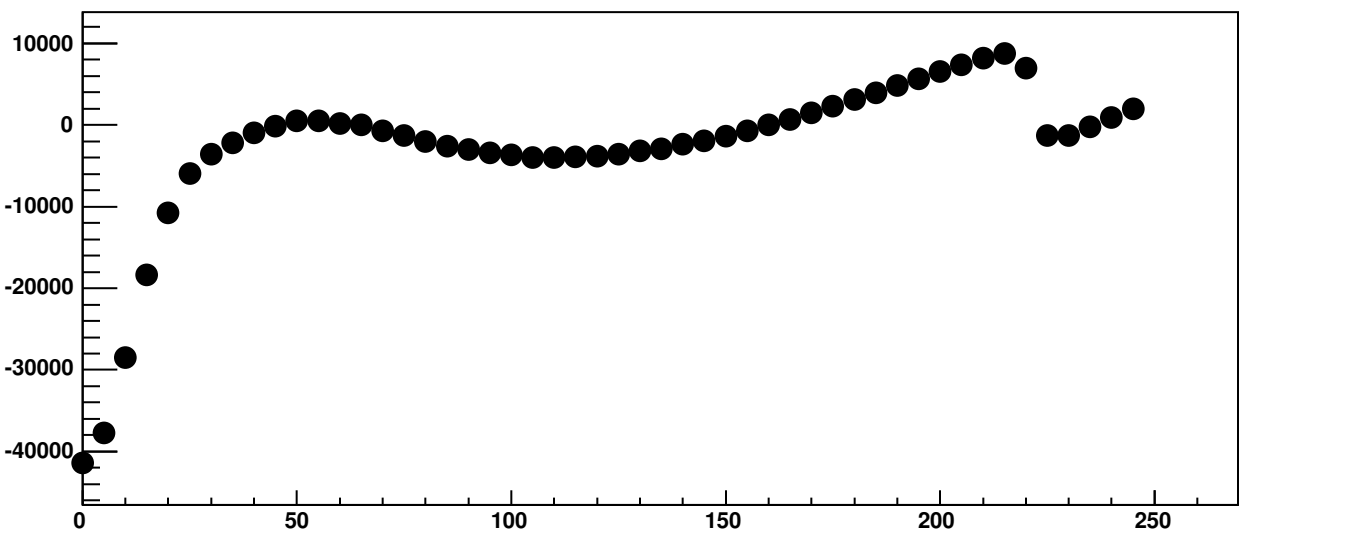
Chip 6, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold

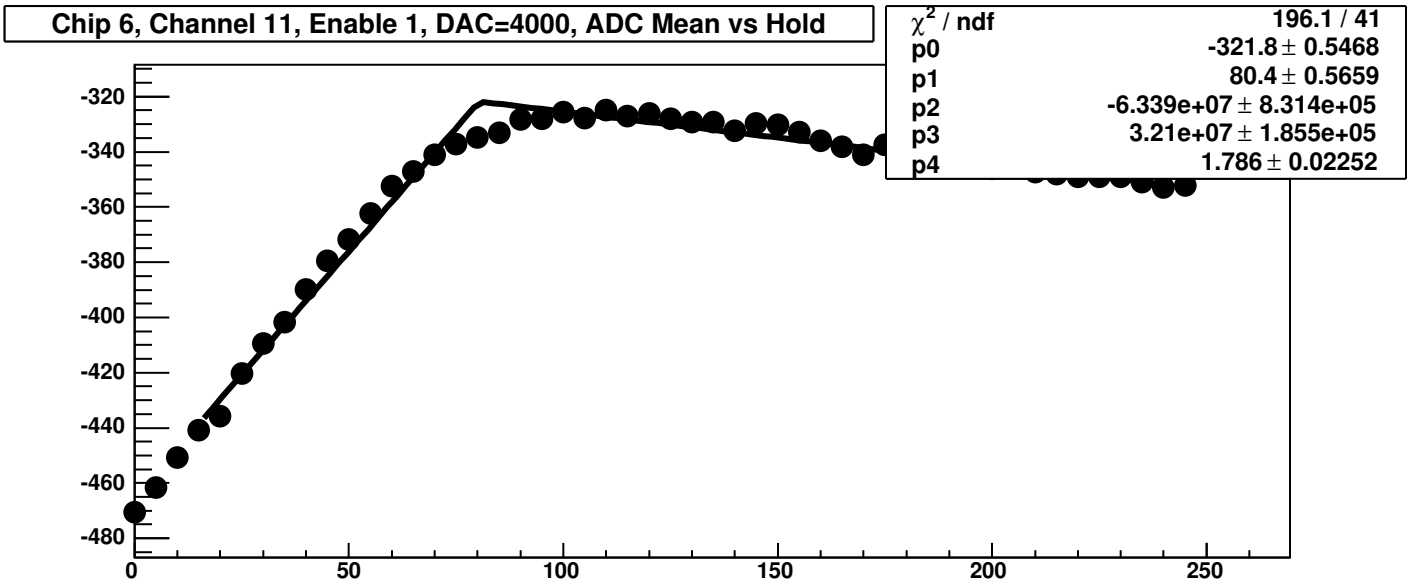


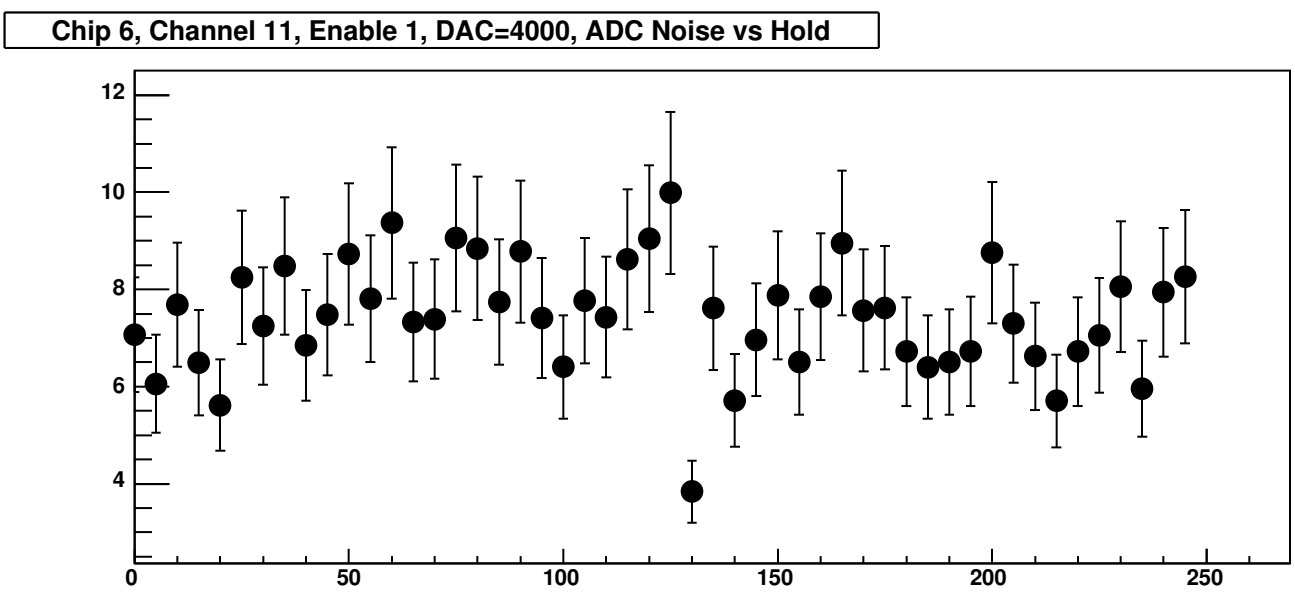
Chip 6, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold

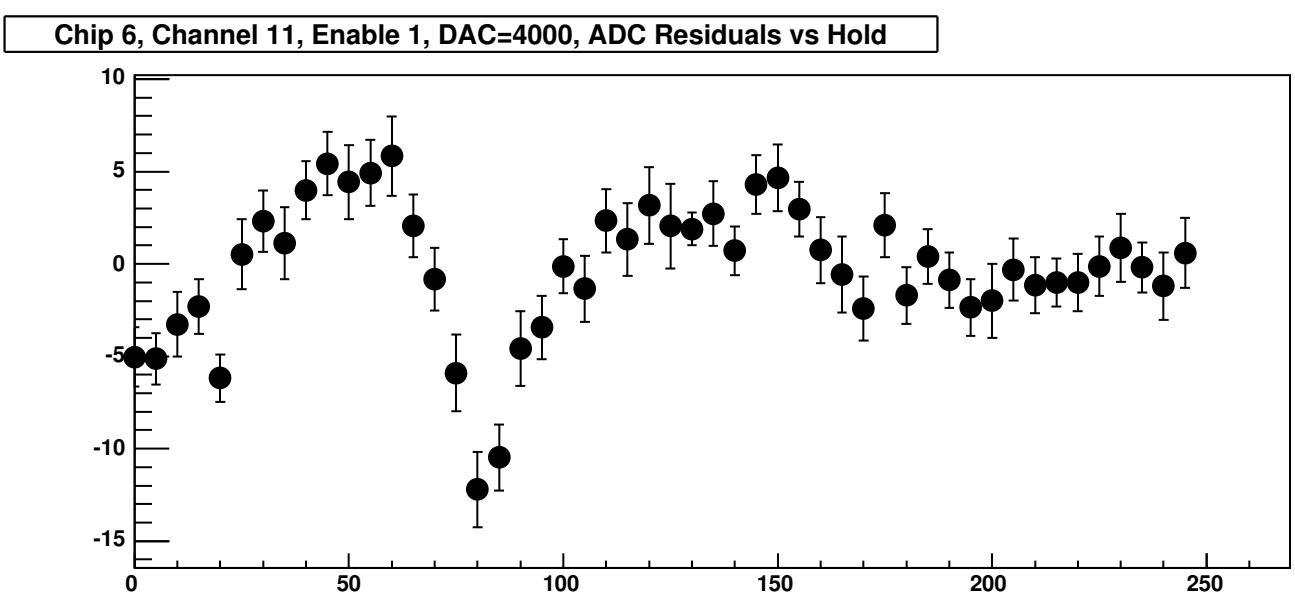


Chip 6, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold

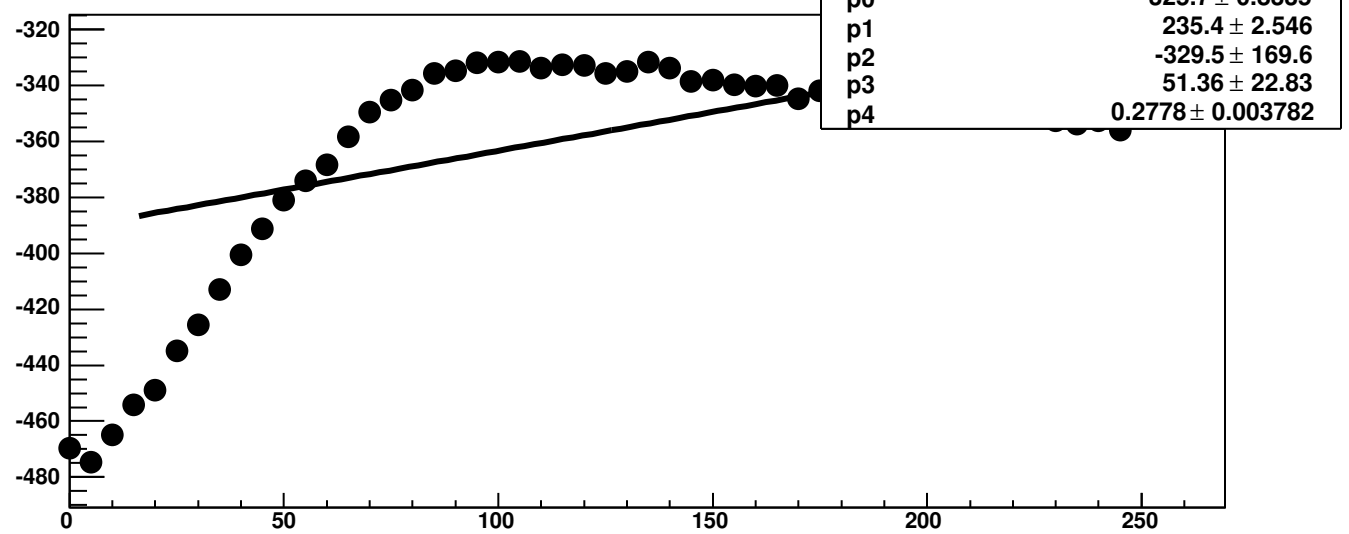



Chip 6, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 196.1 / 41
p0 -321.8 ± 0.5468
p1 80.4 ± 0.5659
p2 -6.339e+07 ± 8.314e+05
p3 3.21e+07 ± 1.855e+05
p4 1.786 ± 0.02252
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
50
100
150
200
250

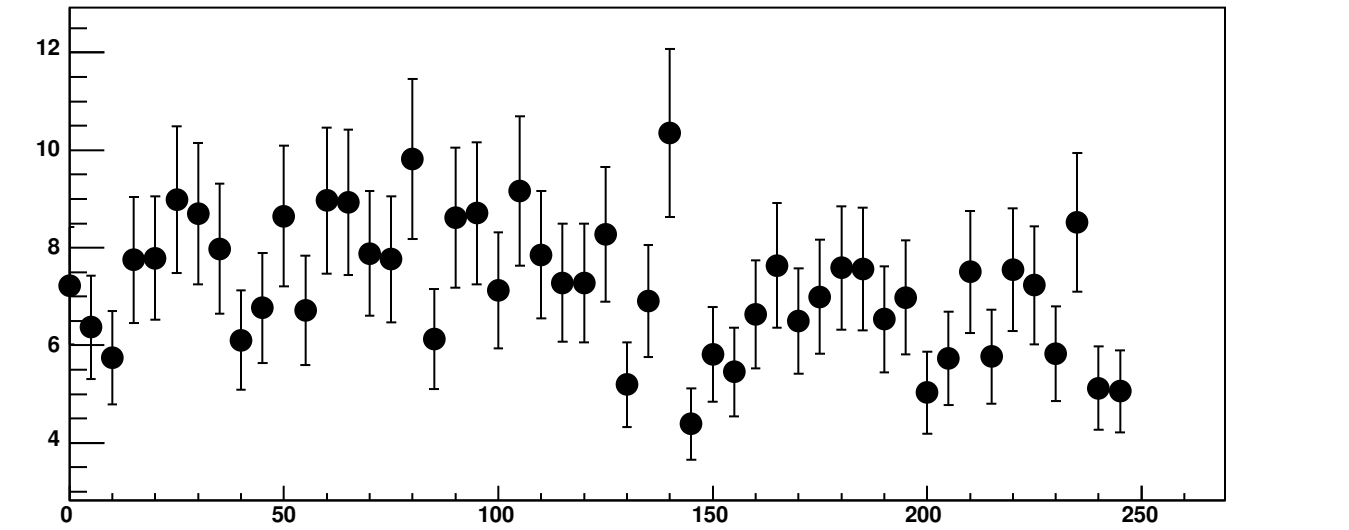

Chip 6, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 6, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
0
50
100
150
200
250

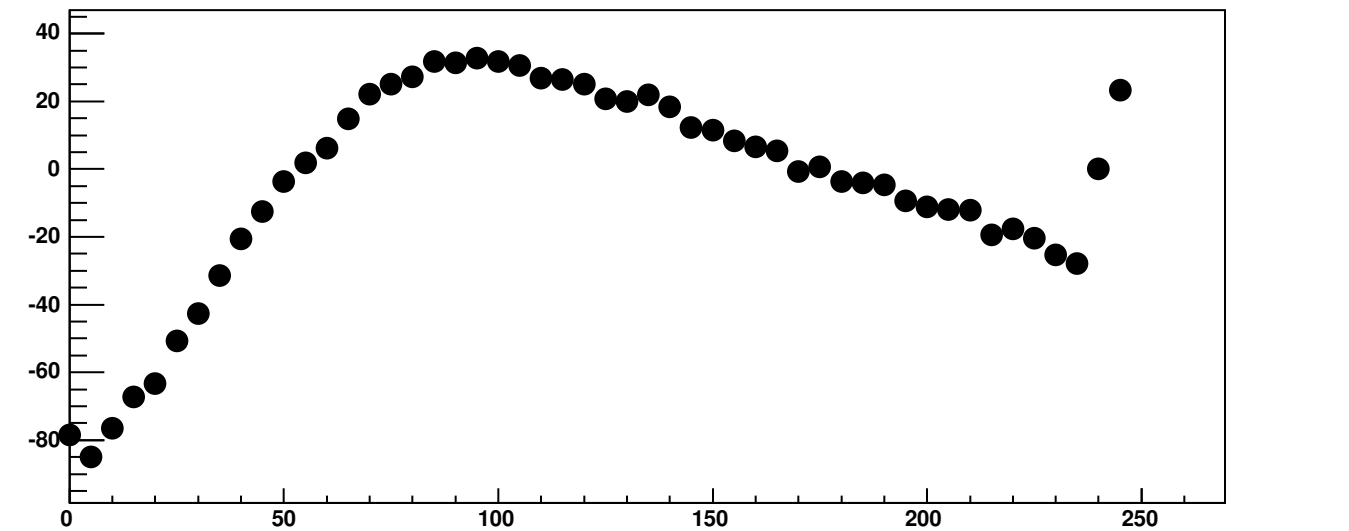
Chip 6, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold



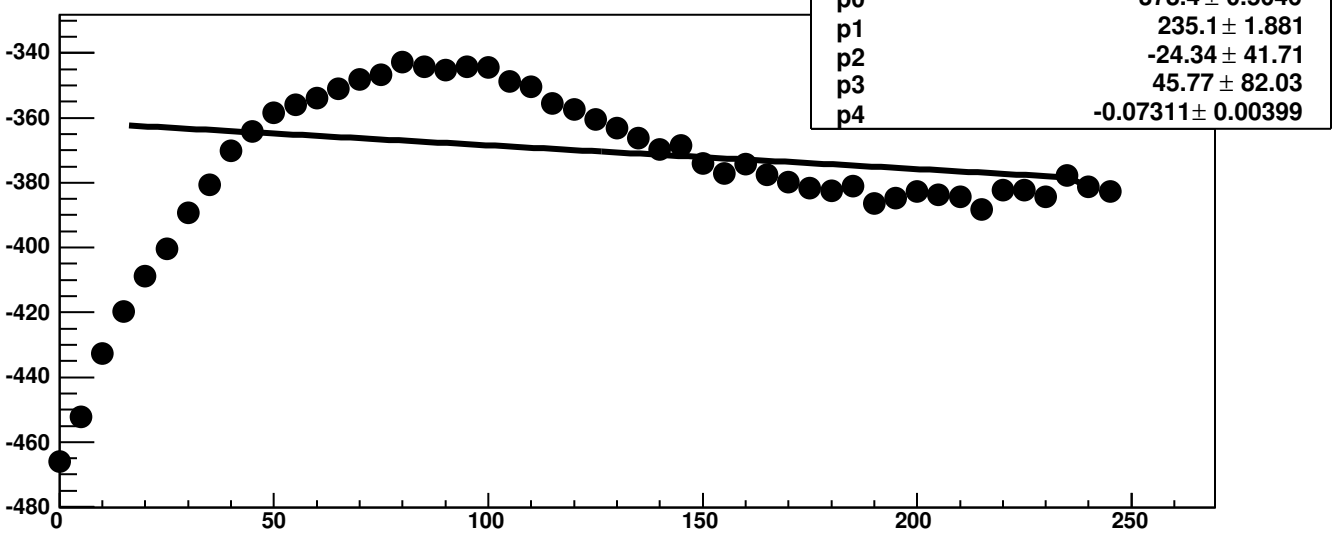
Chip 6, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



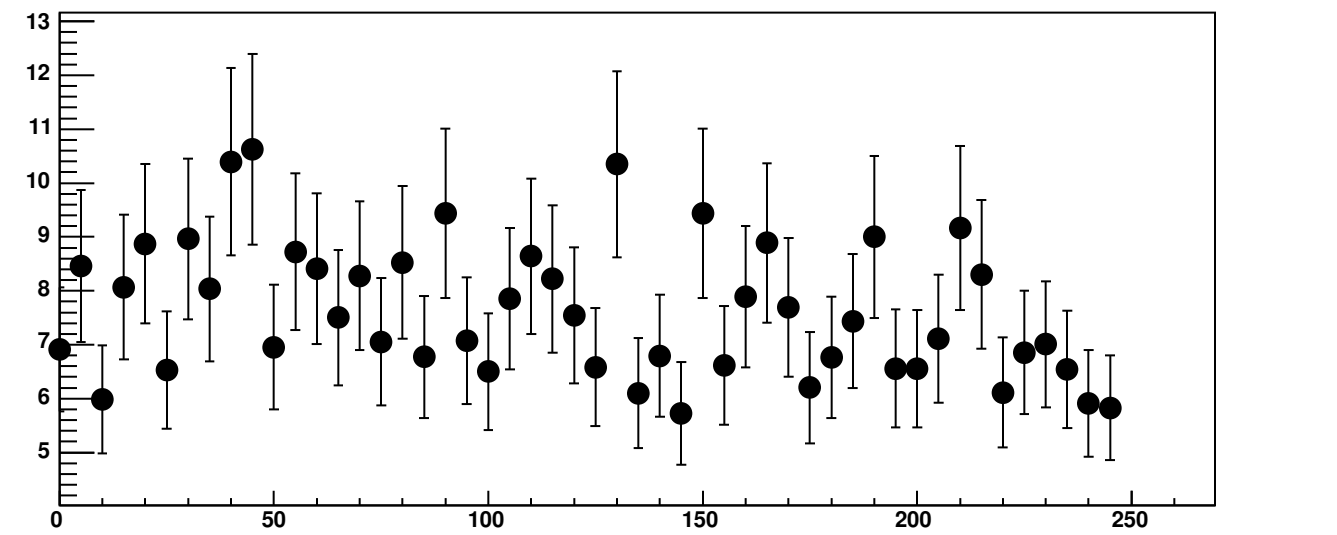
Chip 6, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold



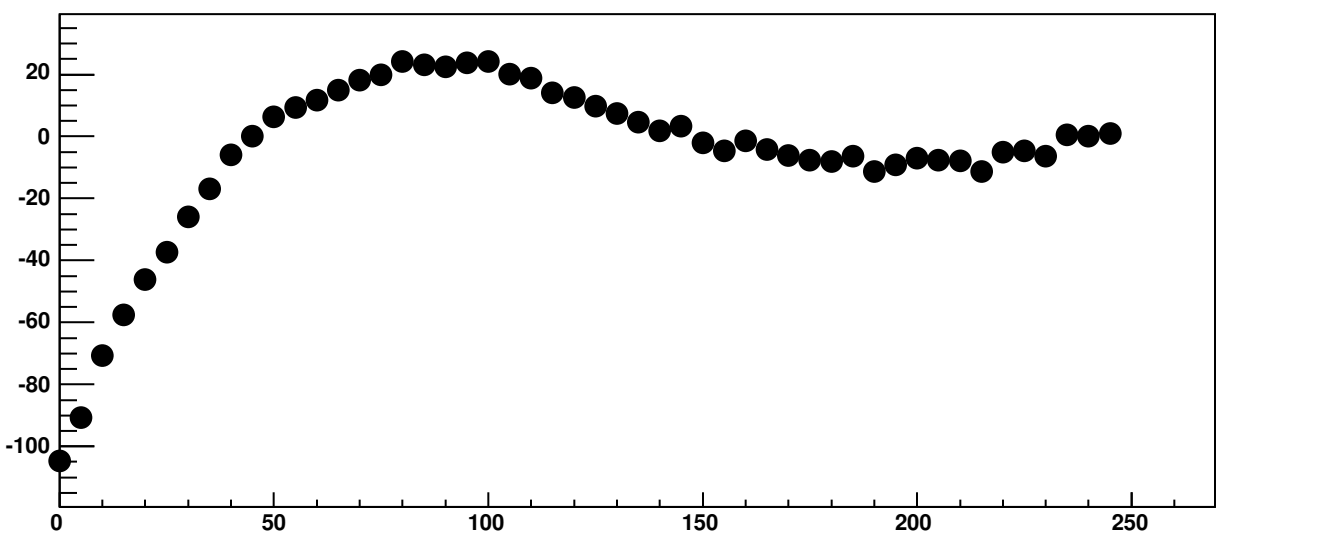
Chip 6, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold



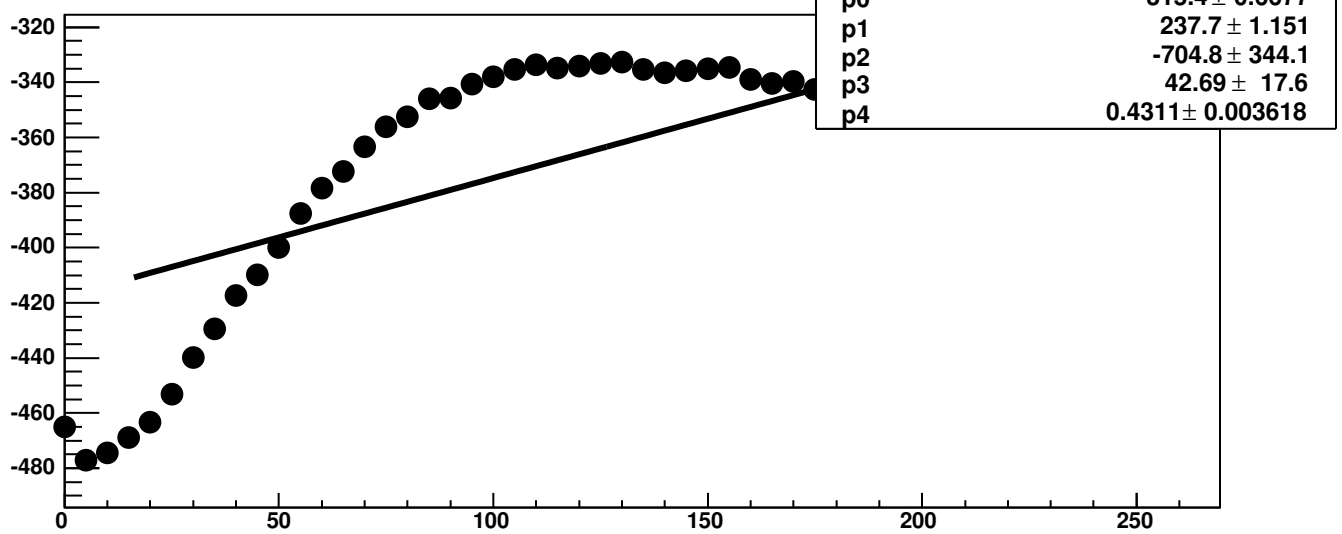
Chip 6, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold



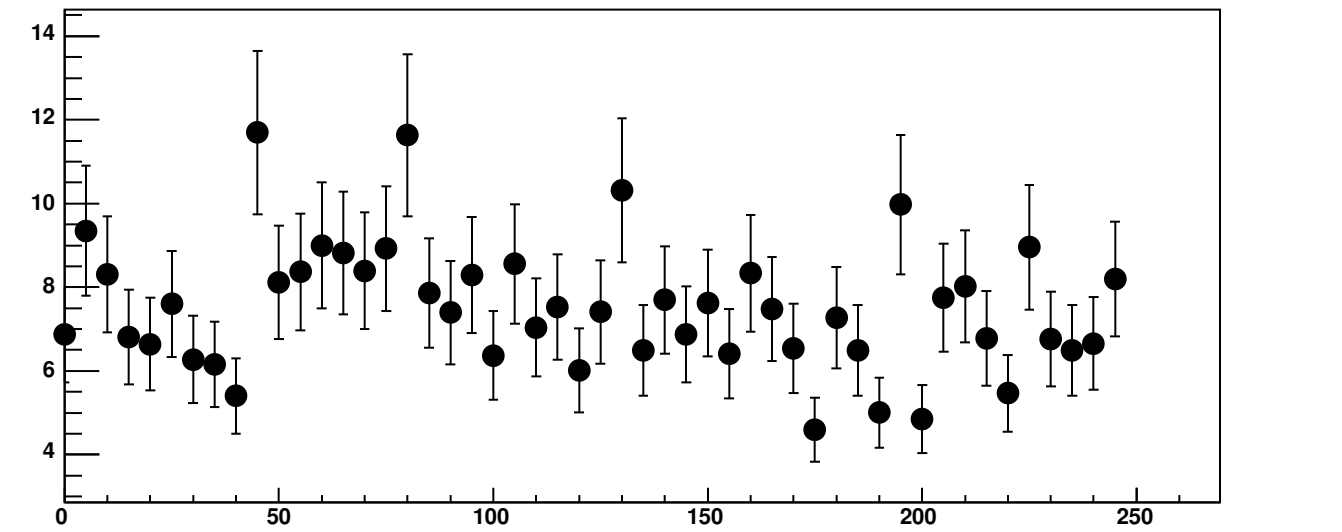
Chip 6, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold



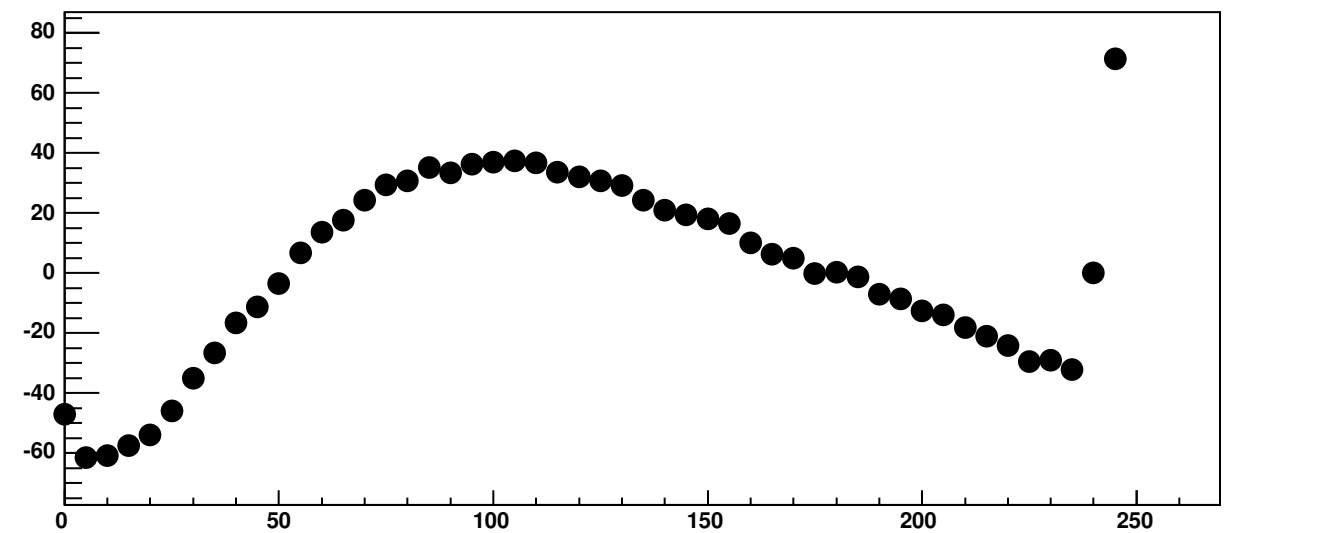
Chip 6, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold



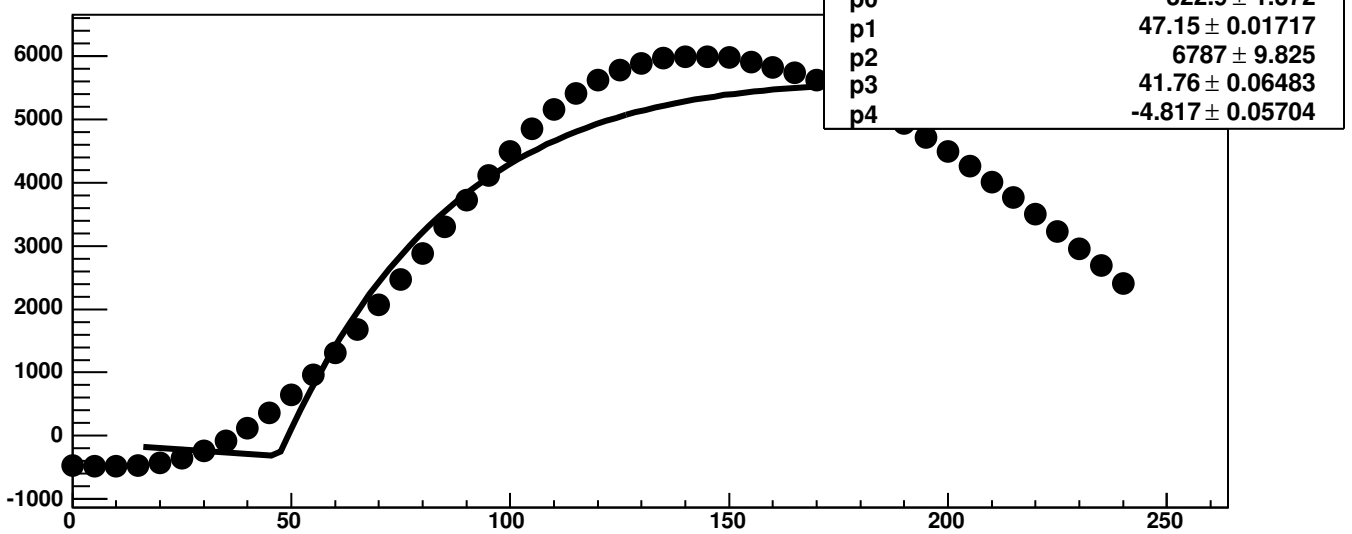
Chip 6, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



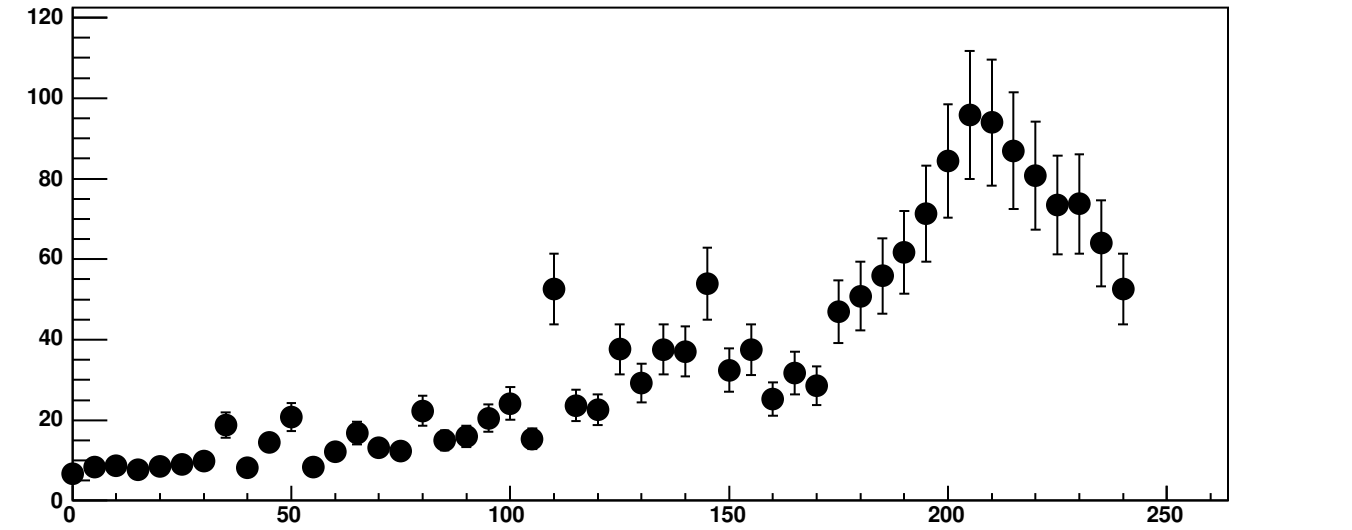
Chip 6, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold



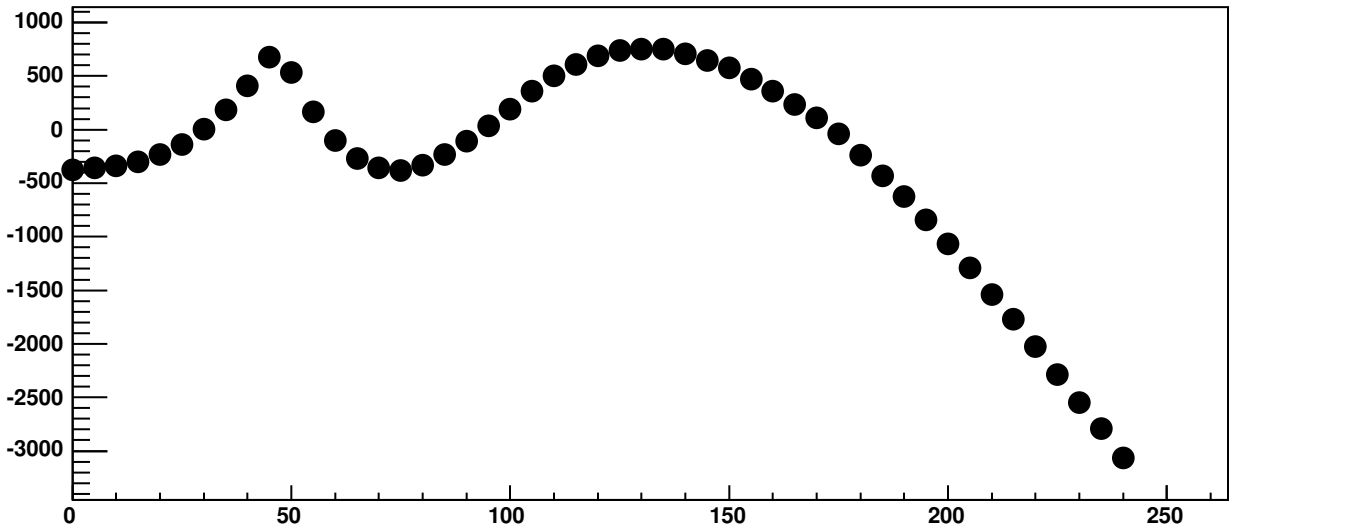
Chip 6, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold



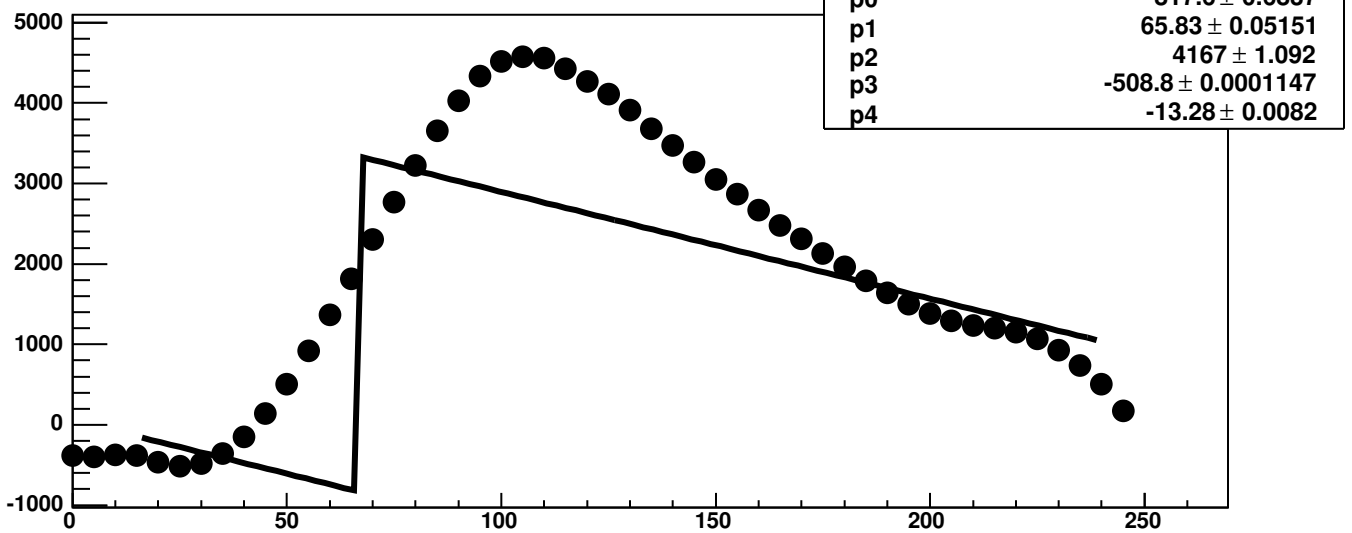
Chip 6, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



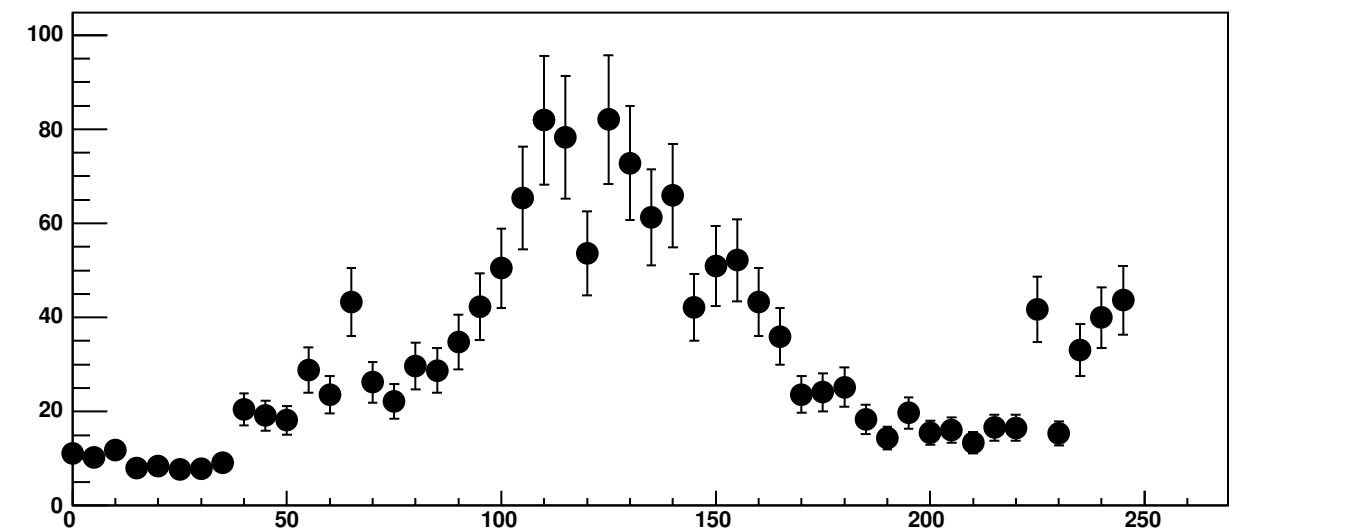
Chip 6, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold



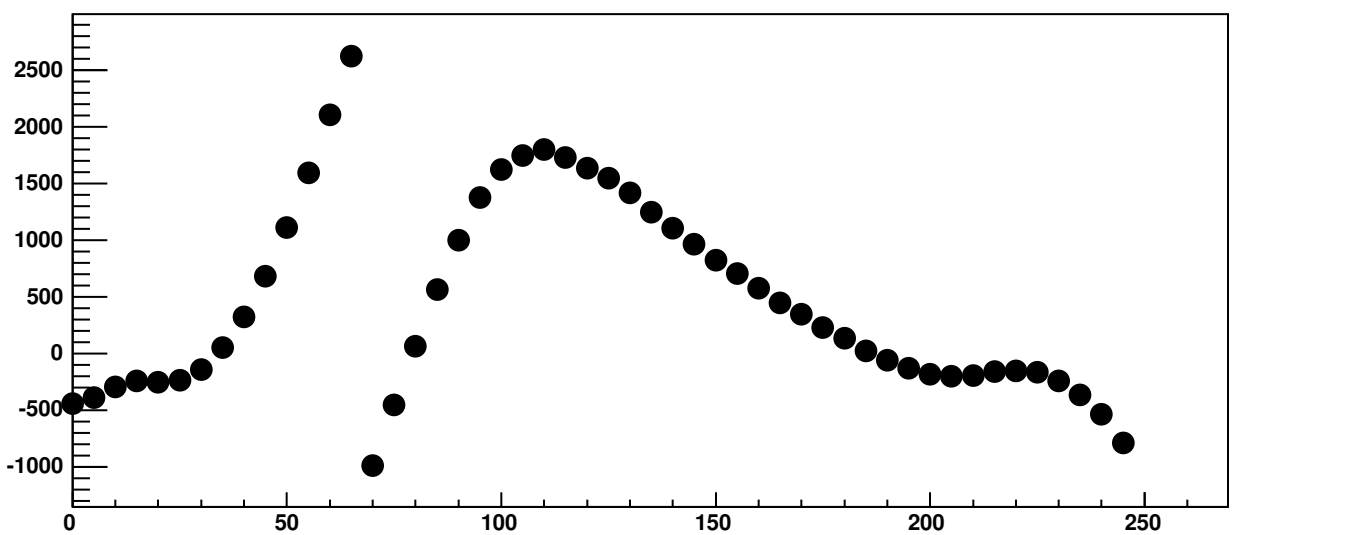
Chip 6, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold



Chip 6, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold

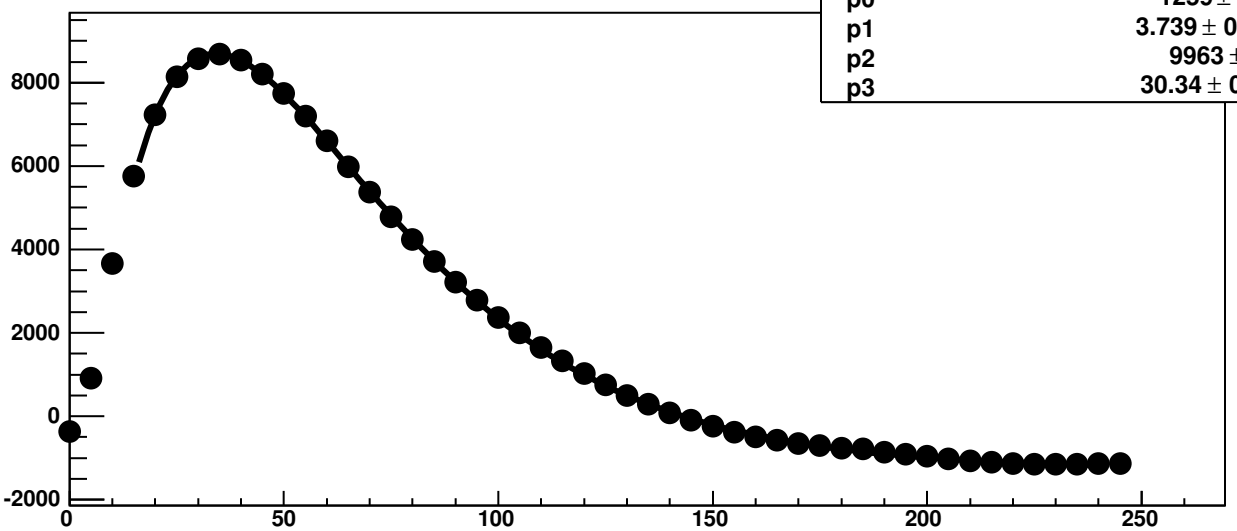


Chip 6, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

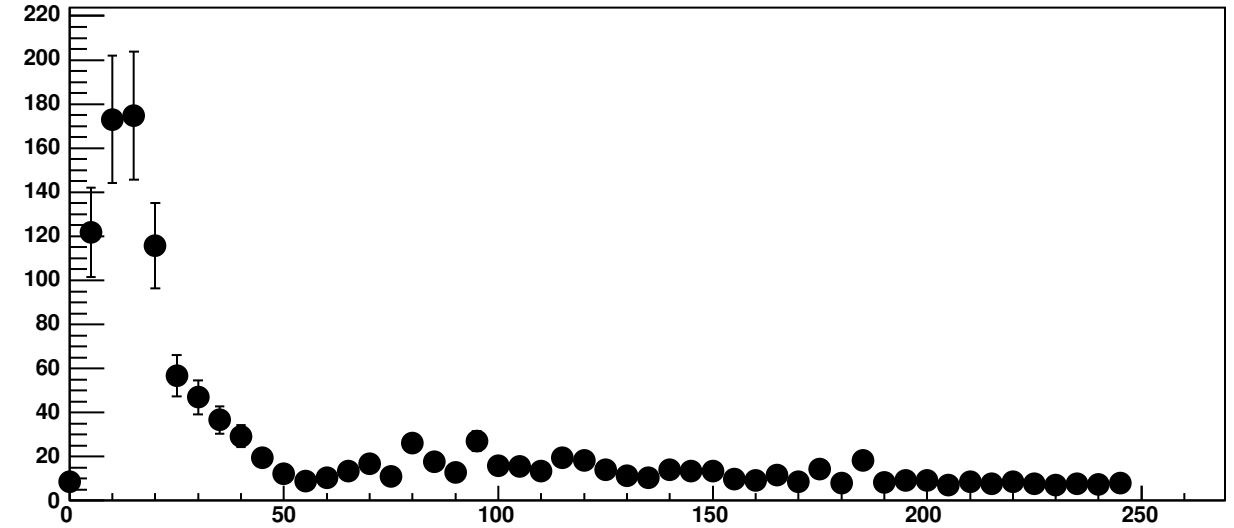


Chip 6, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold

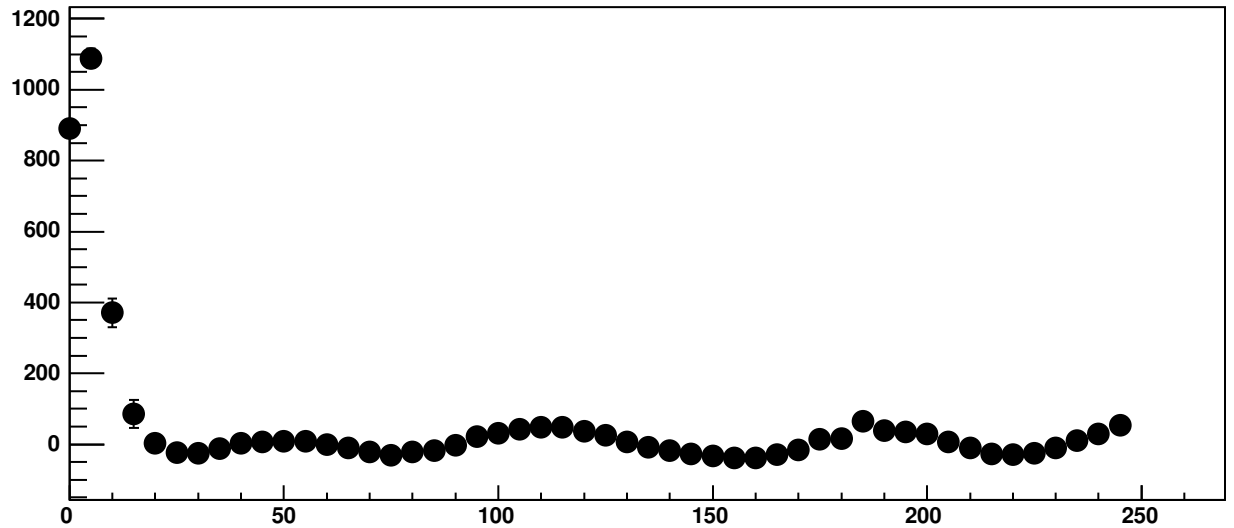
χ^2 / ndf	4310 / 42
p0	-1259 ± 0.7028
p1	3.739 ± 0.03723
p2	9963 ± 3.343
p3	30.34 ± 0.01191

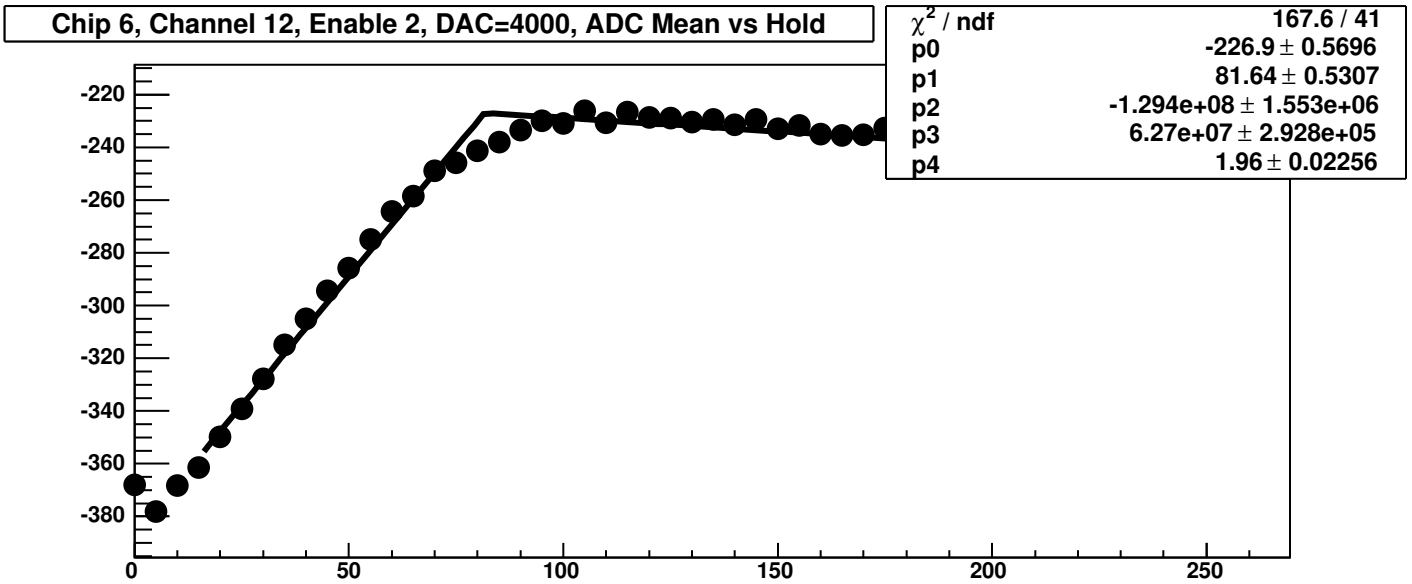


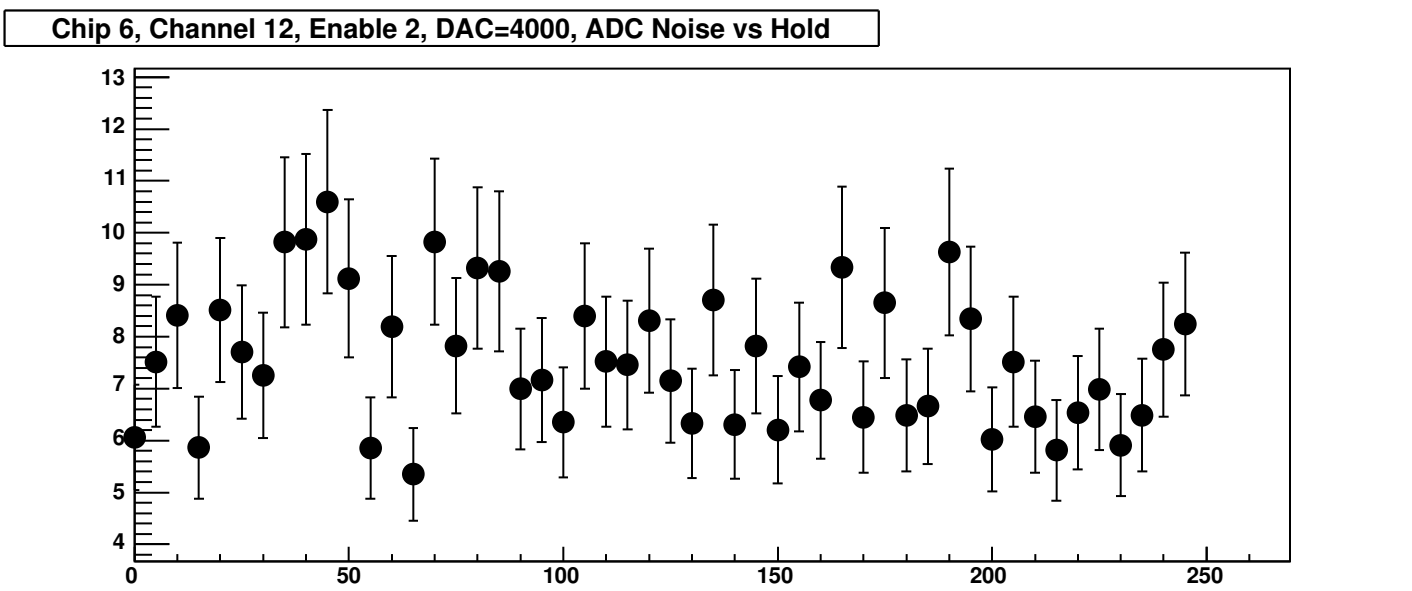
Chip 6, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold

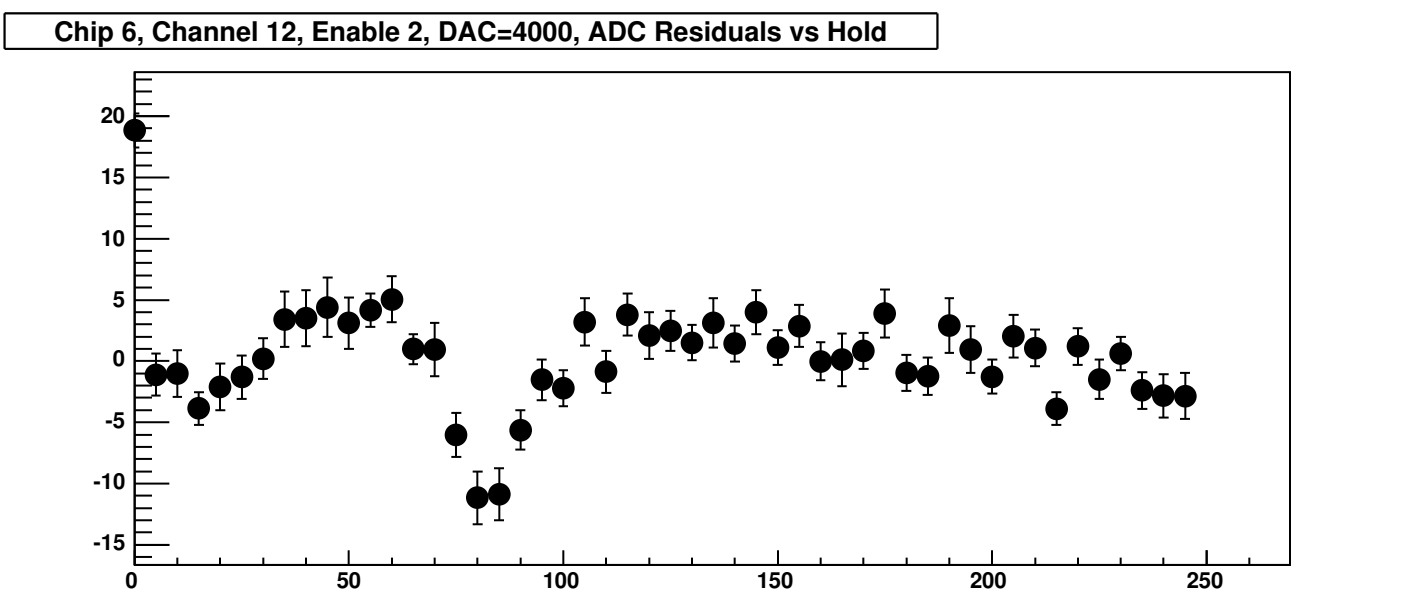


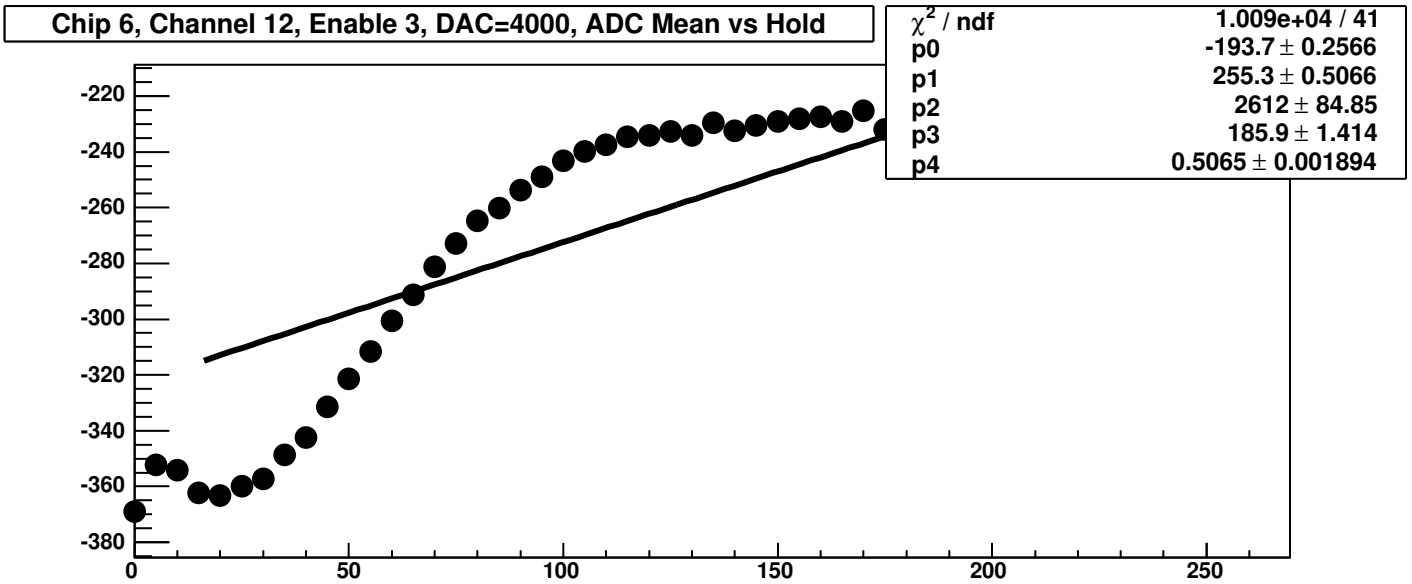
Chip 6, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold

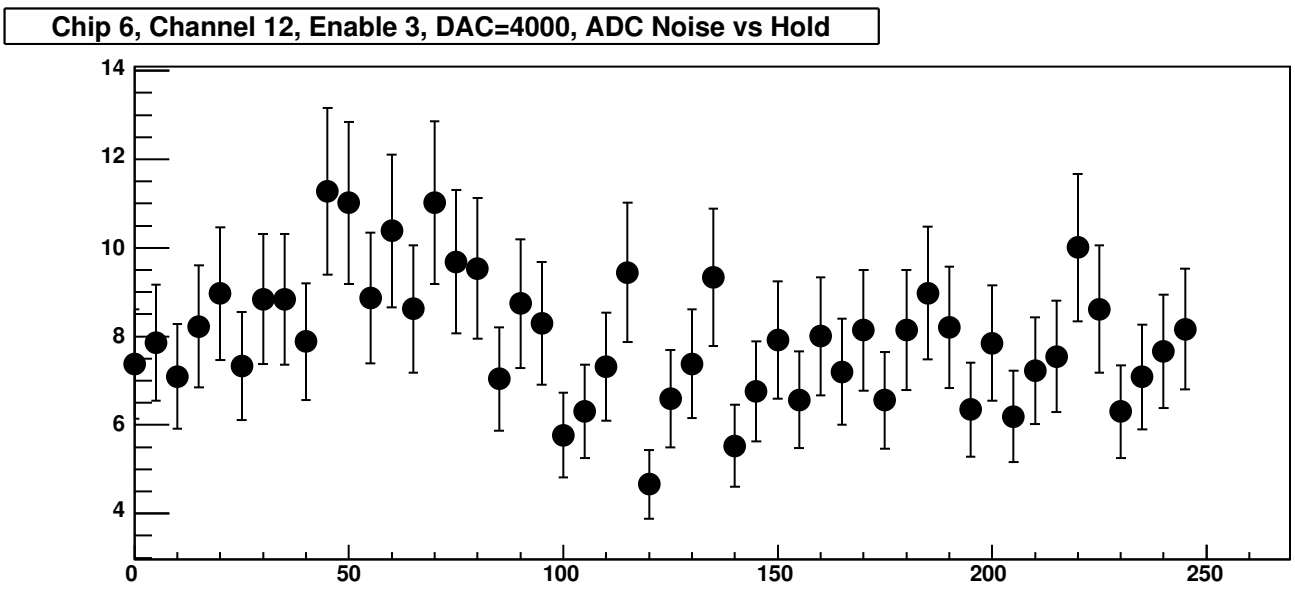


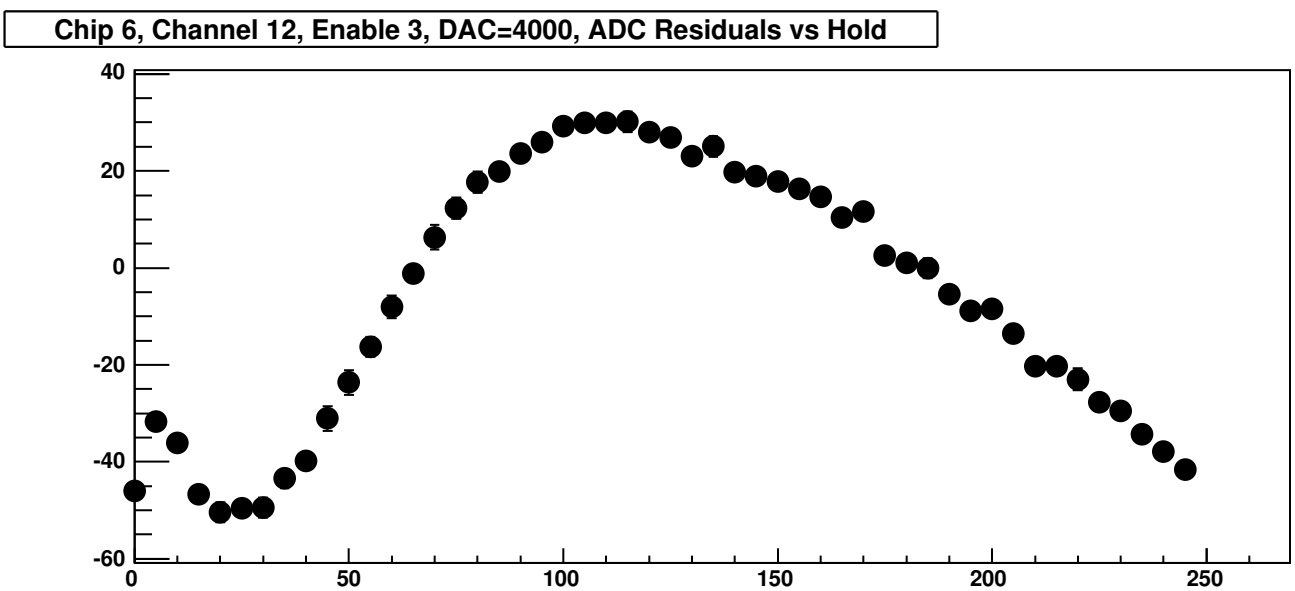

Chip 6, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 167.6 / 41
p0 -226.9 ± 0.5696
p1 81.64 ± 0.5307
p2 -1.294e+08 ± 1.553e+06
p3 6.27e+07 ± 2.928e+05
p4 1.96 ± 0.02256
-220
-240
-260
-280
-300
-320
-340
-360
-380
0
50
100
150
200
250

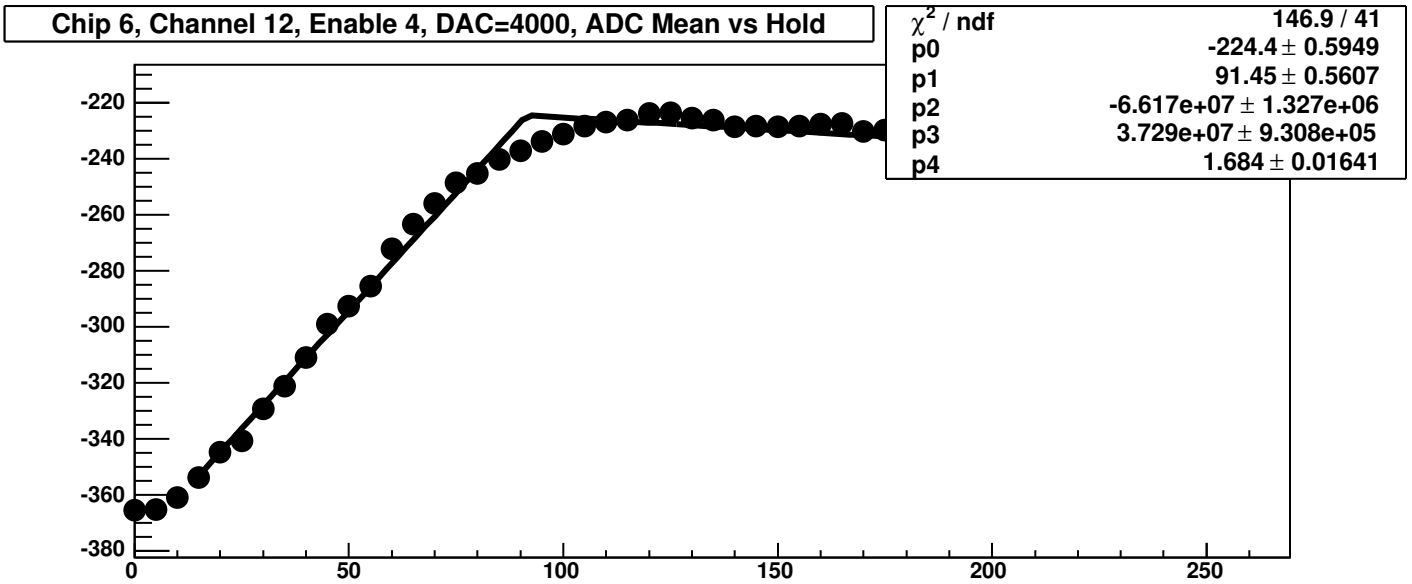

Chip 6, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

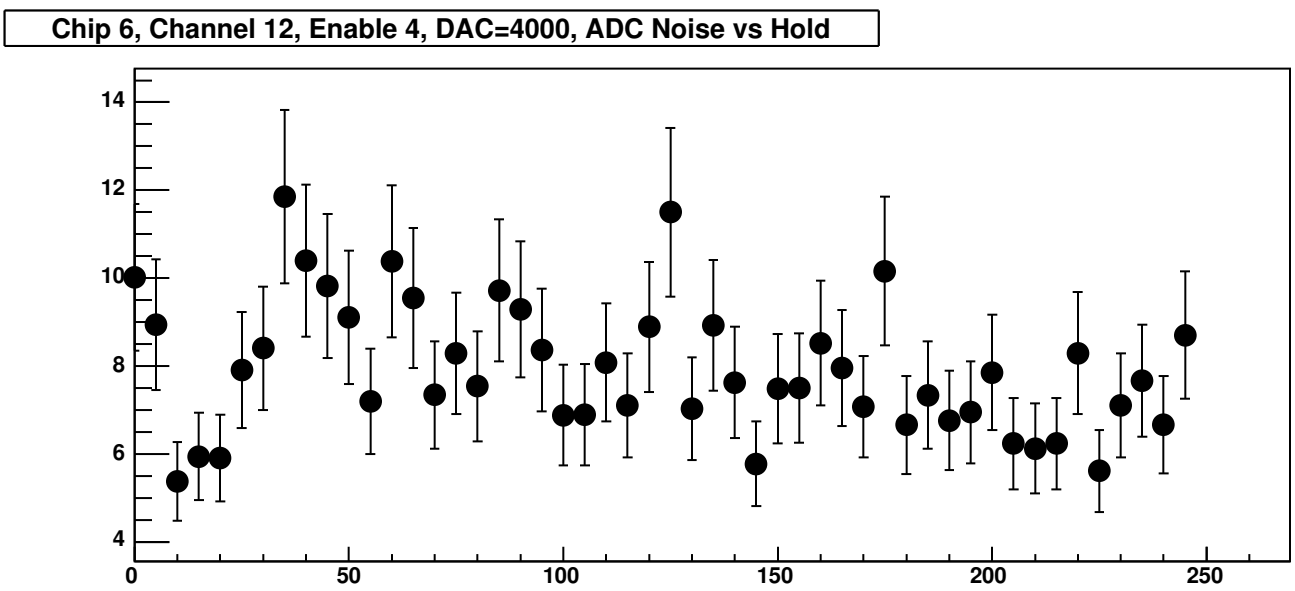

Chip 6, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

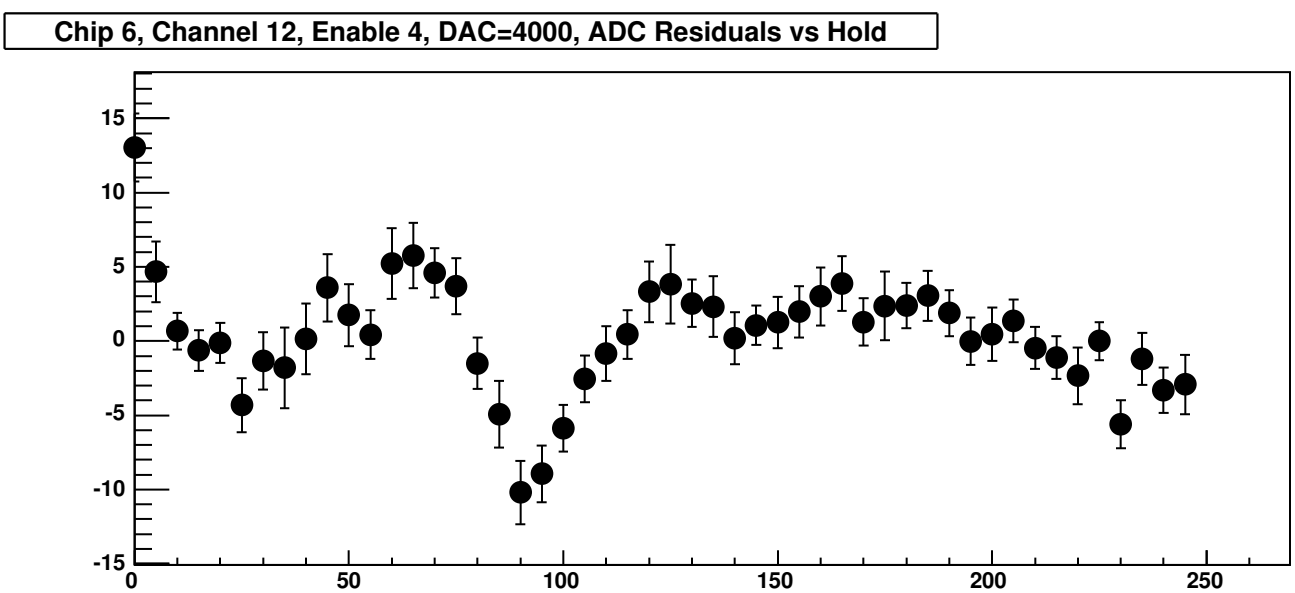

Chip 6, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.009e+04 / 41
p0 -193.7 ± 0.2566
p1 255.3 ± 0.5066
p2 2612 ± 84.85
p3 185.9 ± 1.414
p4 0.5065 ± 0.001894
-220
-240
-260
-280
-300
-320
-340
-360
-380
0
50
100
150
200
250


Chip 6, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

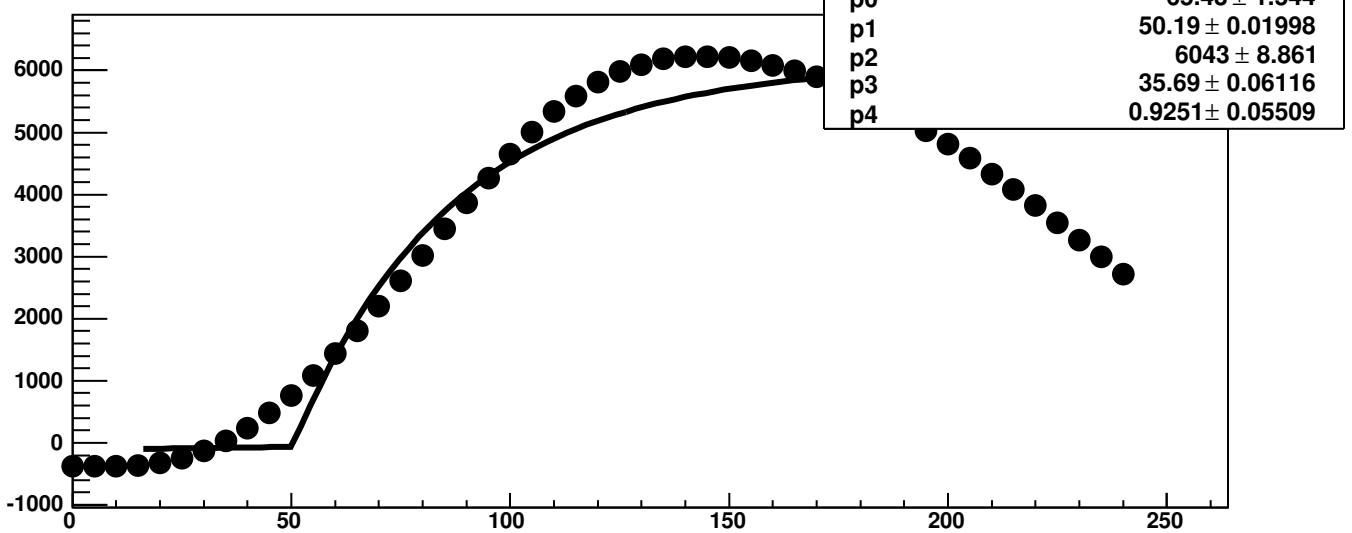

Chip 6, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
0
50
100
150
200
250


Chip 6, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 146.9 / 41
p0 -224.4 ± 0.5949
p1 91.45 ± 0.5607
p2 -6.617e+07 ± 1.327e+06
p3 3.729e+07 ± 9.308e+05
p4 1.684 ± 0.01641
-220
-240
-260
-280
-300
-320
-340
-360
-380
0
50
100
150
200
250

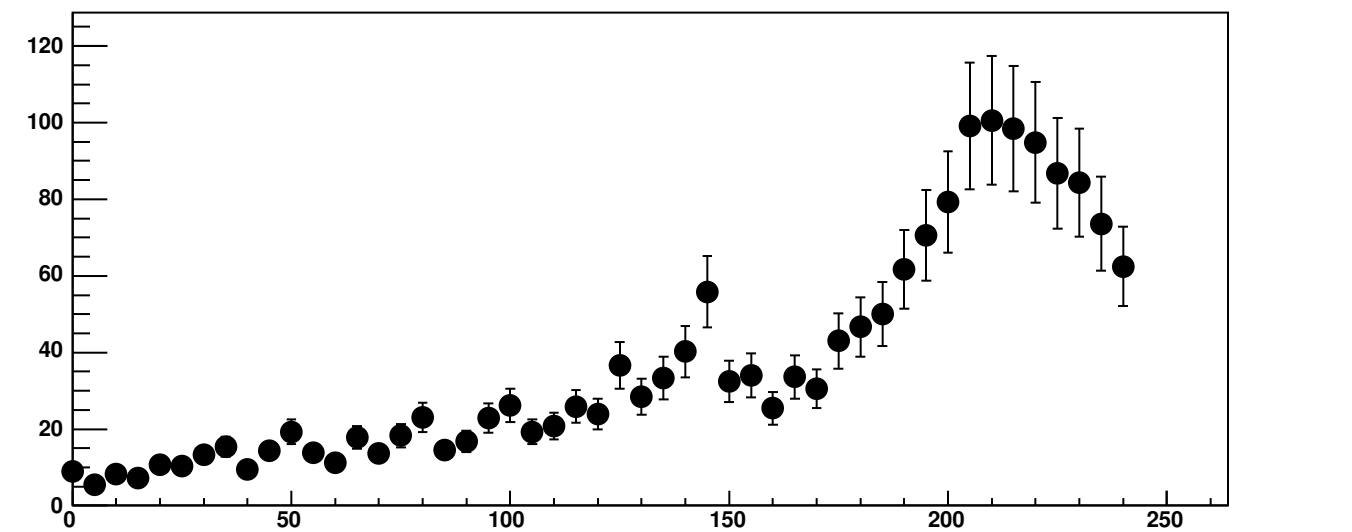

Chip 6, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 6, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

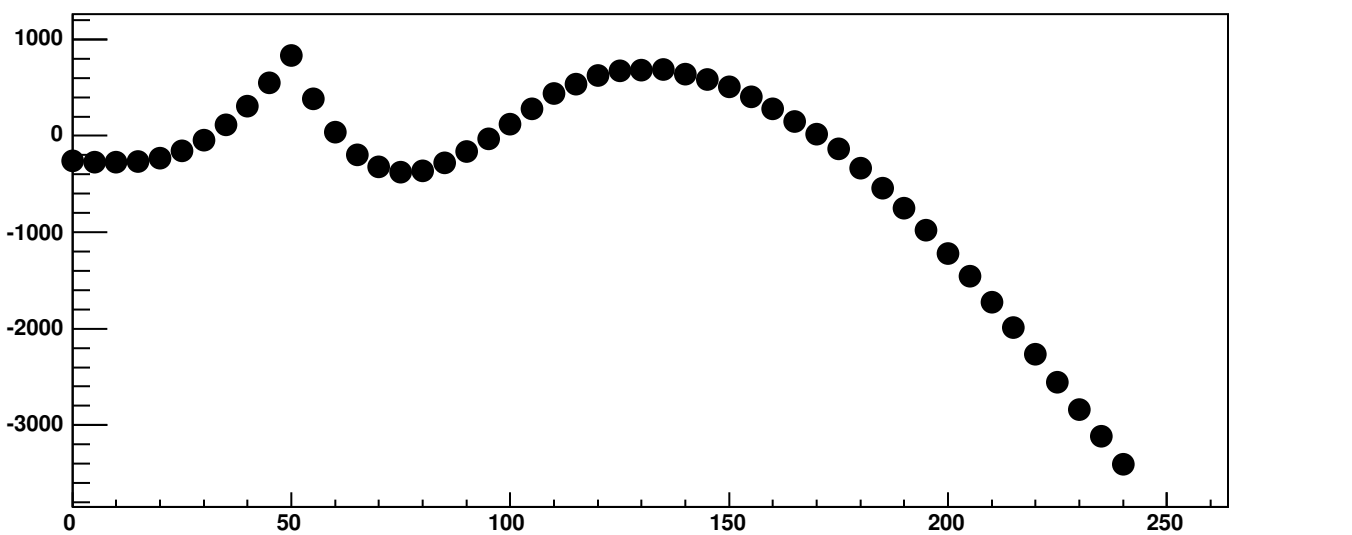
Chip 6, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold

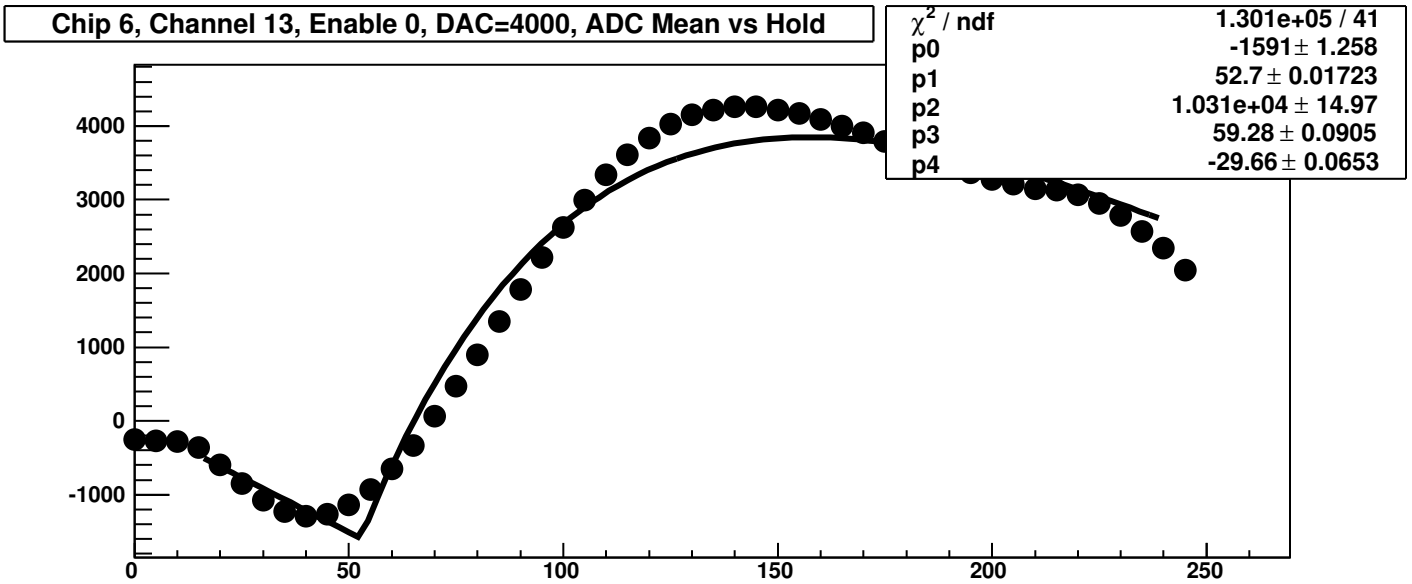


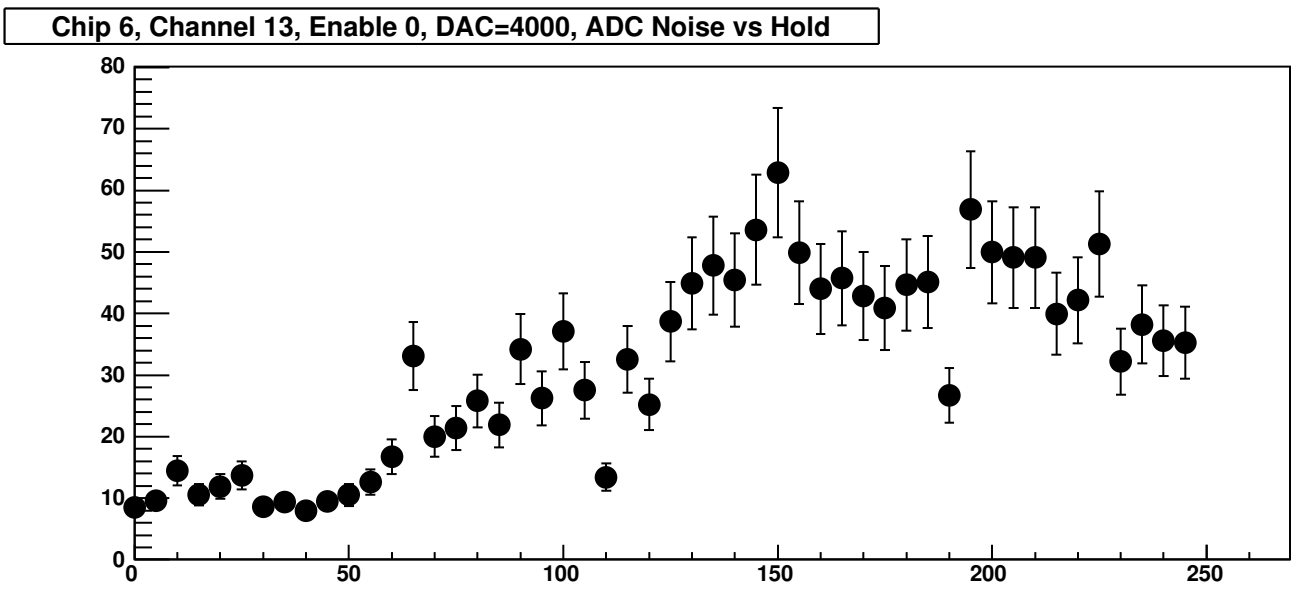
Chip 6, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold

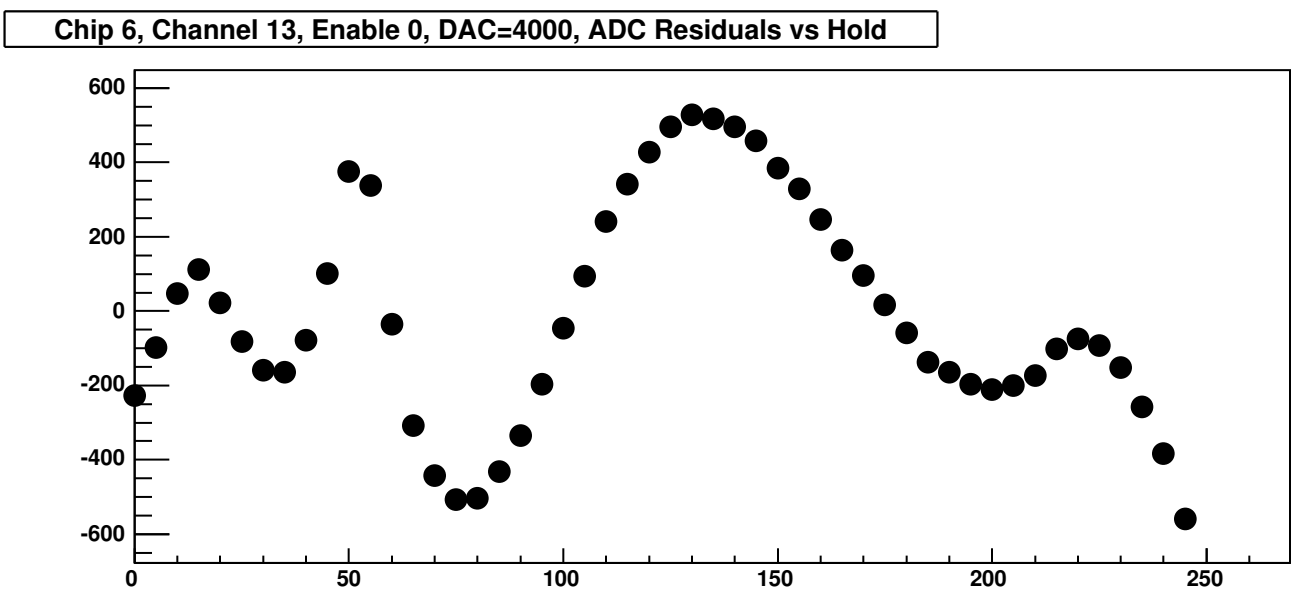


Chip 6, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold

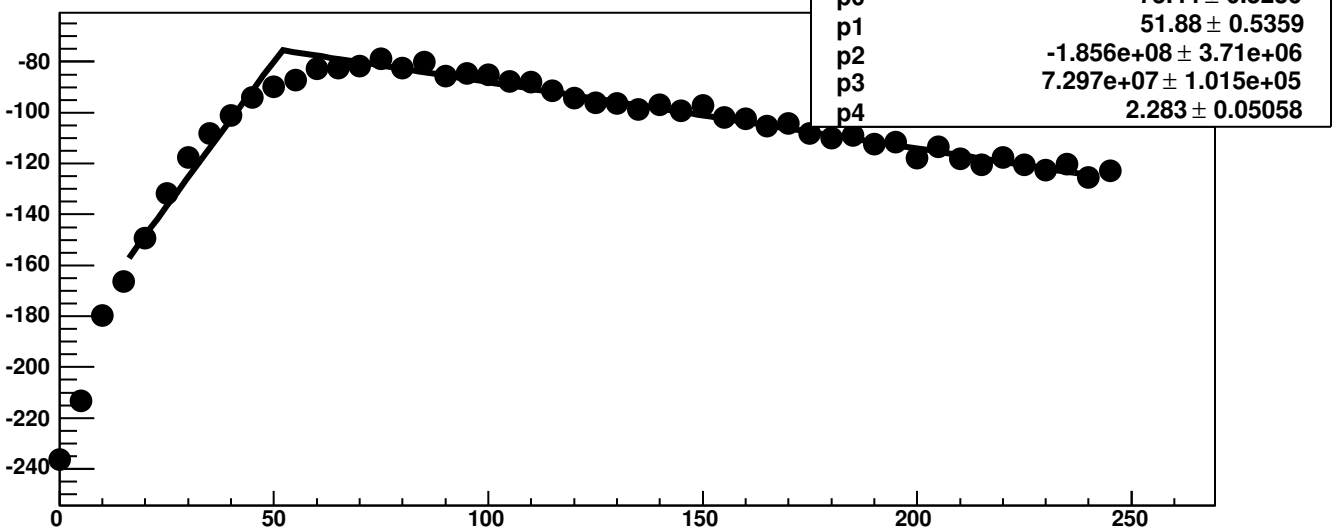



Chip 6, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.301e+05 / 41
p0 -1591 ± 1.258
p1 52.7 ± 0.01723
p2 1.031e+04 ± 14.97
p3 59.28 ± 0.0905
p4 -29.66 ± 0.0653
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

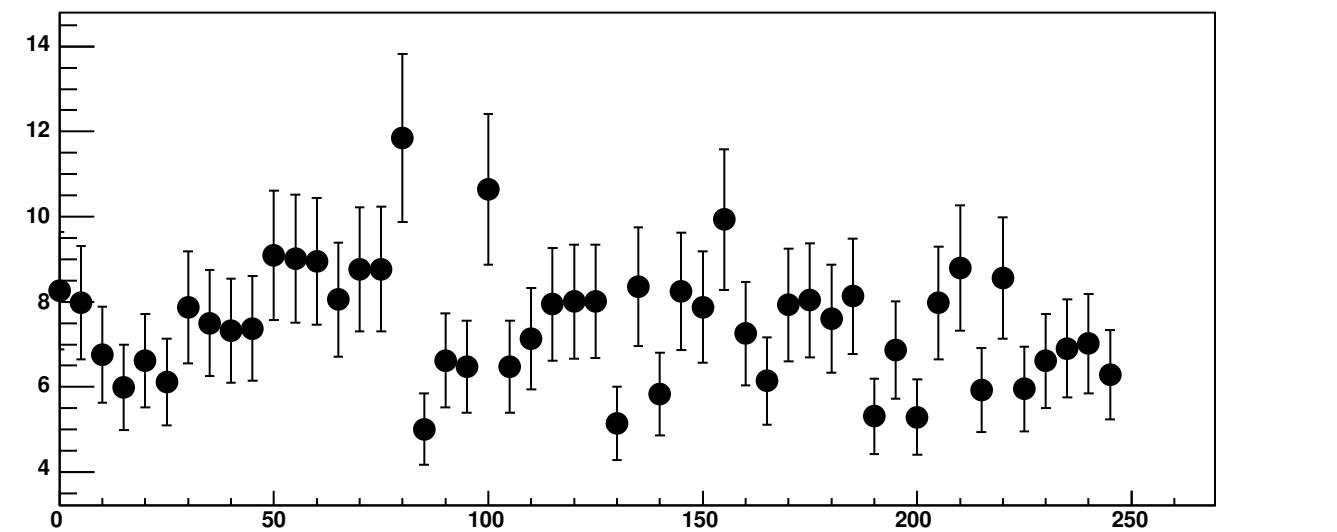

Chip 6, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250


Chip 6, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold
600
400
200
0
-200
-400
-600
0
50
100
150
200
250

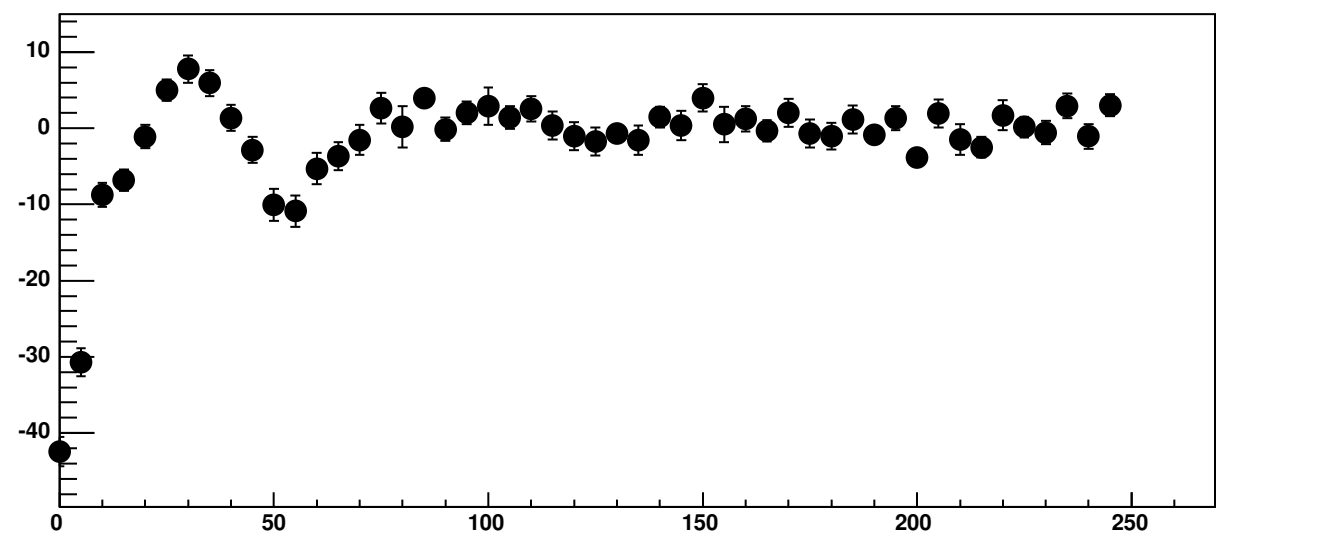
Chip 6, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold



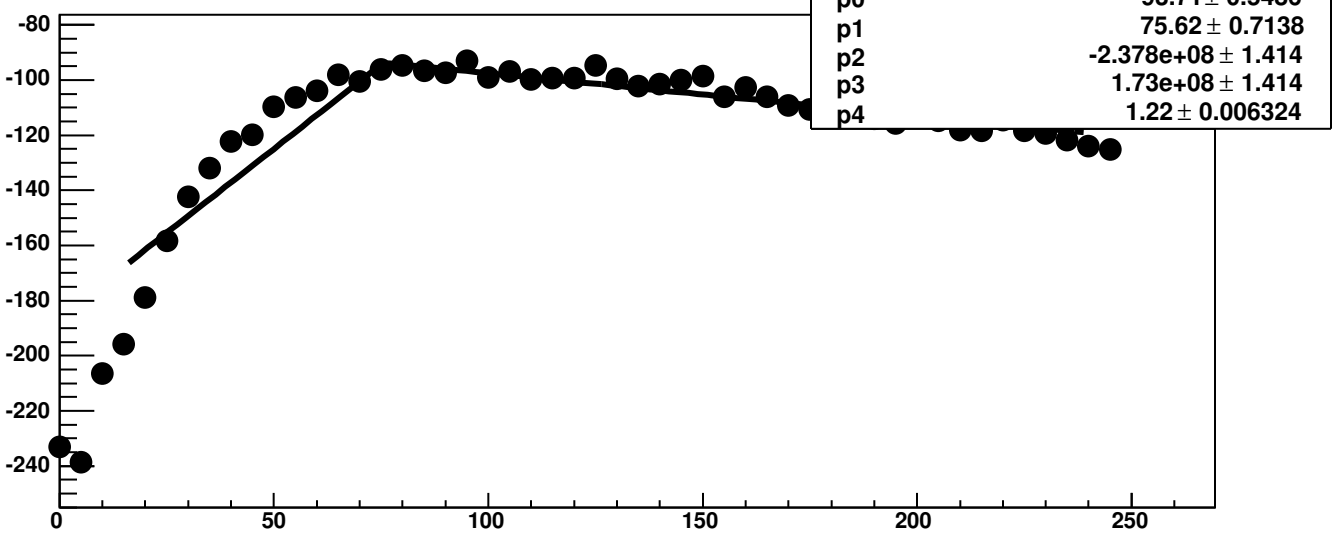
Chip 6, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold



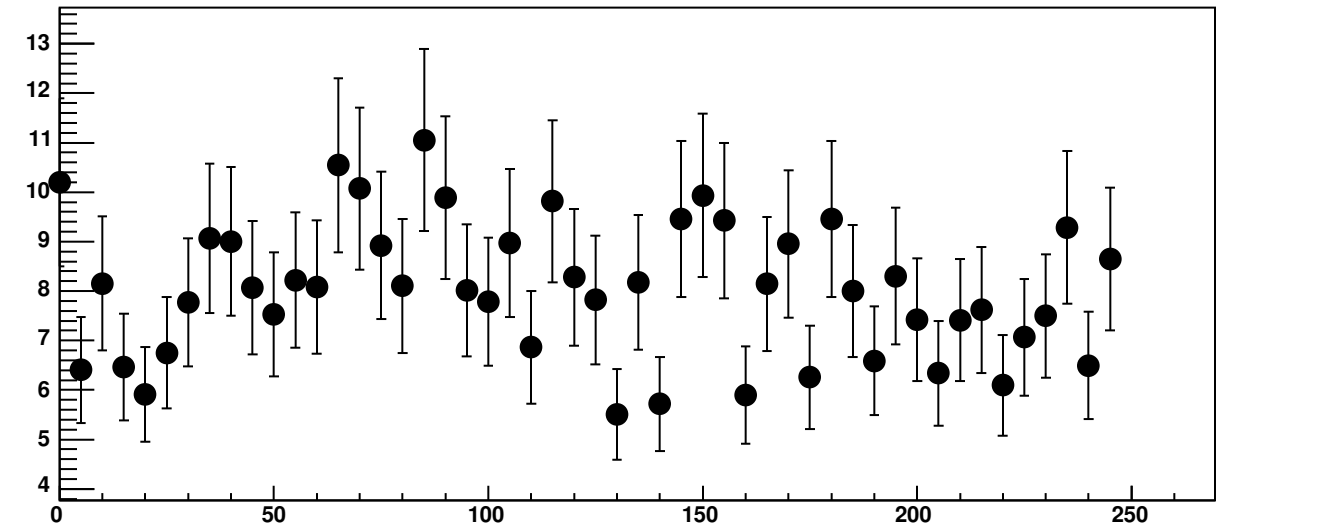
Chip 6, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold



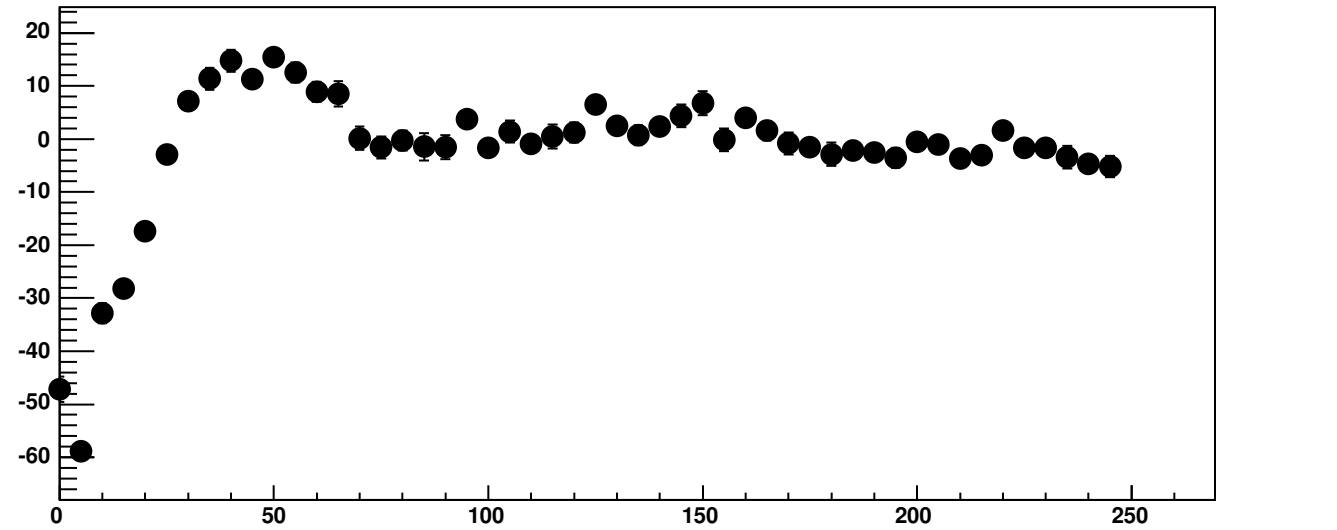
Chip 6, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold



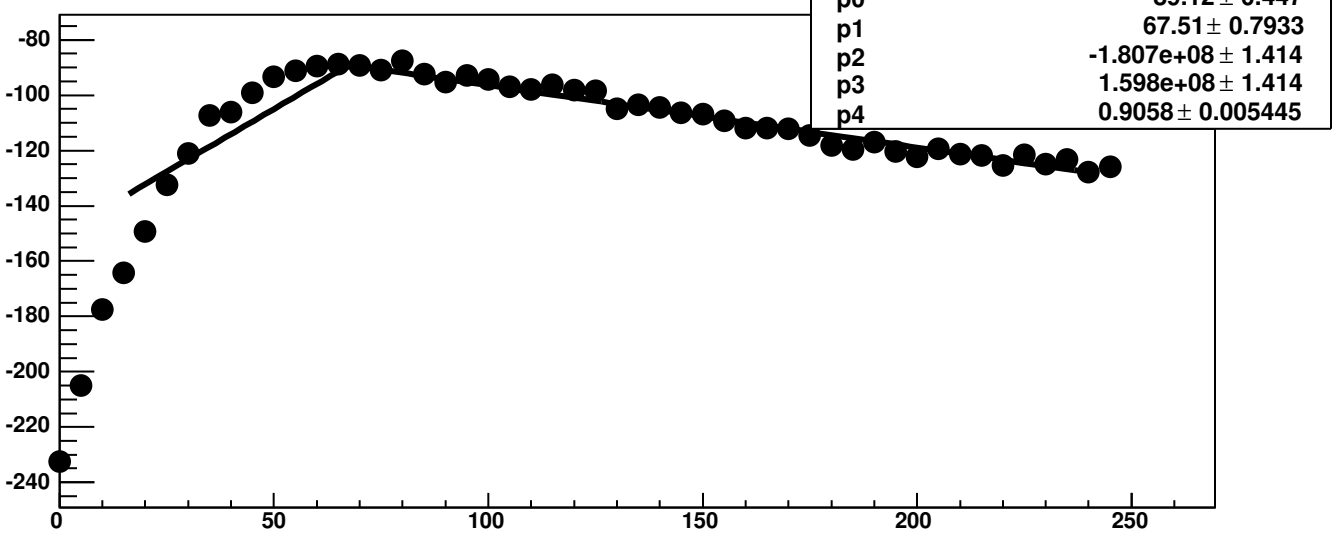
Chip 6, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



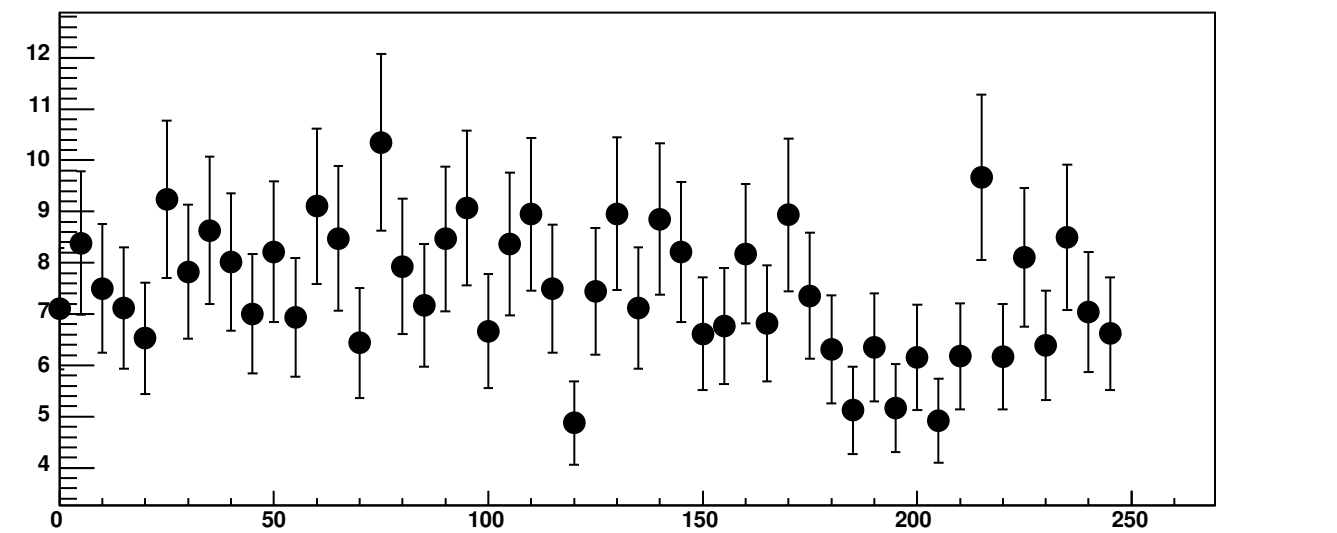
Chip 6, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold



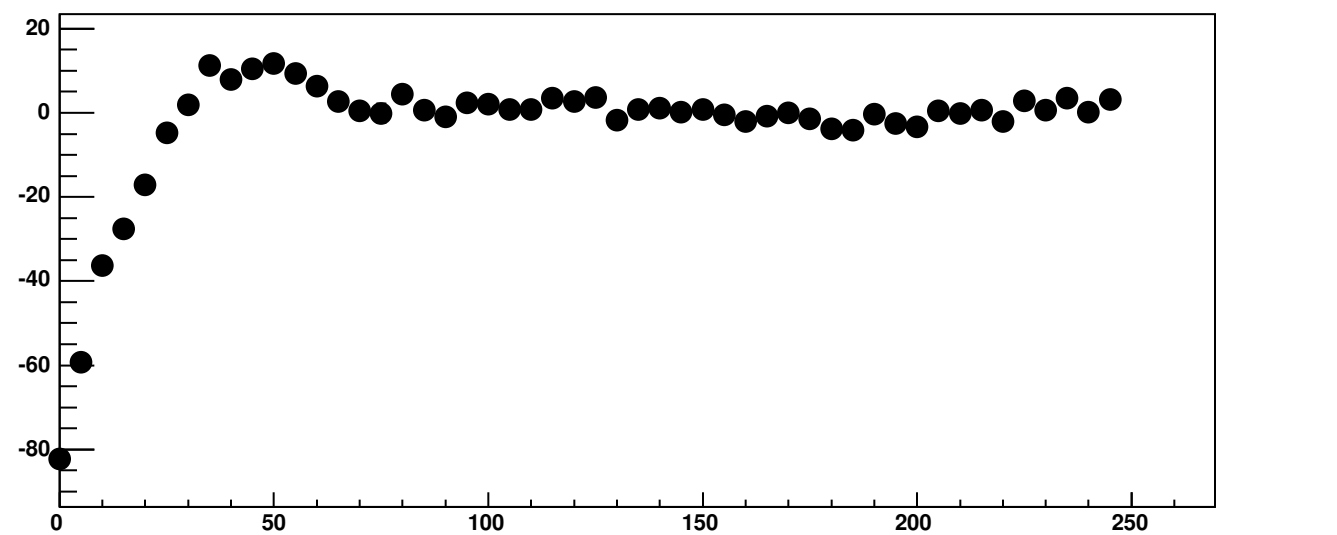
Chip 6, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold



Chip 6, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold

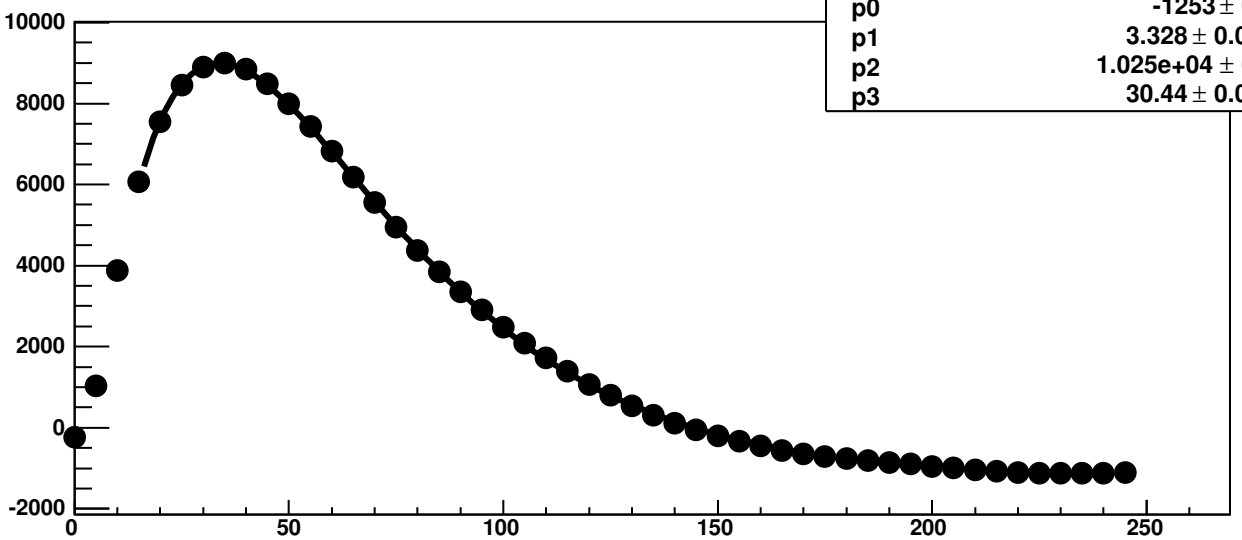


Chip 6, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

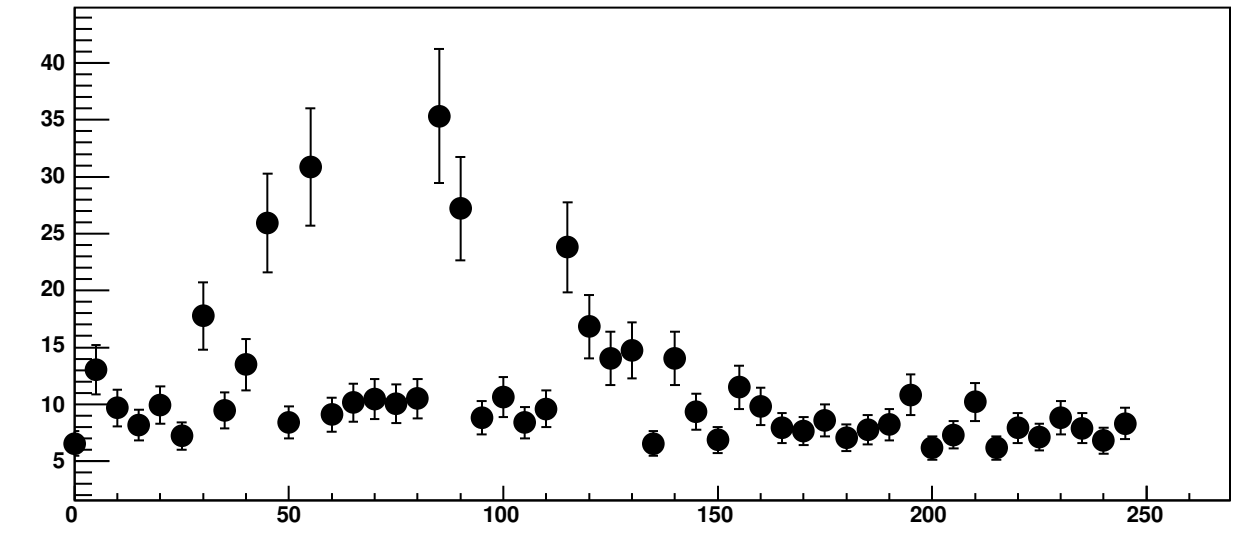


Chip 6, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold

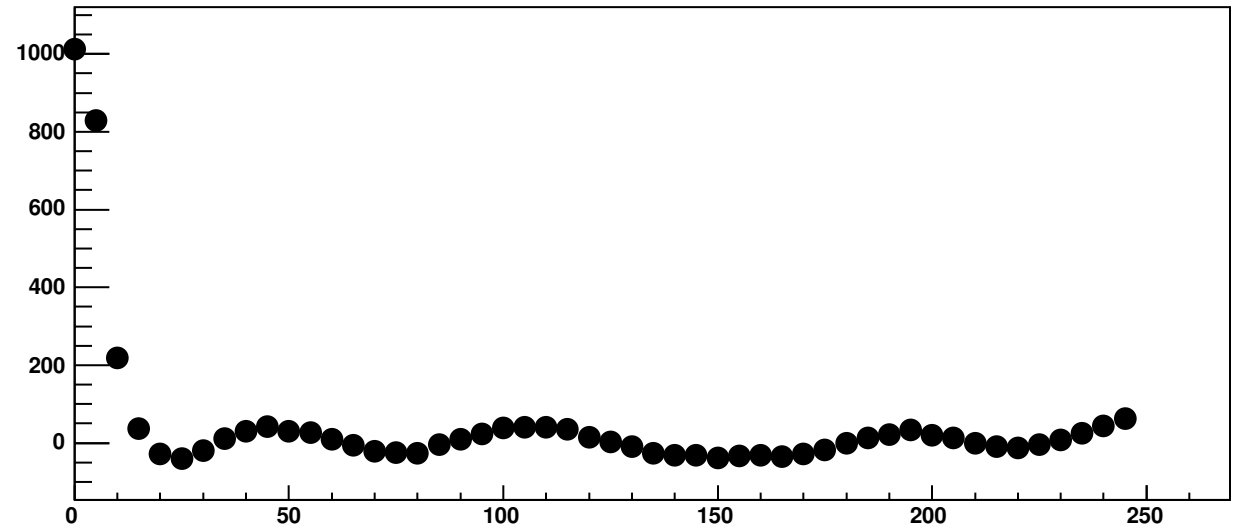
χ^2 / ndf	7146 / 42
p0	-1253 ± 0.5566
p1	3.328 ± 0.006483
p2	1.025e+04 ± 0.9894
p3	30.44 ± 0.005457

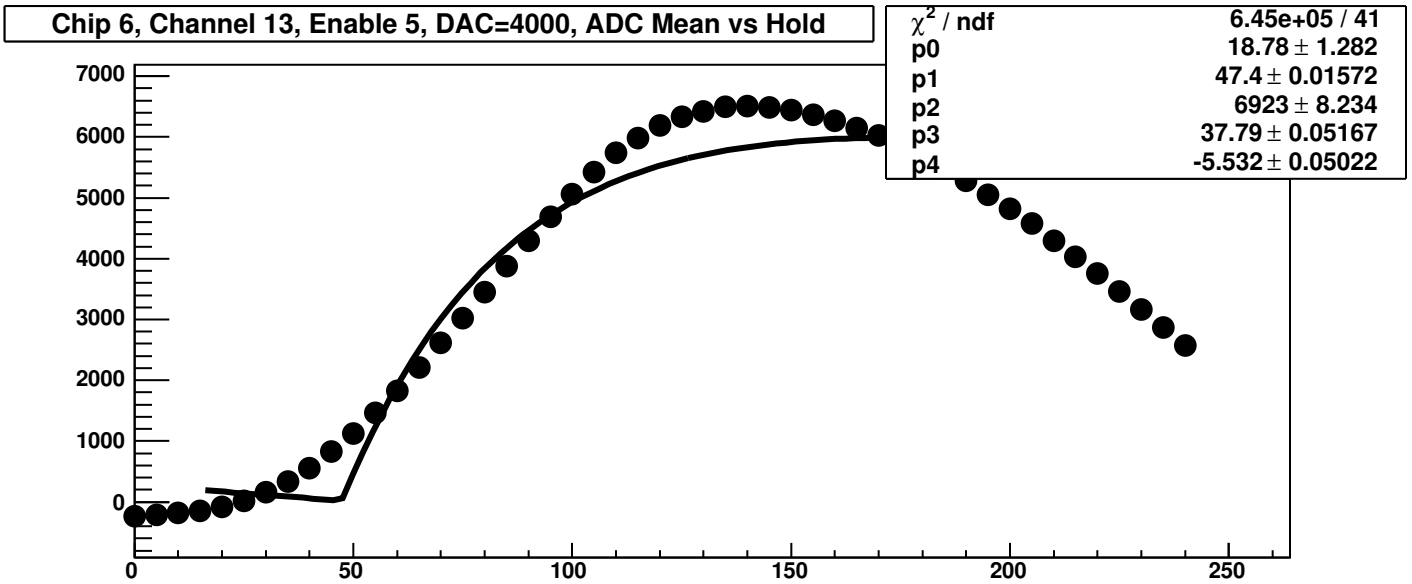


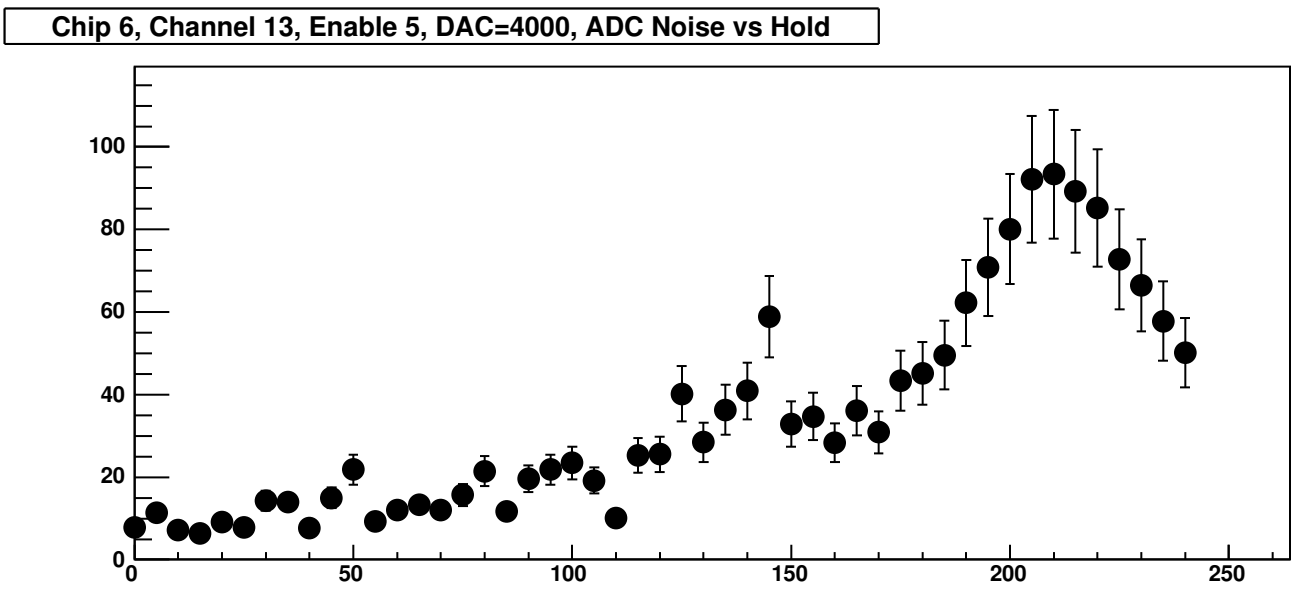
Chip 6, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold

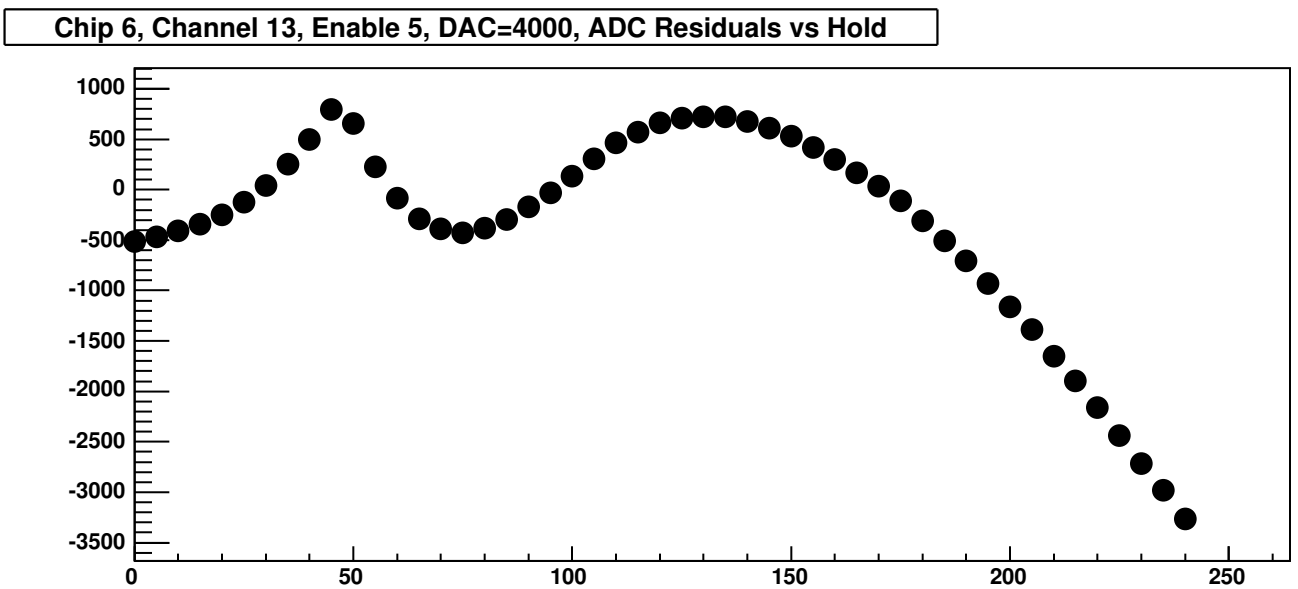


Chip 6, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold

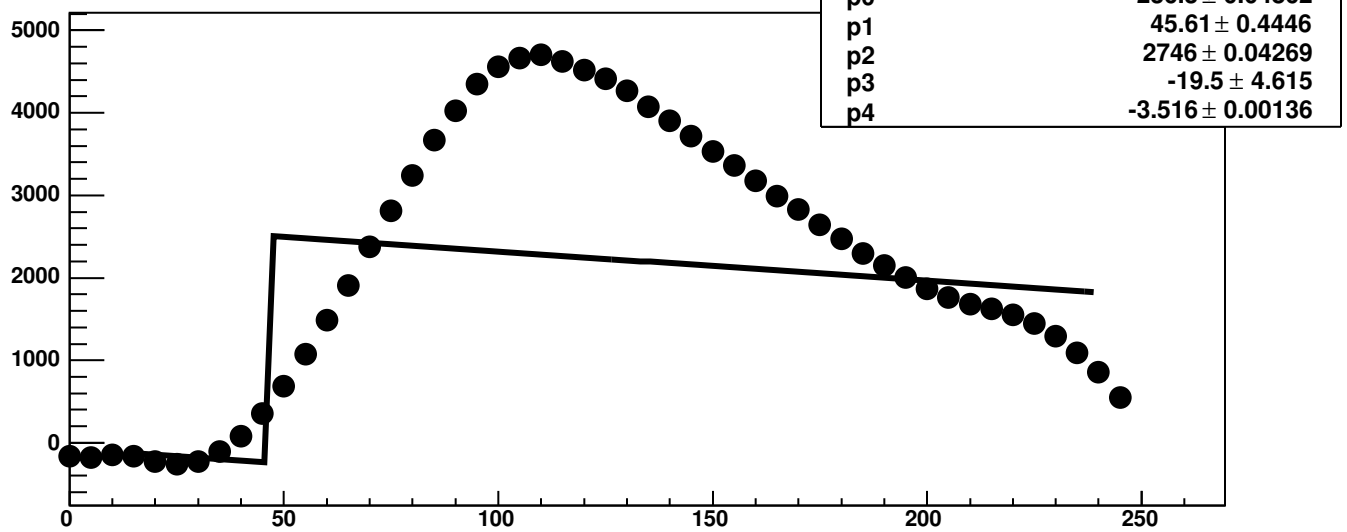



Chip 6, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 6.45e+05 / 41
p0 18.78 ± 1.282
p1 47.4 ± 0.01572
p2 6923 ± 8.234
p3 37.79 ± 0.05167
p4 -5.532 ± 0.05022
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

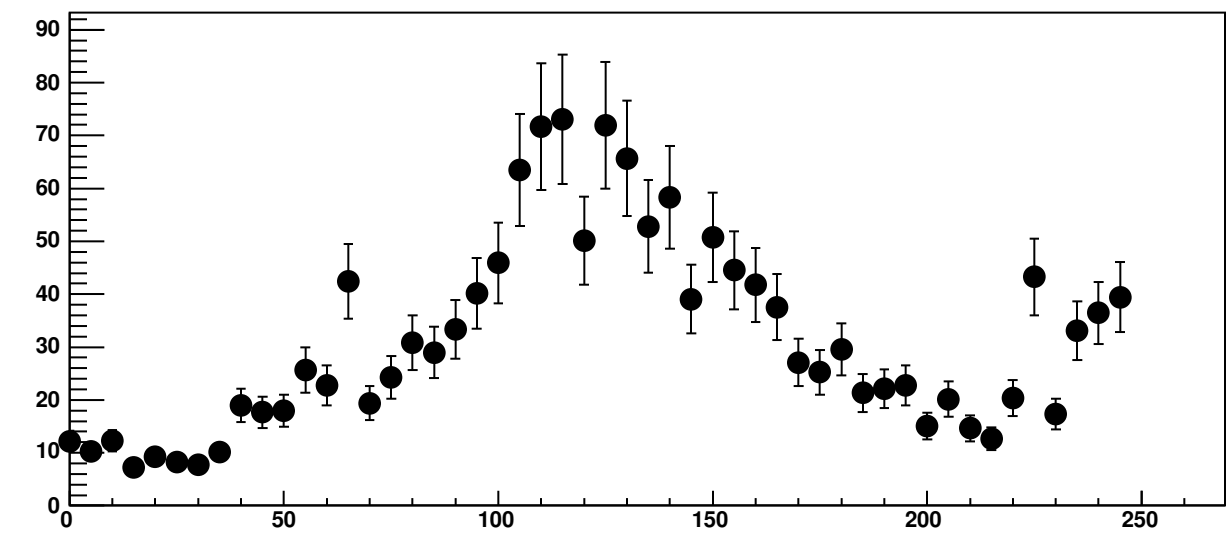

Chip 6, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250


Chip 6, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
0
50
100
150
200
250

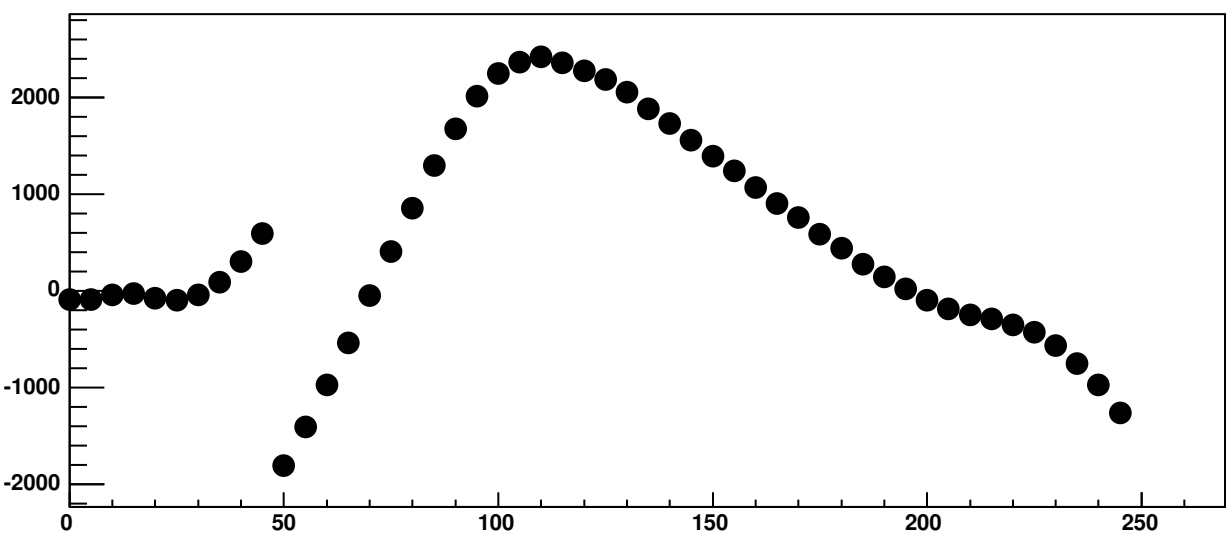
Chip 6, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold



Chip 6, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold

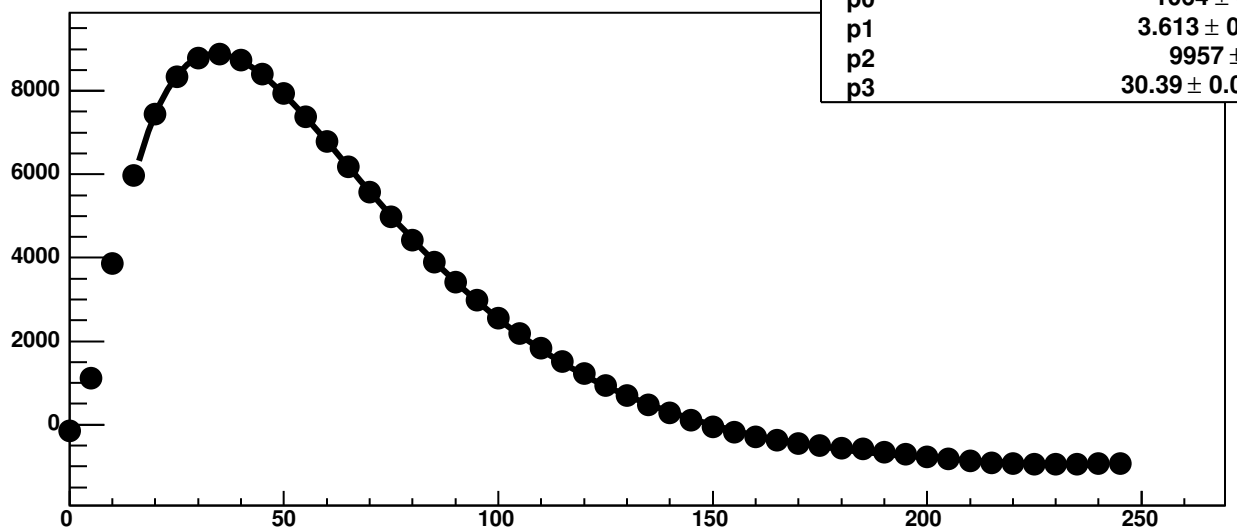


Chip 6, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

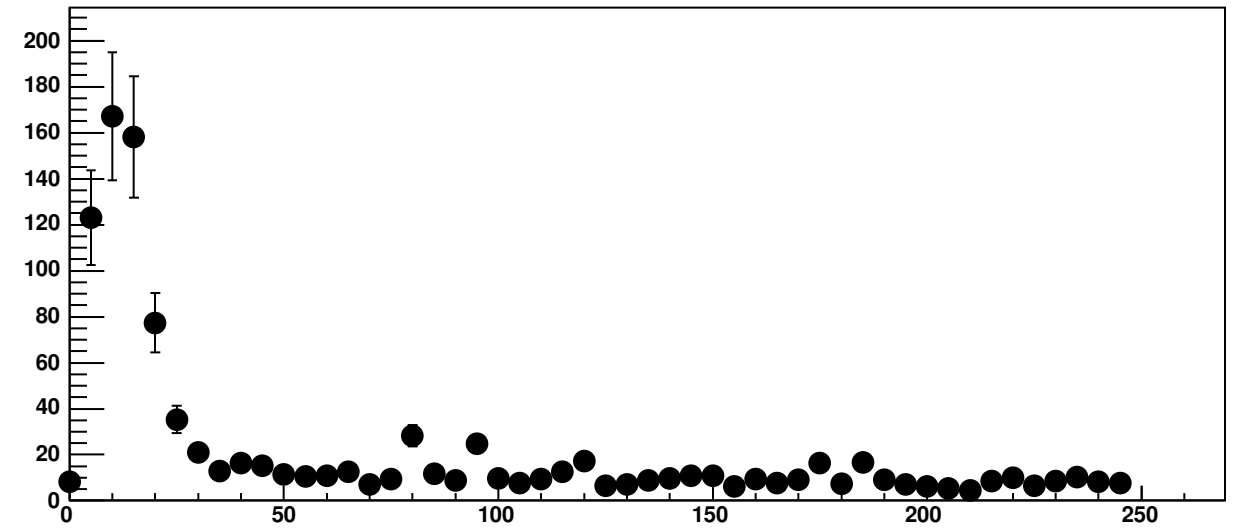


Chip 6, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold

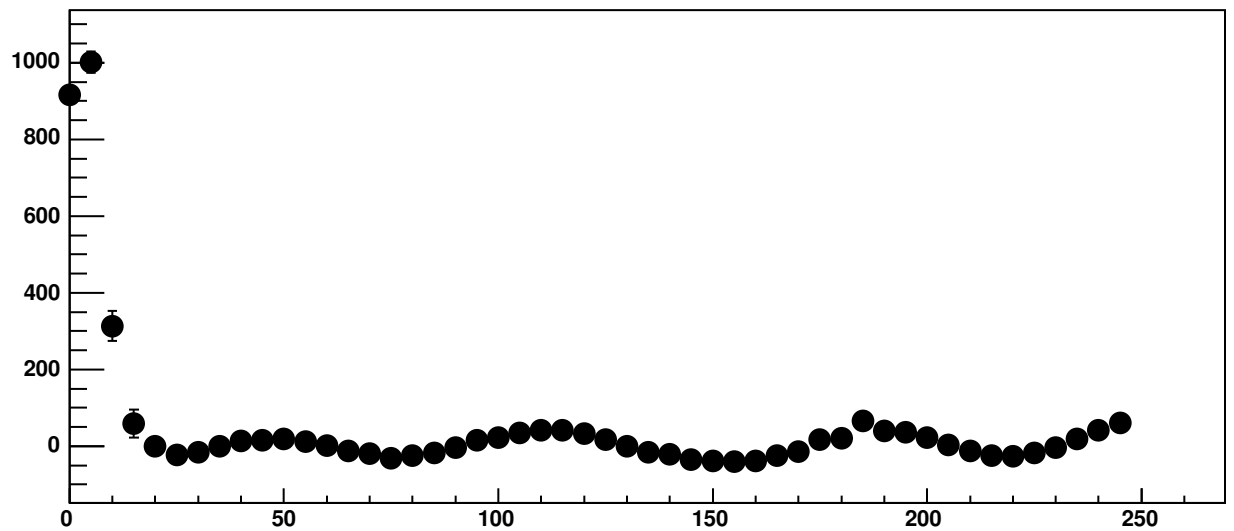
χ^2 / ndf	6420 / 42
p0	-1064 ± 0.6016
p1	3.613 ± 0.02451
p2	9957 ± 1.882
p3	30.39 ± 0.008887



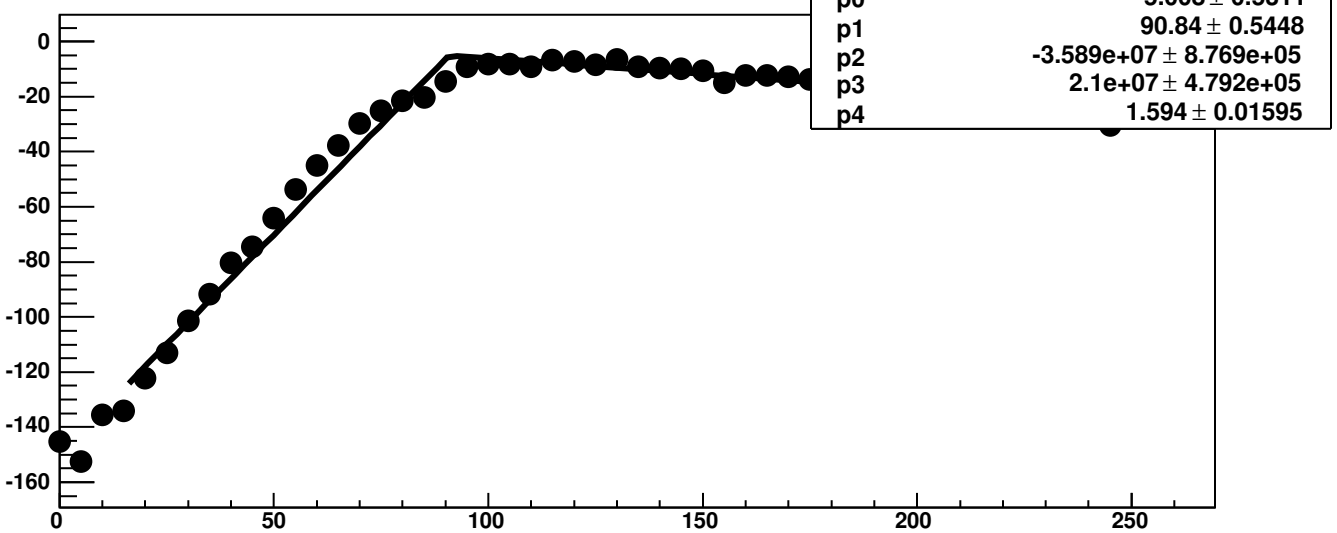
Chip 6, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold



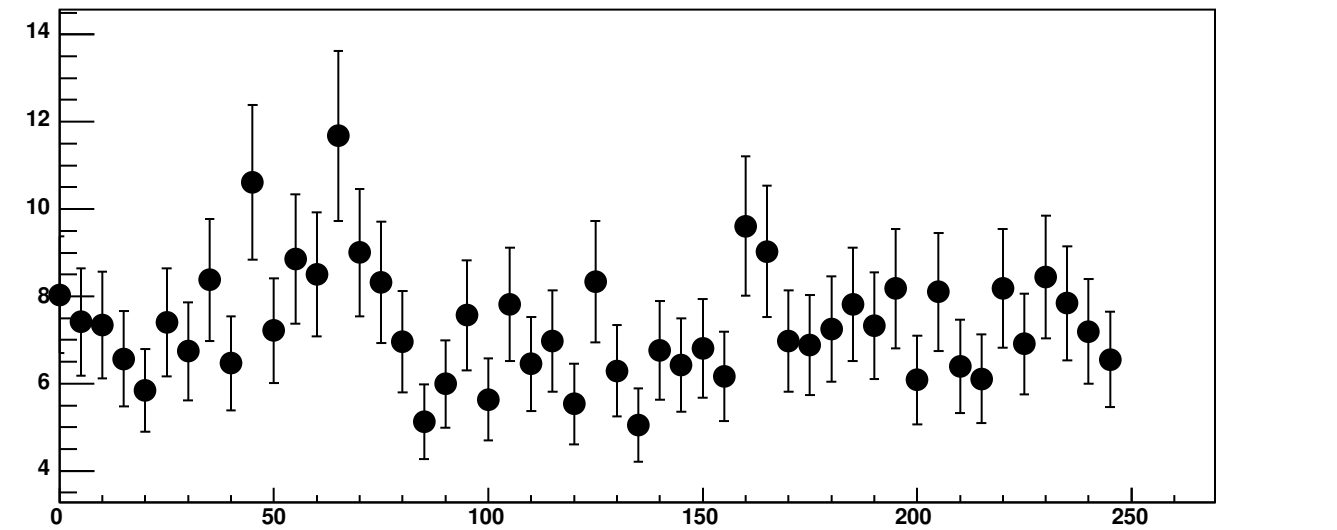
Chip 6, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold



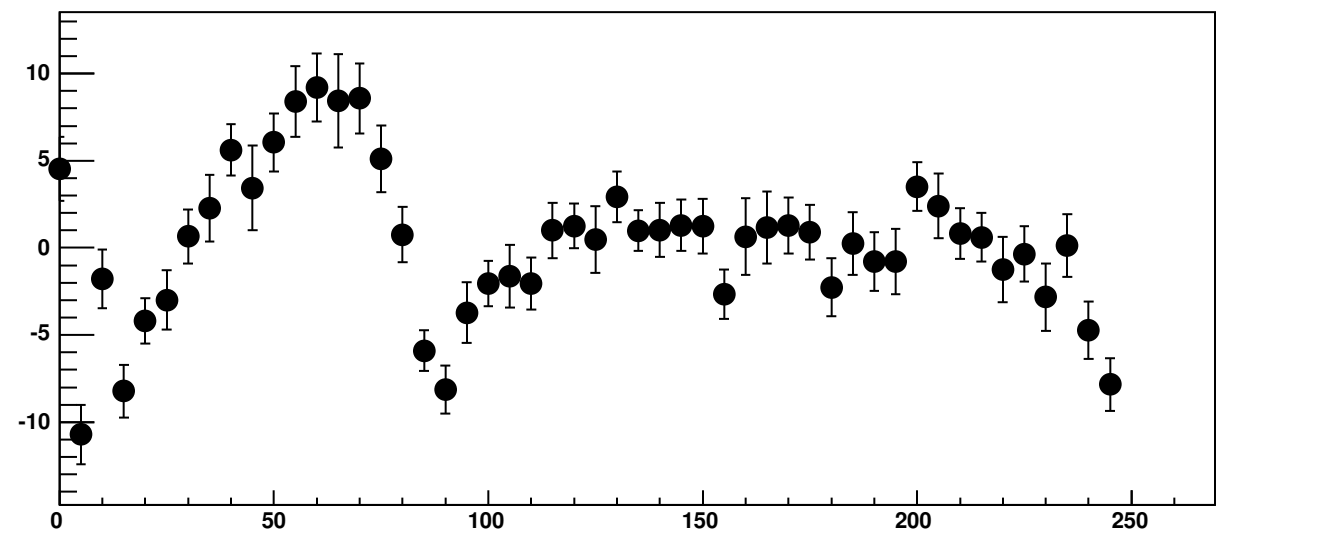
Chip 6, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold



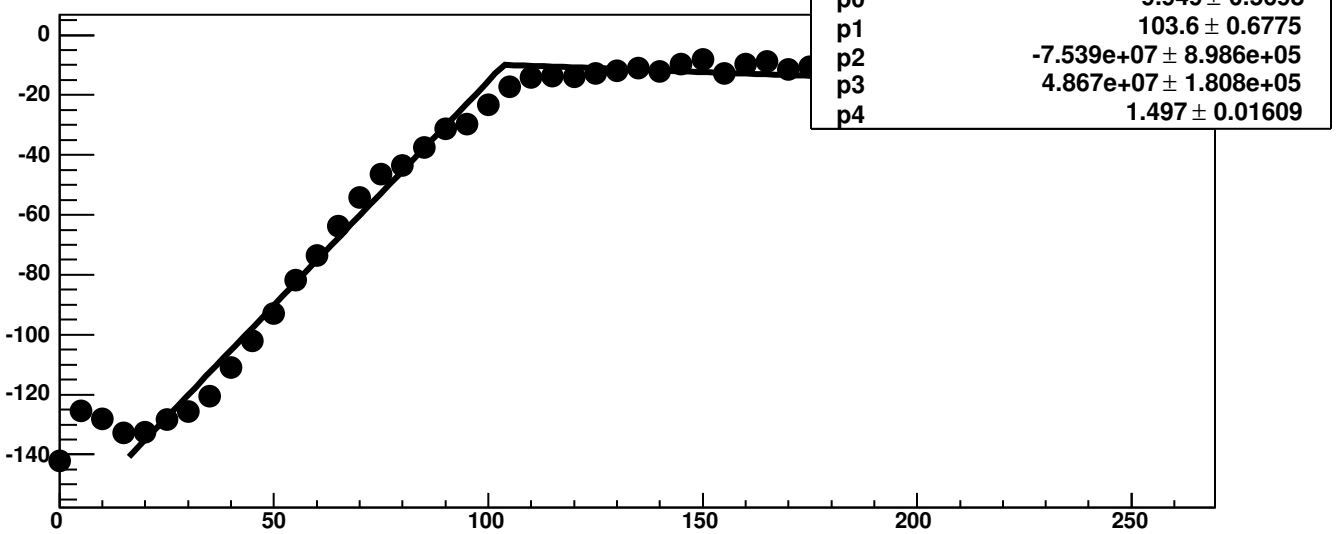
Chip 6, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



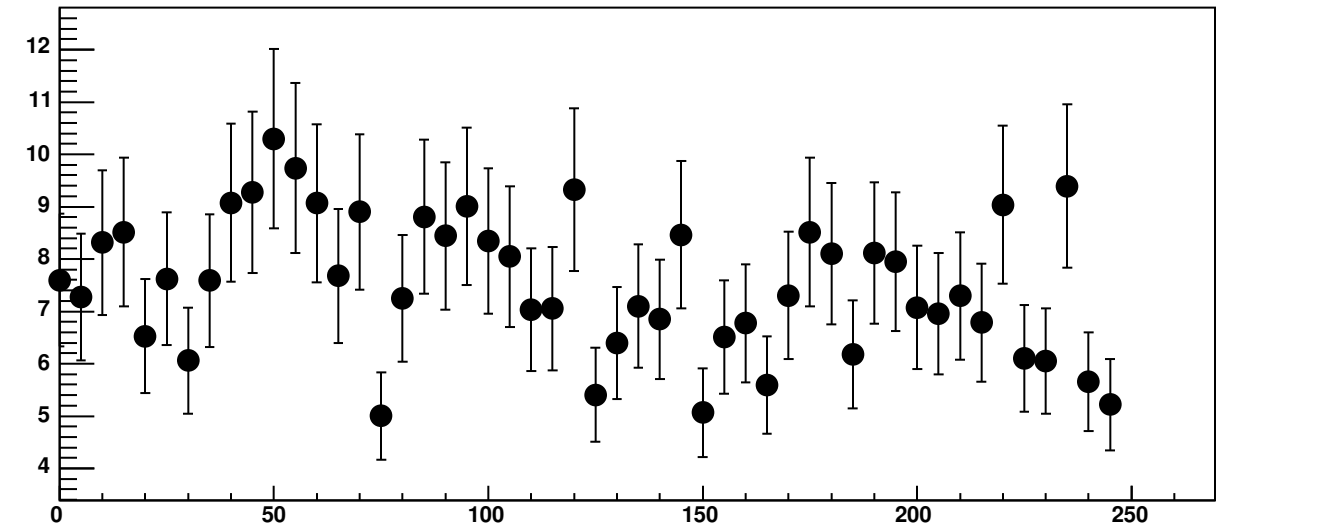
Chip 6, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold



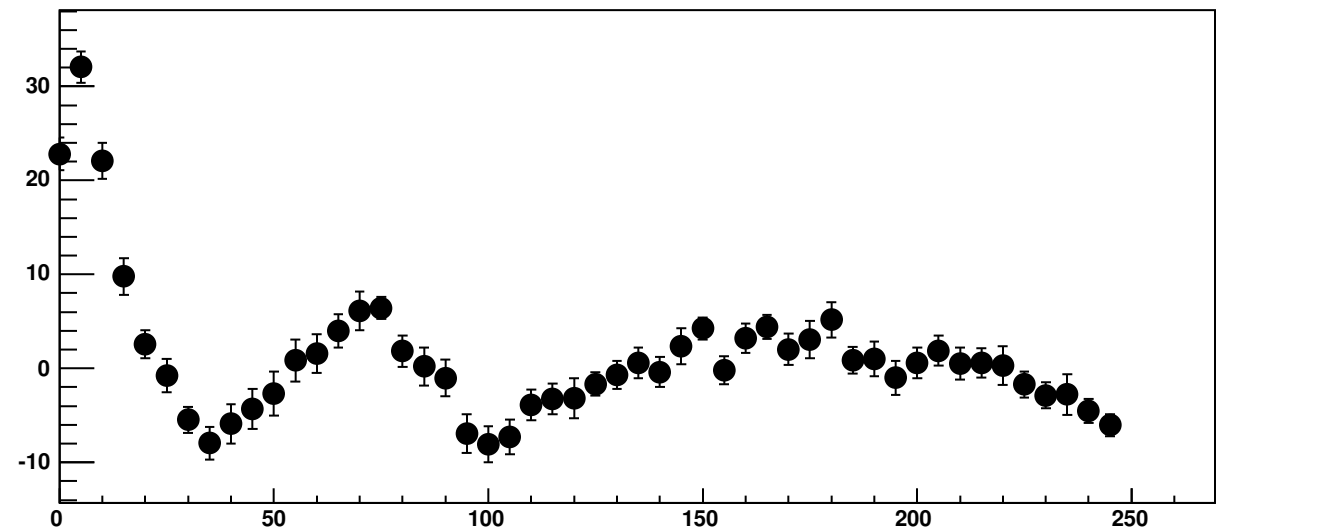
Chip 6, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold



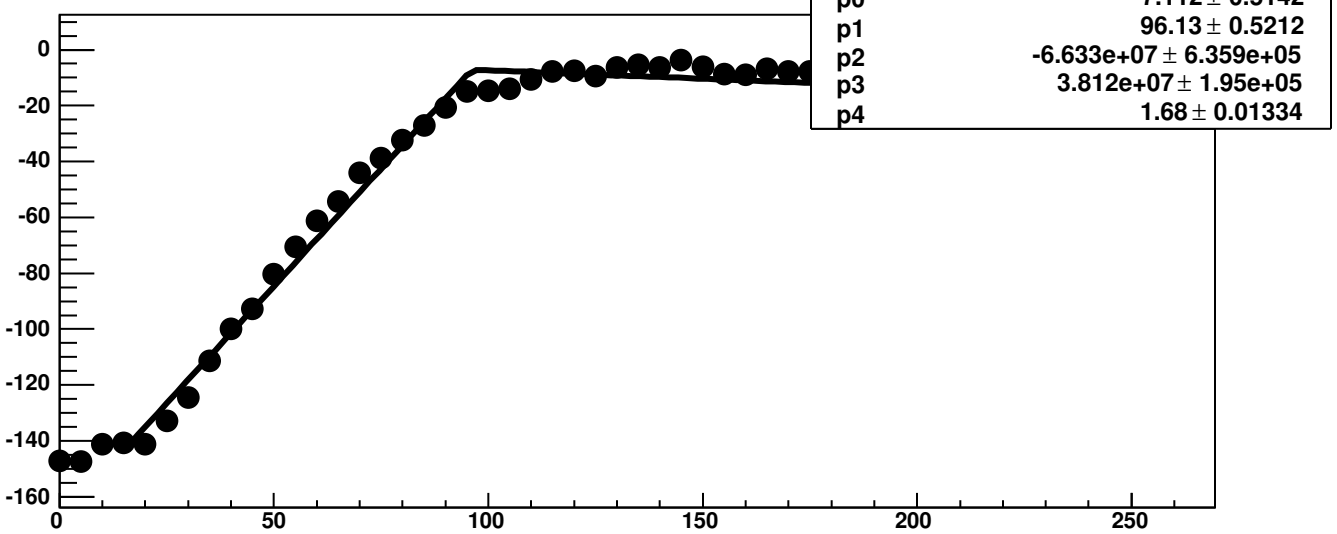
Chip 6, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



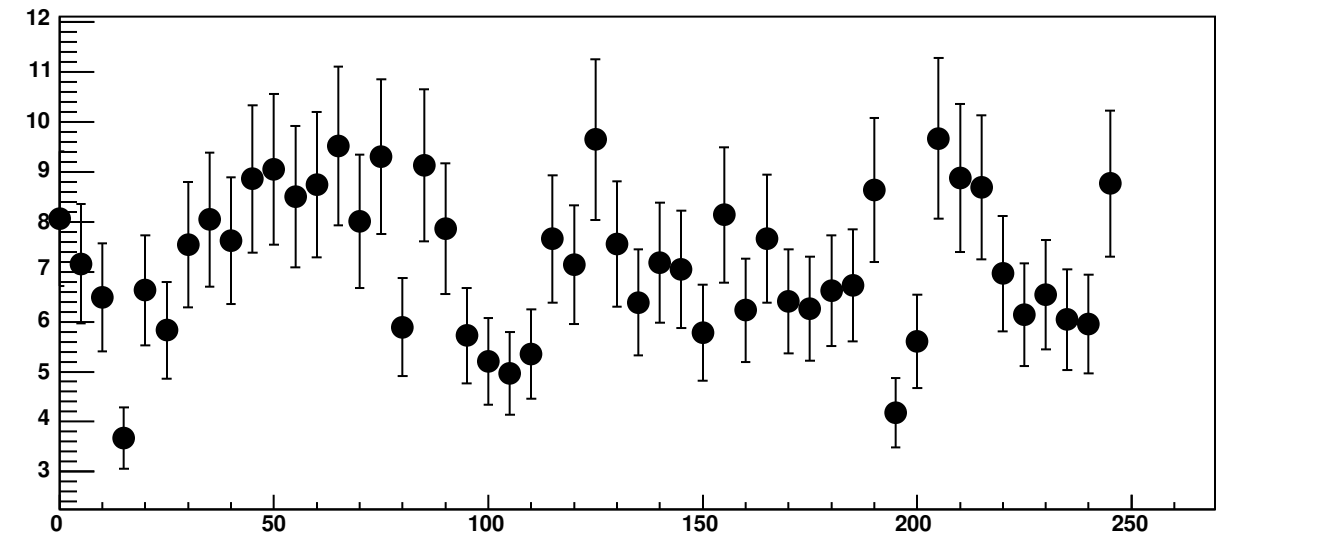
Chip 6, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold



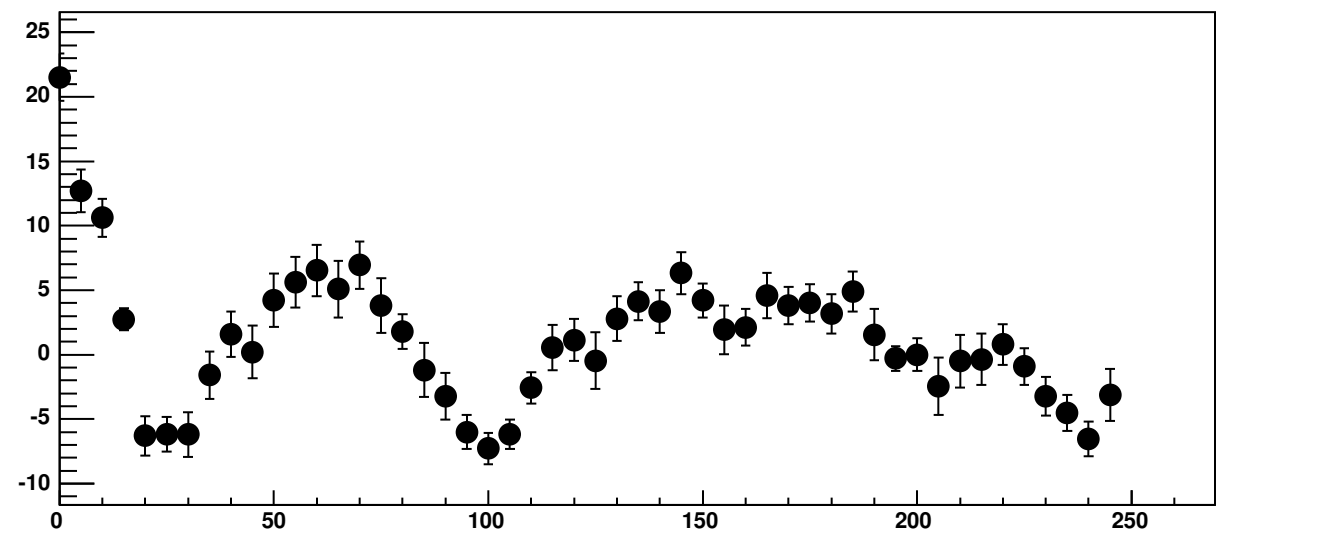
Chip 6, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold

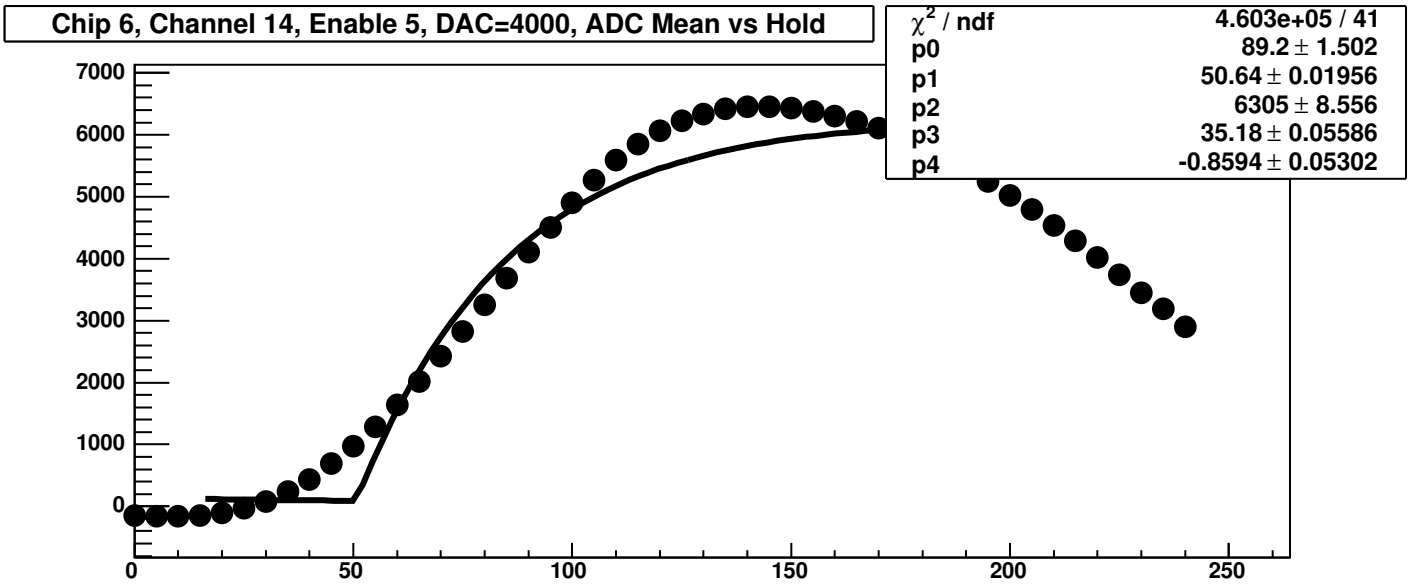


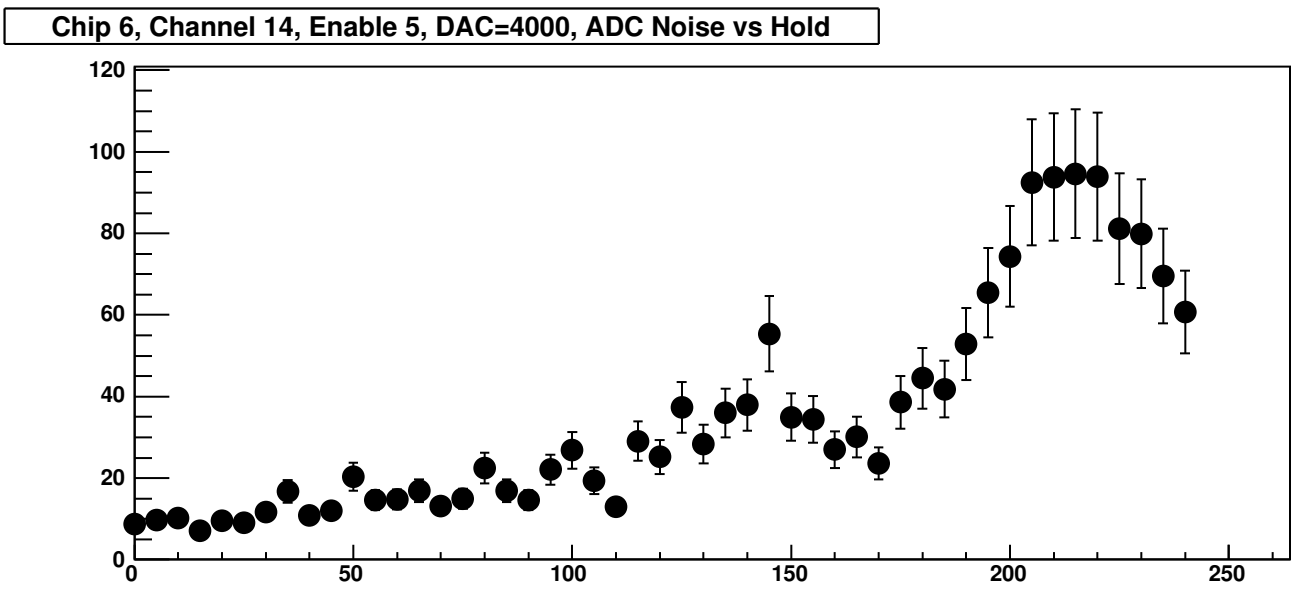
Chip 6, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold

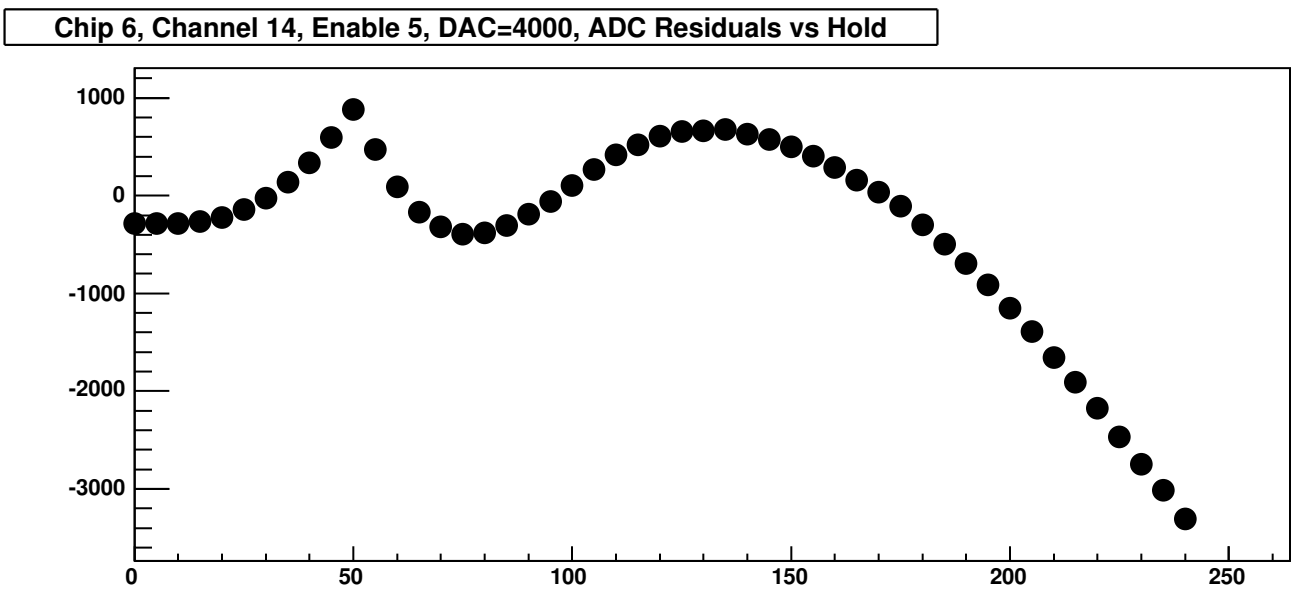


Chip 6, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold

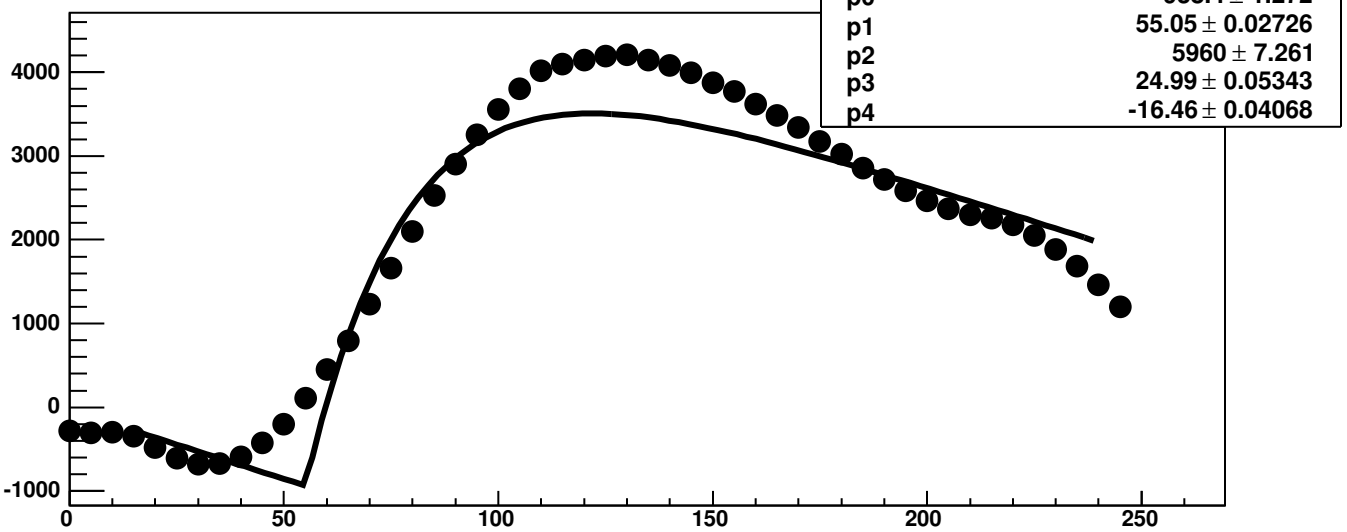



Chip 6, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.603e+05 / 41
p0 89.2 ± 1.502
p1 50.64 ± 0.01956
p2 6305 ± 8.556
p3 35.18 ± 0.05586
p4 -0.8594 ± 0.05302
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

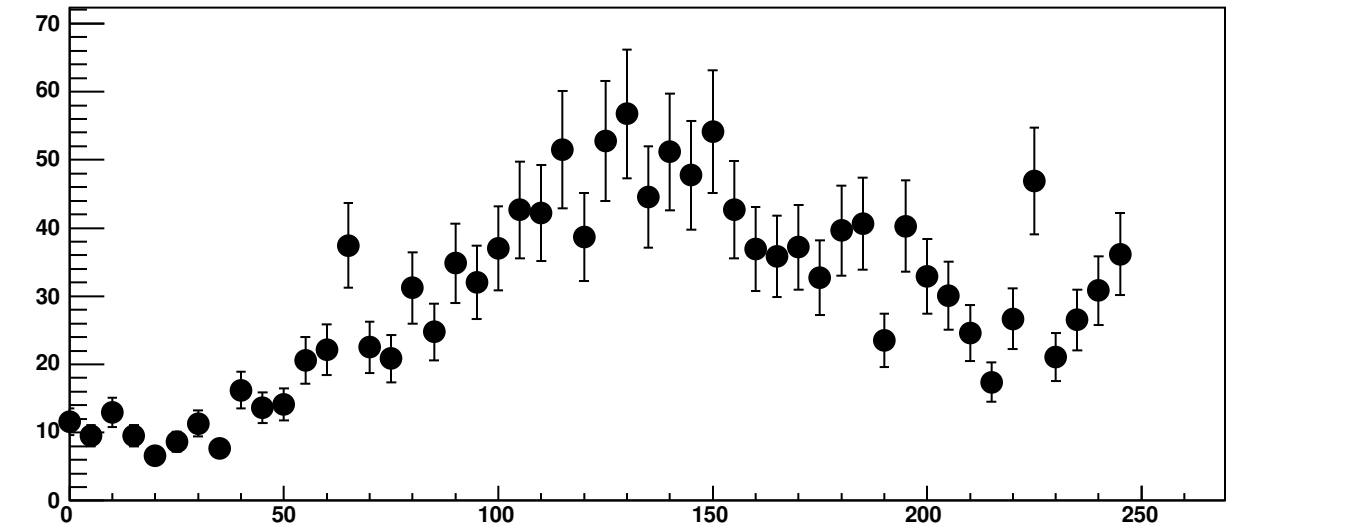

Chip 6, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 6, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

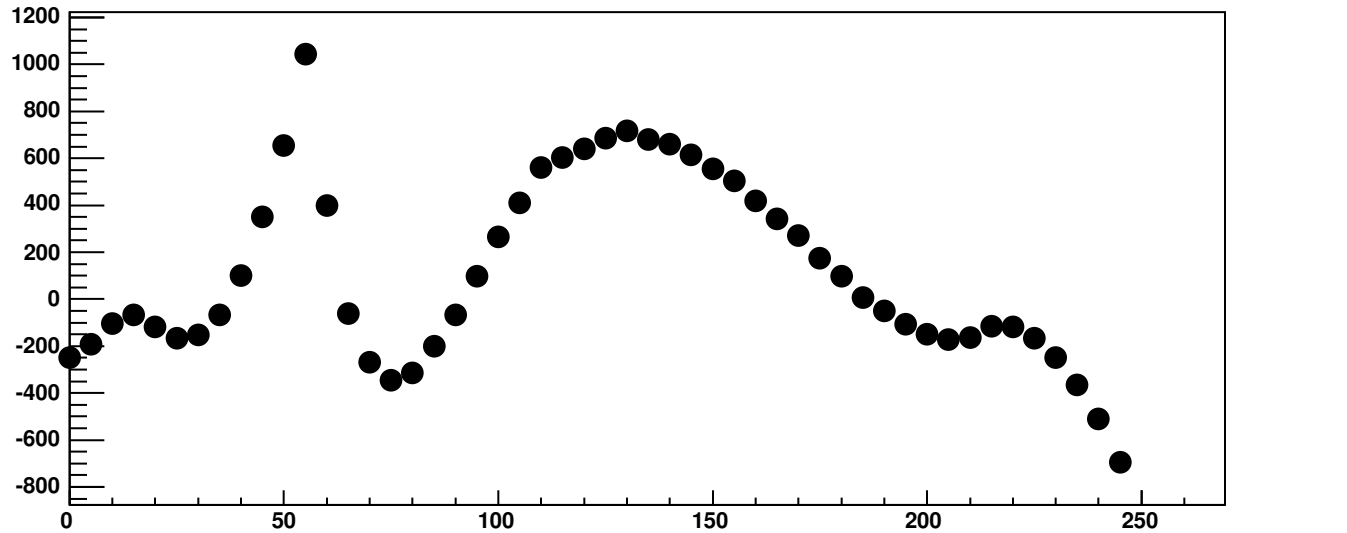
Chip 6, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold



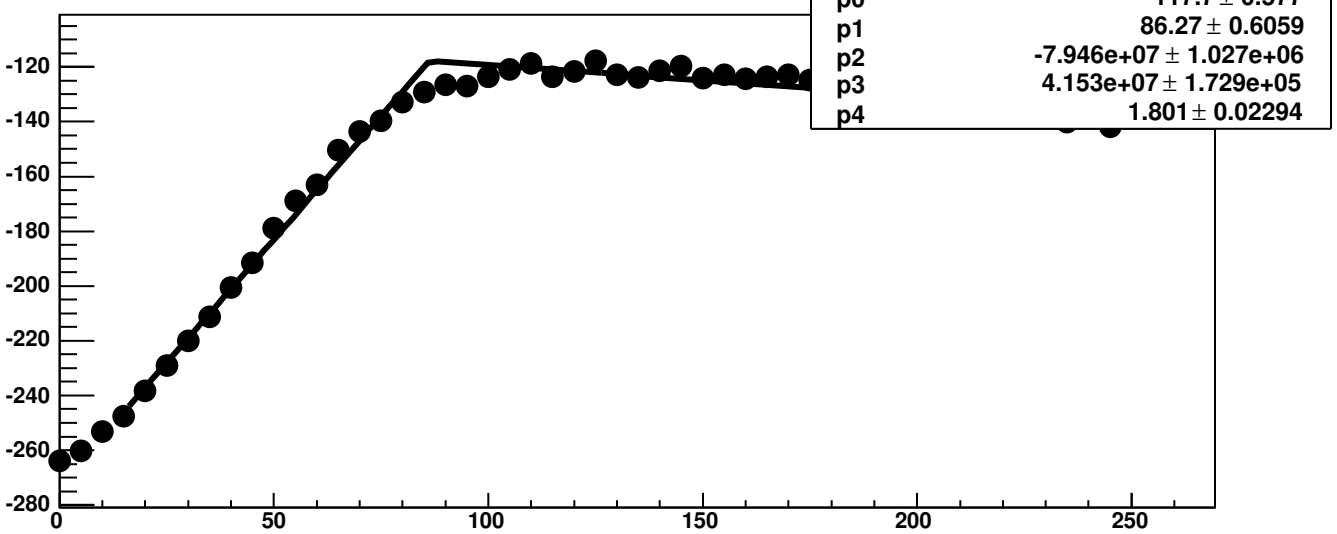
Chip 6, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



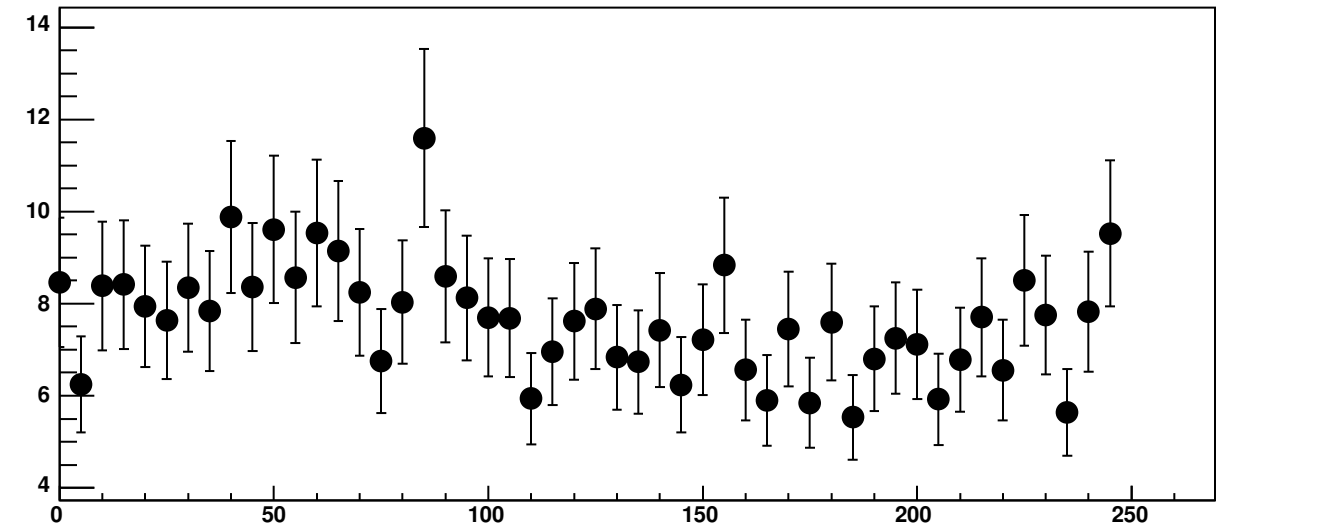
Chip 6, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold



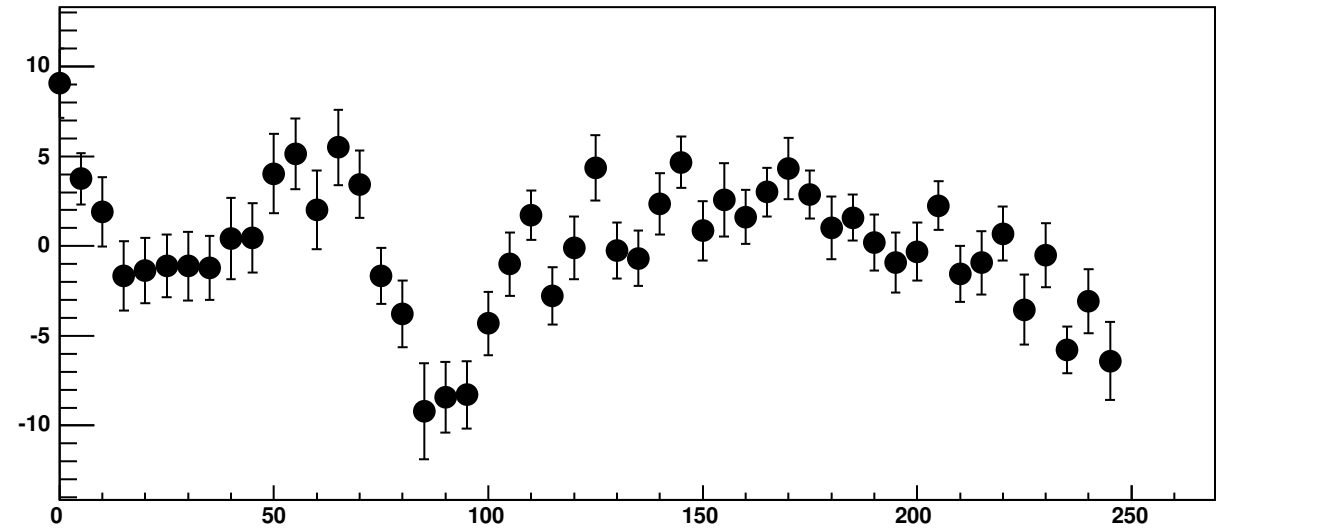
Chip 6, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 6, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold

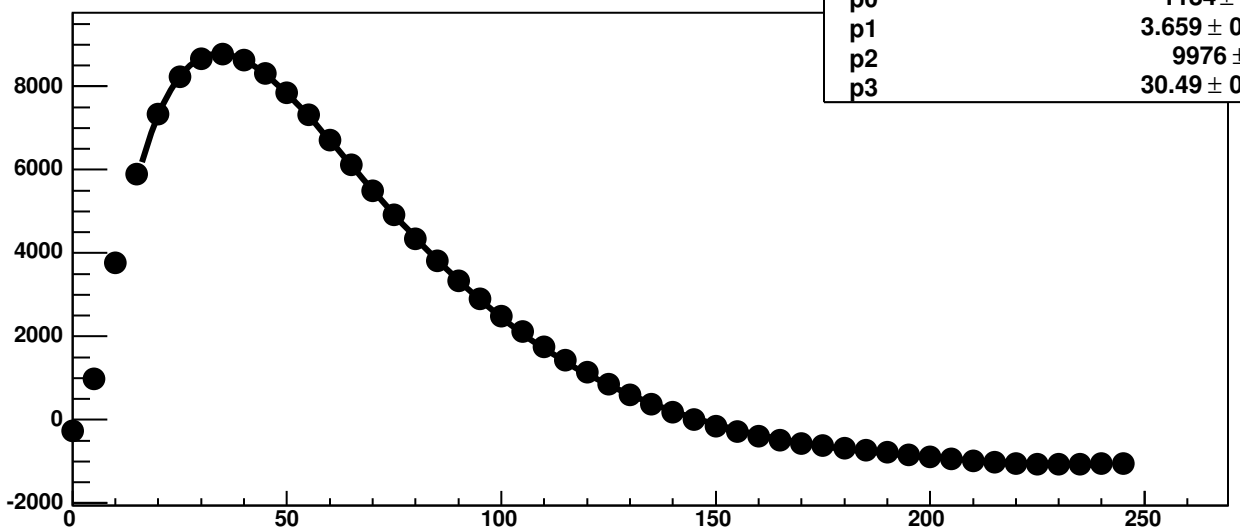


Chip 6, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

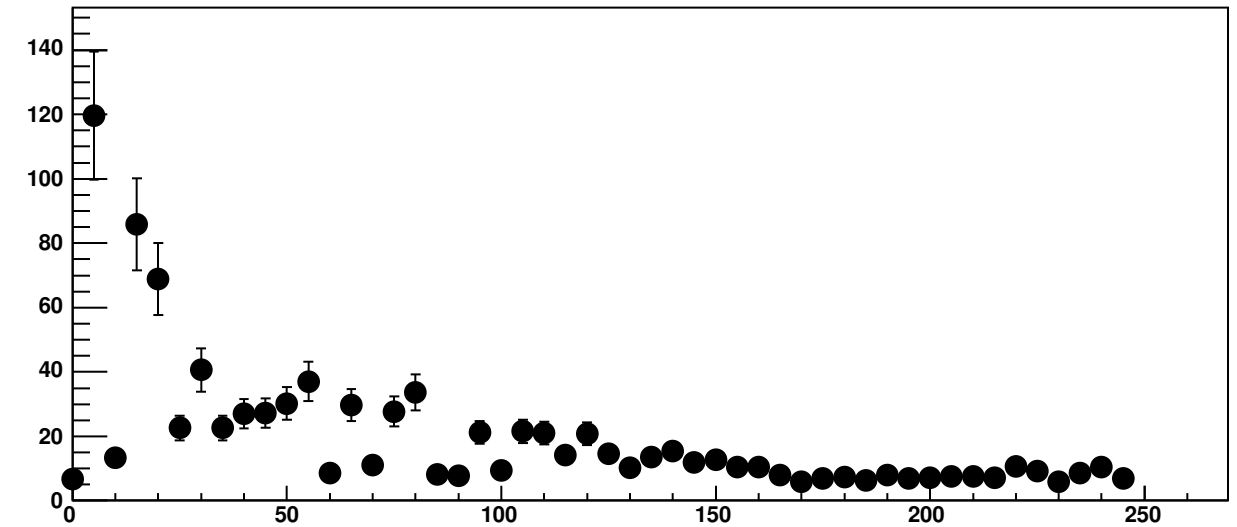


Chip 6, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold

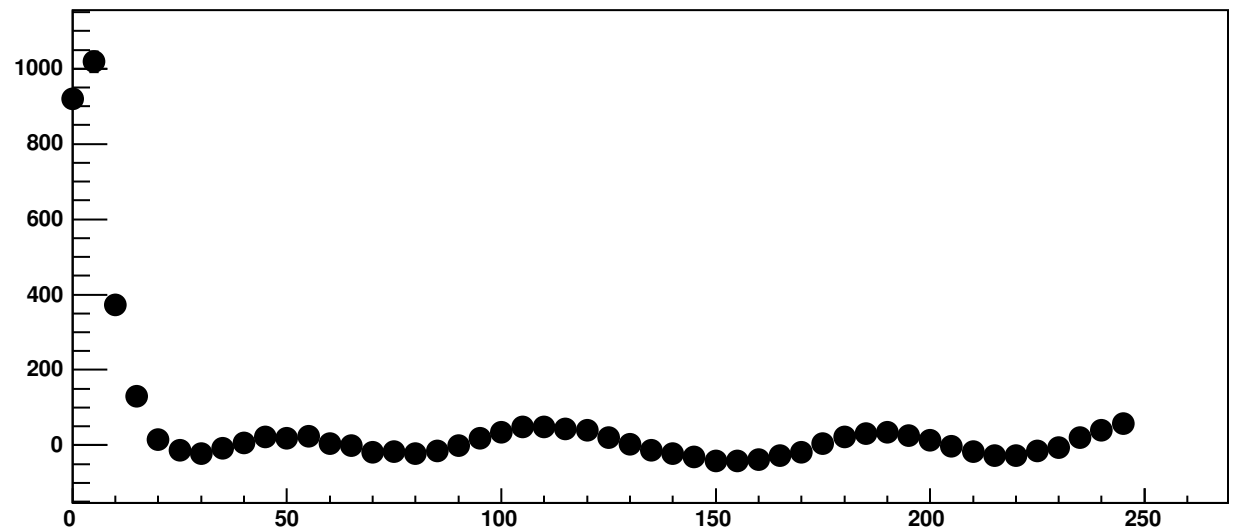
χ^2 / ndf	4522 / 42
p0	-1184 ± 0.6629
p1	3.659 ± 0.02441
p2	9976 ± 2.153
p3	30.49 ± 0.01061



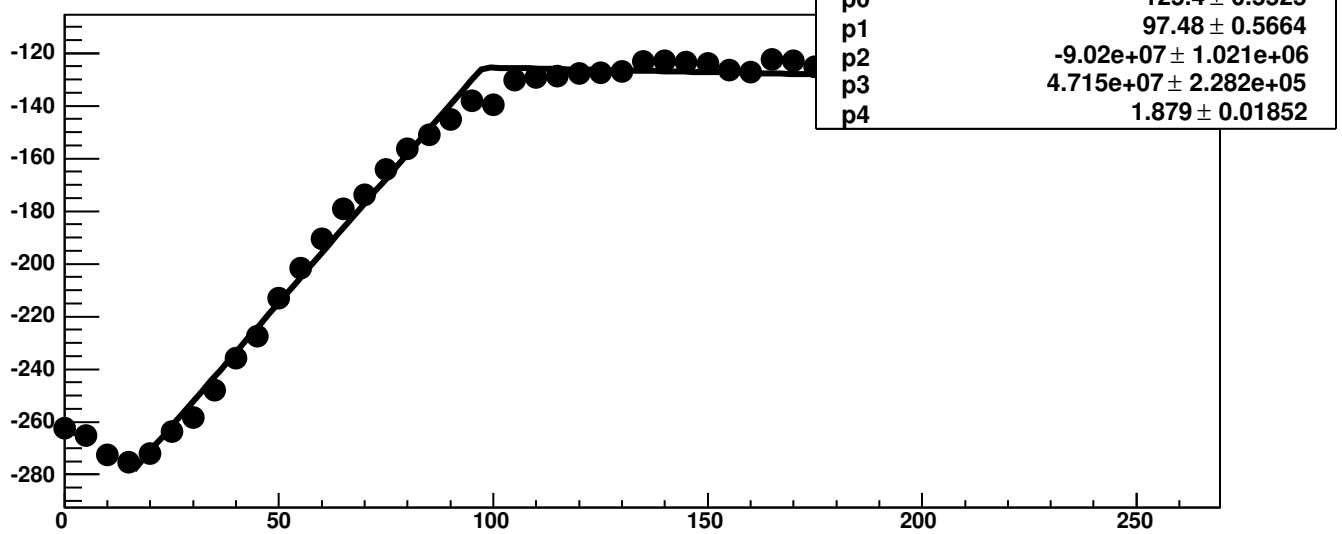
Chip 6, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold



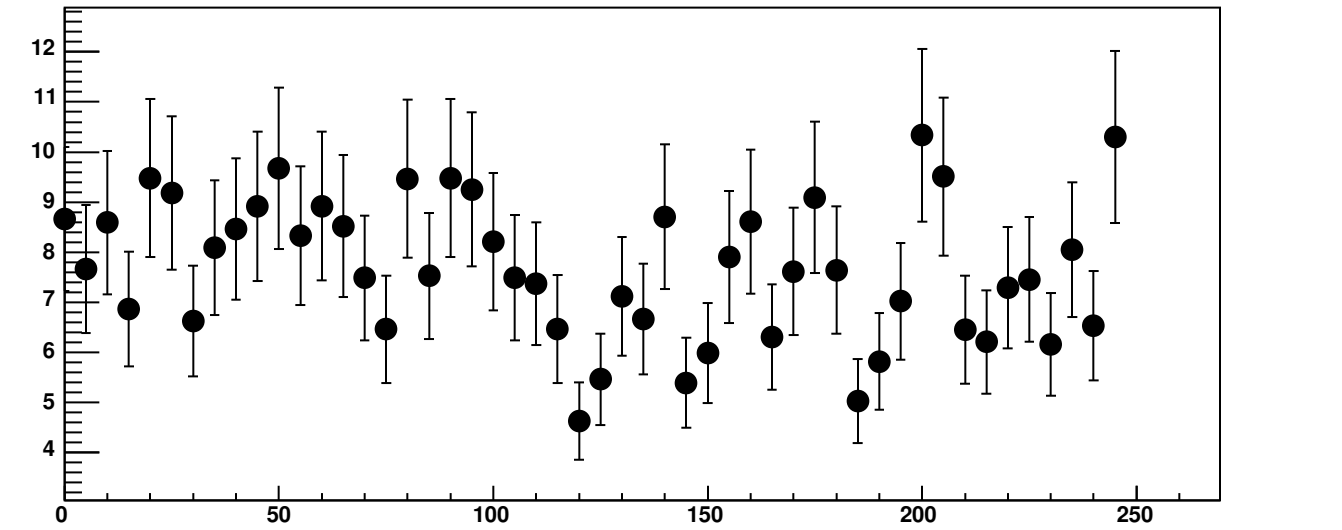
Chip 6, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold



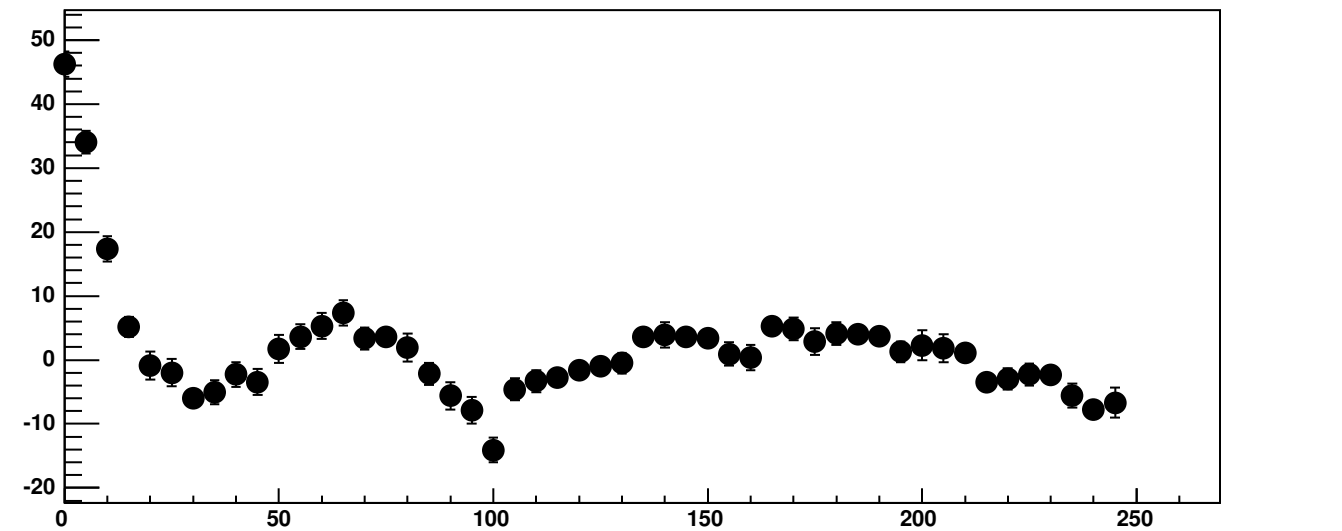
Chip 6, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold



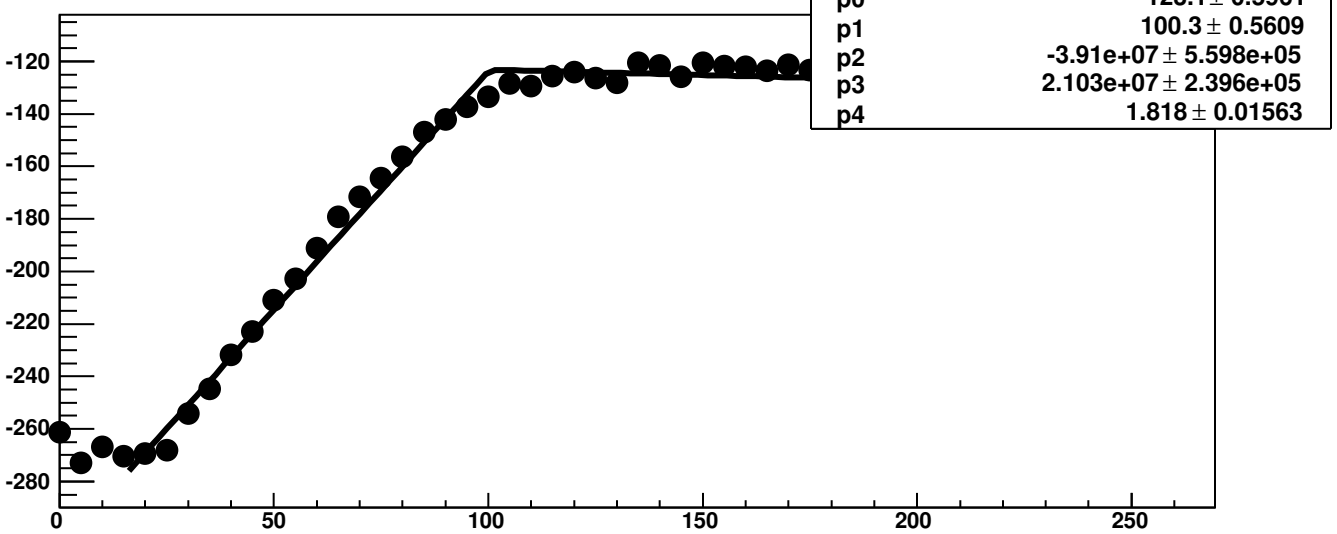
Chip 6, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



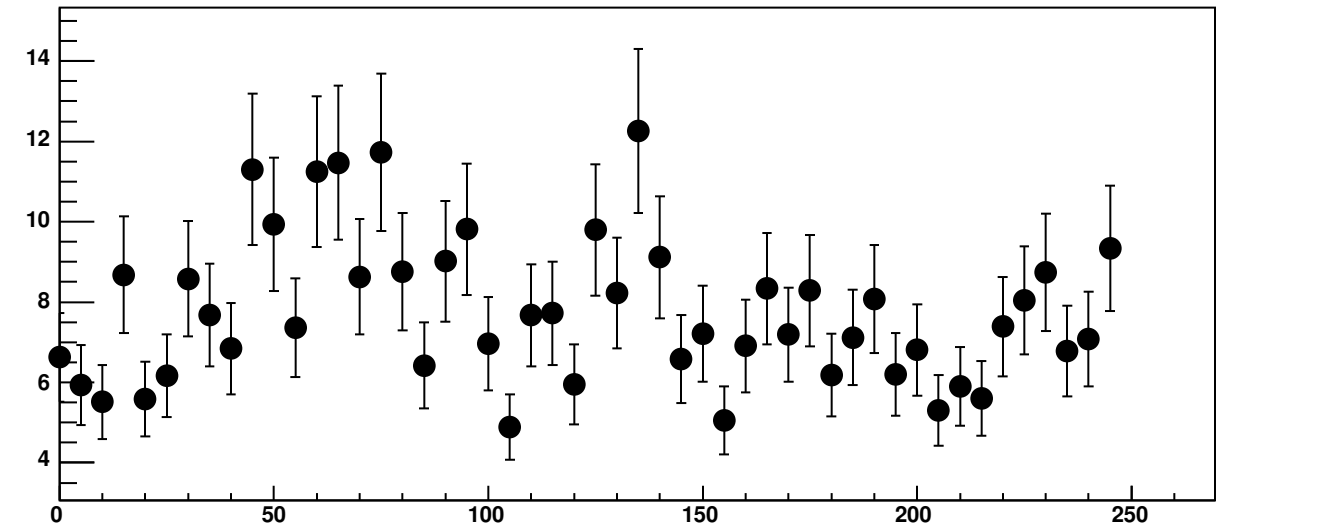
Chip 6, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold



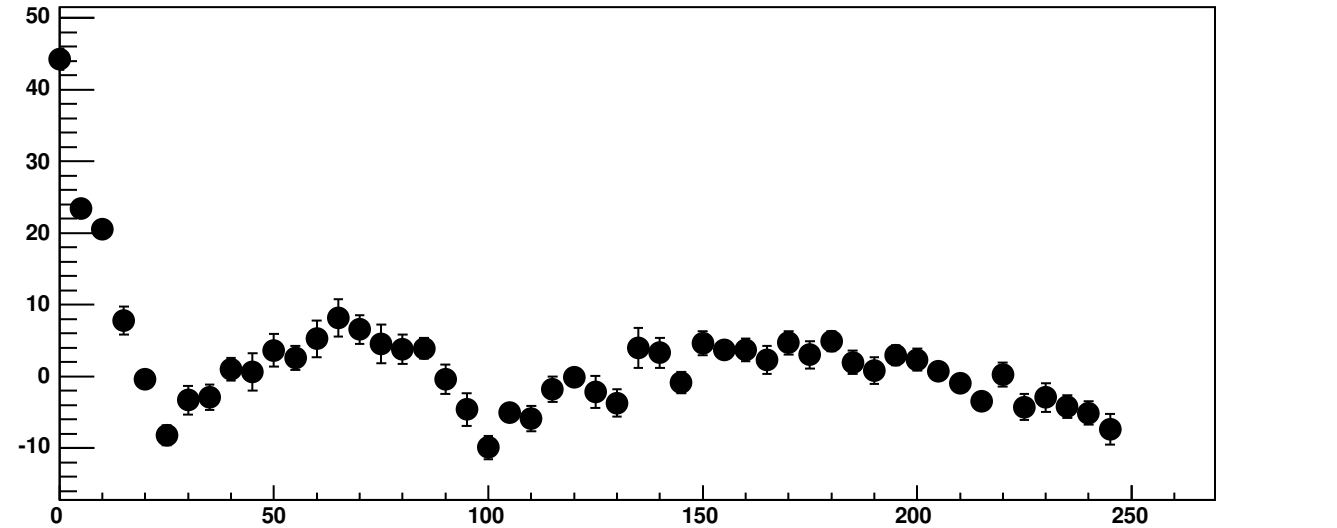
Chip 6, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold



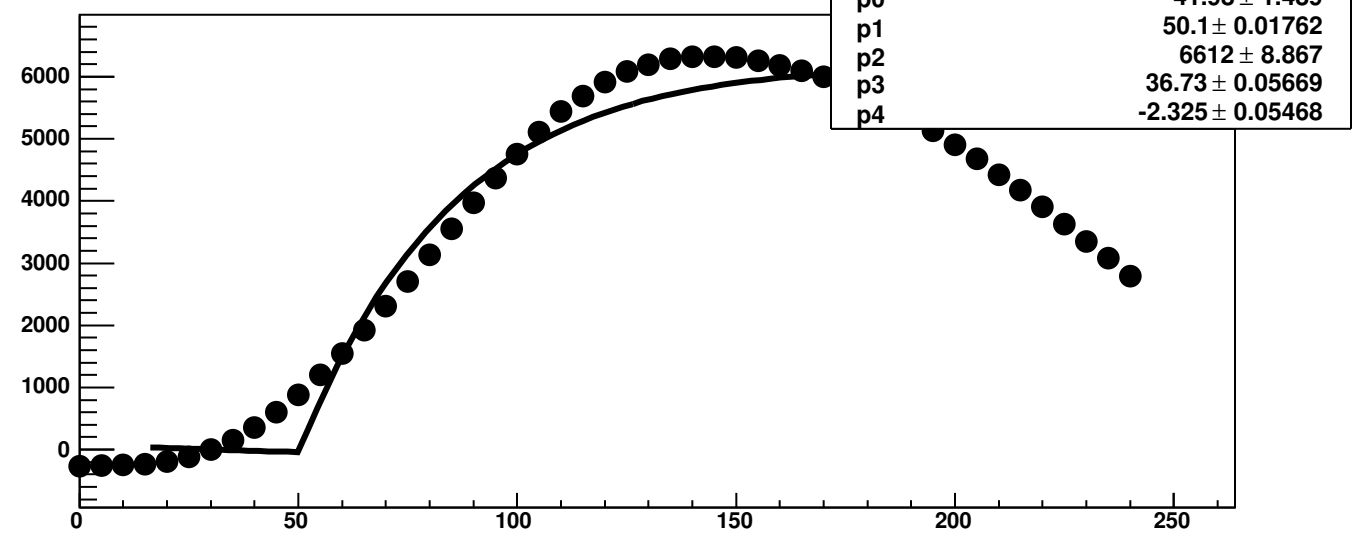
Chip 6, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



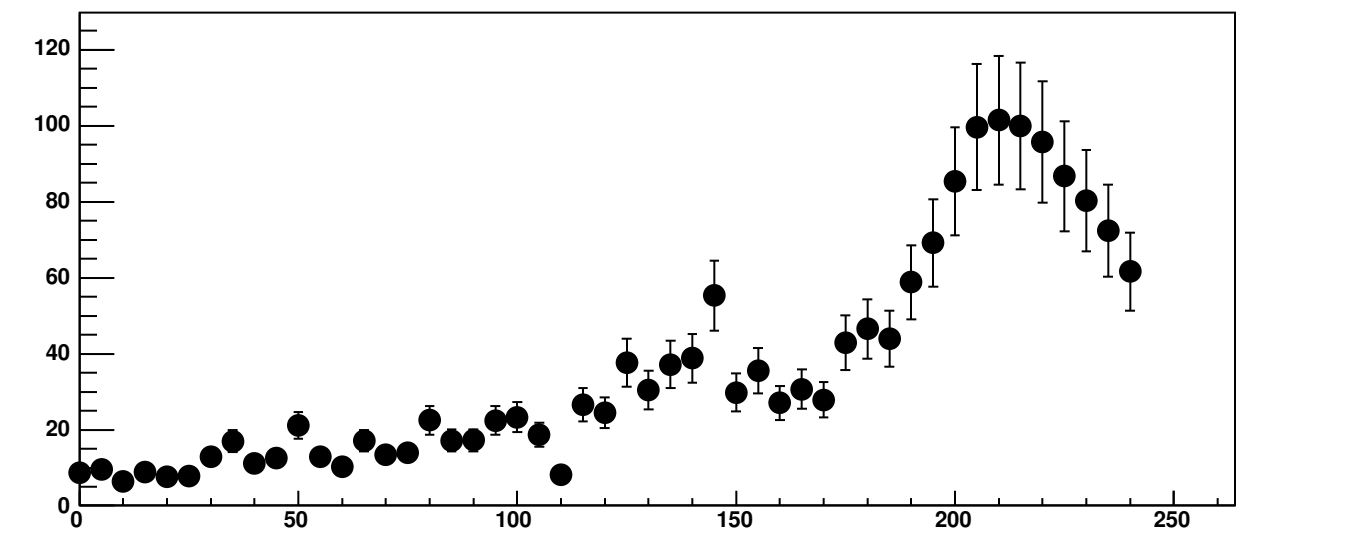
Chip 6, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold



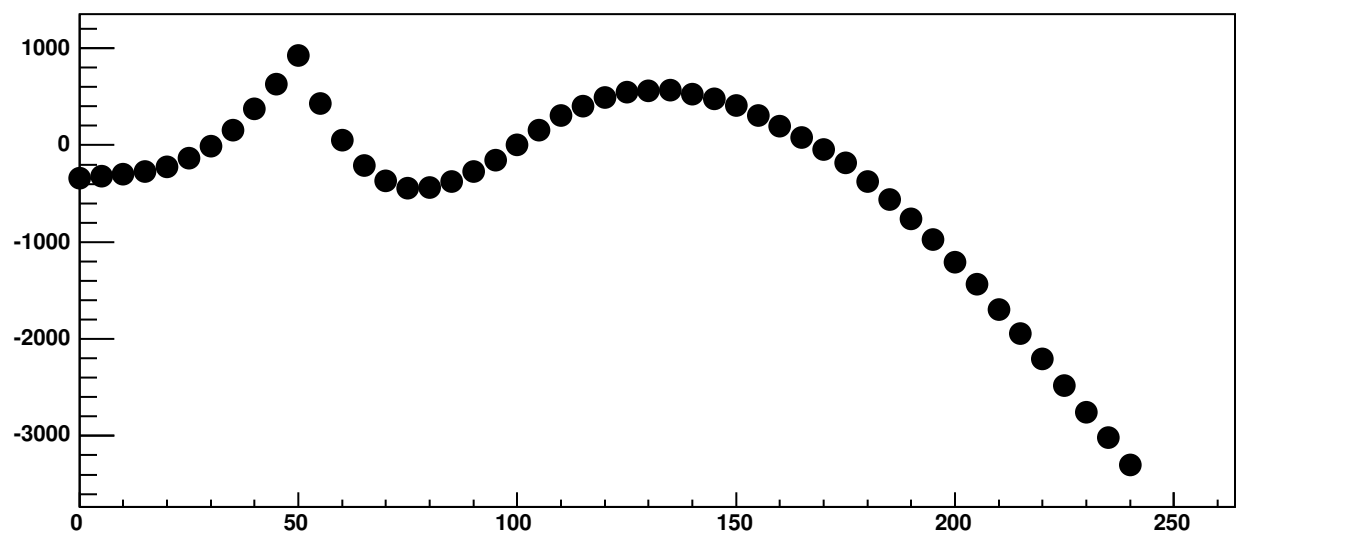
Chip 6, Channel 15, Enable 5, DAC=4000, ADC Mean vs Hold



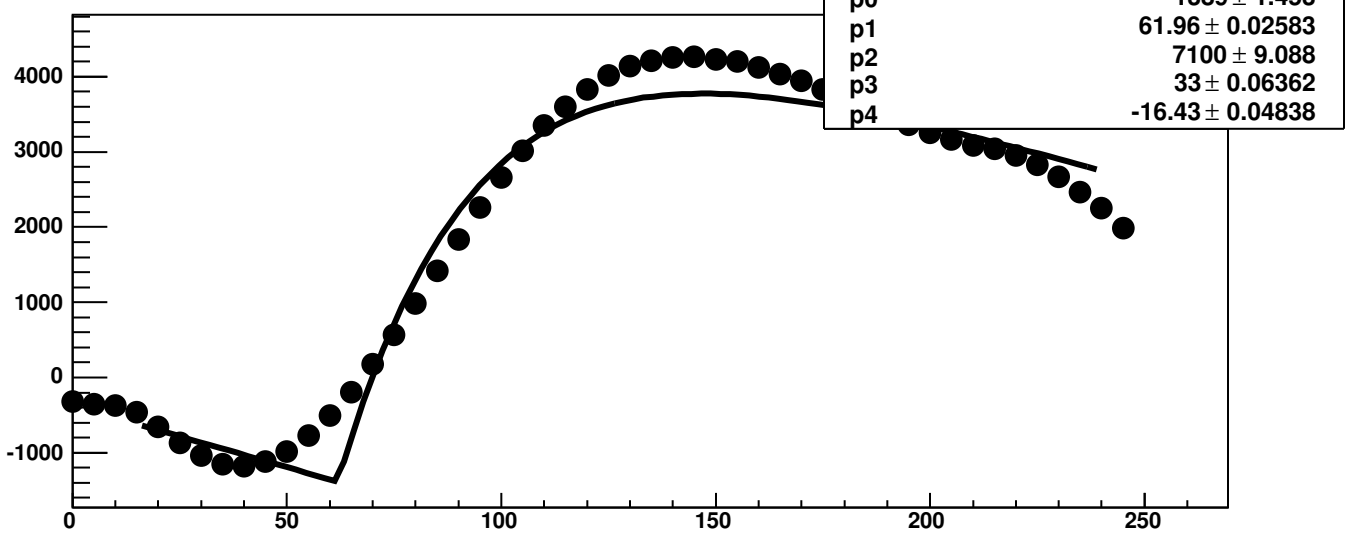
Chip 6, Channel 15, Enable 5, DAC=4000, ADC Noise vs Hold



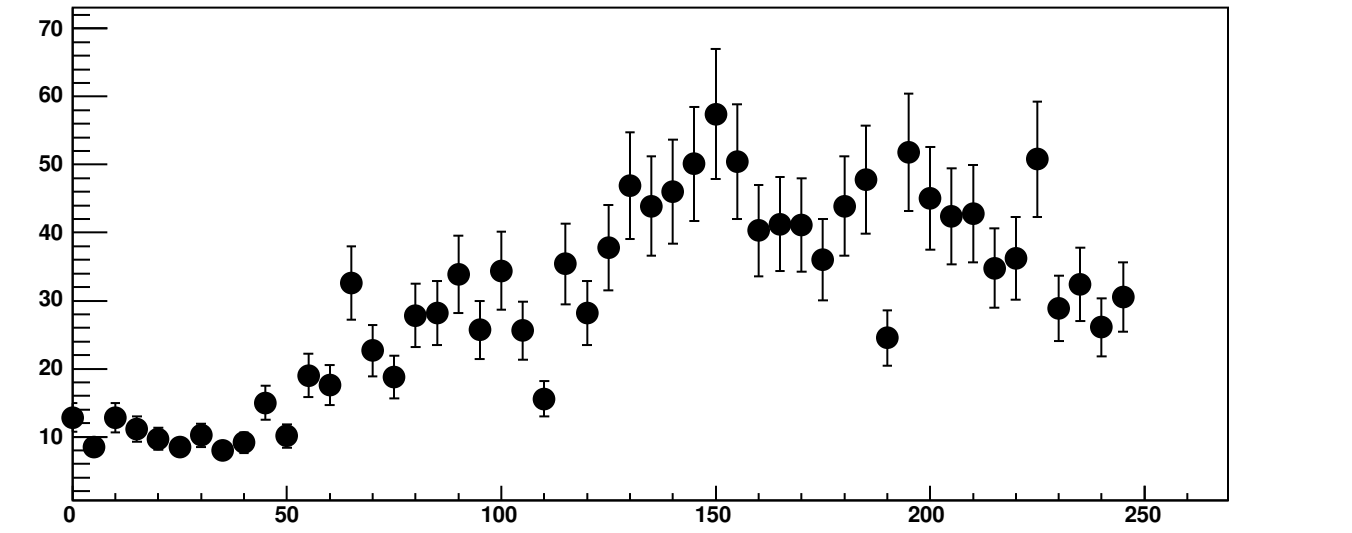
Chip 6, Channel 15, Enable 5, DAC=4000, ADC Residuals vs Hold



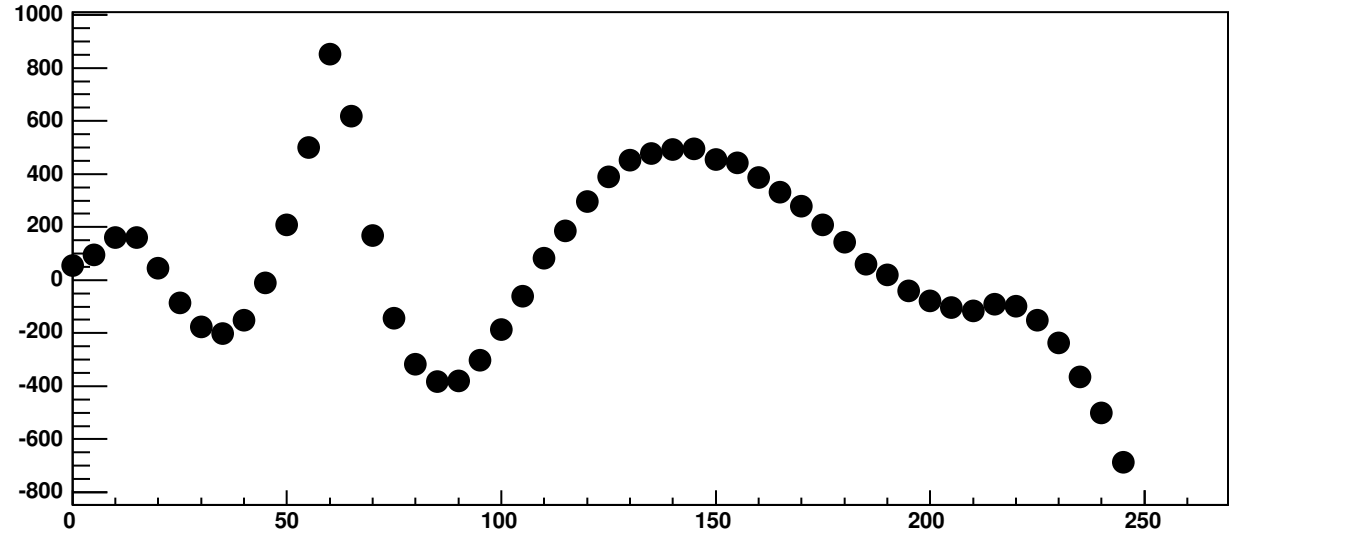
Chip 6, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold



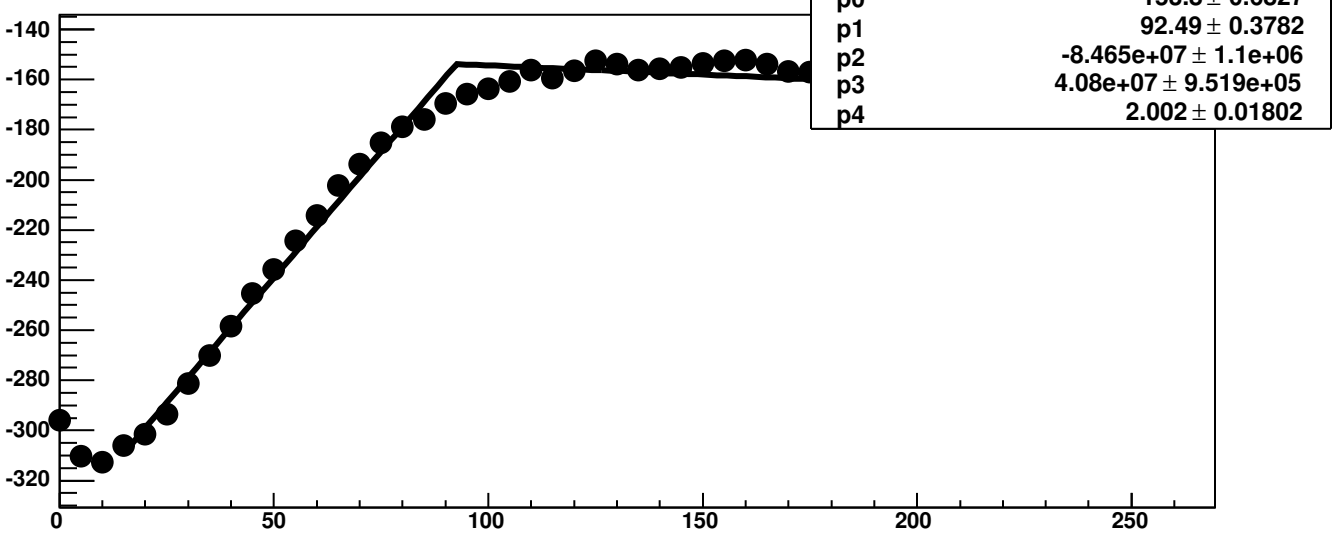
Chip 6, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



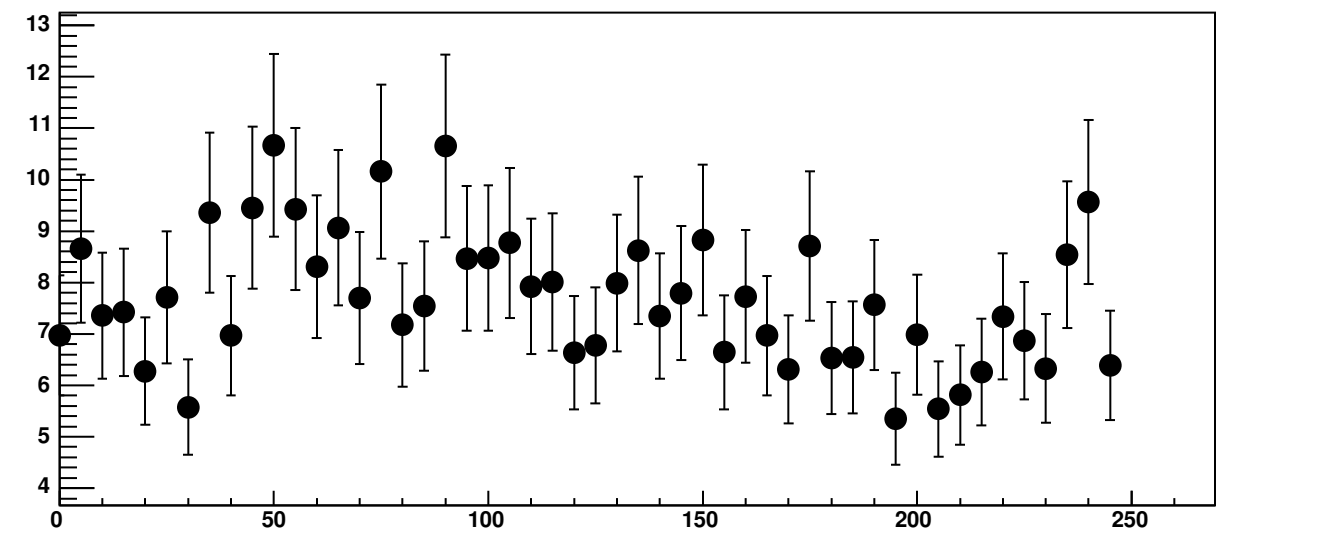
Chip 6, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold



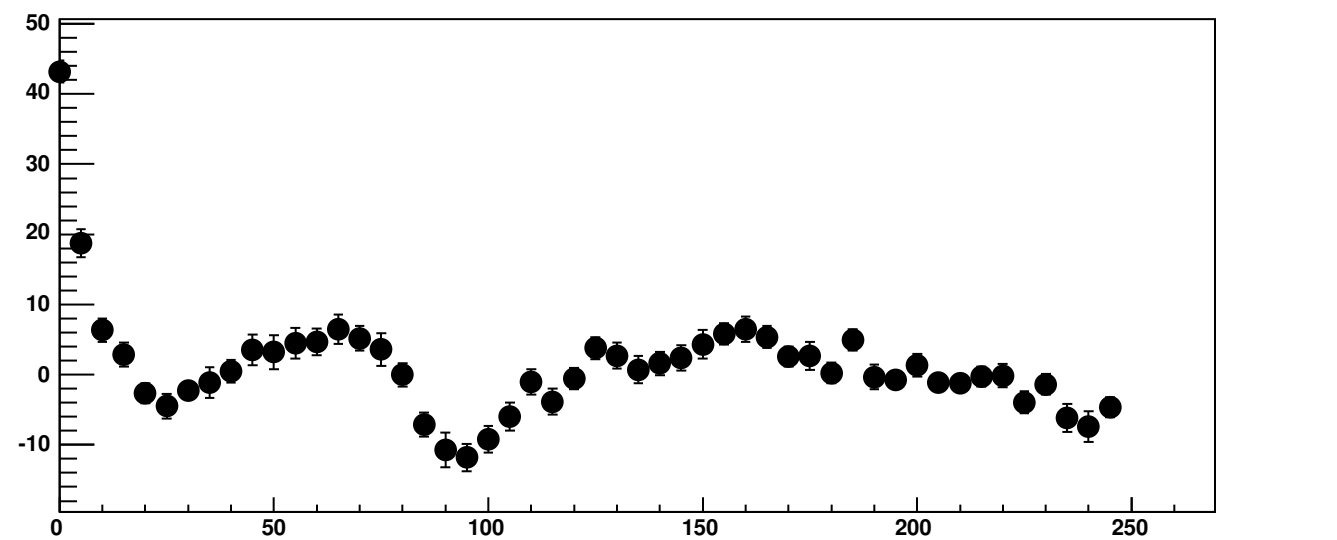
Chip 6, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold



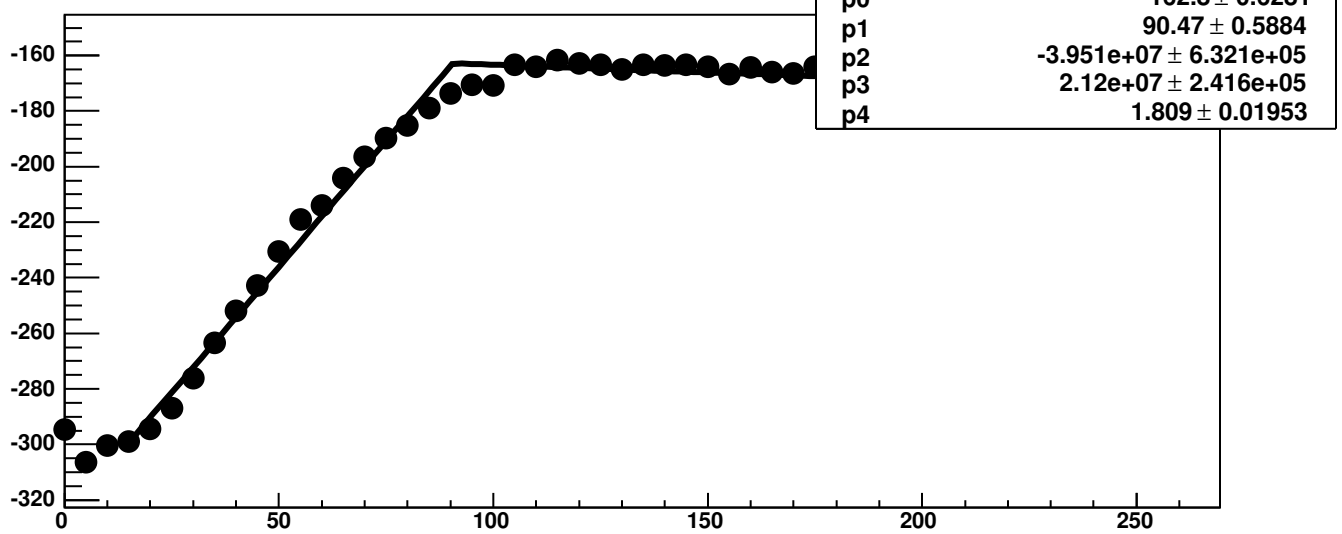
Chip 6, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



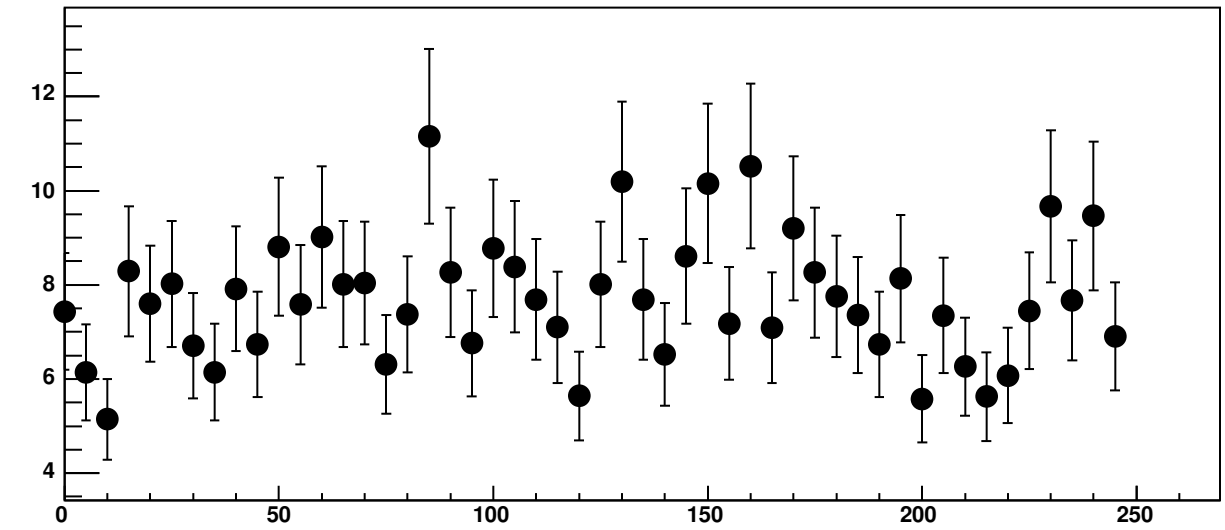
Chip 6, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold



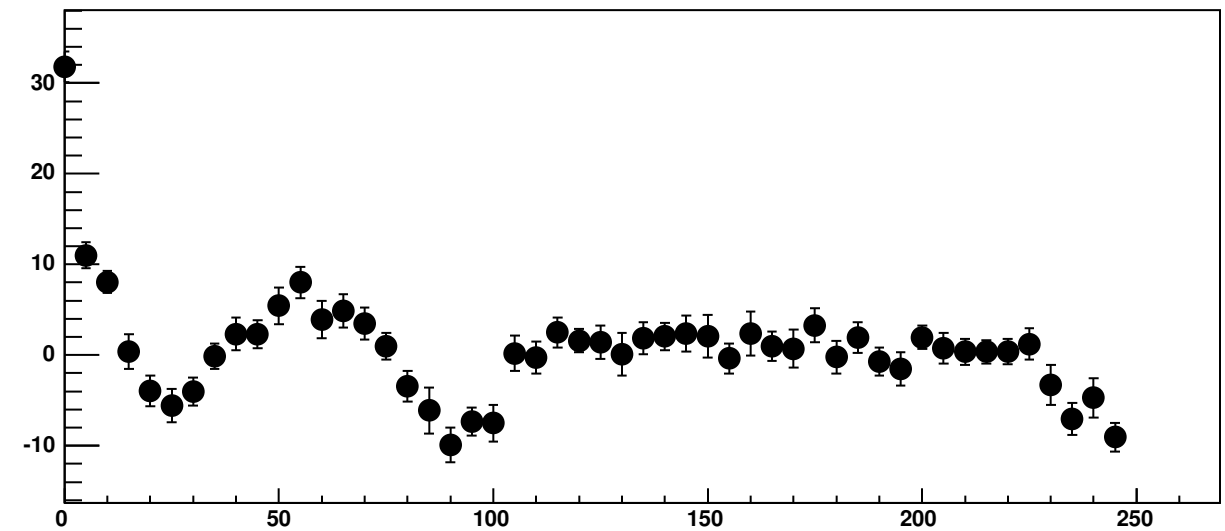
Chip 6, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold



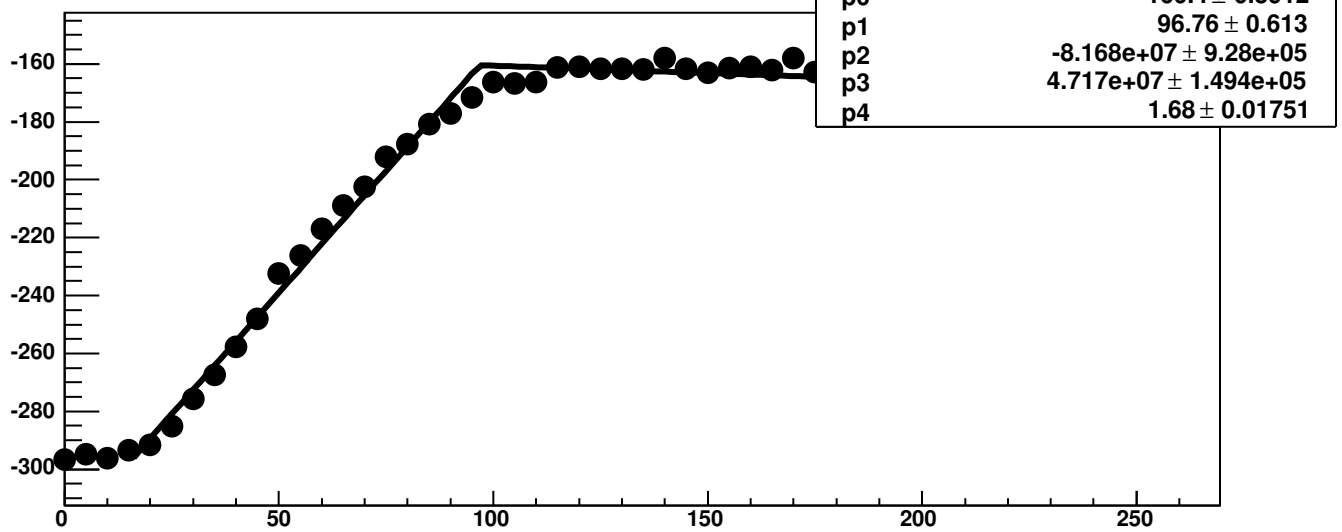
Chip 6, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold



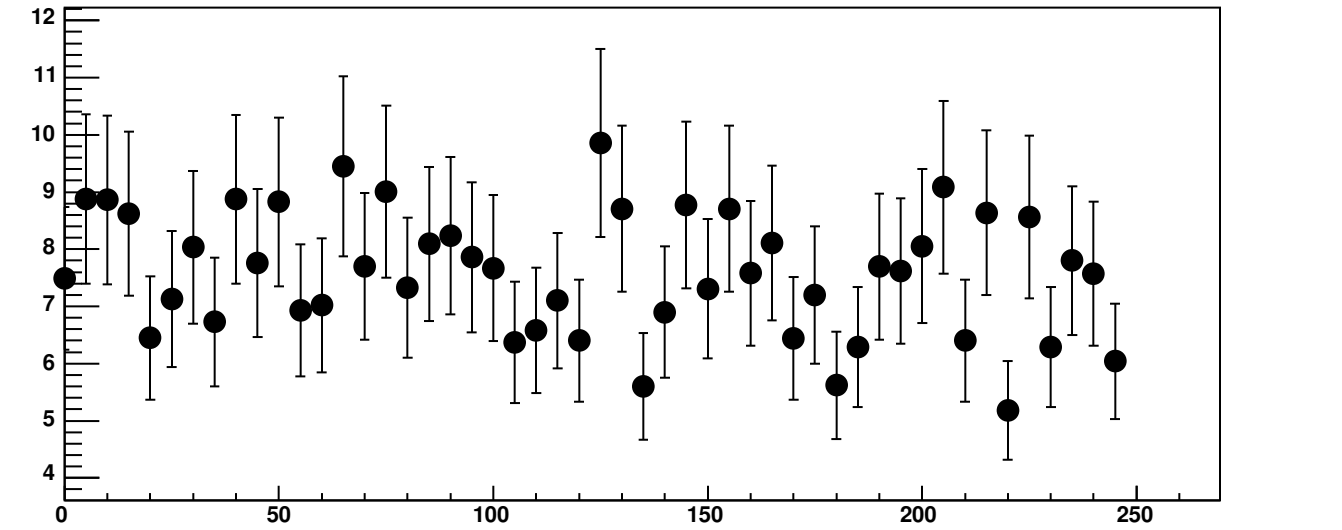
Chip 6, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold



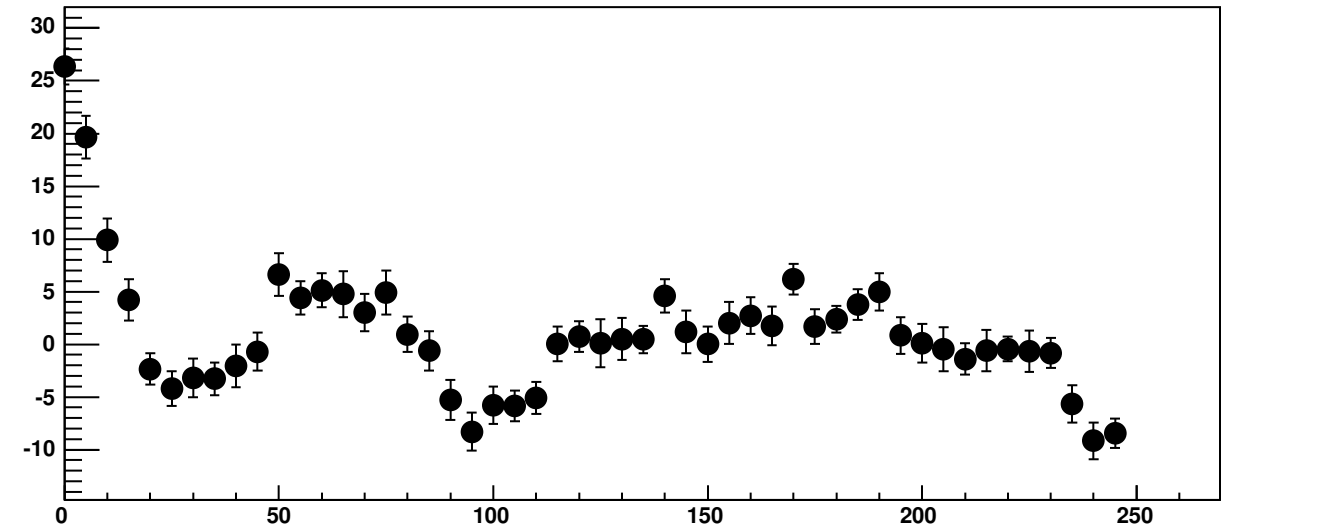
Chip 6, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold



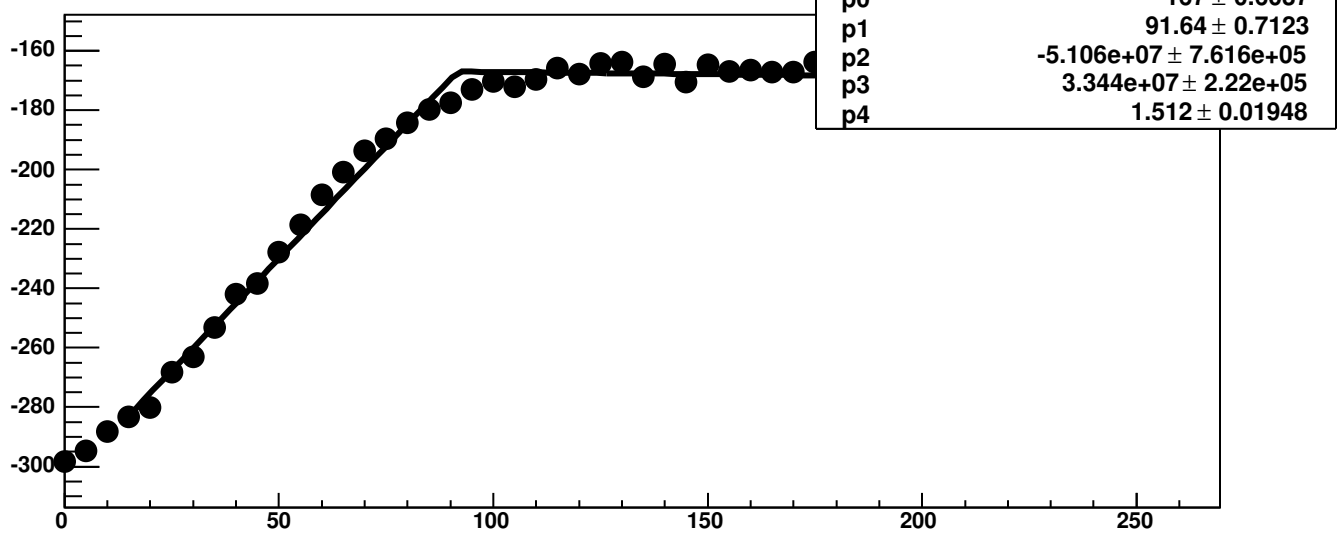
Chip 6, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



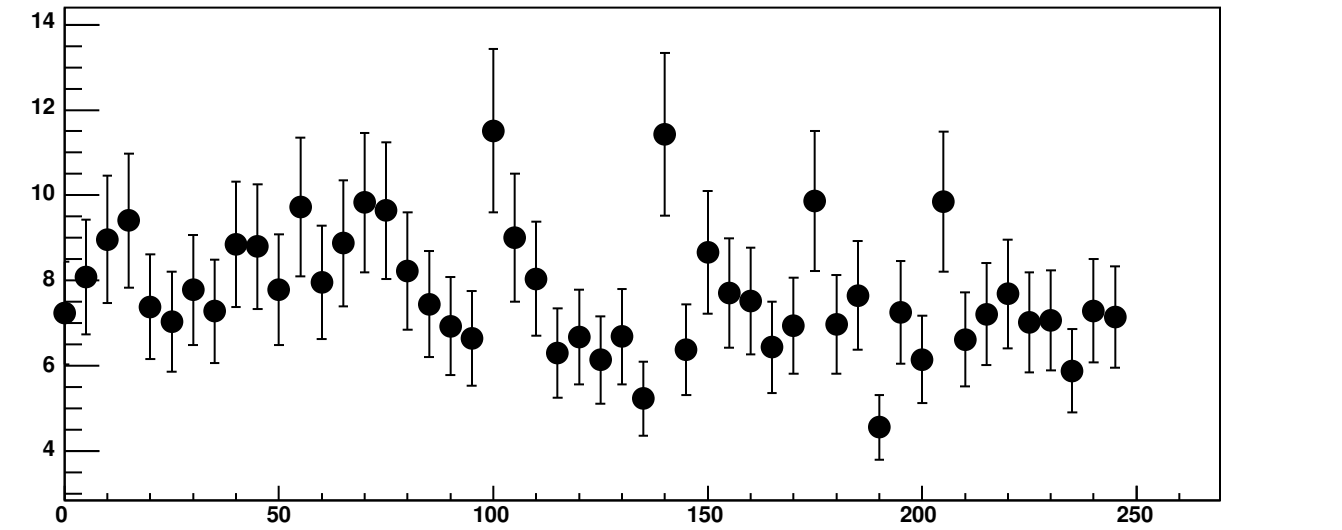
Chip 6, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold



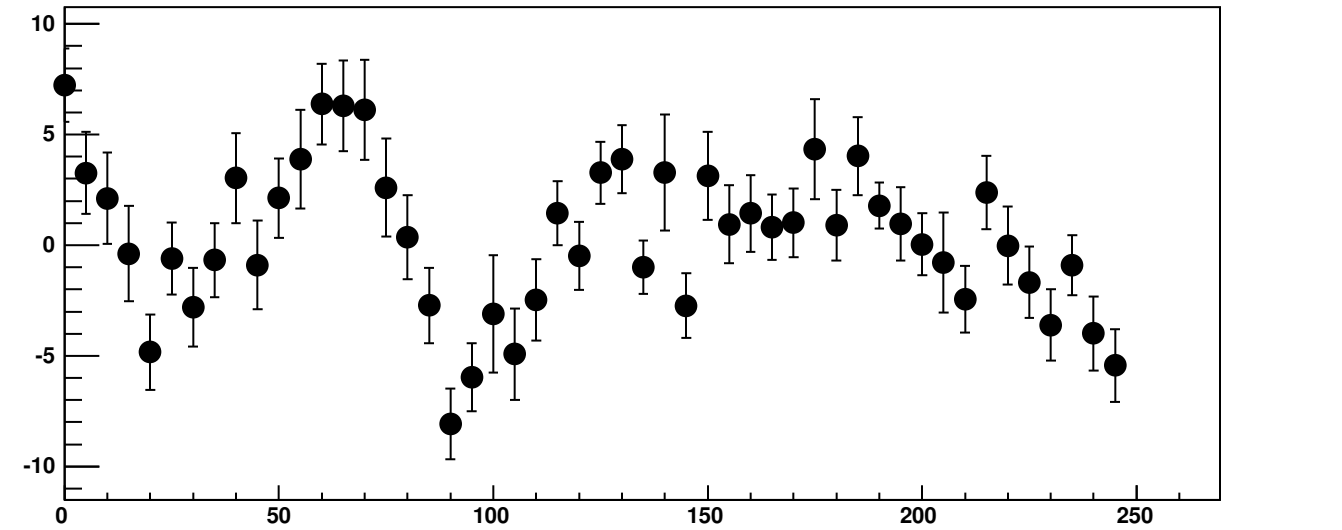
Chip 6, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold



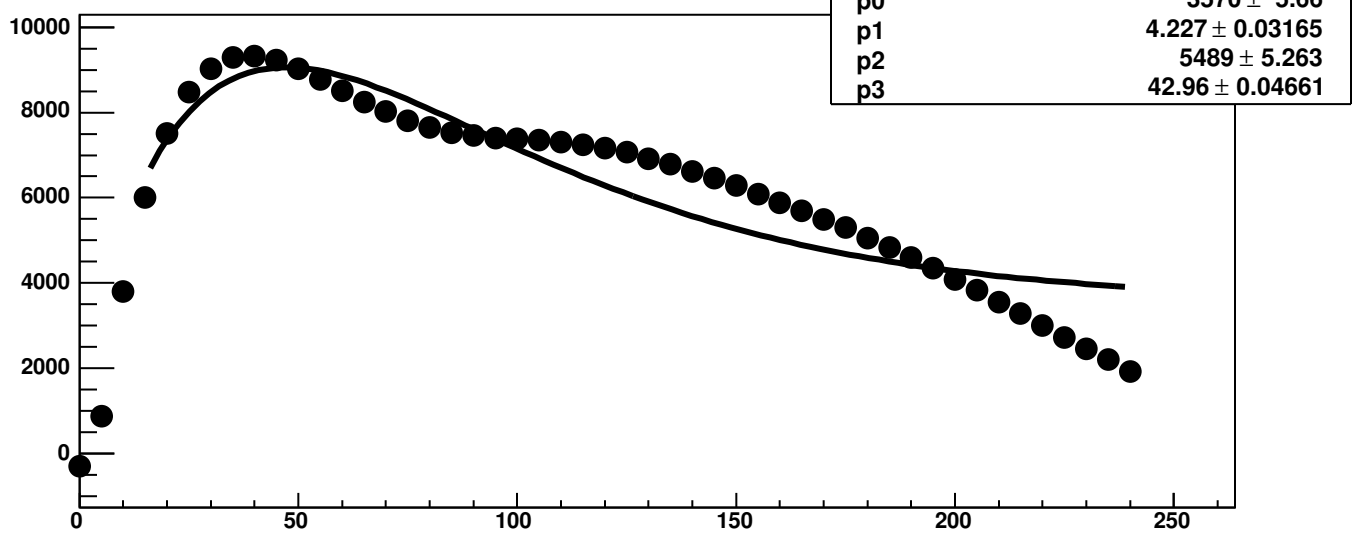
Chip 6, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



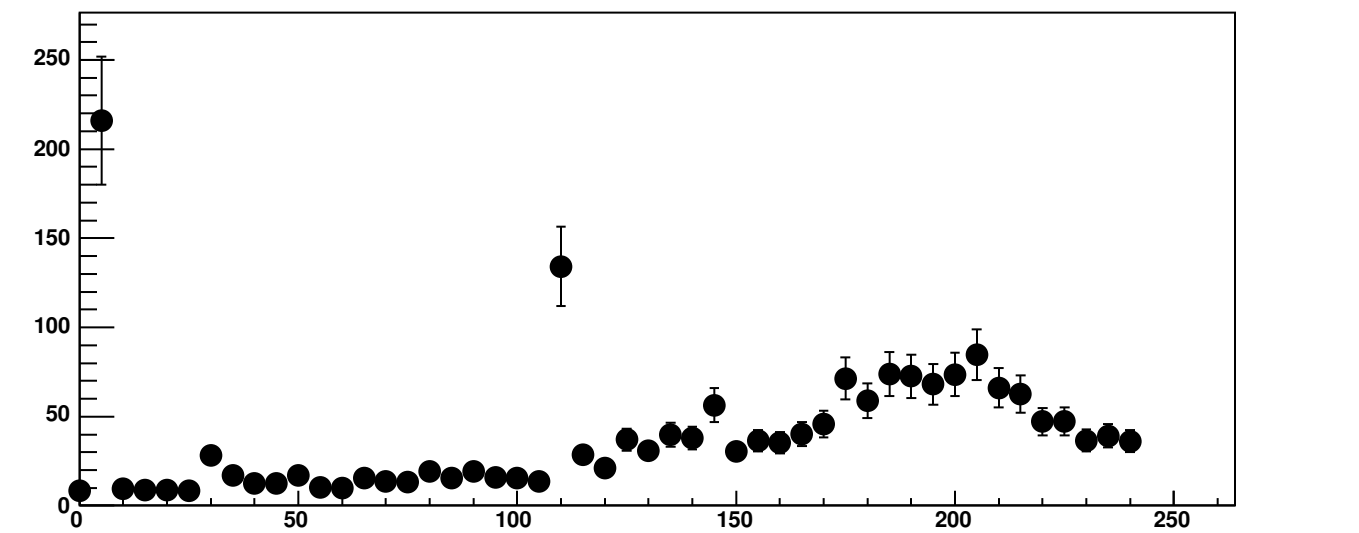
Chip 6, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold



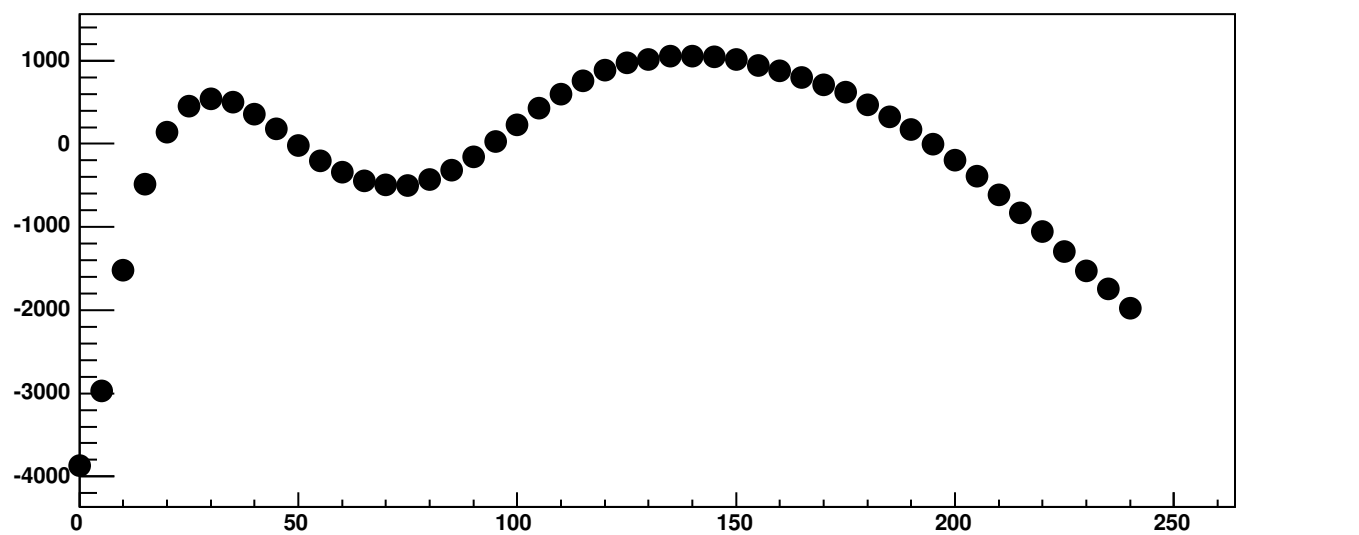
Chip 6, Channel 16, Enable 5!, DAC=4000, ADC Mean vs Hold

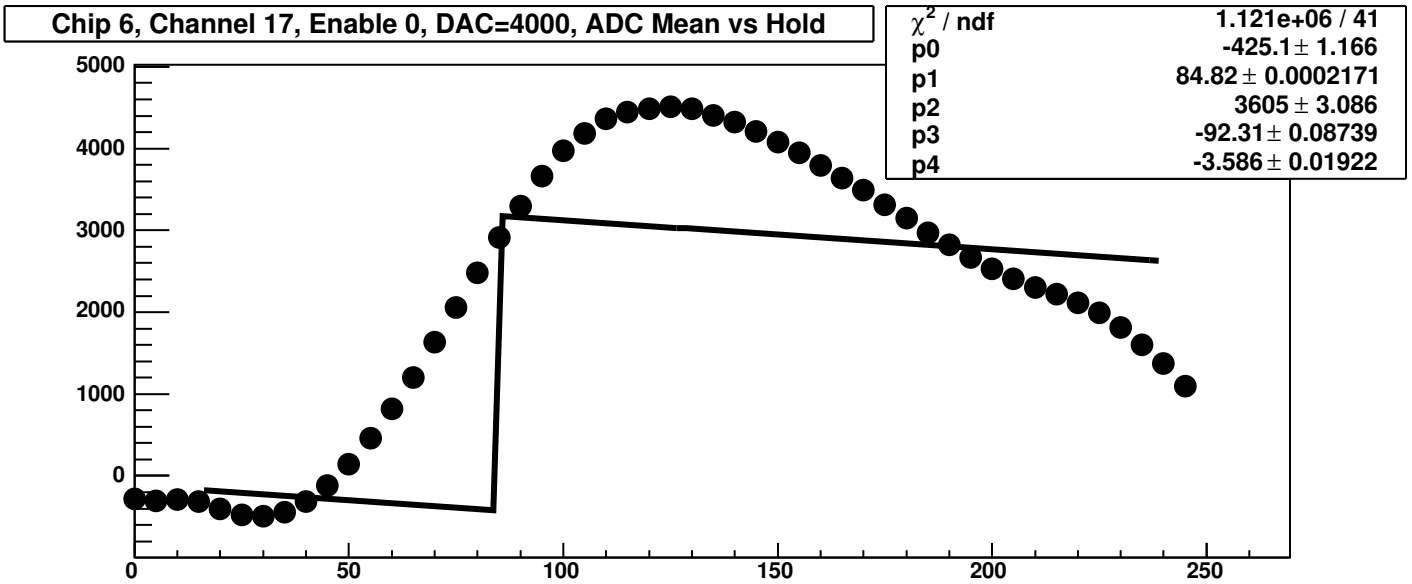


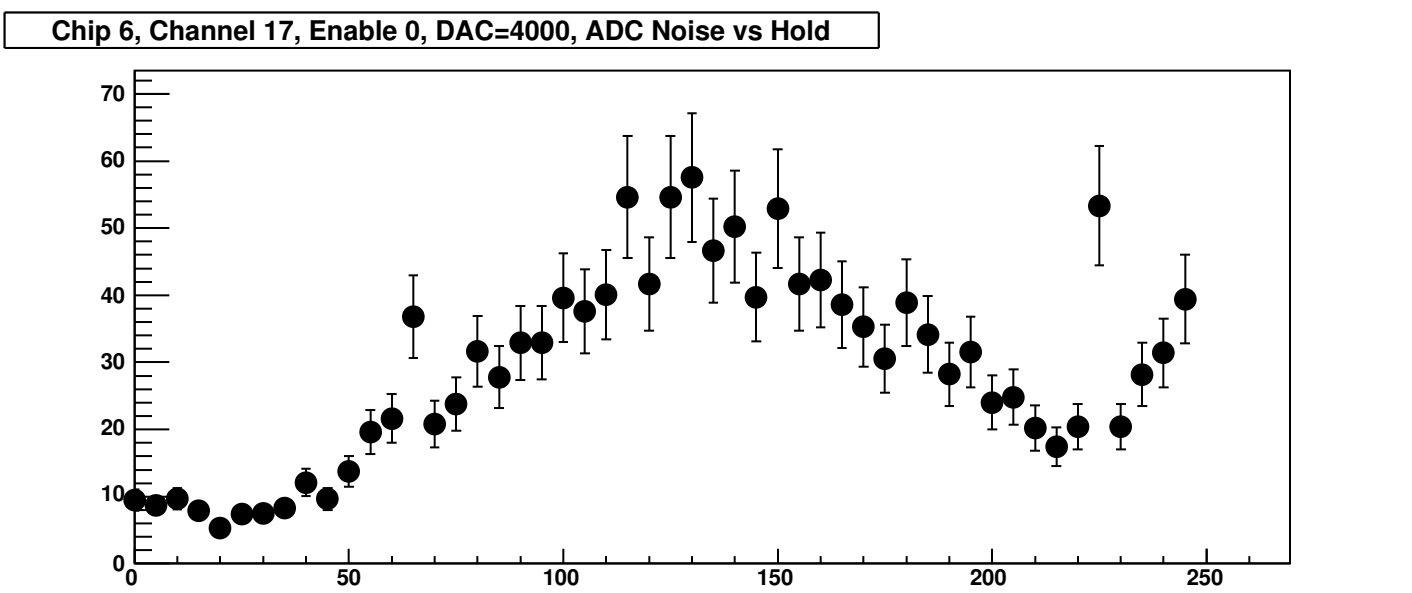
Chip 6, Channel 16, Enable 5!, DAC=4000, ADC Noise vs Hold

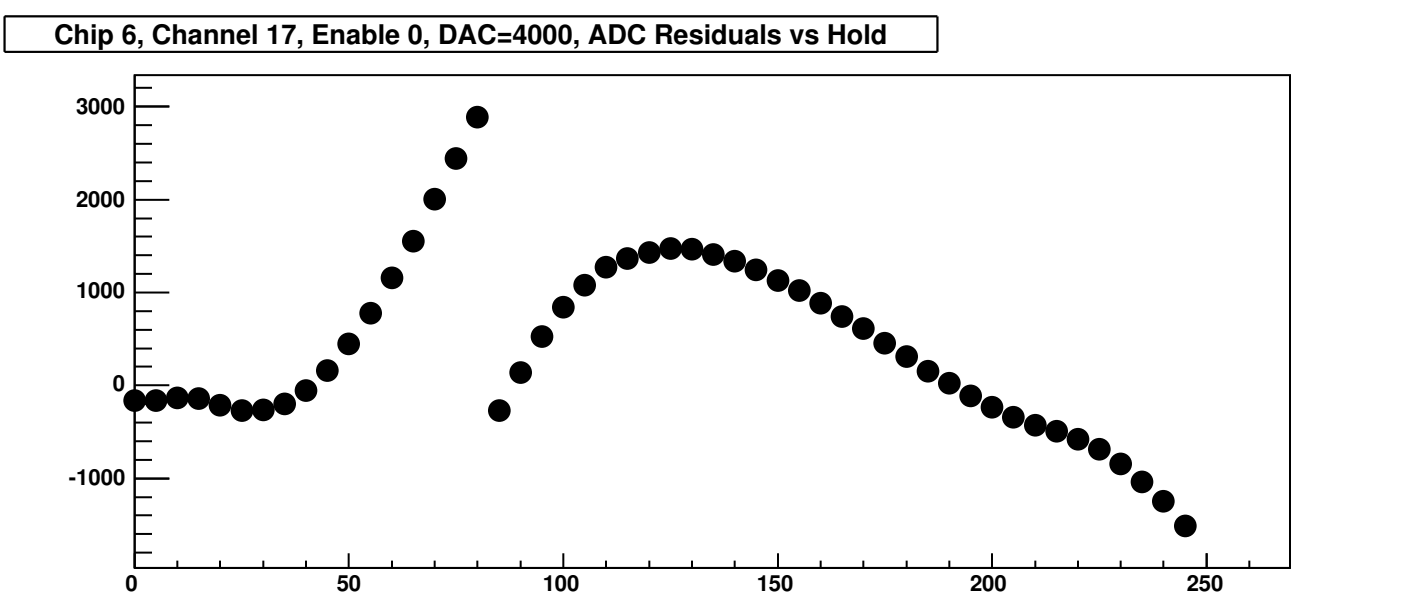


Chip 6, Channel 16, Enable 5!, DAC=4000, ADC Residuals vs Hold

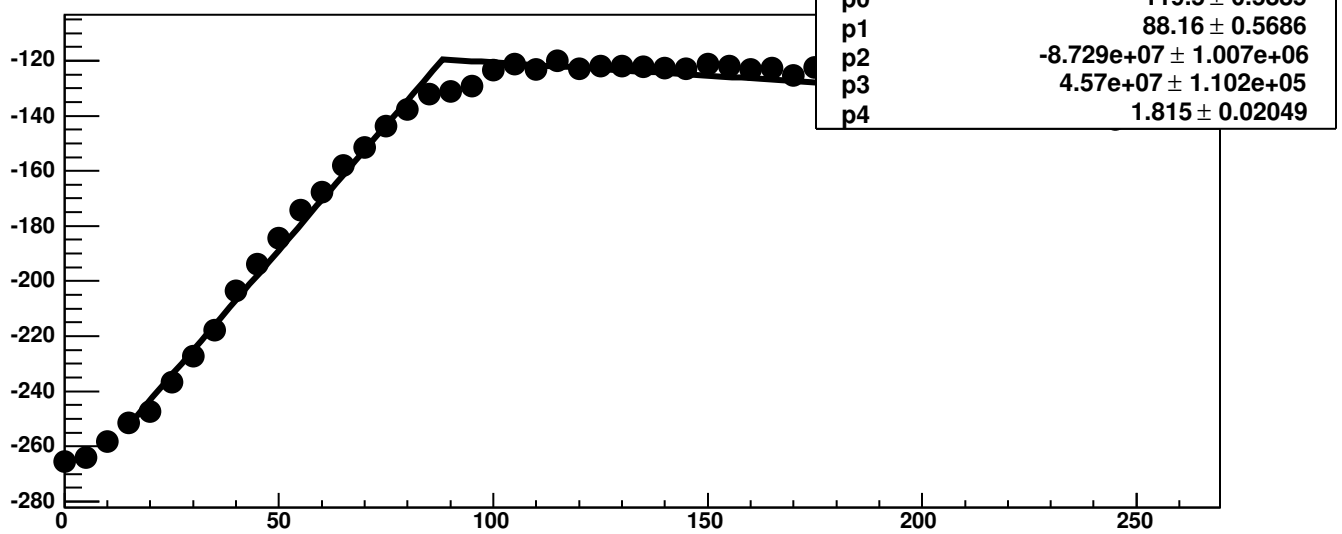



Chip 6, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.121e+06 / 41
p0 -425.1 ± 1.166
p1 84.82 ± 0.0002171
p2 3605 ± 3.086
p3 -92.31 ± 0.08739
p4 -3.586 ± 0.01922
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

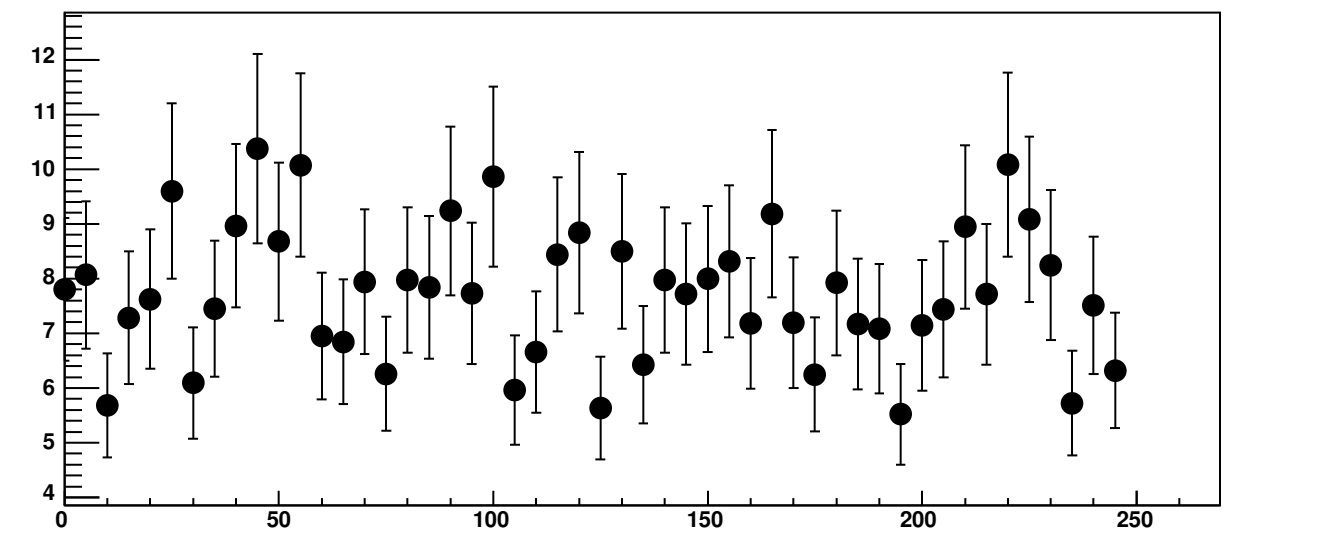

Chip 6, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold
70
60
50
40
30
20
10
0
0
50
100
150
200
250


Chip 6, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold
3000
2000
1000
0
-1000
0
50
100
150
200
250

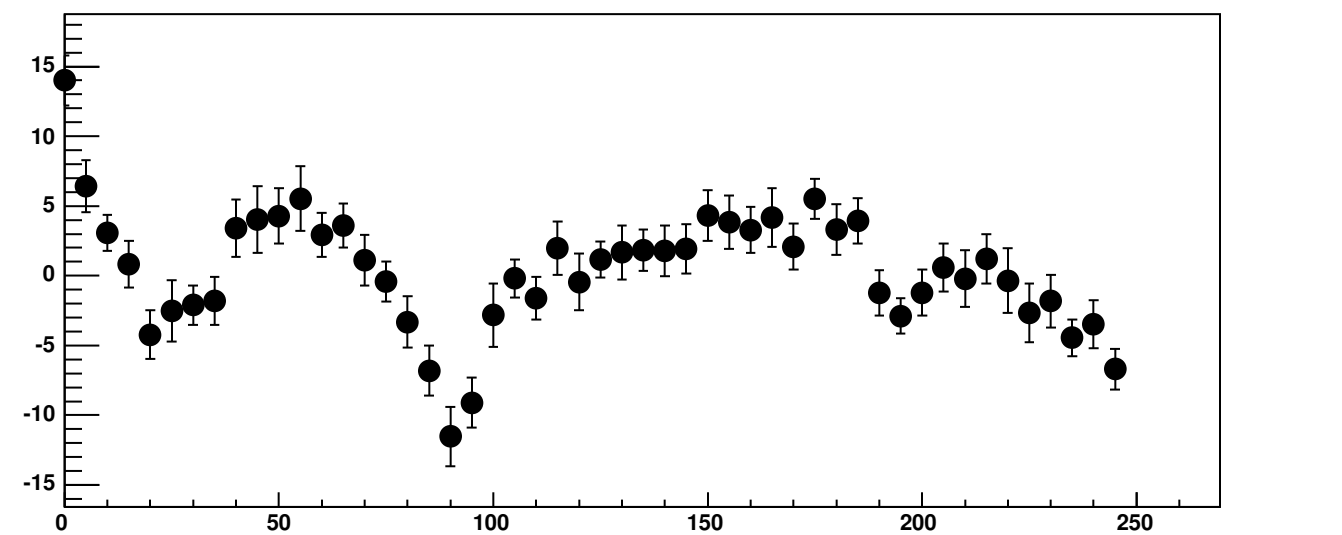
Chip 6, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 6, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold

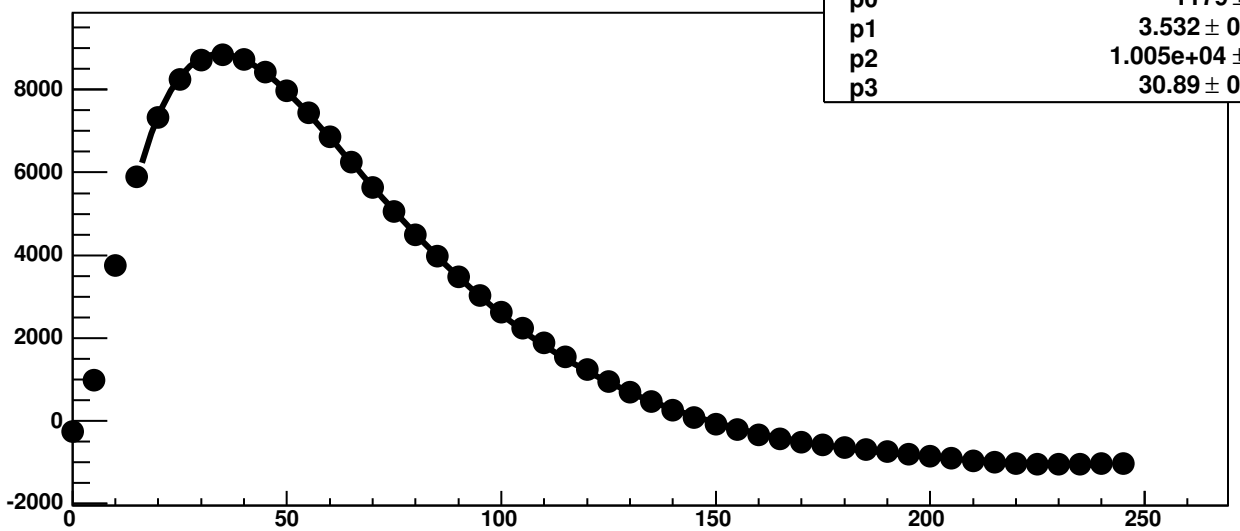


Chip 6, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

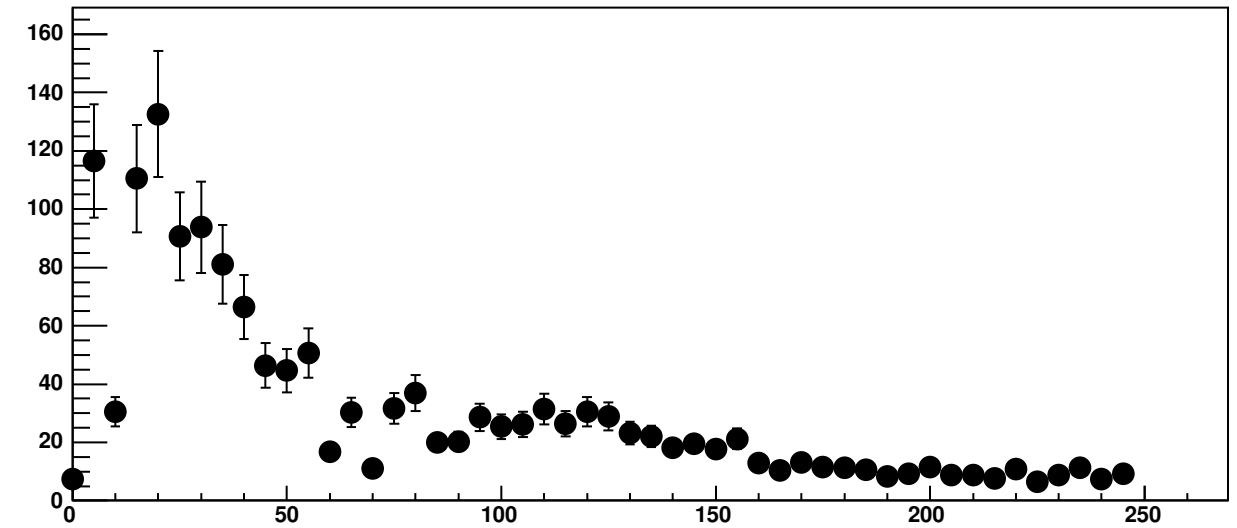


Chip 6, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold

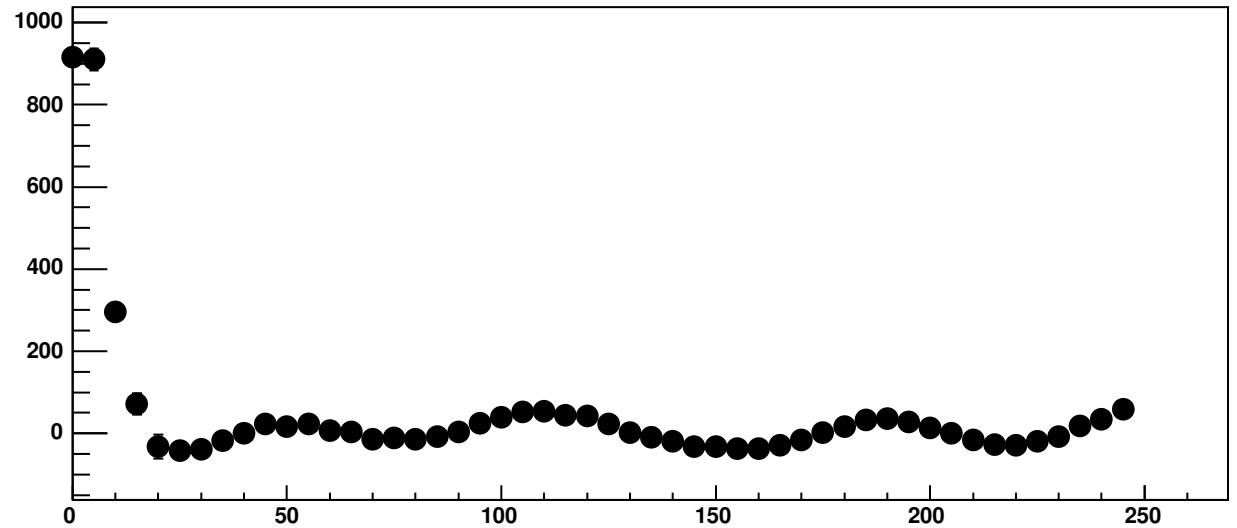
χ^2 / ndf	2747 / 42
p0	-1179 ± 0.862
p1	3.532 ± 0.04444
p2	1.005e+04 ± 4.918
p3	30.89 ± 0.01689



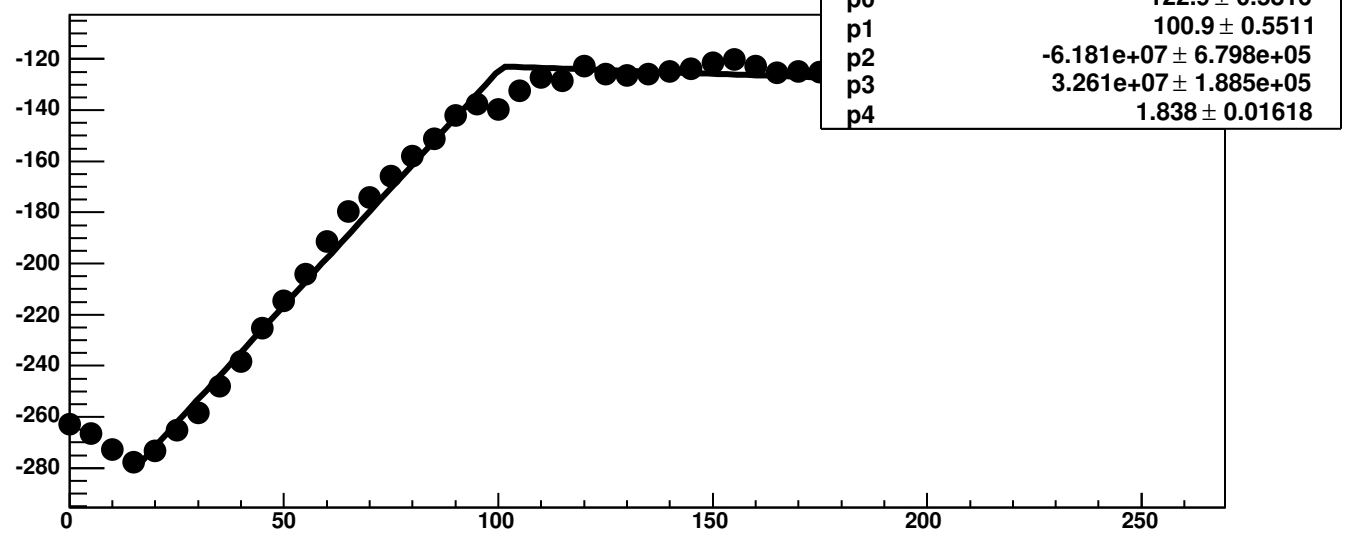
Chip 6, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold



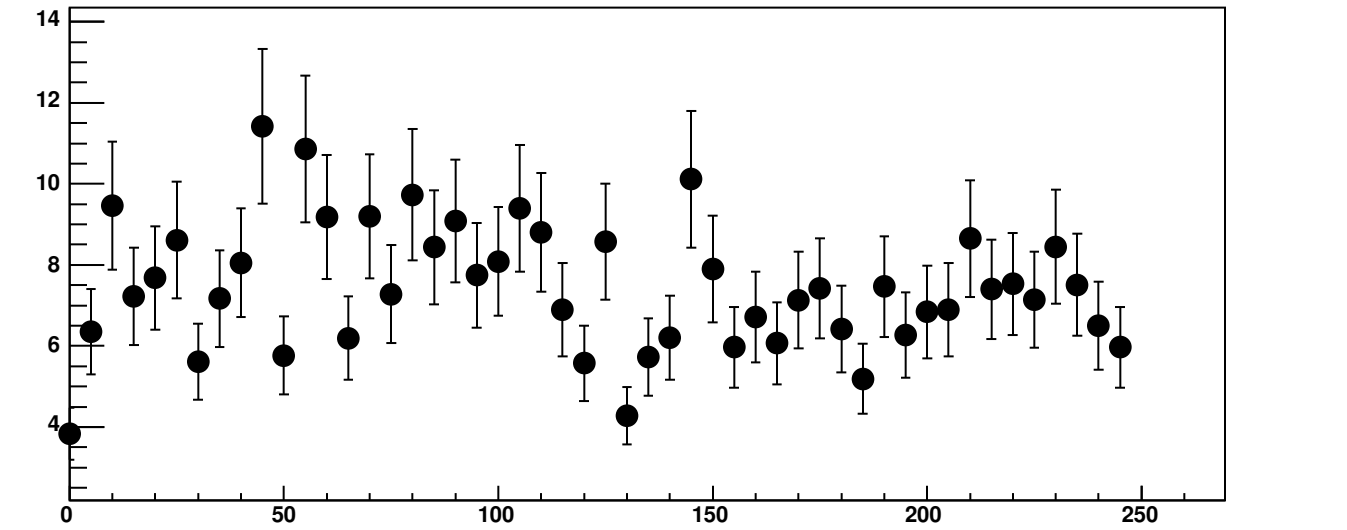
Chip 6, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold



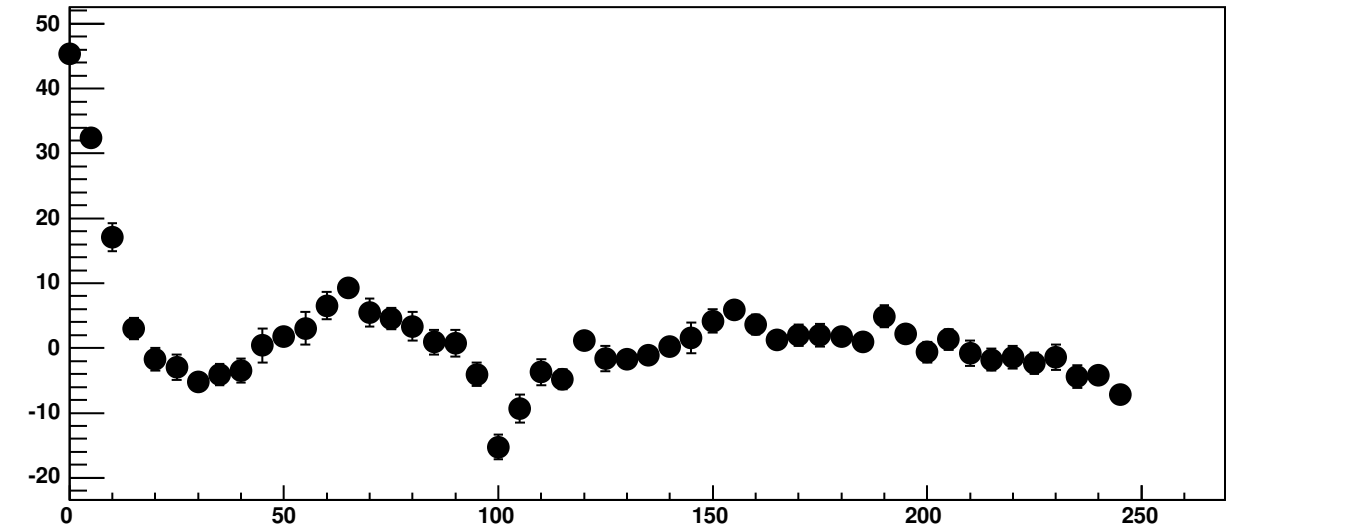
Chip 6, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



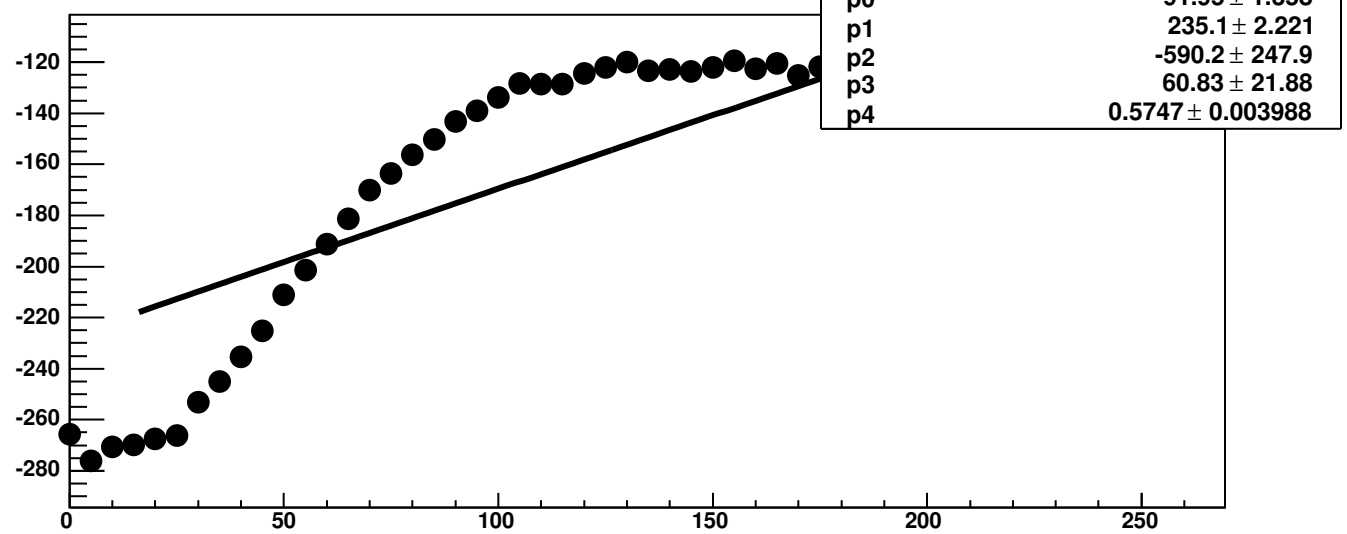
Chip 6, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



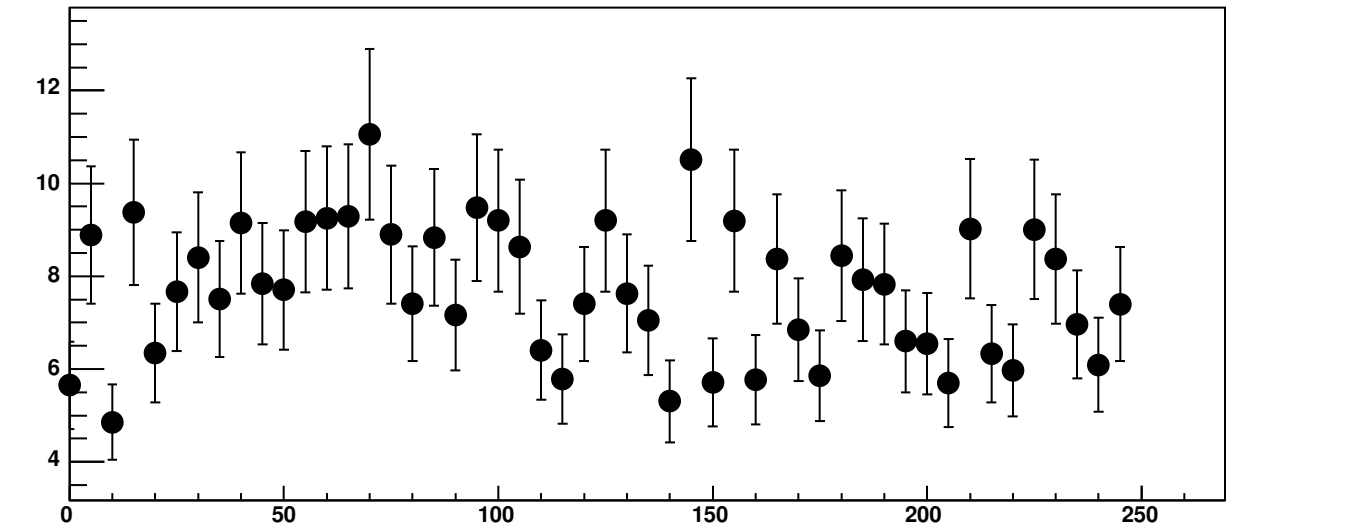
Chip 6, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold



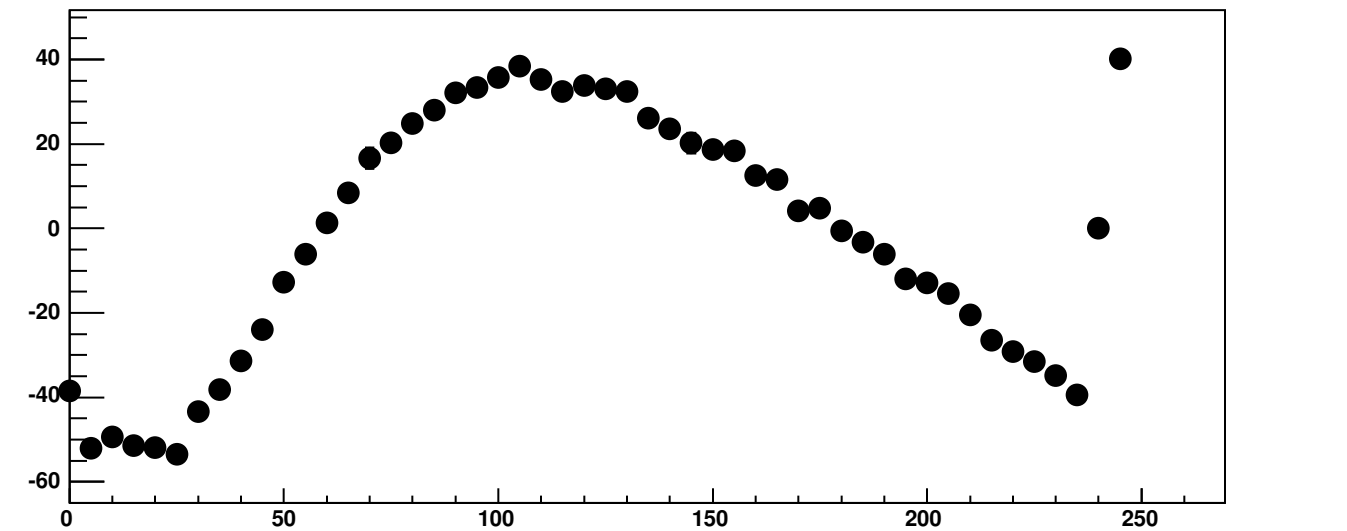
Chip 6, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



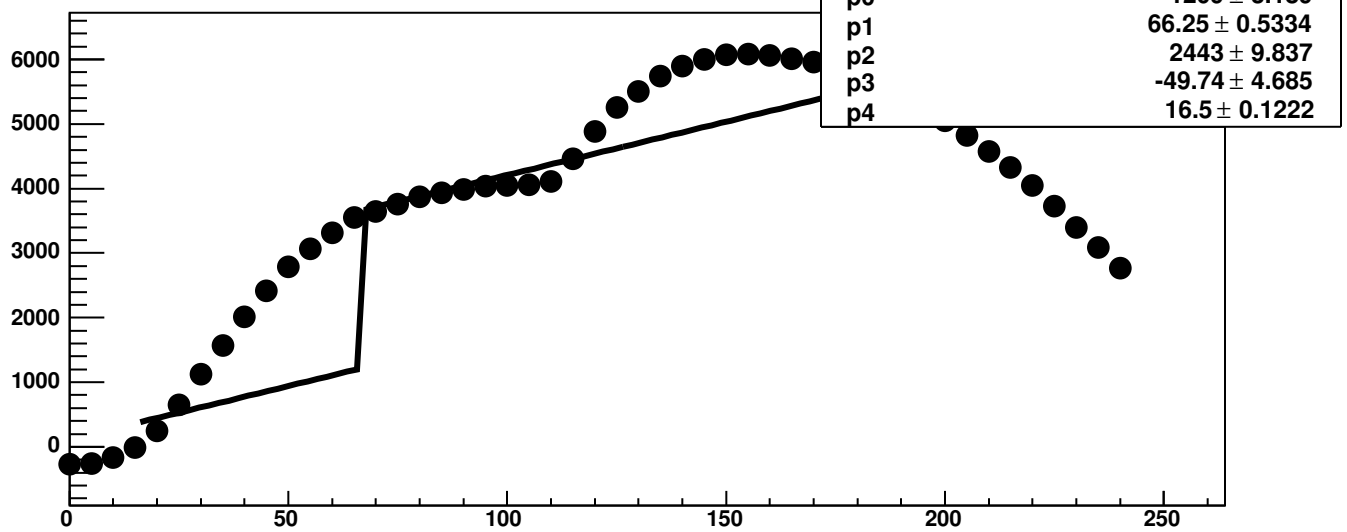
Chip 6, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



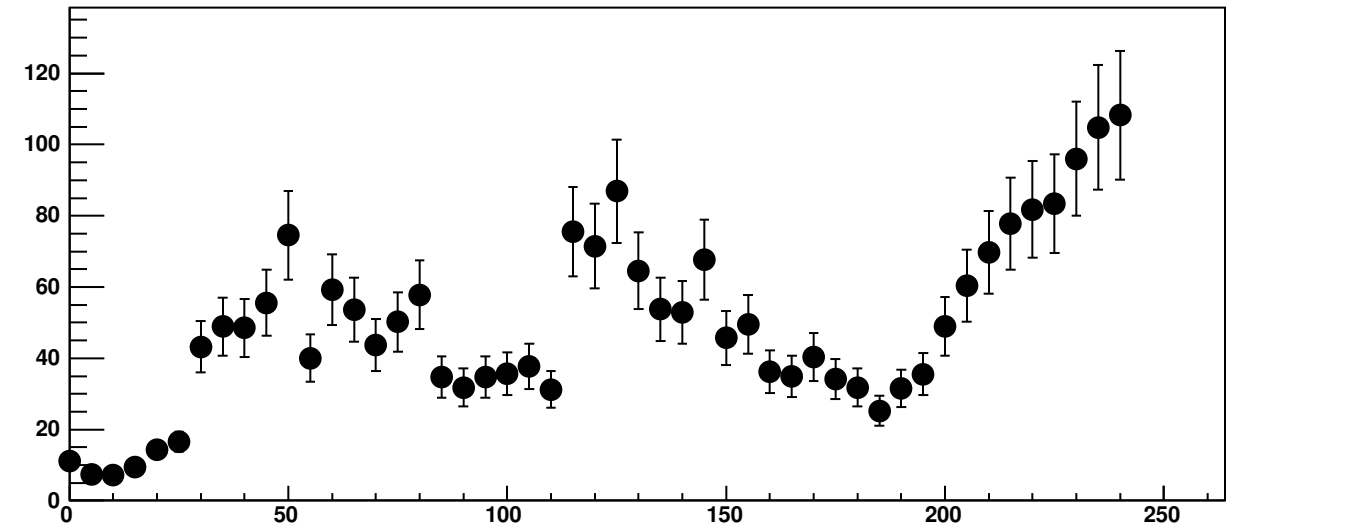
Chip 6, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold



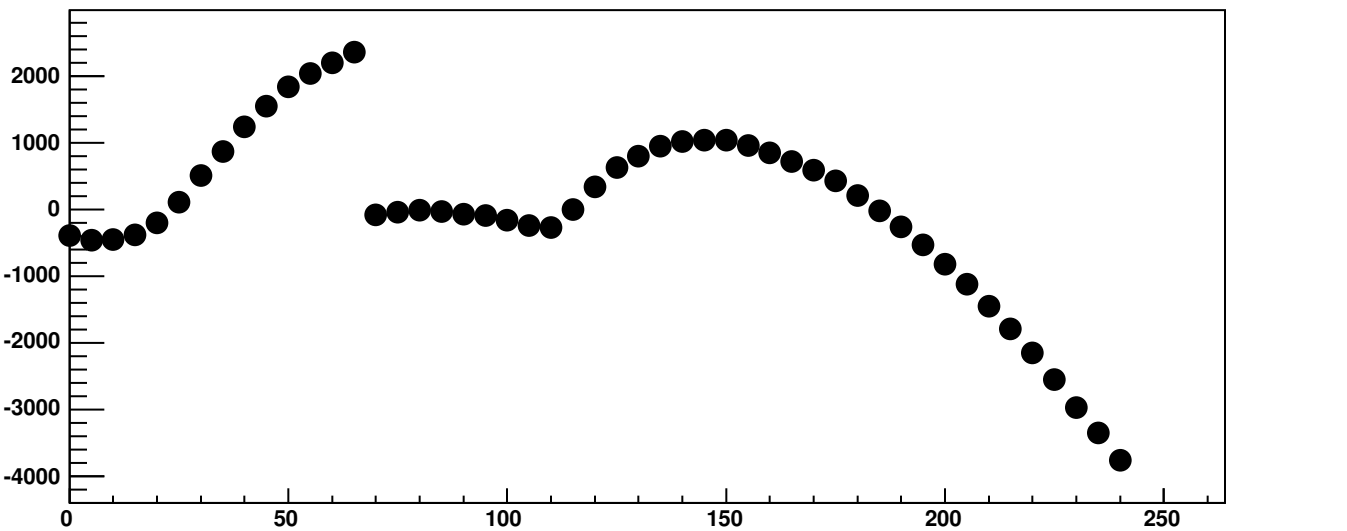
Chip 6, Channel 17, Enable 5, DAC=4000, ADC Mean vs Hold

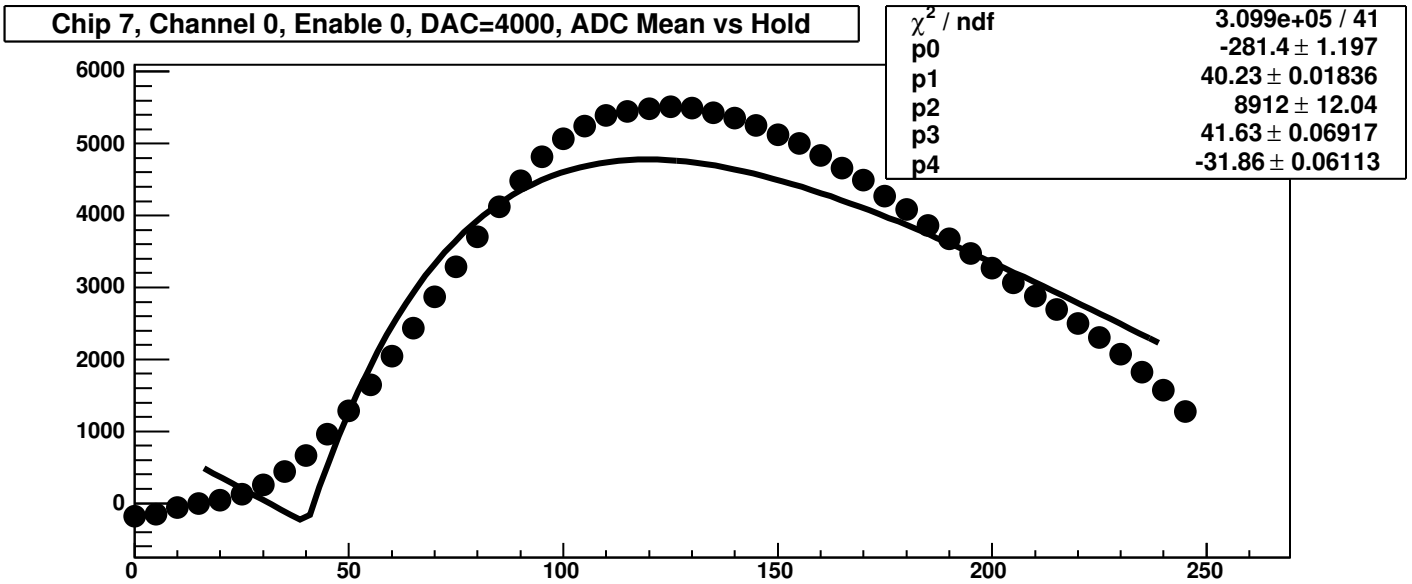


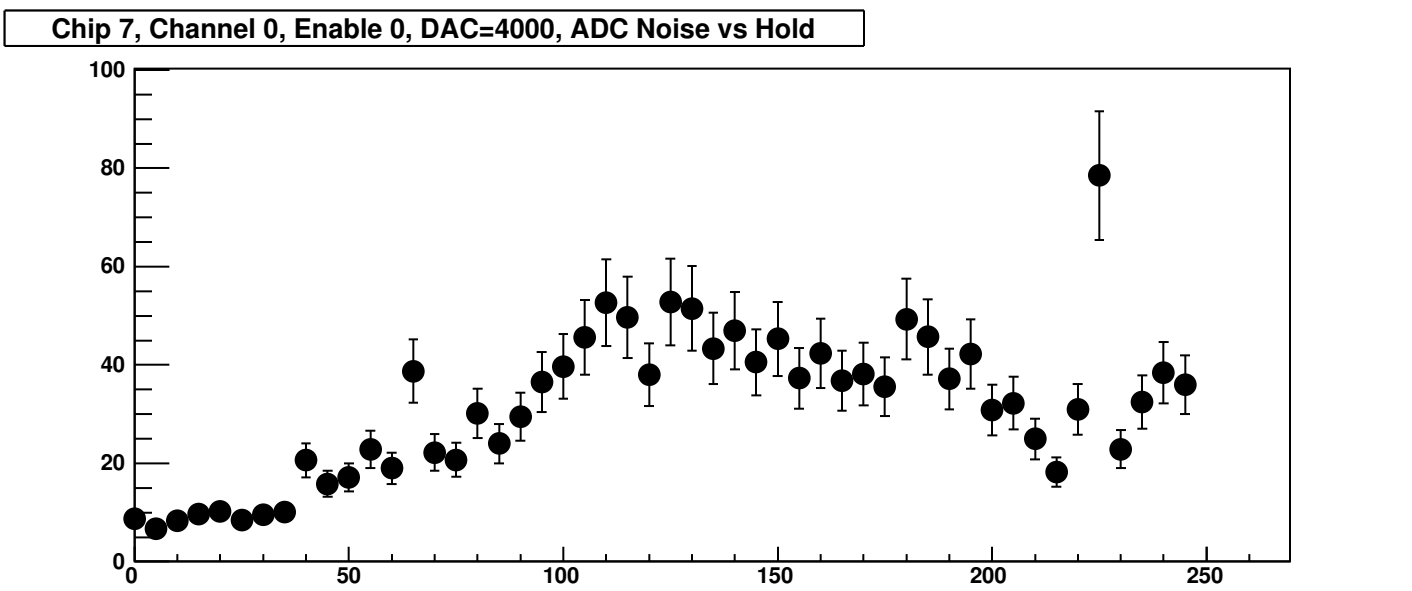
Chip 6, Channel 17, Enable 5, DAC=4000, ADC Noise vs Hold

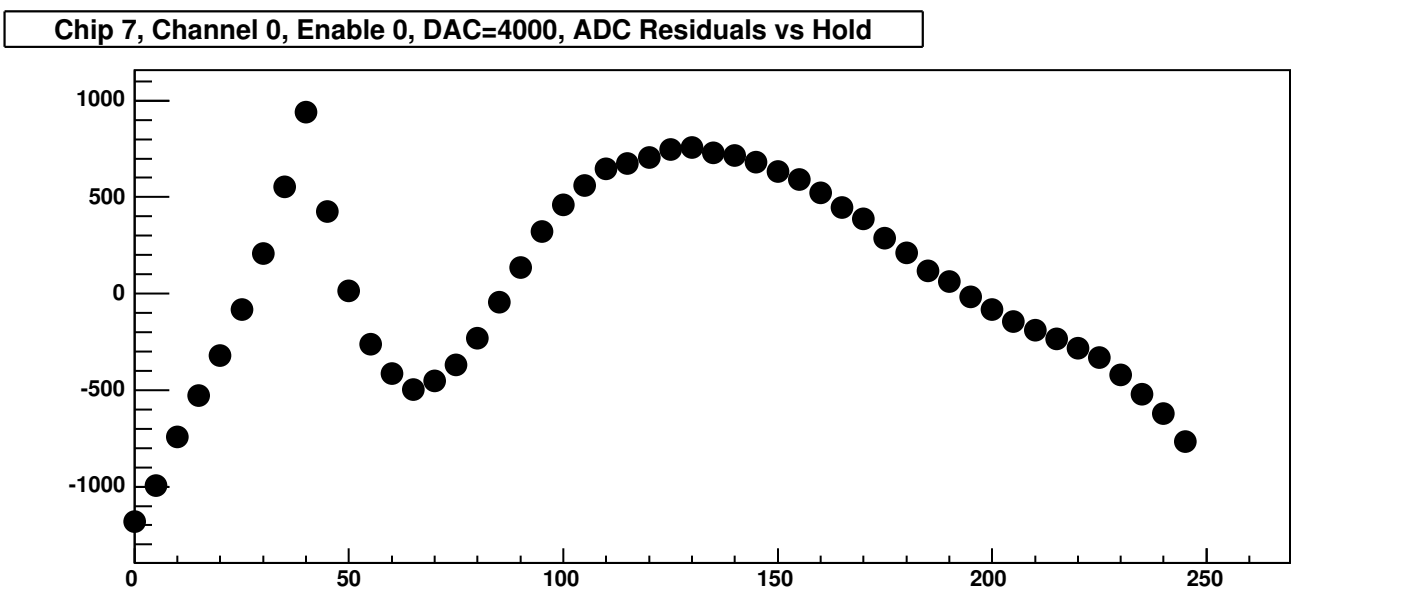


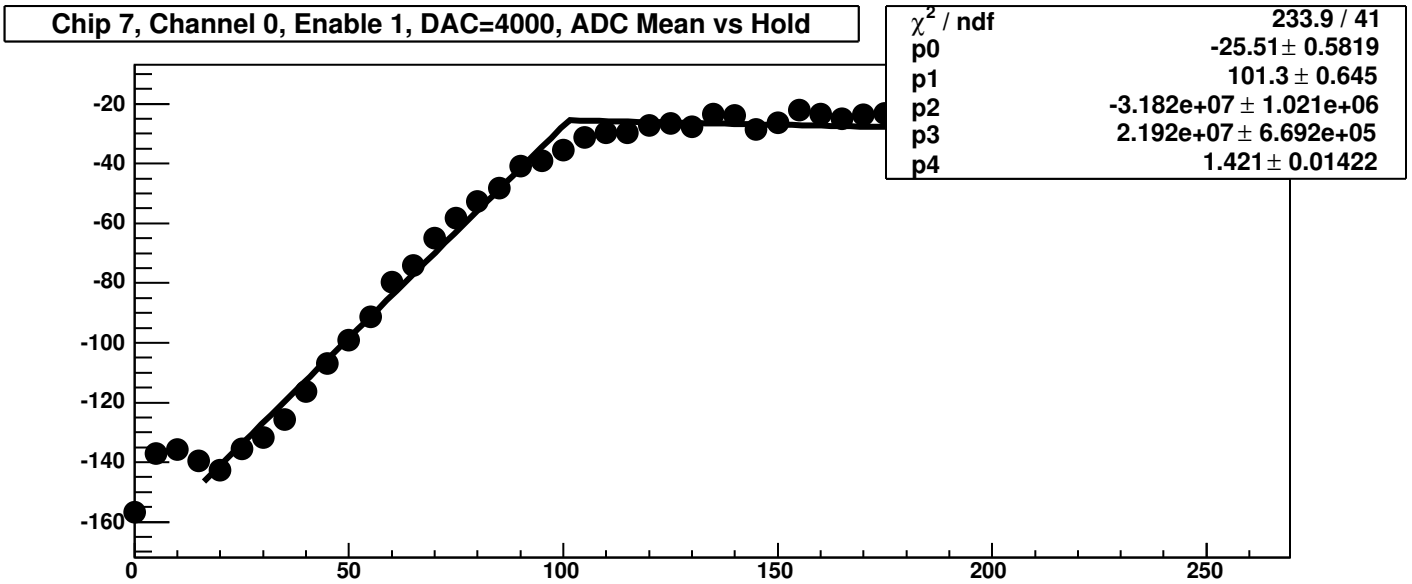
Chip 6, Channel 17, Enable 5, DAC=4000, ADC Residuals vs Hold

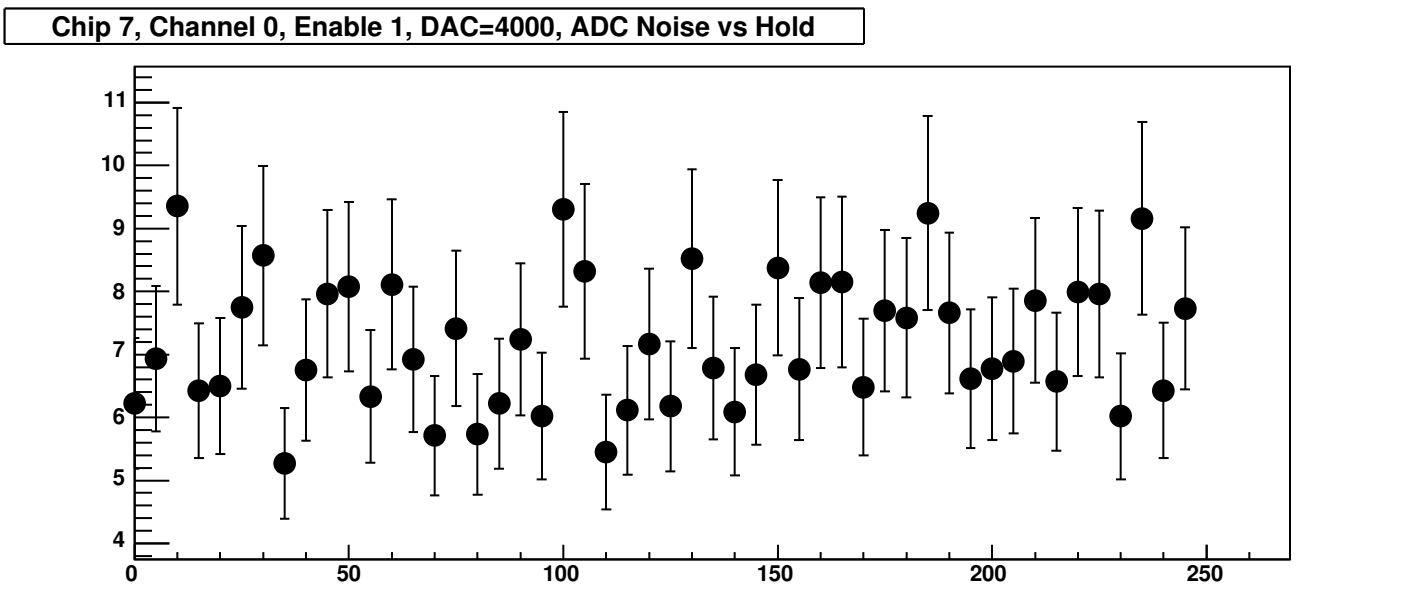


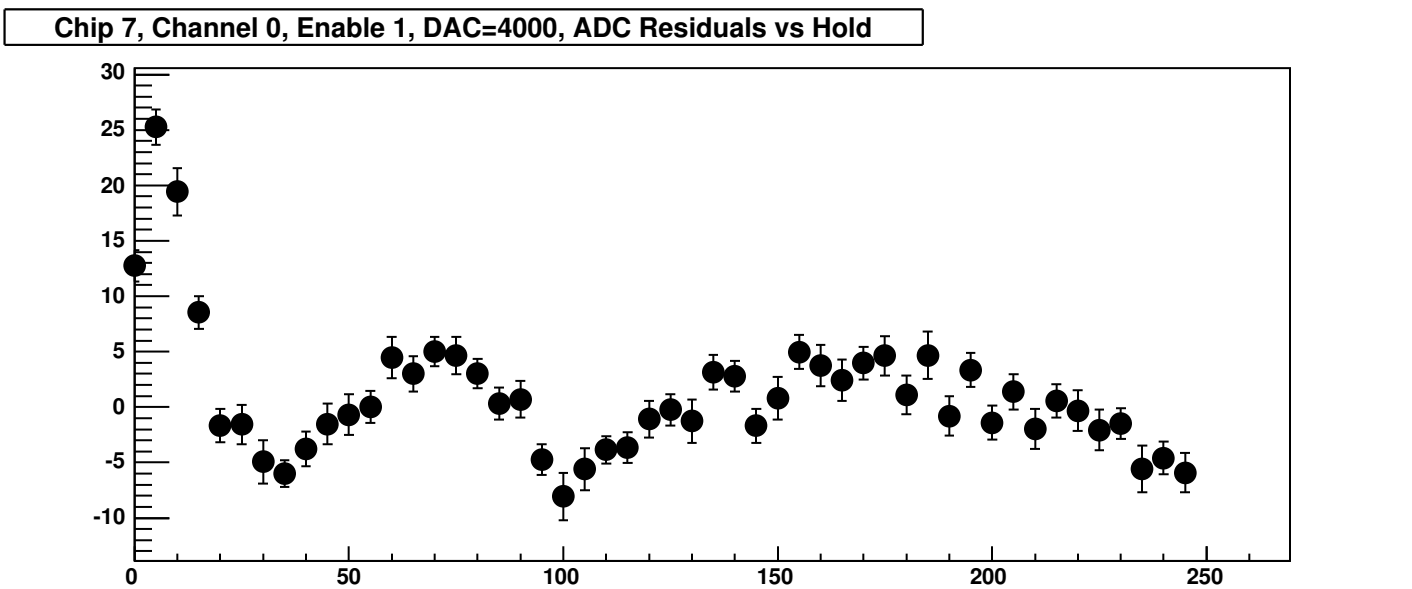

Chip 7, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.099e+05 / 41
p0 -281.4 ± 1.197
p1 40.23 ± 0.01836
p2 8912 ± 12.04
p3 41.63 ± 0.06917
p4 -31.86 ± 0.06113
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

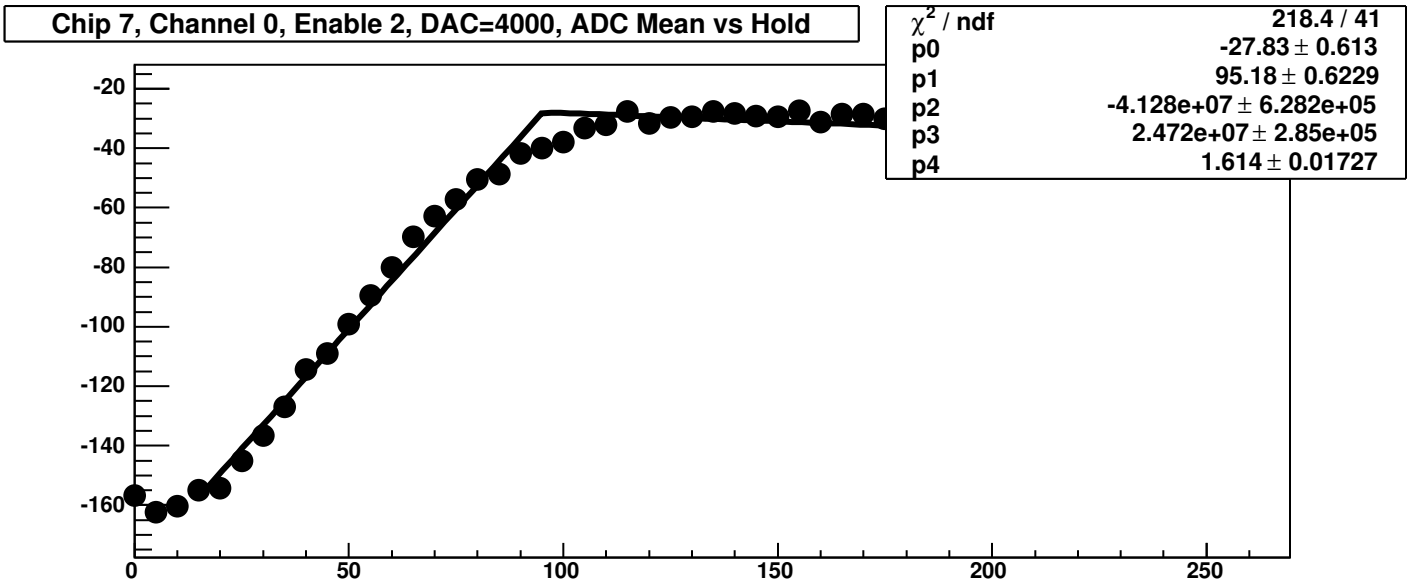

Chip 7, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250

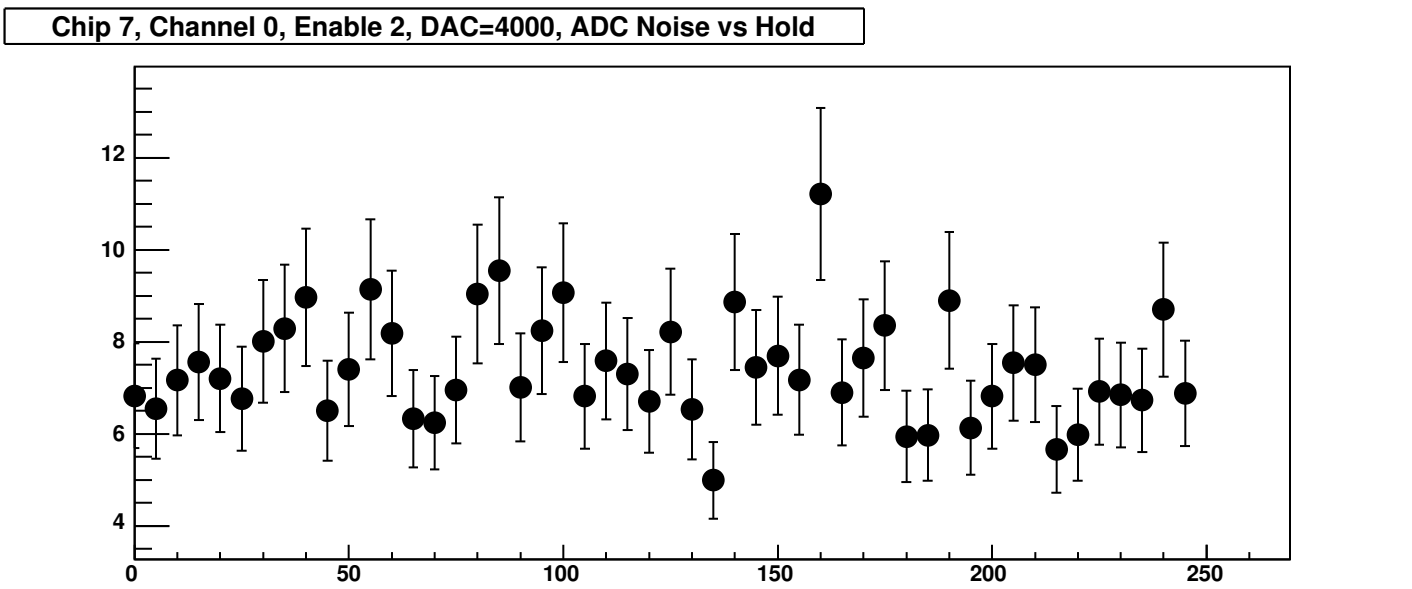

Chip 7, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
0
50
100
150
200
250

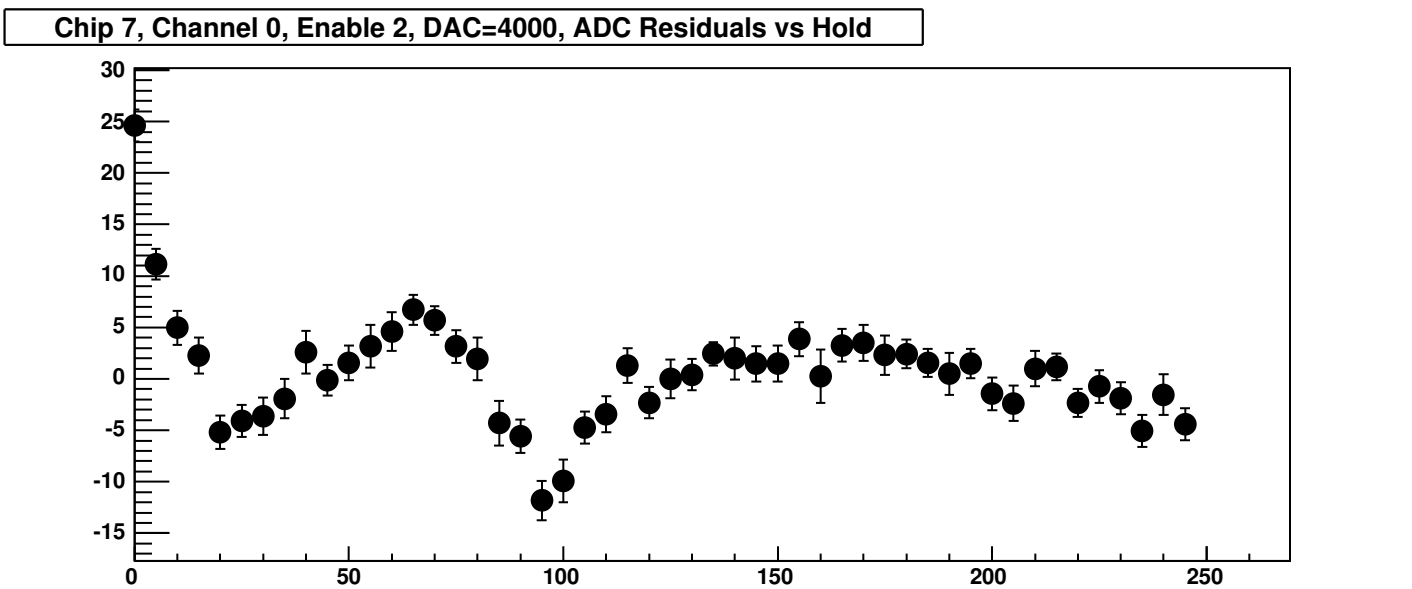

Chip 7, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 233.9 / 41
p0 -25.51 ± 0.5819
p1 101.3 ± 0.645
p2 -3.182e+07 ± 1.021e+06
p3 2.192e+07 ± 6.692e+05
p4 1.421 ± 0.01422
-20
-40
-60
-80
-100
-120
-140
-160
0
50
100
150
200
250


Chip 7, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 7, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
0
50
100
150
200
250

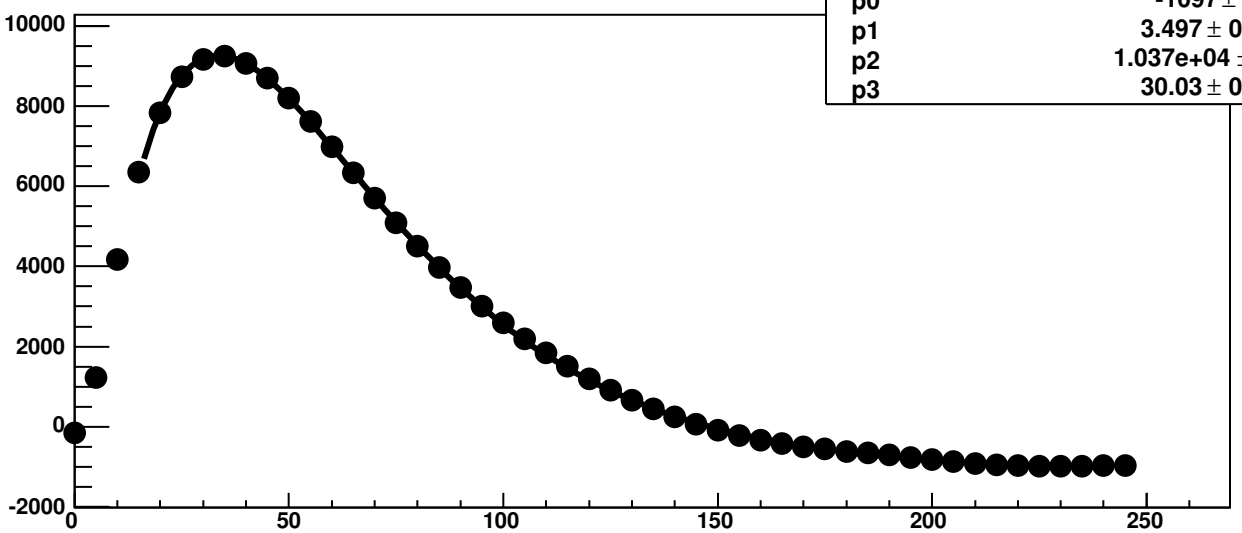

Chip 7, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 218.4 / 41
p0 -27.83 ± 0.613
p1 95.18 ± 0.6229
p2 -4.128e+07 ± 6.282e+05
p3 2.472e+07 ± 2.85e+05
p4 1.614 ± 0.01727
-20
-40
-60
-80
-100
-120
-140
-160
0
50
100
150
200
250


Chip 7, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

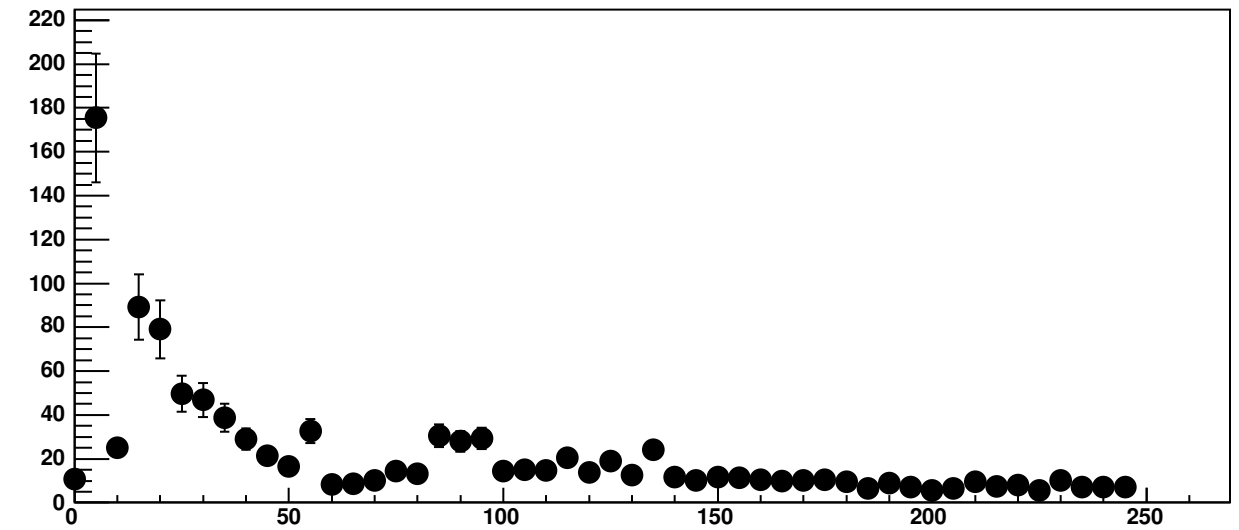

Chip 7, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

Chip 7, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold

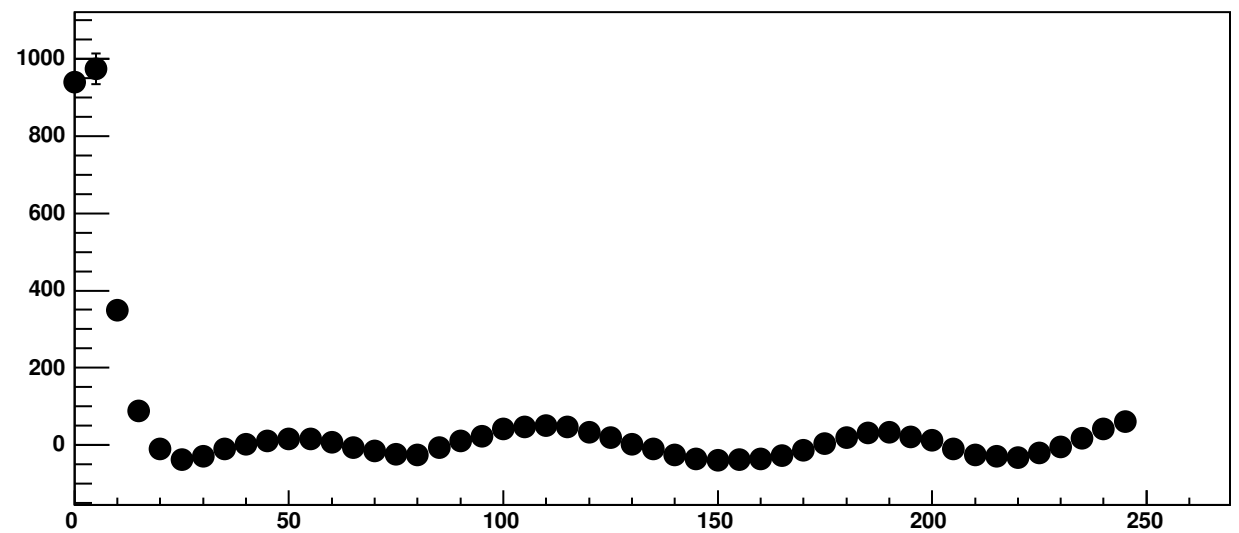
χ^2 / ndf	5086 / 42
p0	-1097 ± 0.6387
p1	3.497 ± 0.02906
p2	1.037e+04 ± 2.991
p3	30.03 ± 0.01088



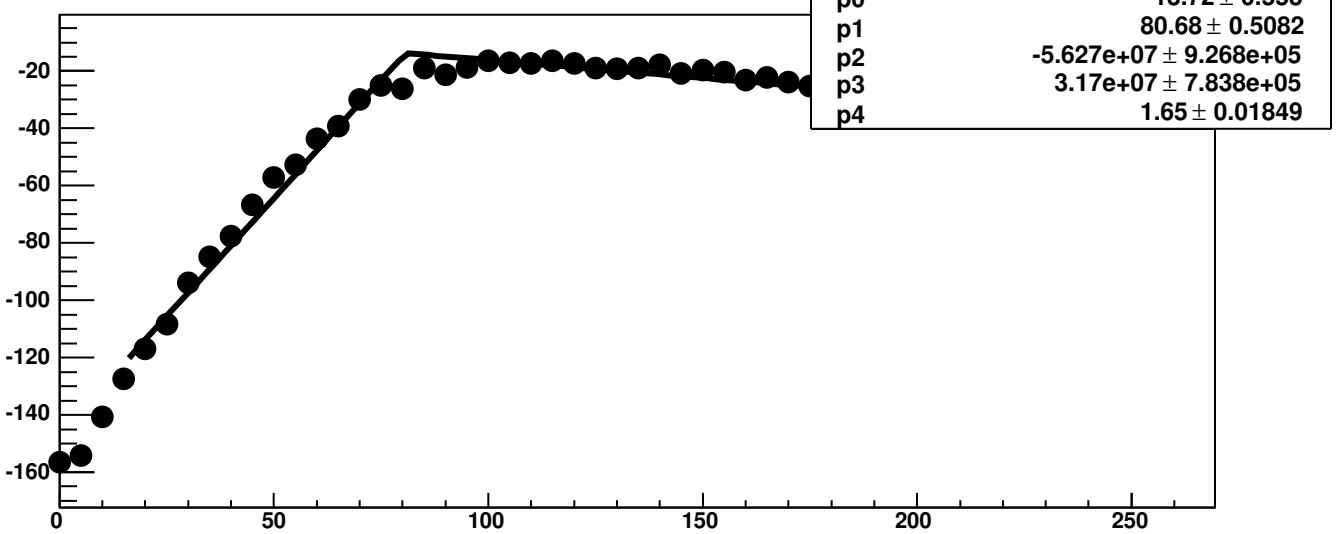
Chip 7, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold



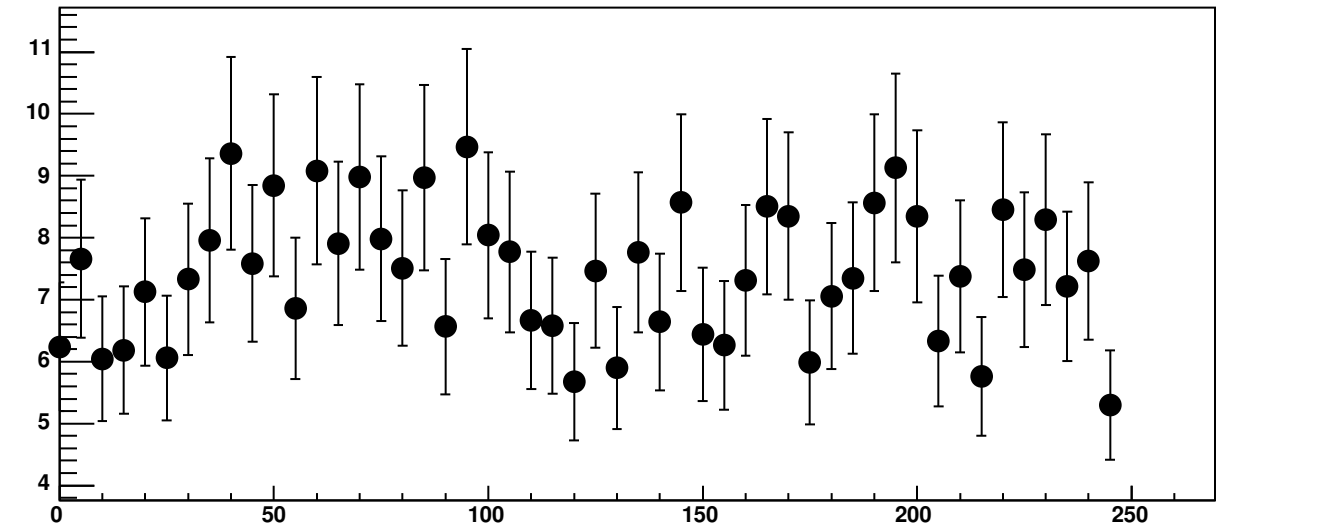
Chip 7, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold



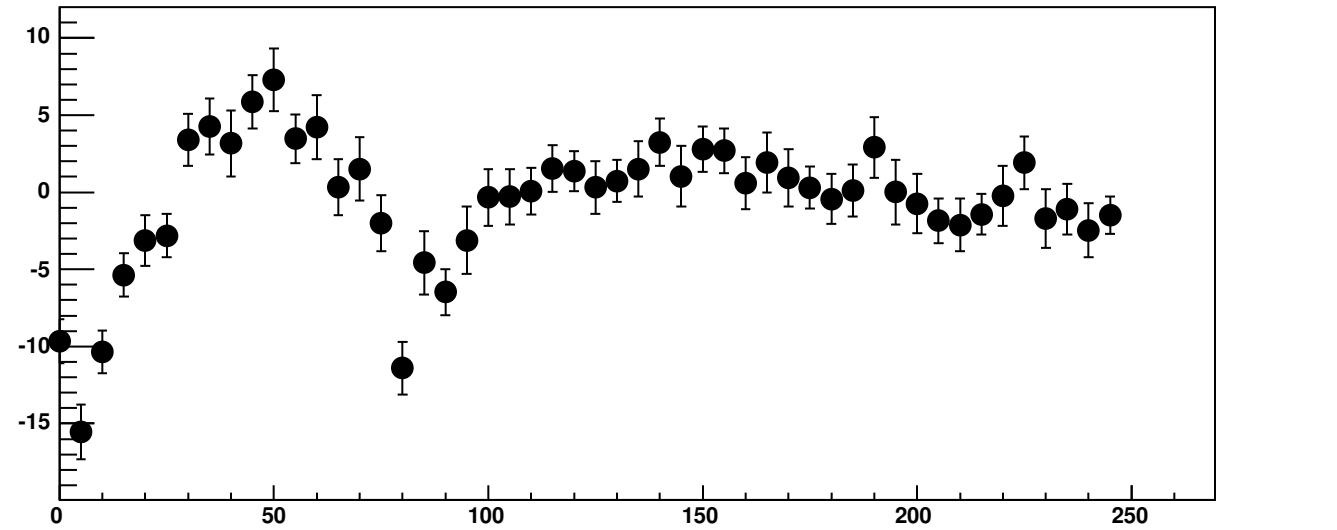
Chip 7, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold



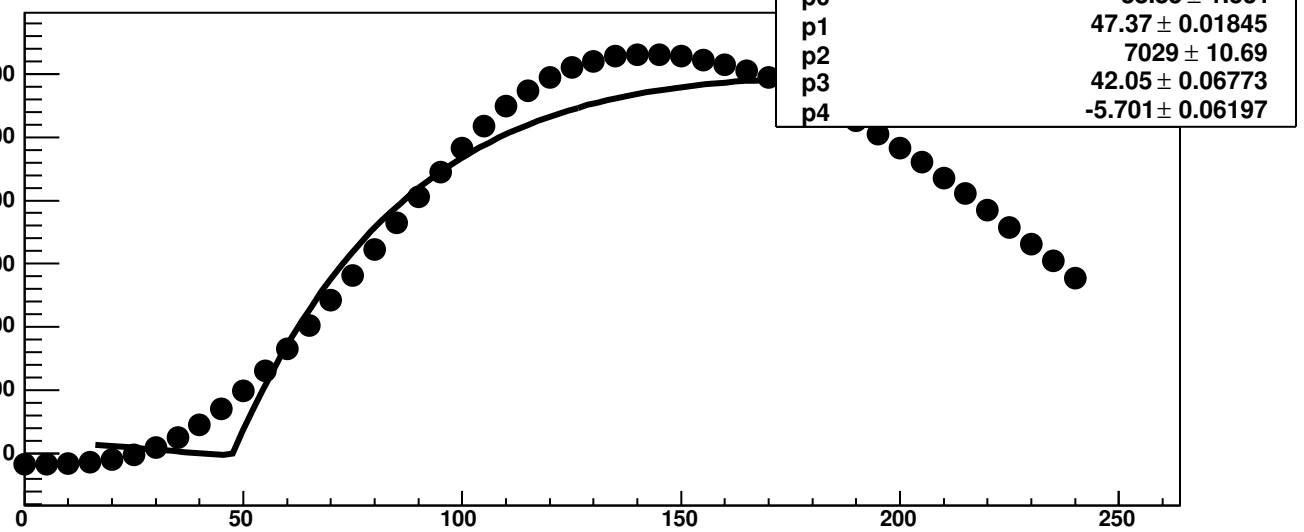
Chip 7, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



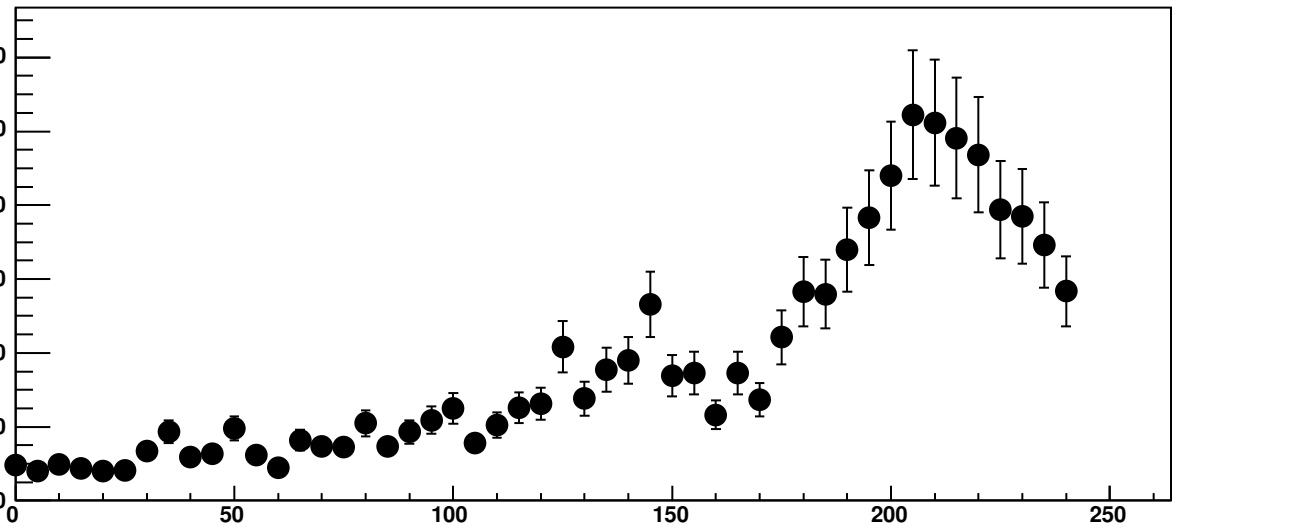
Chip 7, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold



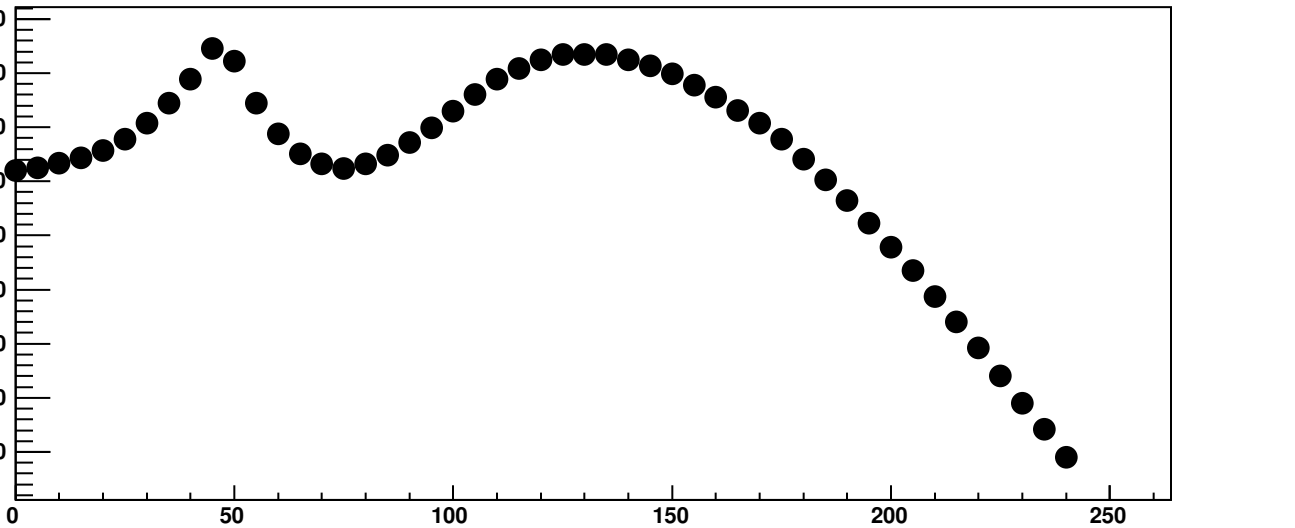
Chip 7, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold



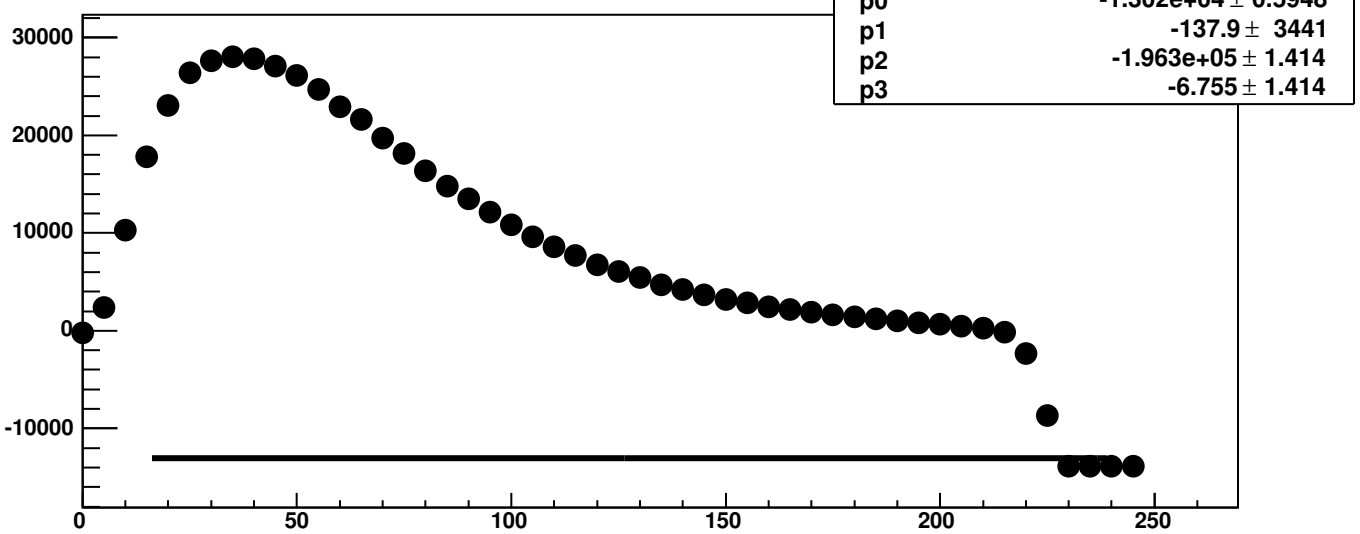
Chip 7, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



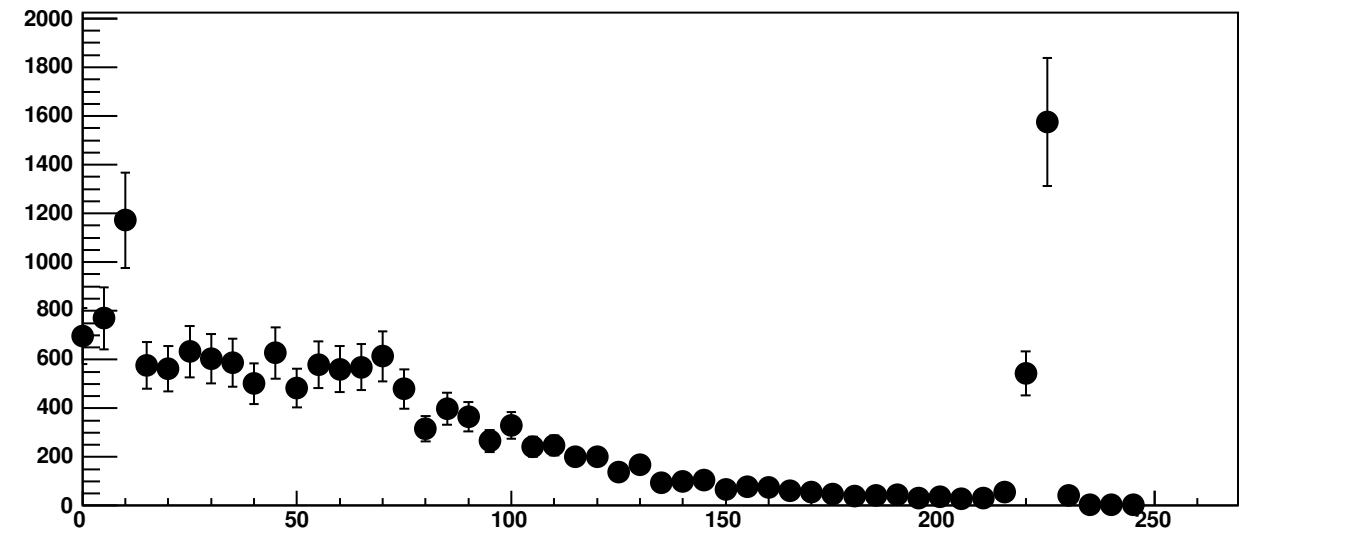
Chip 7, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold



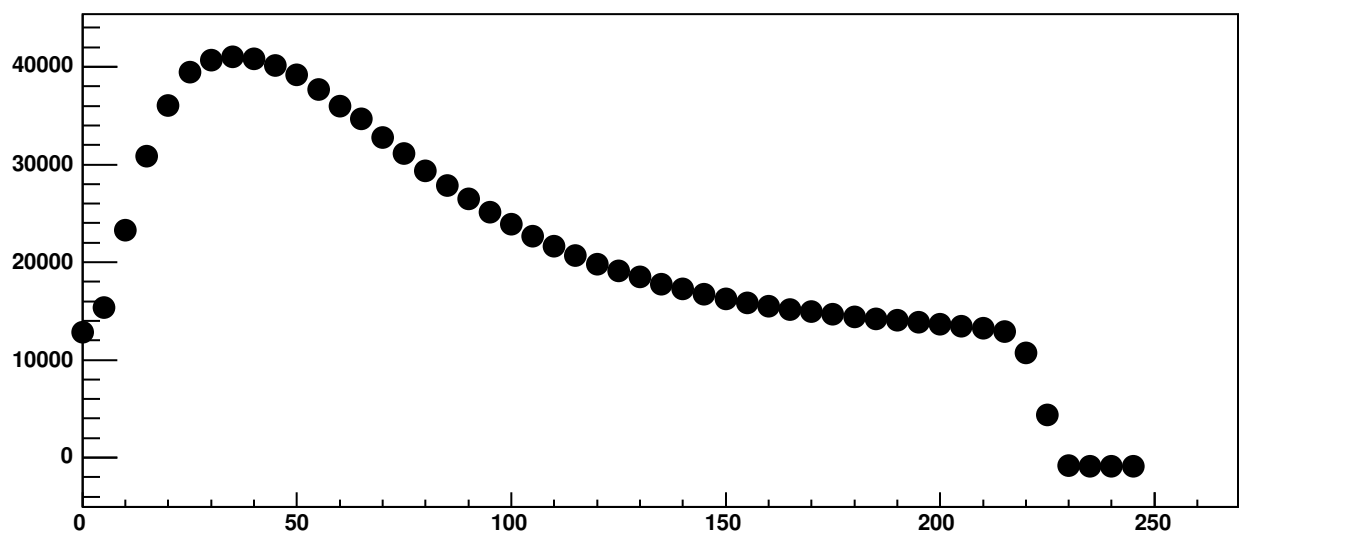
Chip 7, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold

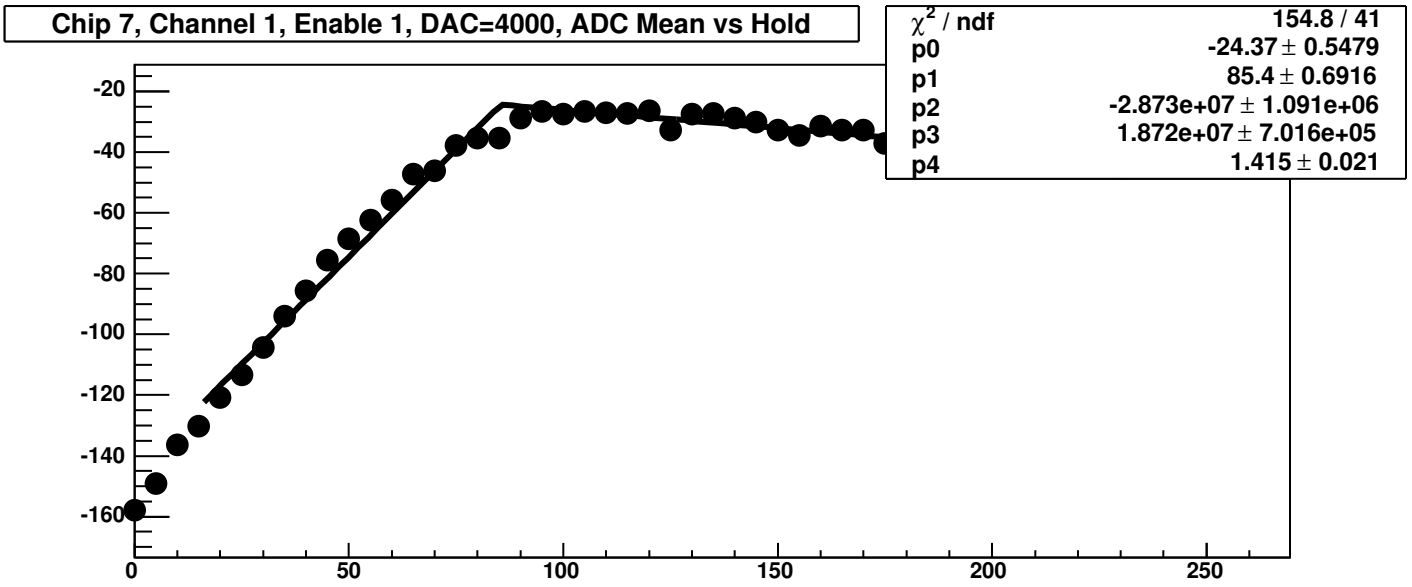


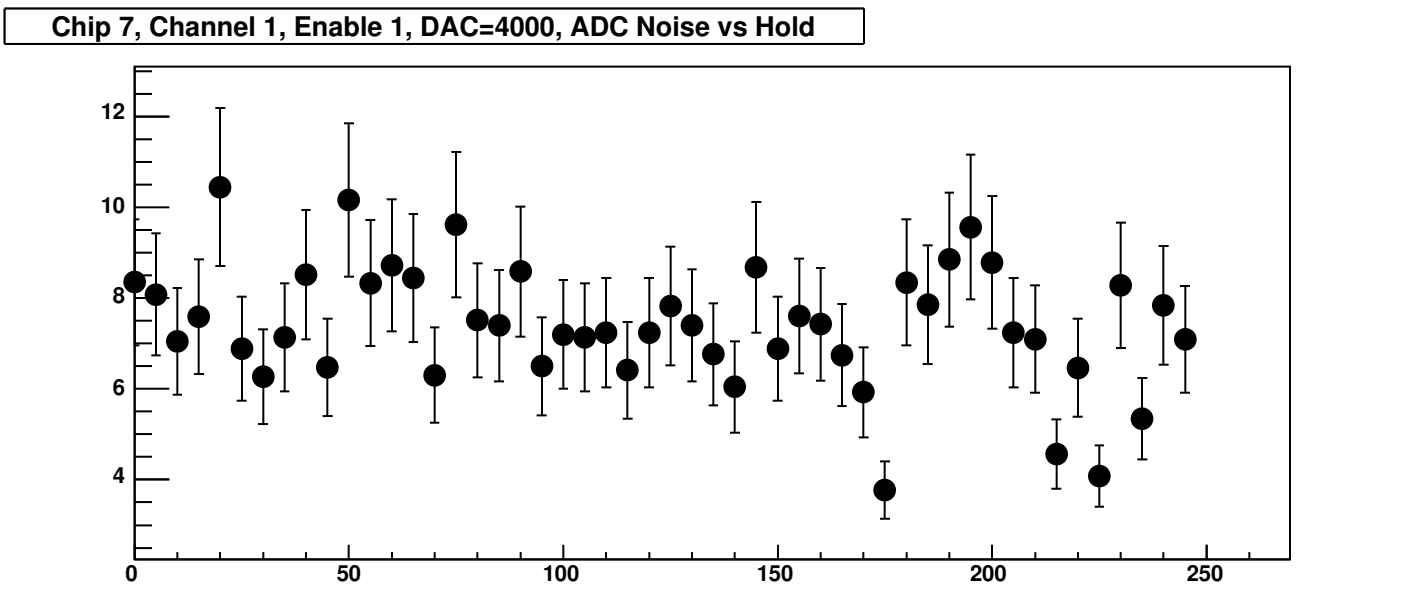
Chip 7, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold

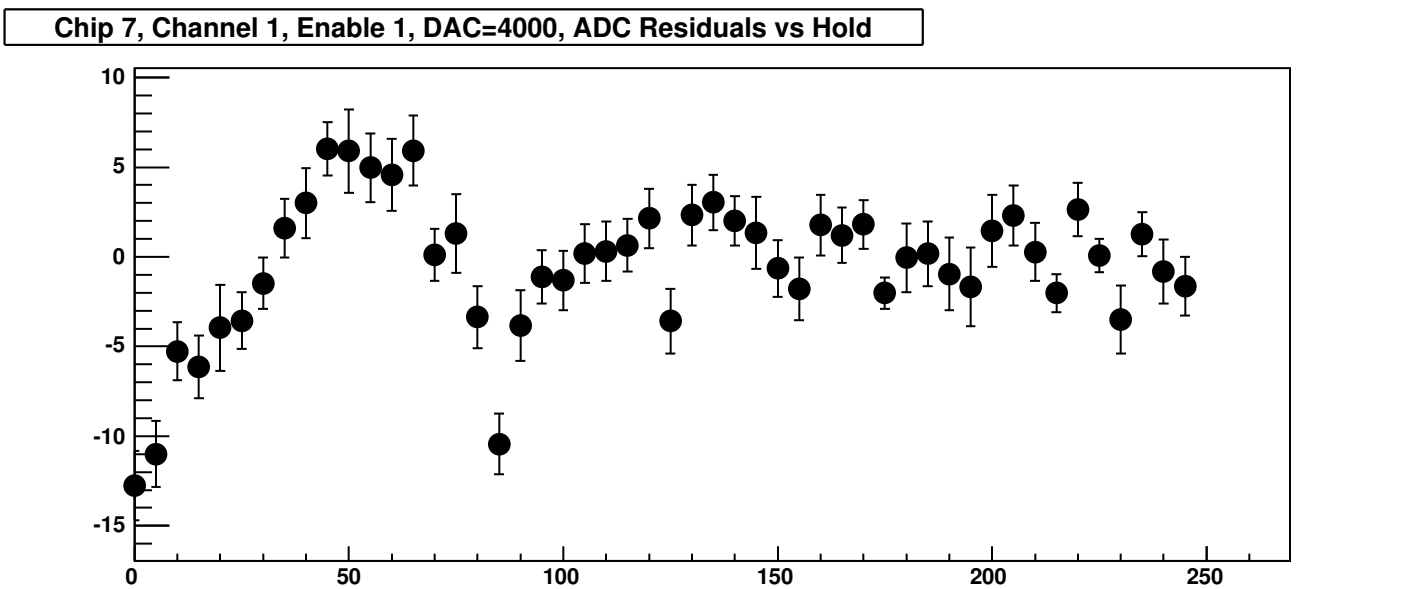


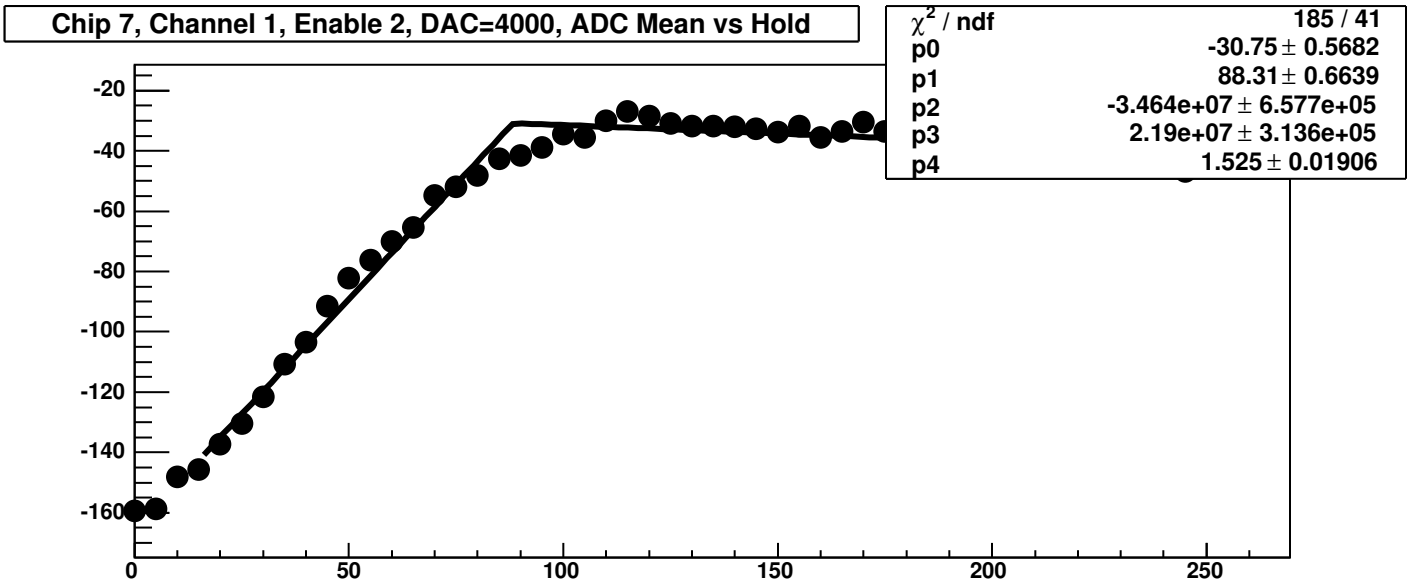
Chip 7, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold

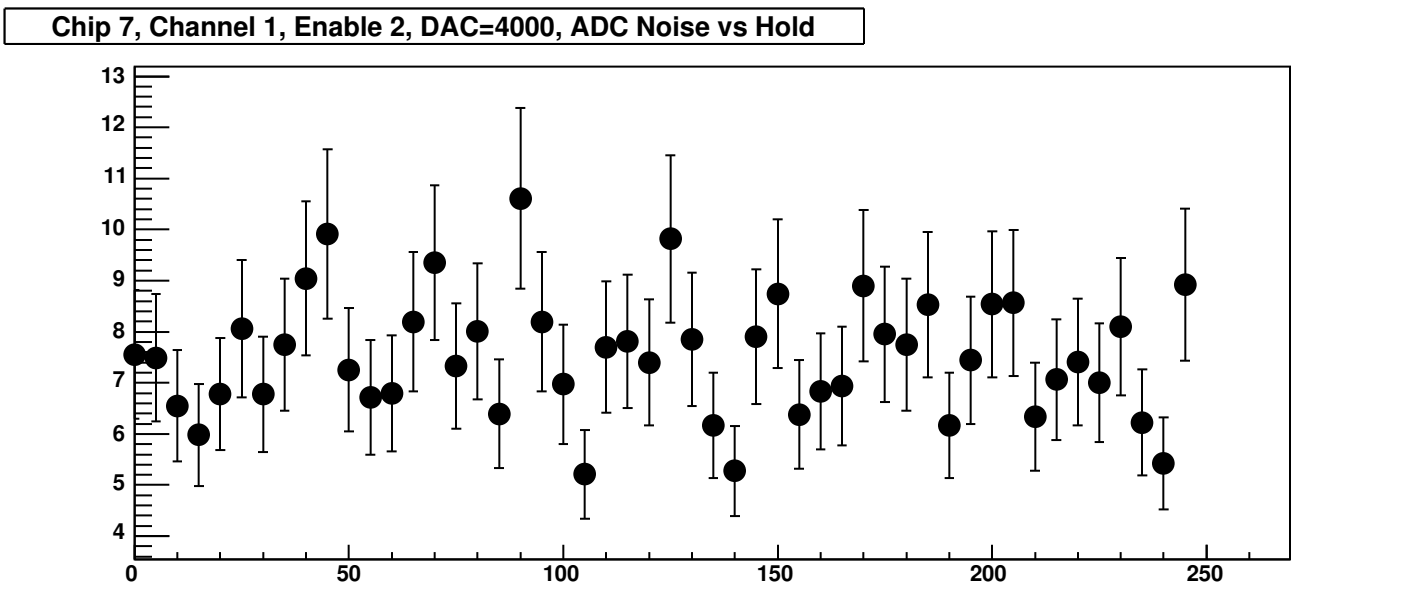


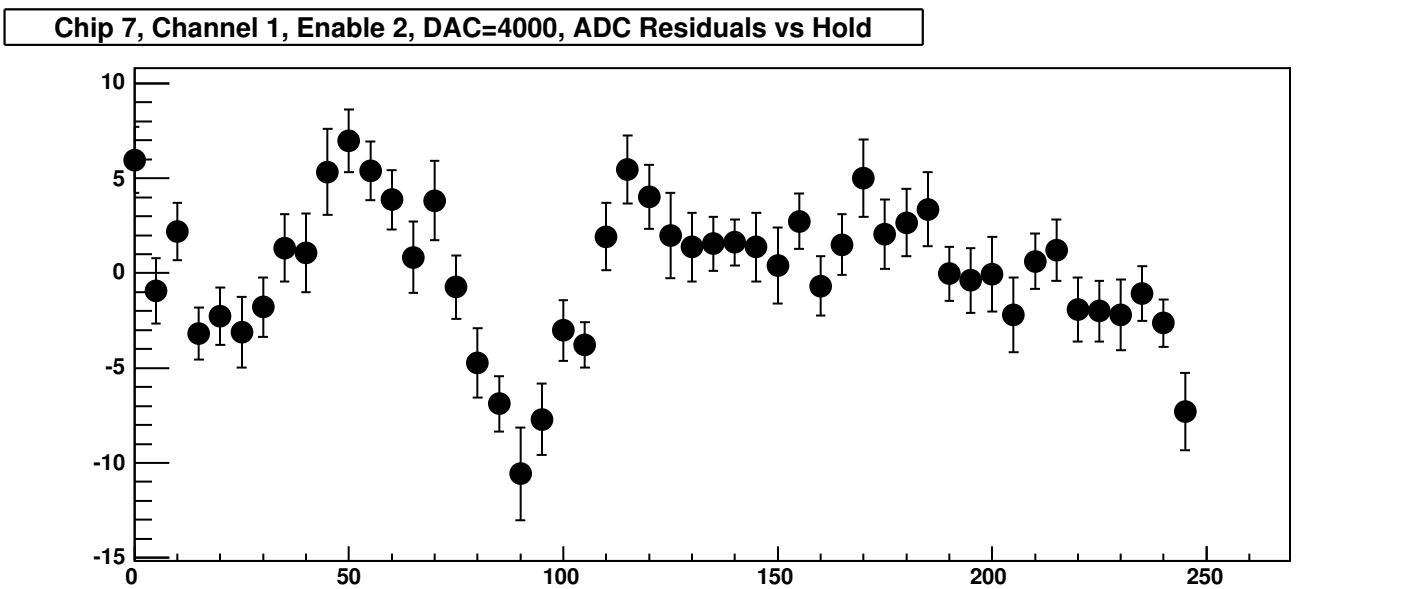

Chip 7, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 154.8 / 41
p0 -24.37 ± 0.5479
p1 85.4 ± 0.6916
p2 -2.873e+07 ± 1.091e+06
p3 1.872e+07 ± 7.016e+05
p4 1.415 ± 0.021
-20
-40
-60
-80
-100
-120
-140
-160
0
50
100
150
200
250


Chip 7, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

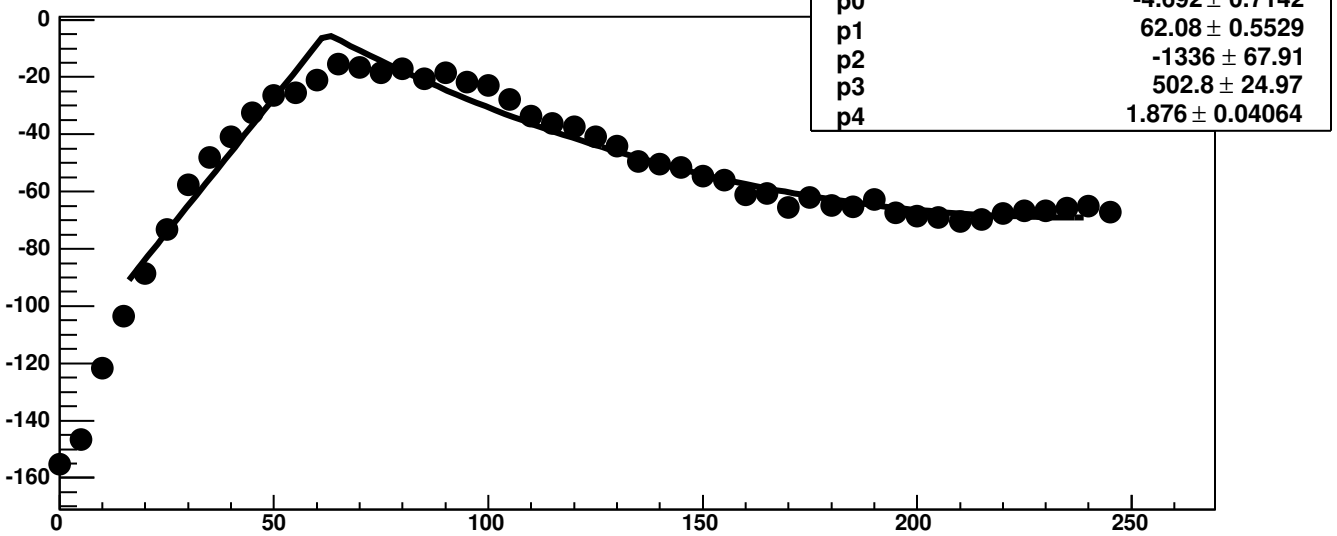

Chip 7, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
0
50
100
150
200
250


Chip 7, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 185 / 41
p0 -30.75 ± 0.5682
p1 88.31 ± 0.6639
p2 -3.464e+07 ± 6.577e+05
p3 2.19e+07 ± 3.136e+05
p4 1.525 ± 0.01906
-20
-40
-60
-80
-100
-120
-140
-160
0
50
100
150
200
250

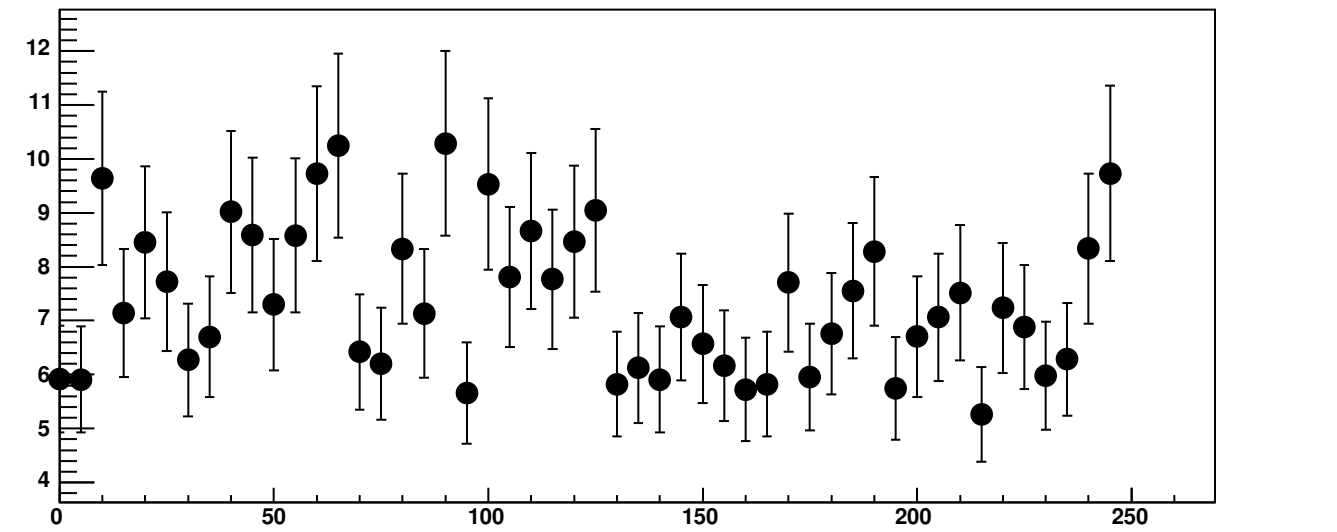

Chip 7, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 7, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
0
50
100
150
200
250

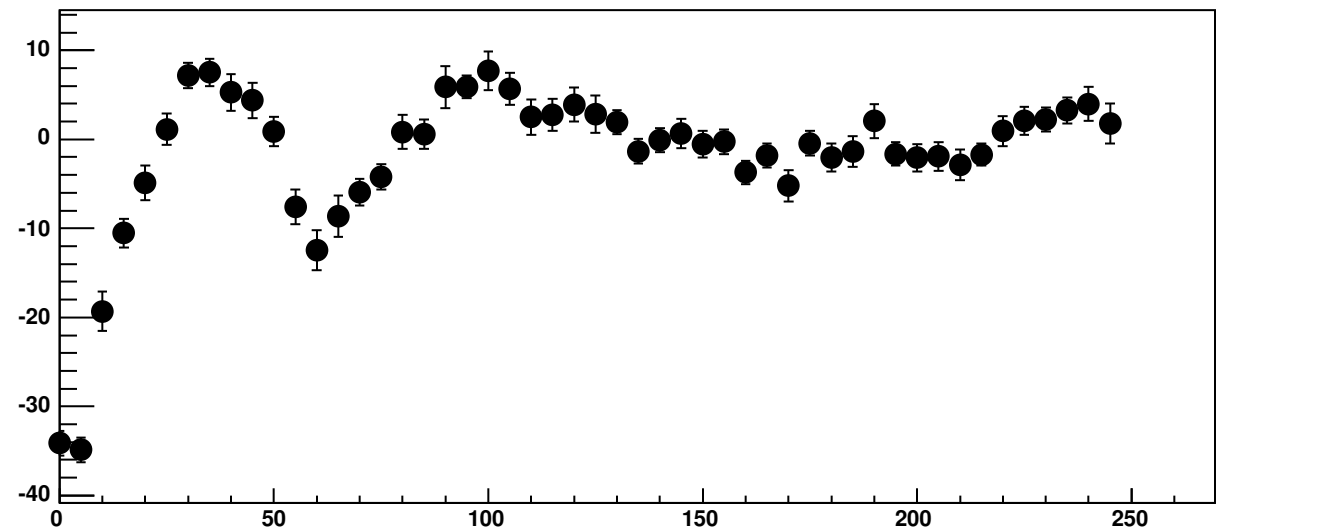
Chip 7, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold

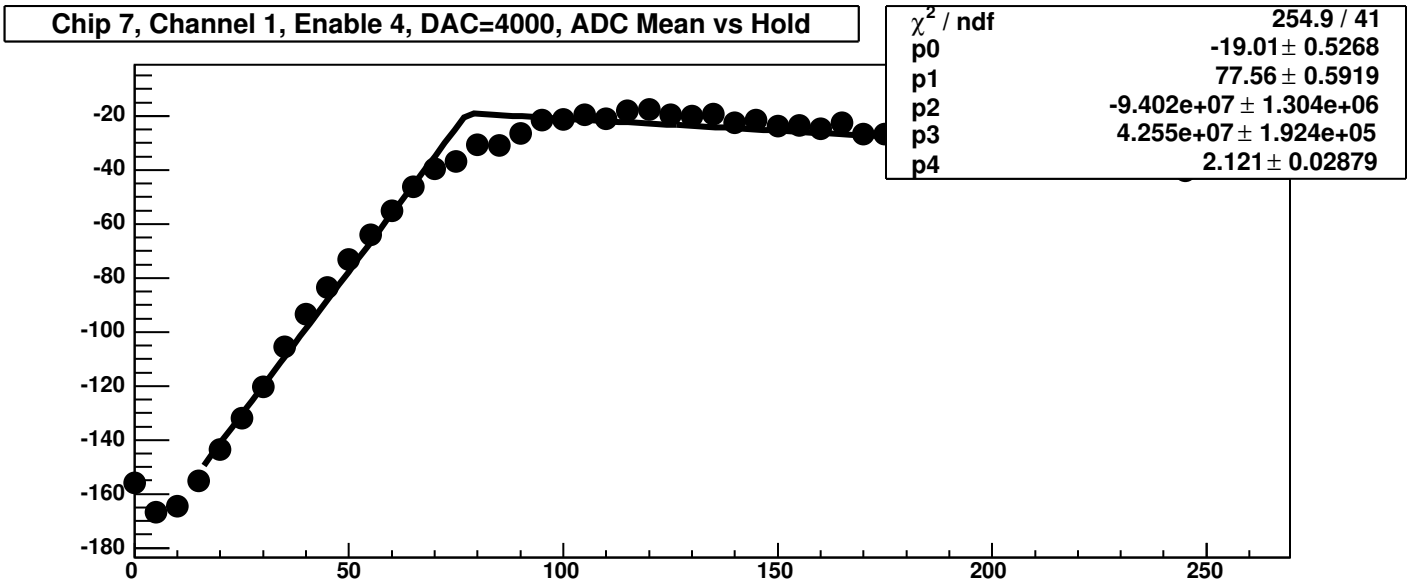


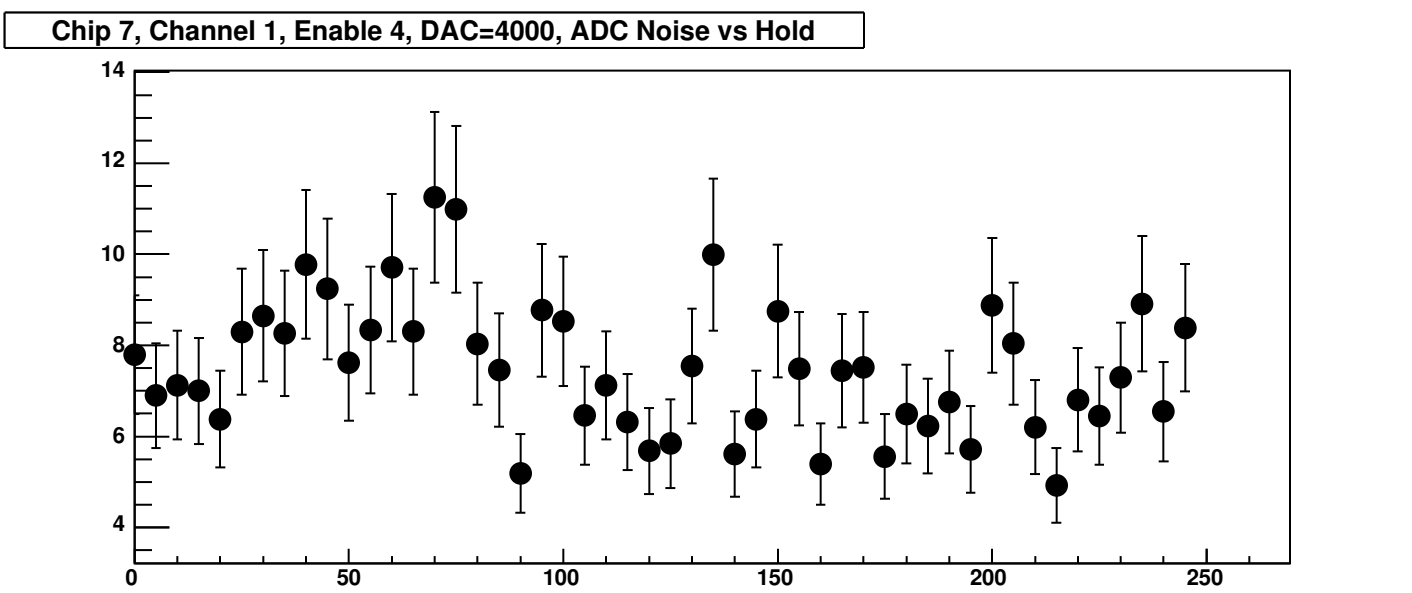
Chip 7, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold

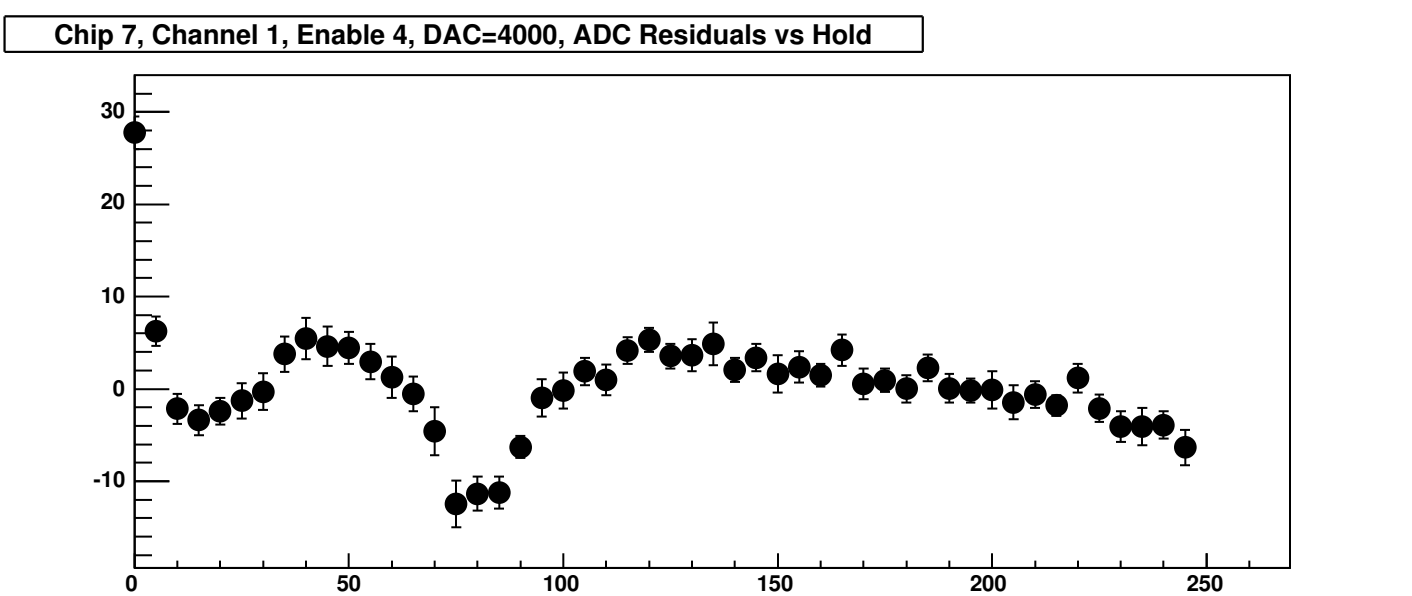


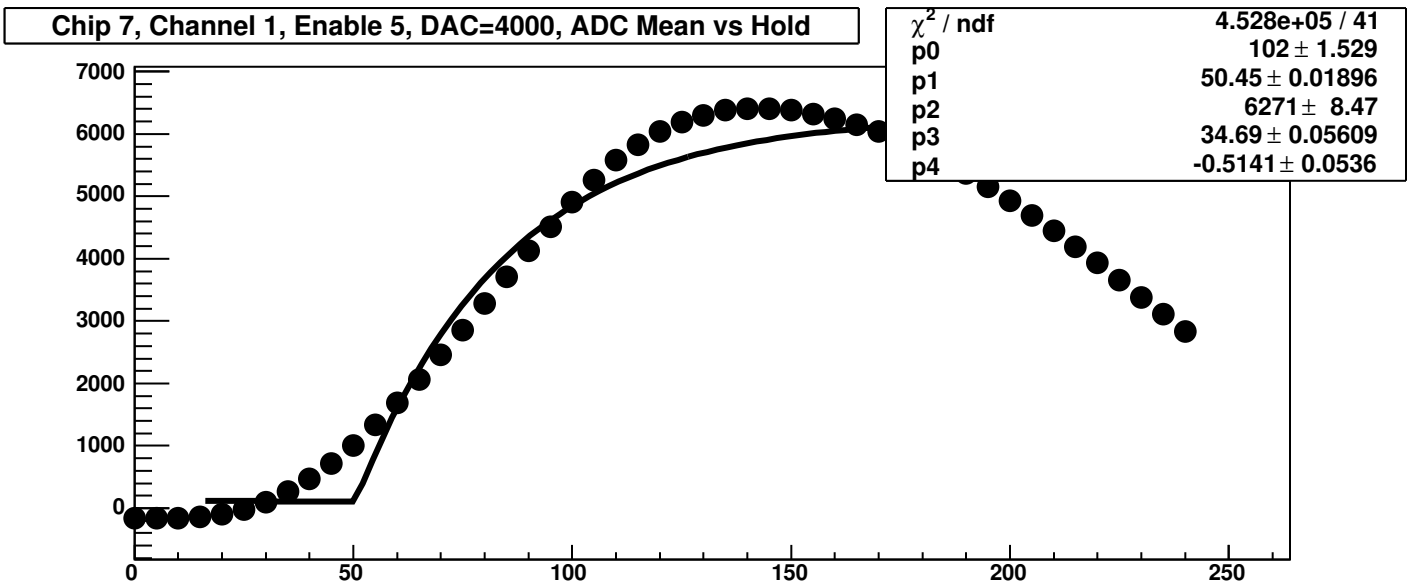
Chip 7, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold

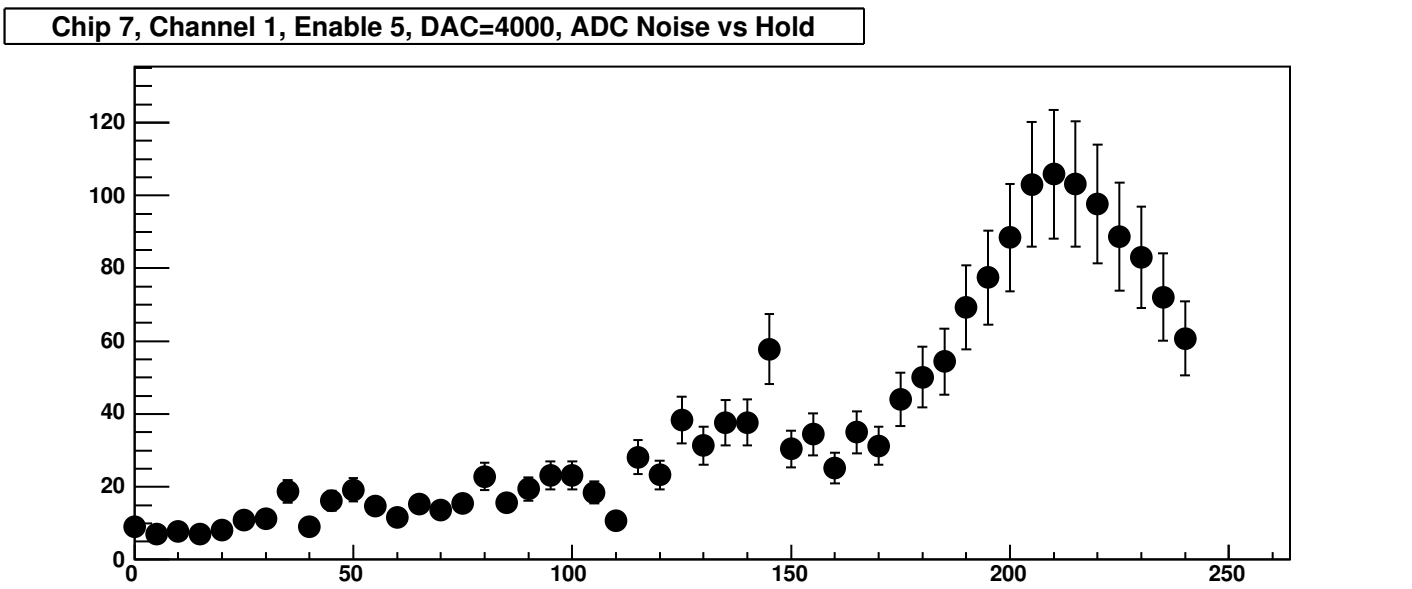


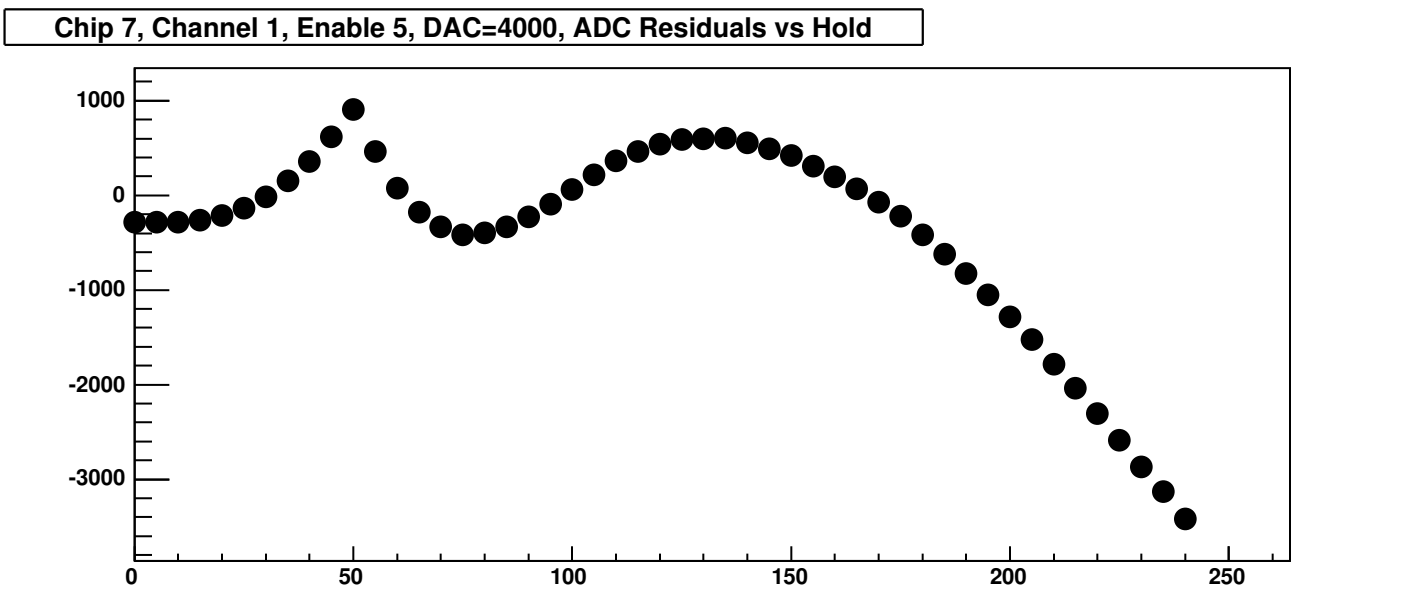

Chip 7, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 254.9 / 41
p0 -19.01 ± 0.5268
p1 77.56 ± 0.5919
p2 -9.402e+07 ± 1.304e+06
p3 4.255e+07 ± 1.924e+05
p4 2.121 ± 0.02879
-20
-40
-60
-80
-100
-120
-140
-160
-180
0
50
100
150
200
250


Chip 7, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

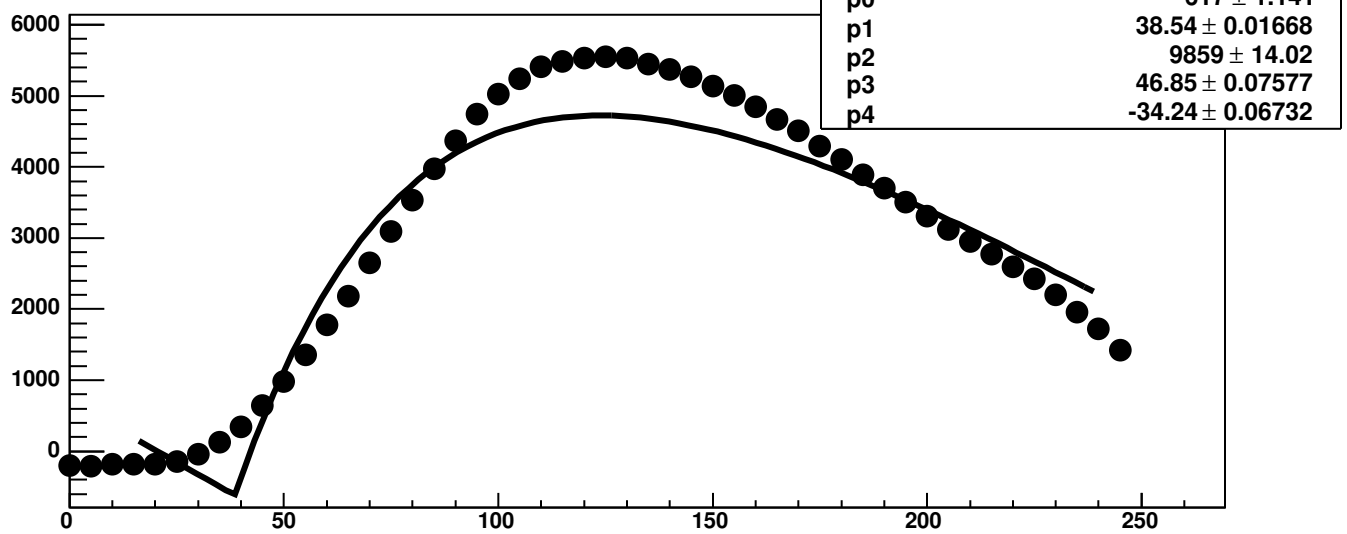

Chip 7, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
0
50
100
150
200
250


Chip 7, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.528e+05 / 41
p0 102 ± 1.529
p1 50.45 ± 0.01896
p2 6271 ± 8.47
p3 34.69 ± 0.05609
p4 -0.5141 ± 0.0536
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

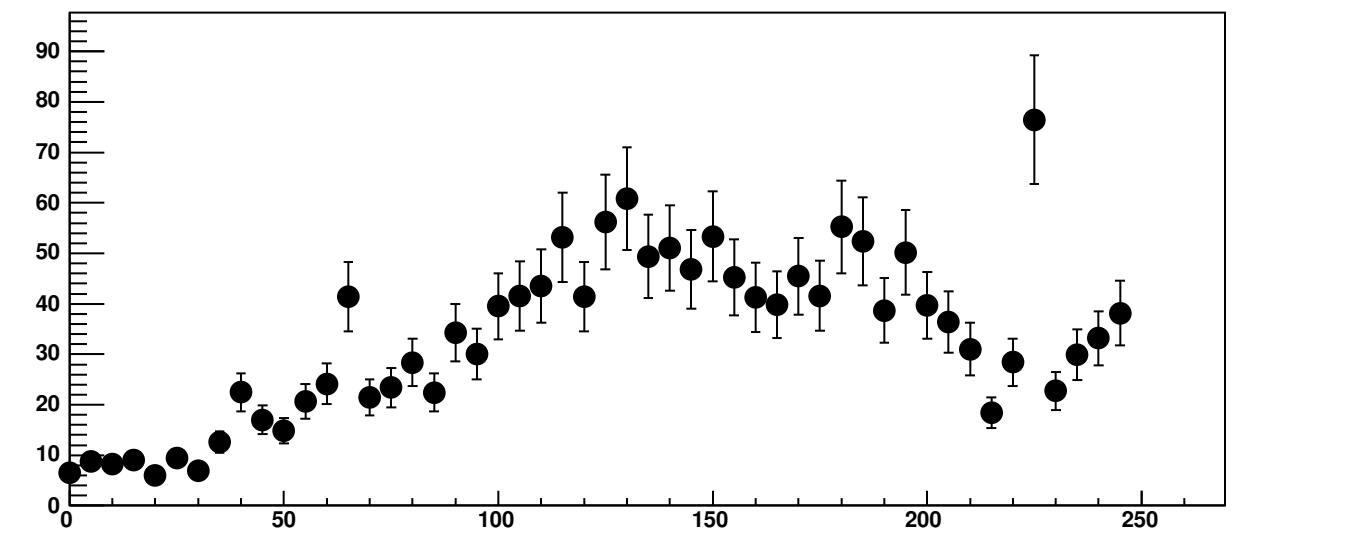

Chip 7, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 7, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

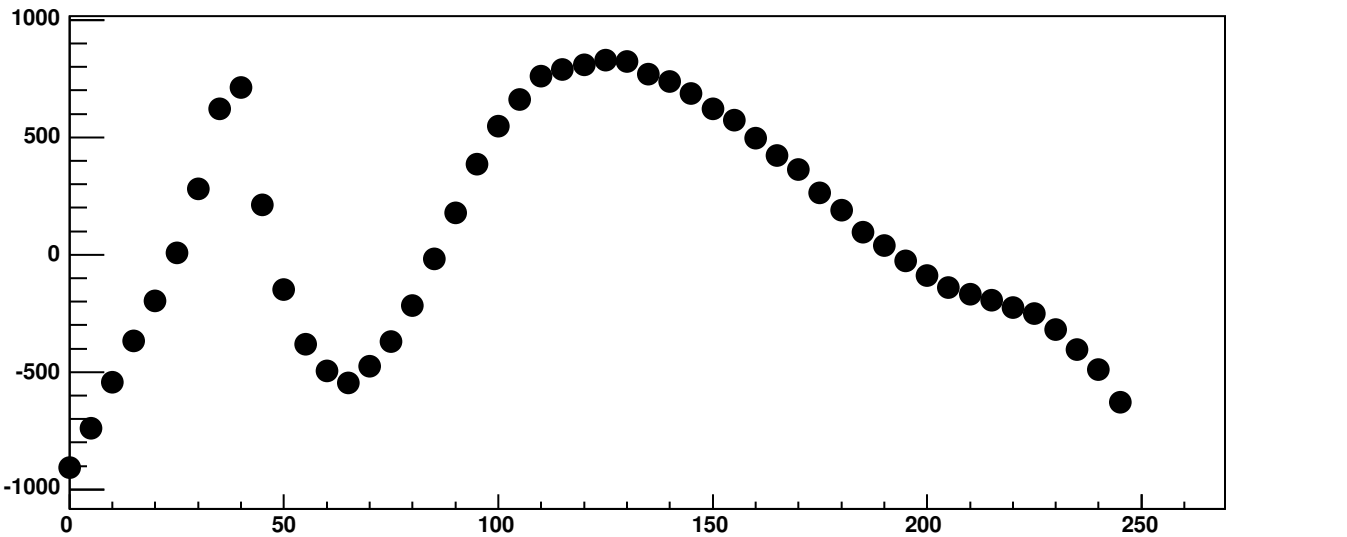
Chip 7, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold



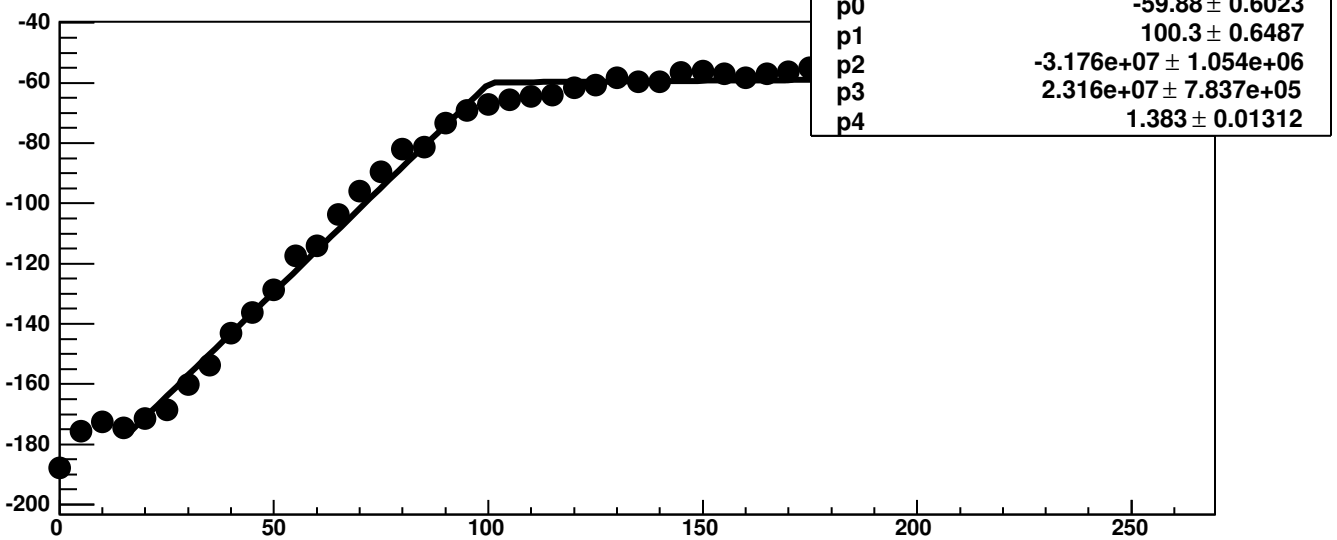
Chip 7, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold



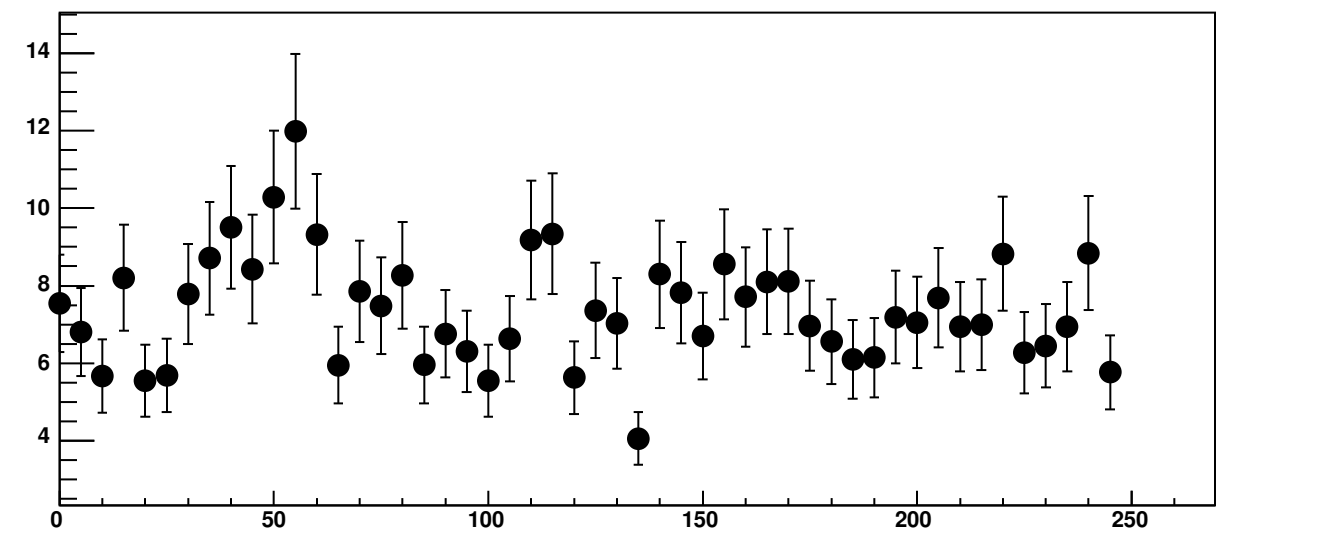
Chip 7, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold



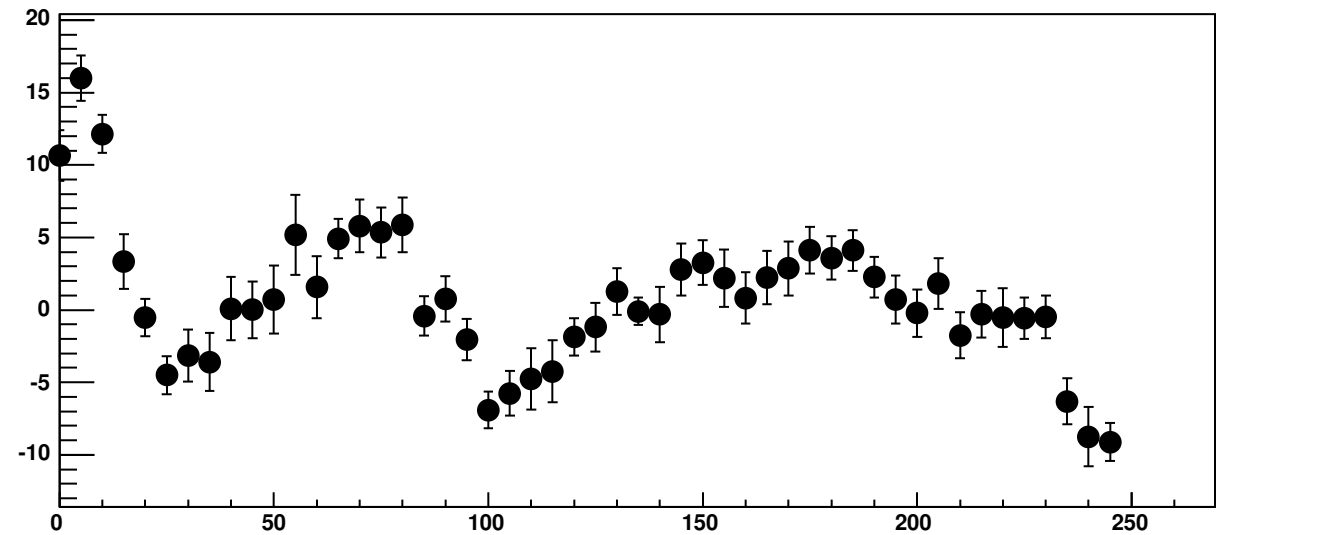
Chip 7, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold



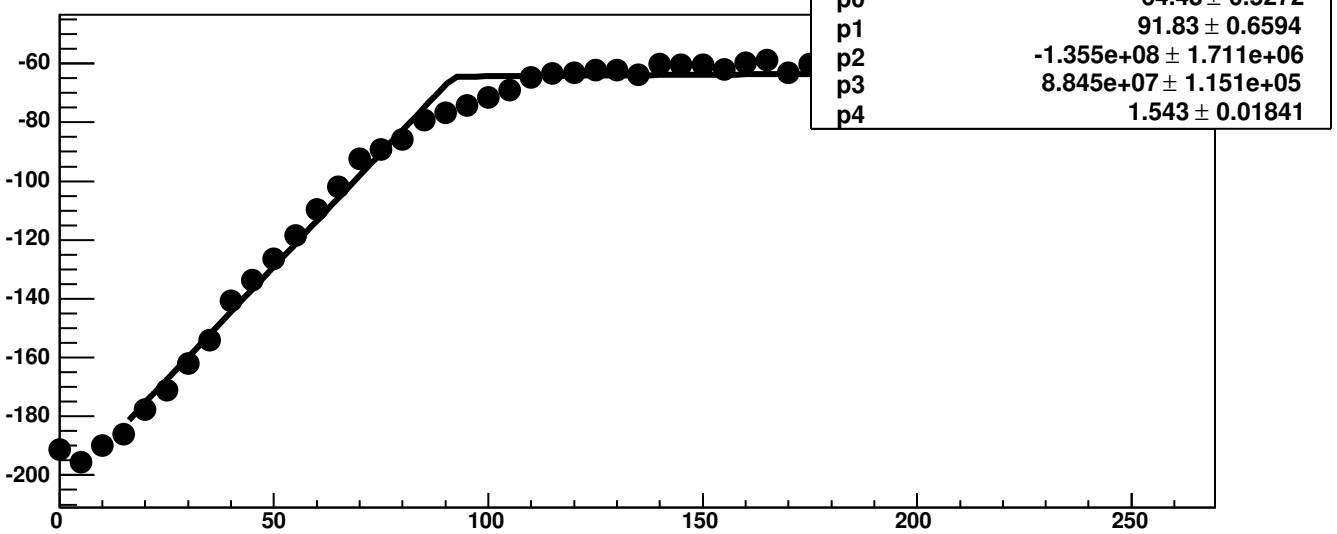
Chip 7, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



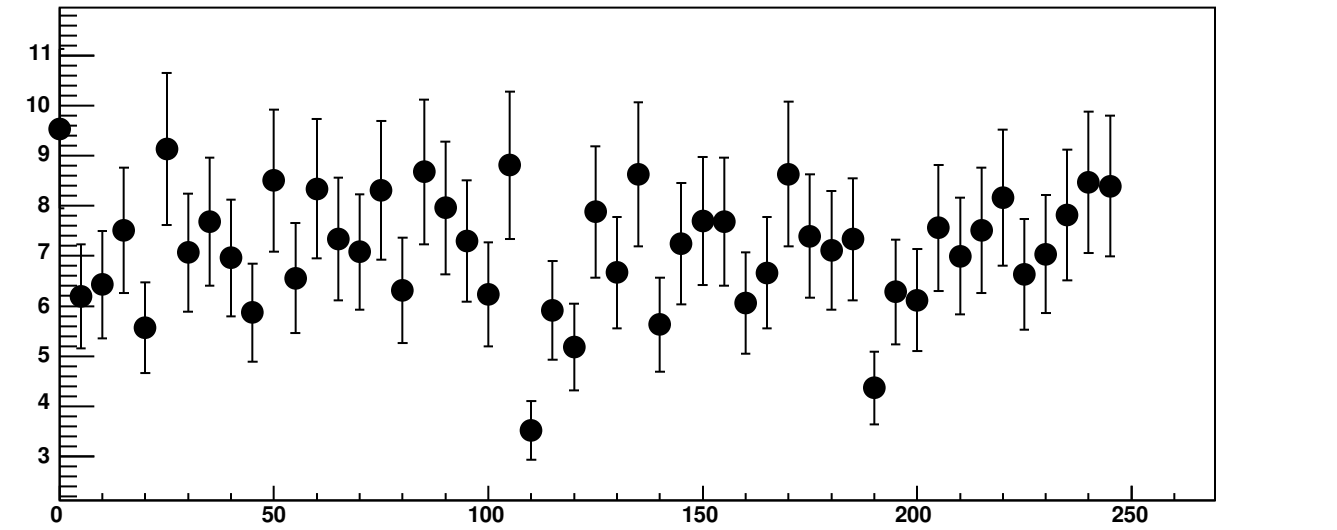
Chip 7, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold



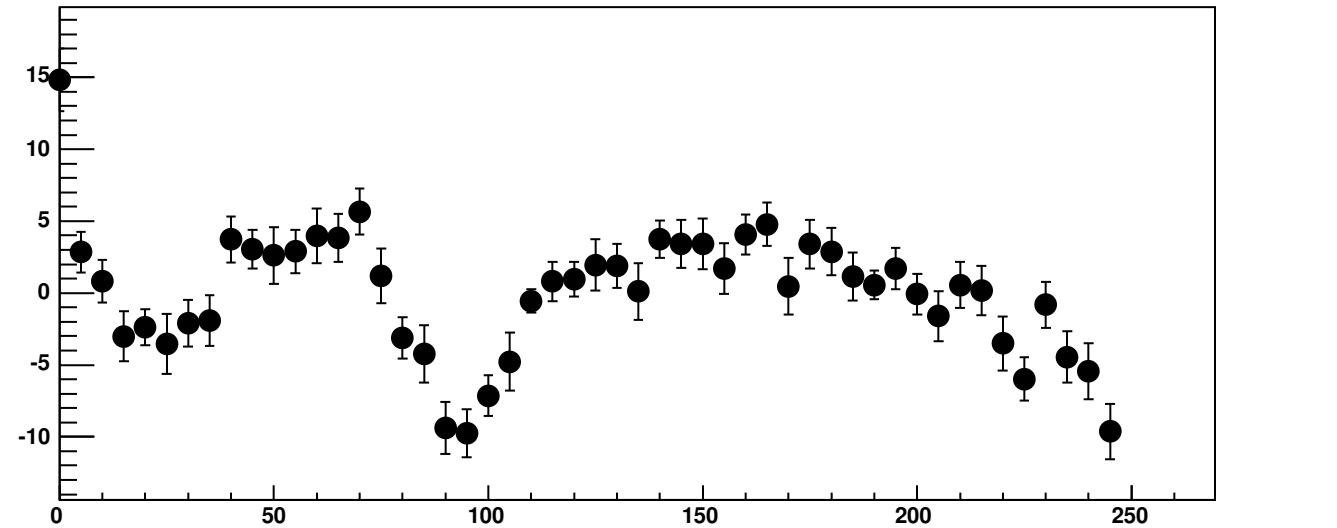
Chip 7, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold



Chip 7, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold

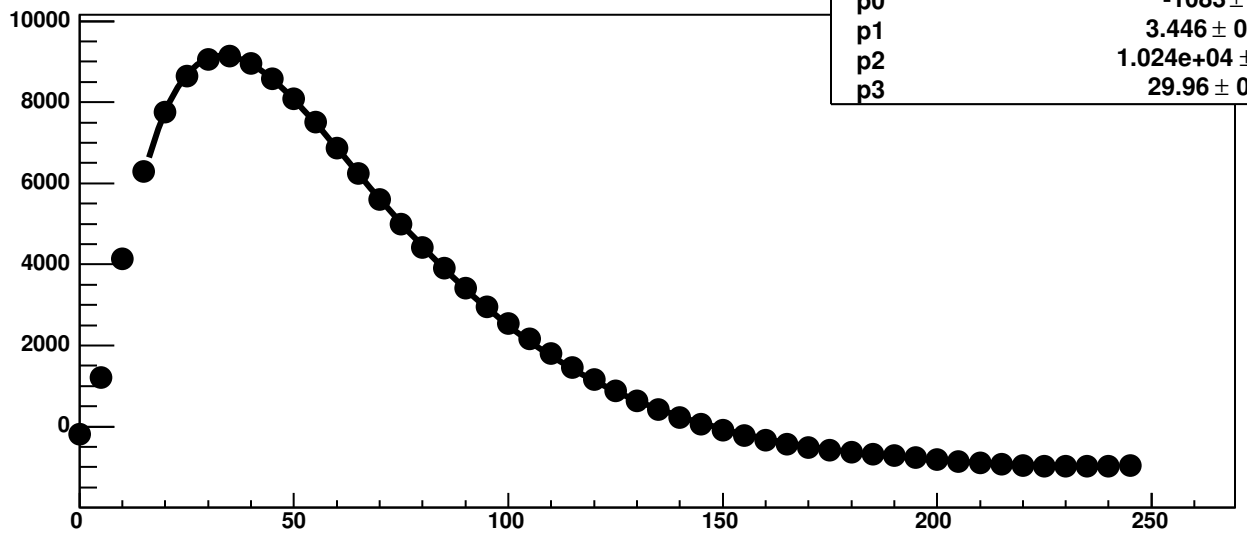


Chip 7, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

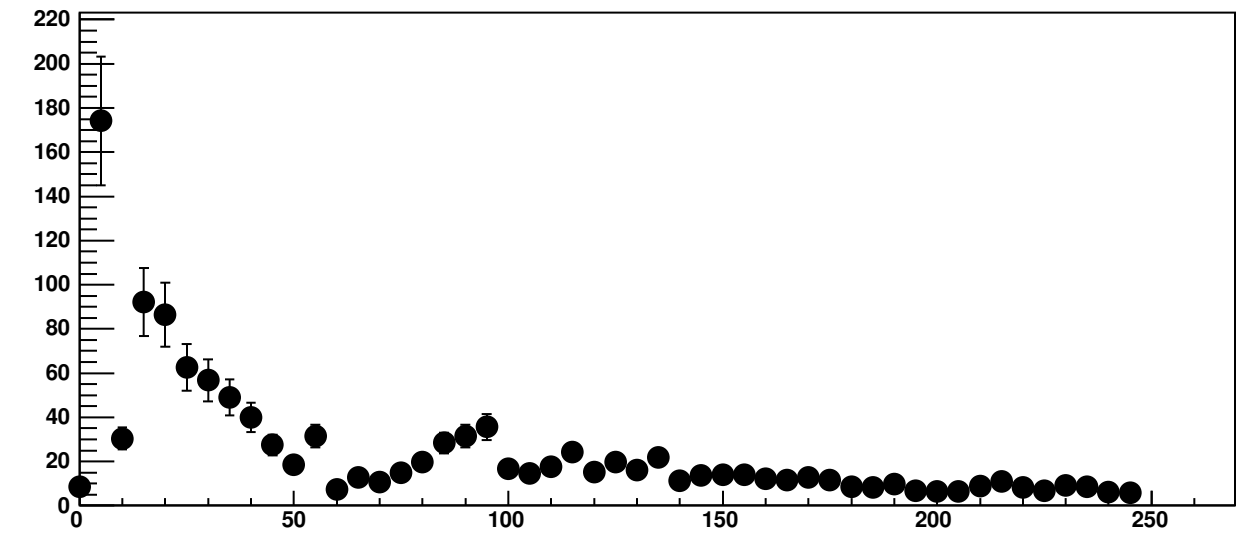


Chip 7, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold

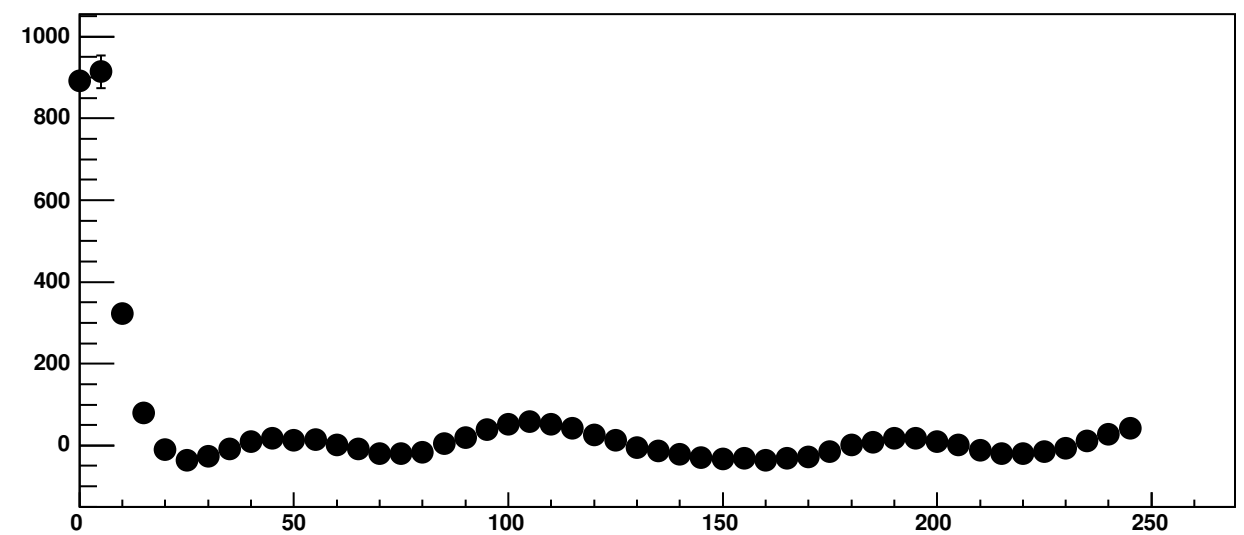
χ^2 / ndf	2740 / 42
p0	-1083 ± 0.6821
p1	3.446 ± 0.03255
p2	1.024e+04 ± 3.473
p3	29.96 ± 0.01201

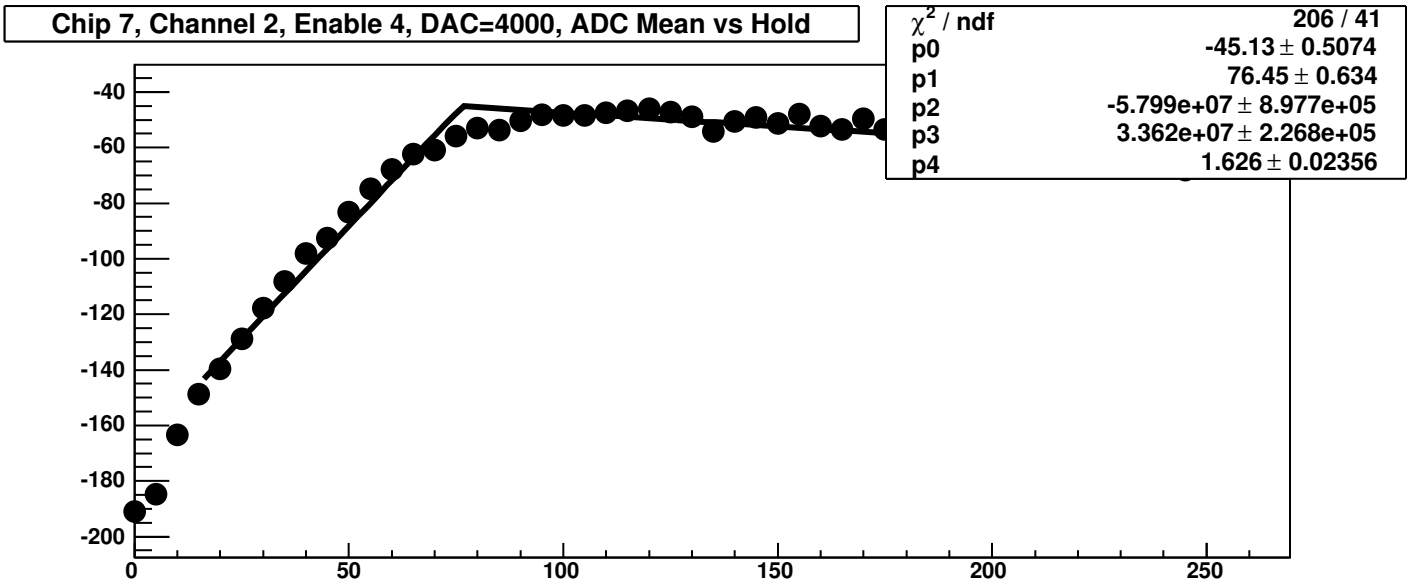


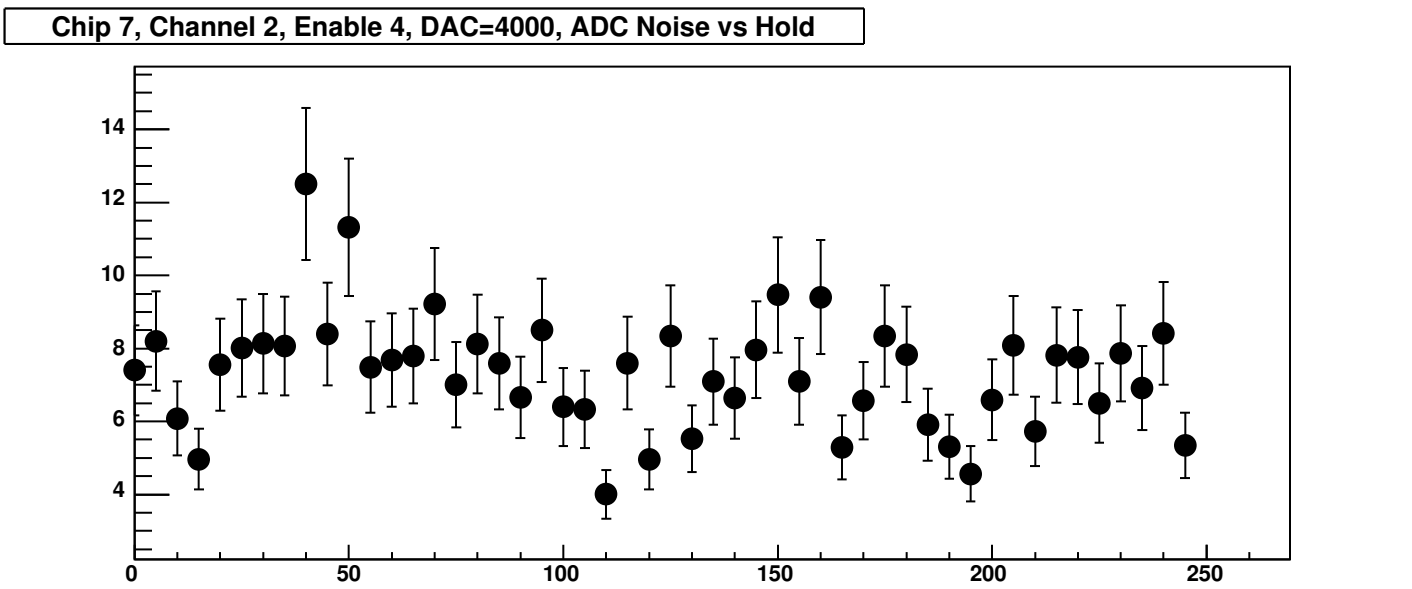
Chip 7, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold

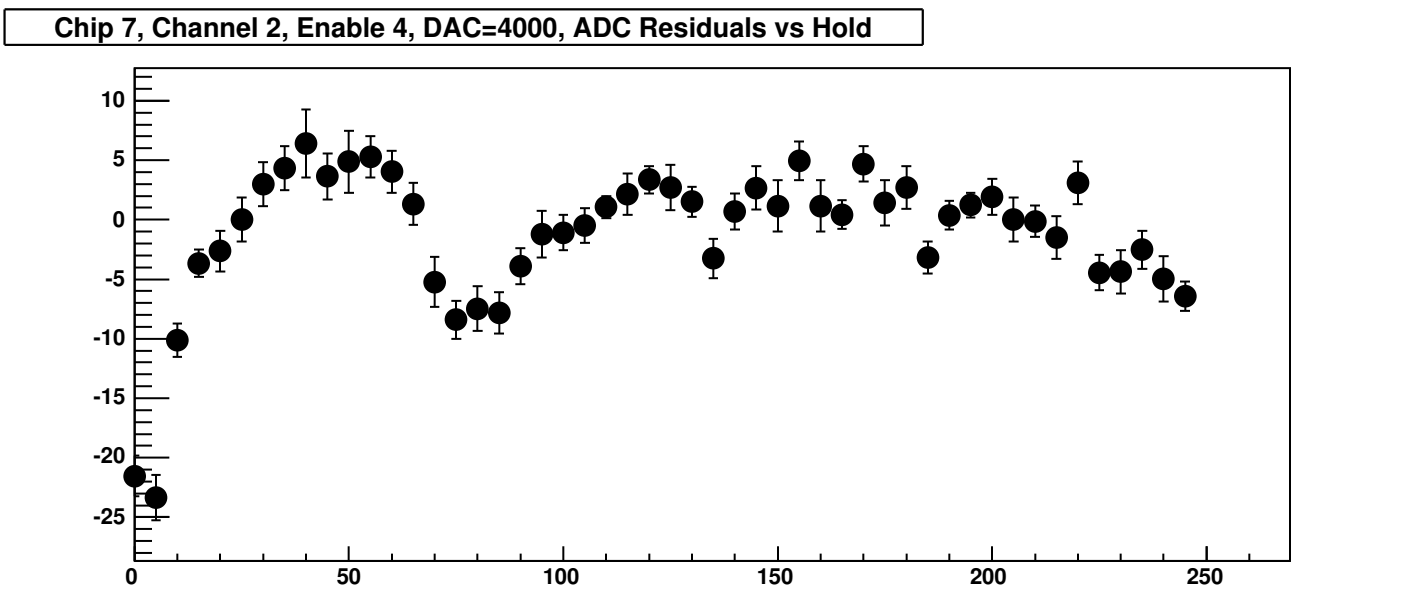


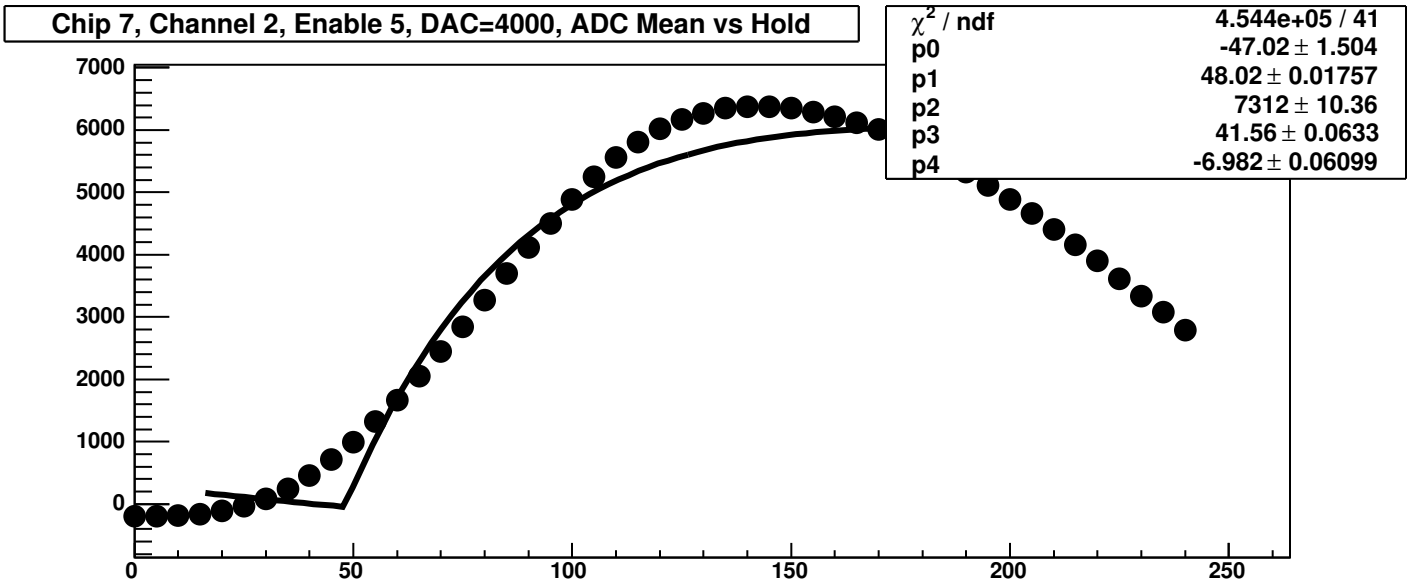
Chip 7, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold

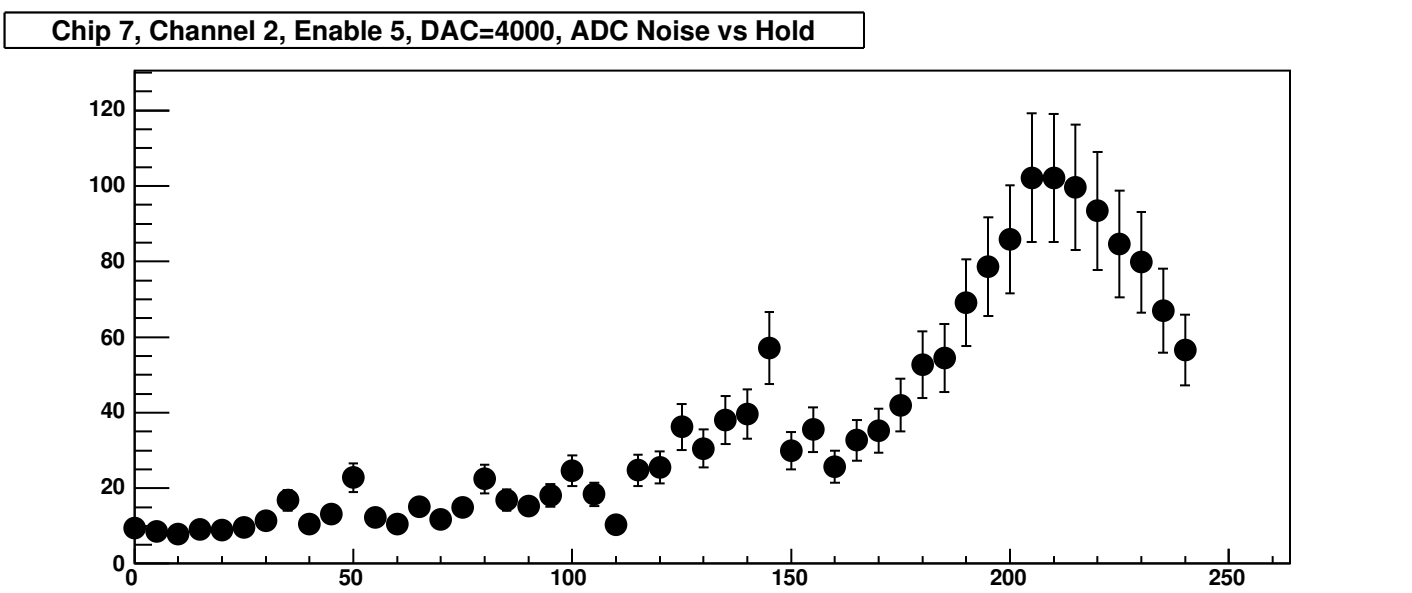


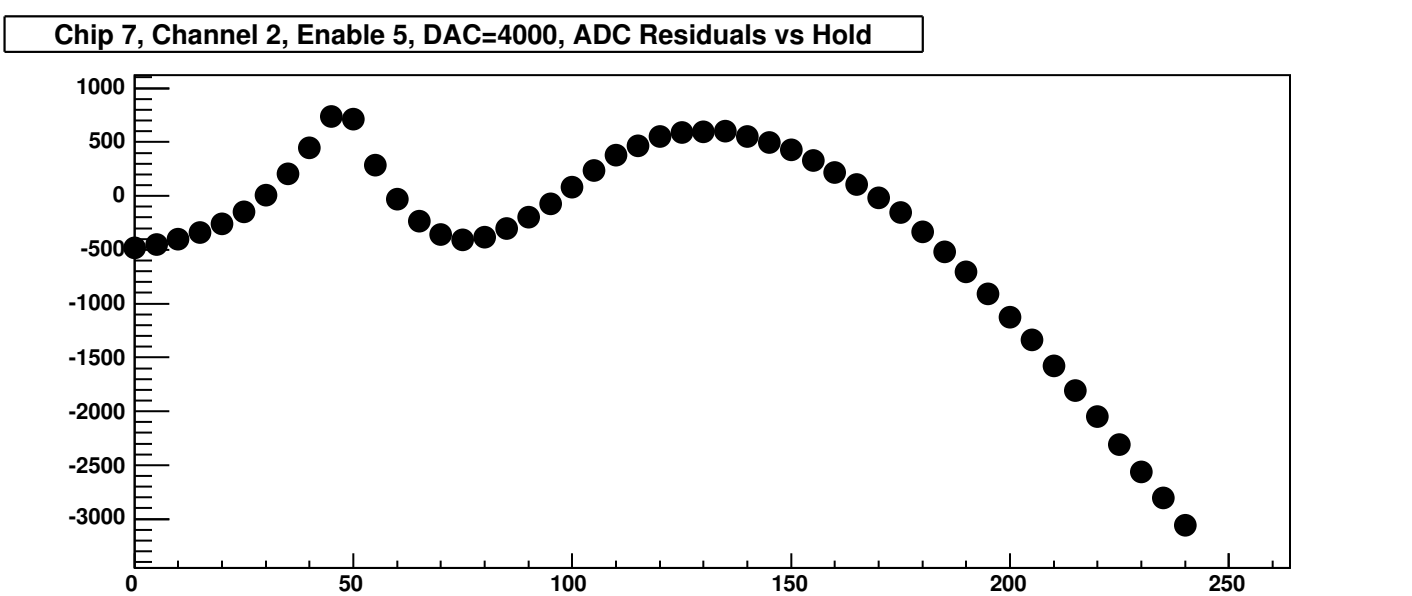

Chip 7, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 206 / 41
p0 -45.13 ± 0.5074
p1 76.45 ± 0.634
p2 -5.799e+07 ± 8.977e+05
p3 3.362e+07 ± 2.268e+05
p4 1.626 ± 0.02356
-40
-60
-80
-100
-120
-140
-160
-180
-200
0
50
100
150
200
250


Chip 7, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

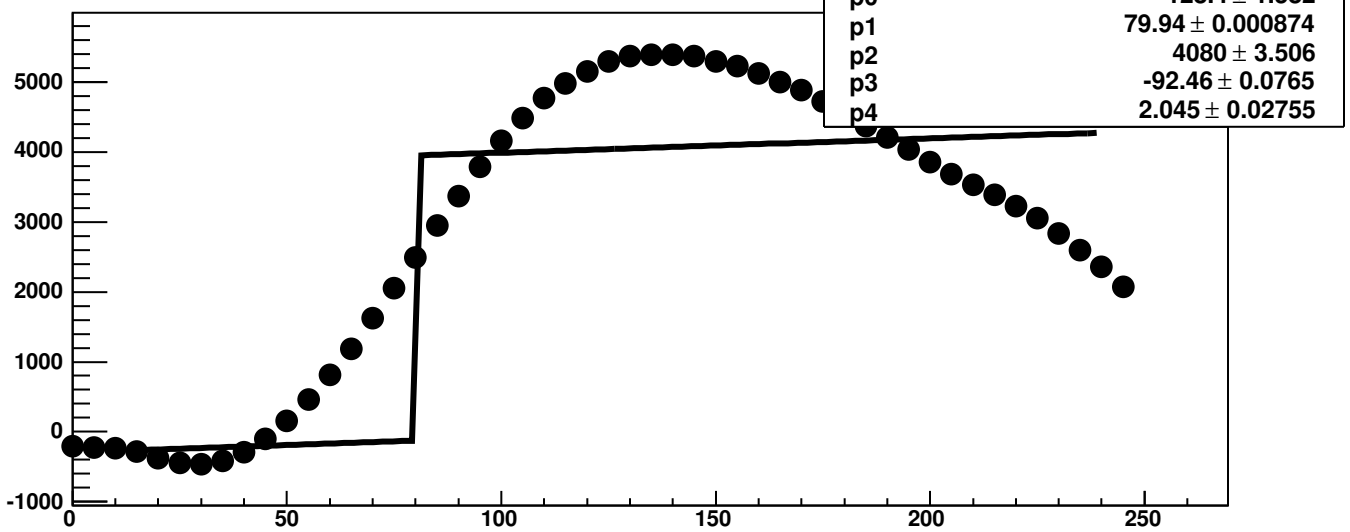

Chip 7, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
-20
-25
0
50
100
150
200
250


Chip 7, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.544e+05 / 41
p0 -47.02 ± 1.504
p1 48.02 ± 0.01757
p2 7312 ± 10.36
p3 41.56 ± 0.0633
p4 -6.982 ± 0.06099
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

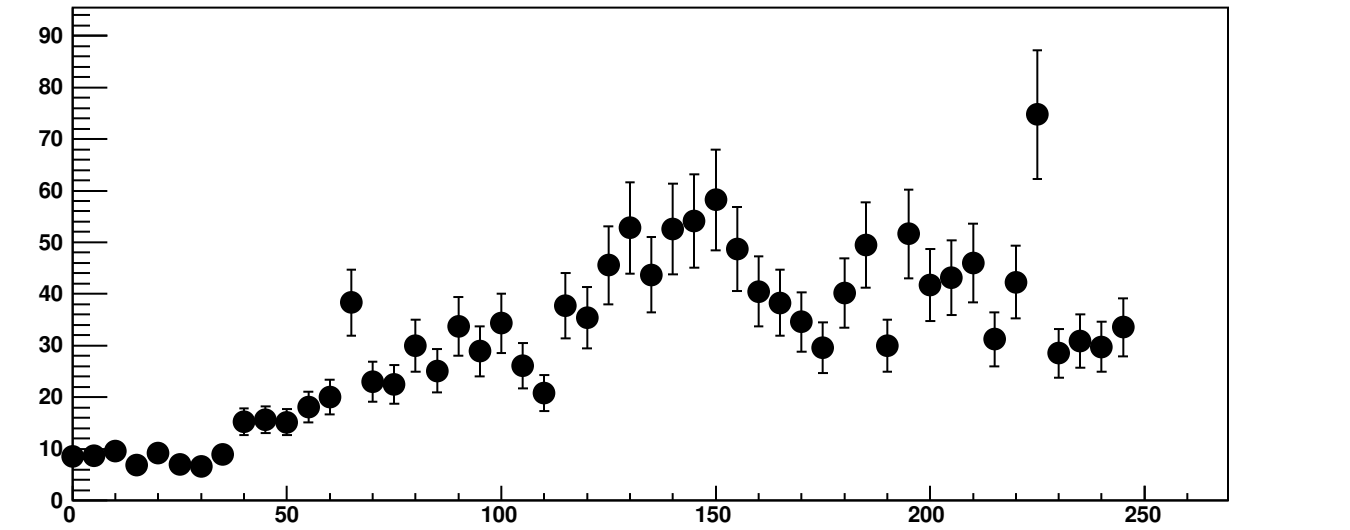

Chip 7, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 7, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250

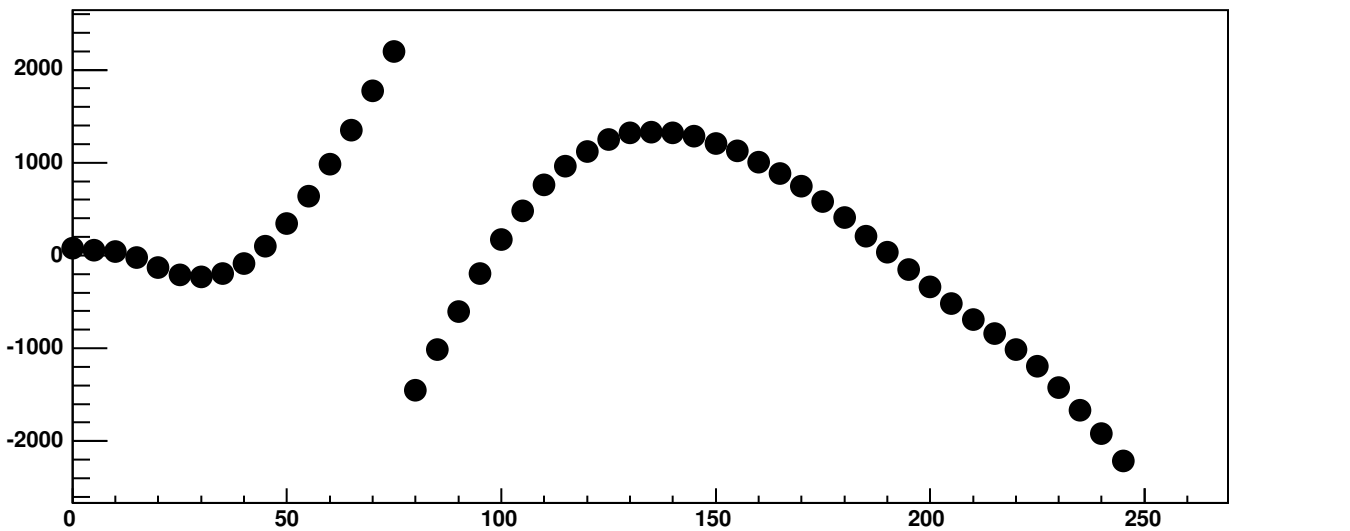
Chip 7, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold



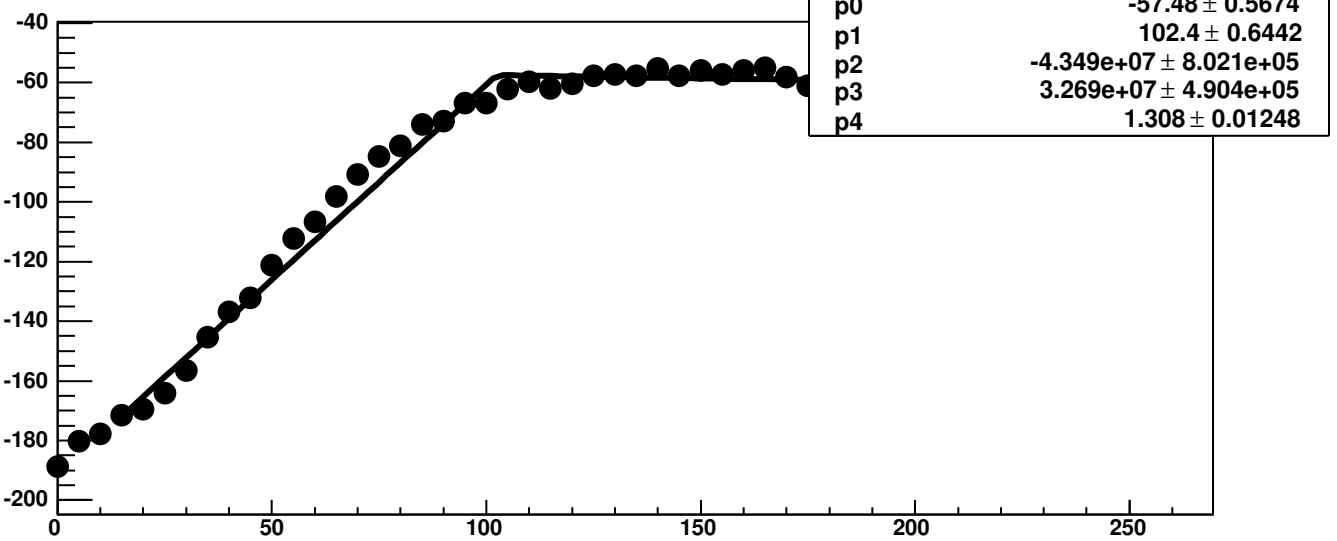
Chip 7, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



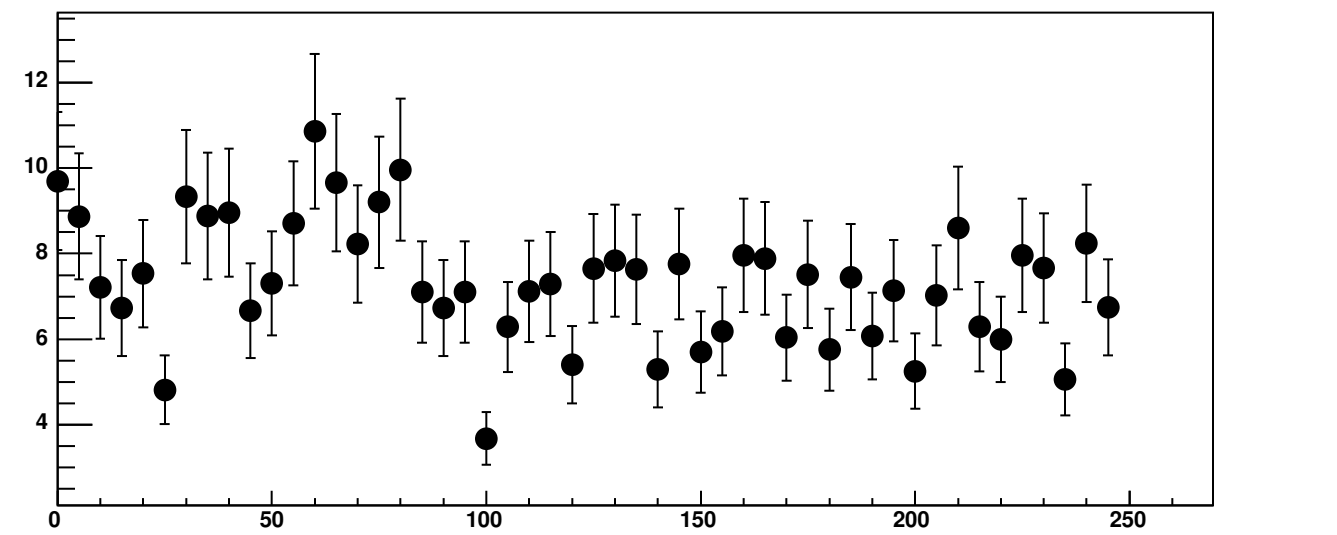
Chip 7, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold



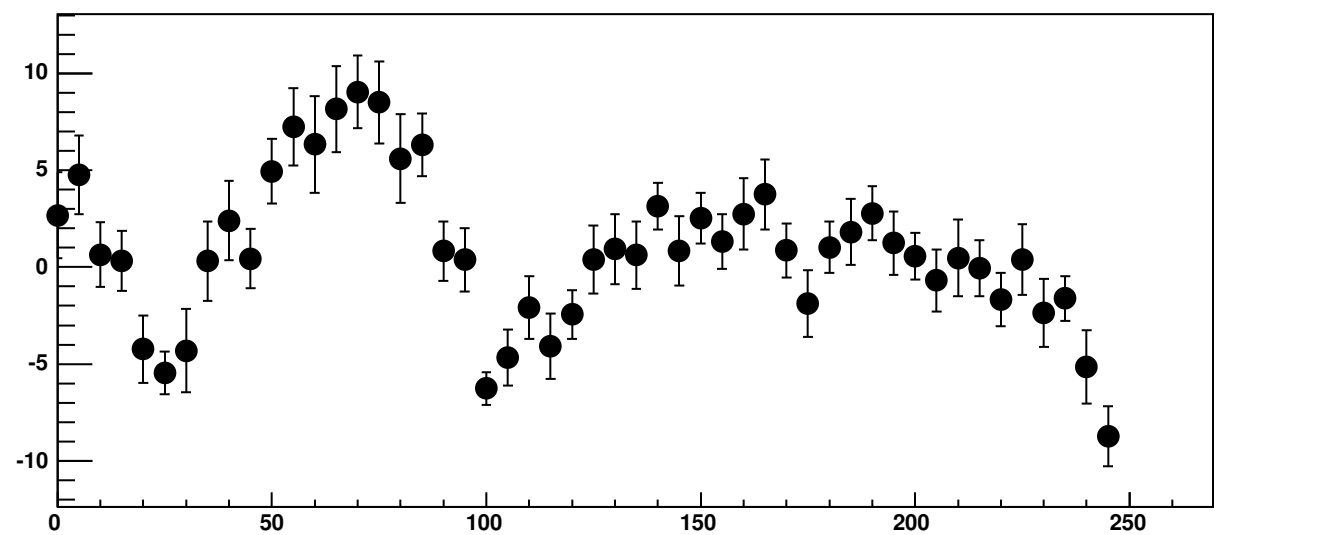
Chip 7, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold

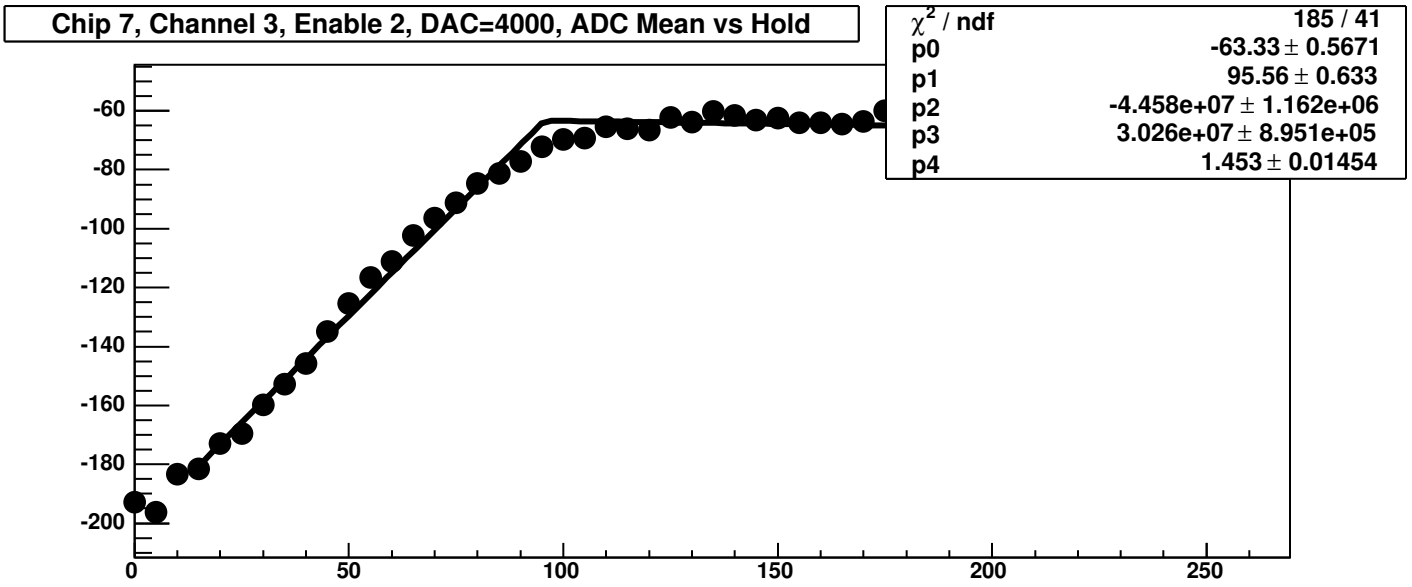


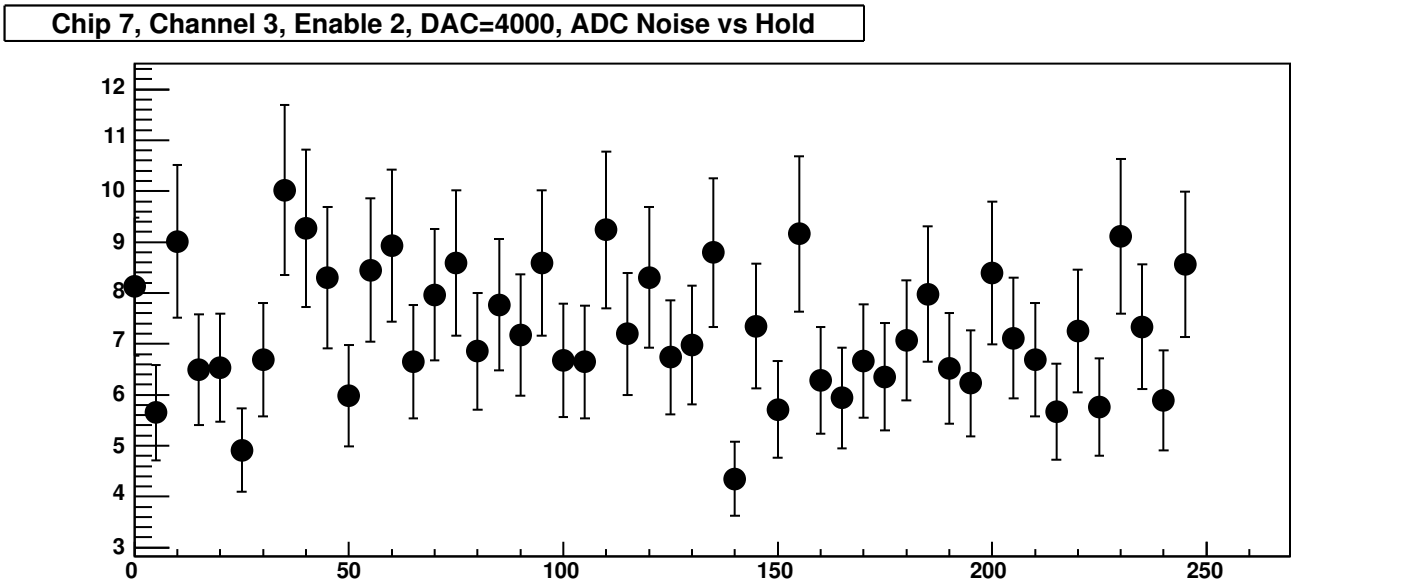
Chip 7, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold

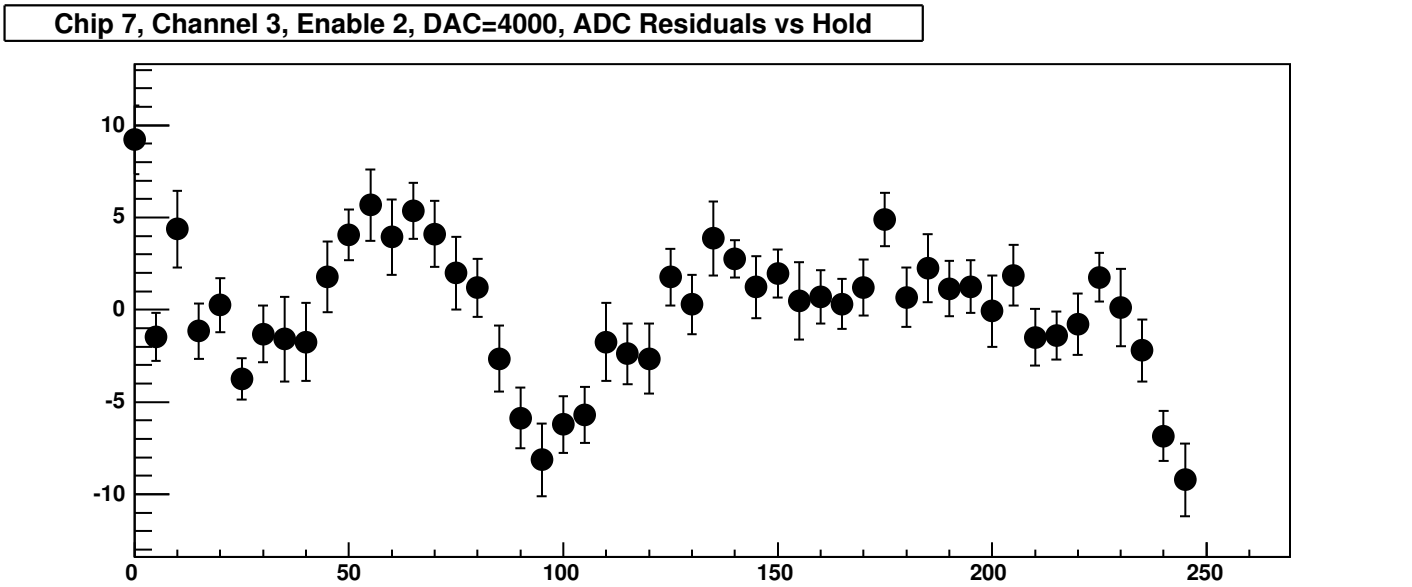


Chip 7, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold

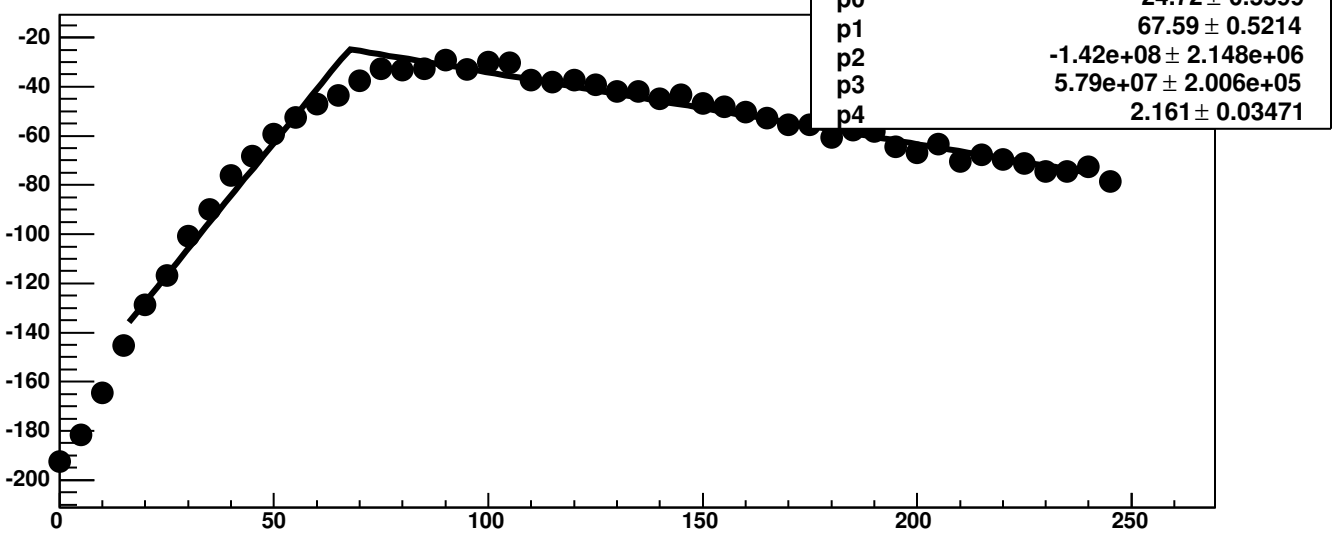



Chip 7, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 185 / 41
p0 -63.33 ± 0.5671
p1 95.56 ± 0.633
p2 -4.458e+07 ± 1.162e+06
p3 3.026e+07 ± 8.951e+05
p4 1.453 ± 0.01454
-60
-80
-100
-120
-140
-160
-180
-200
0
50
100
150
200
250

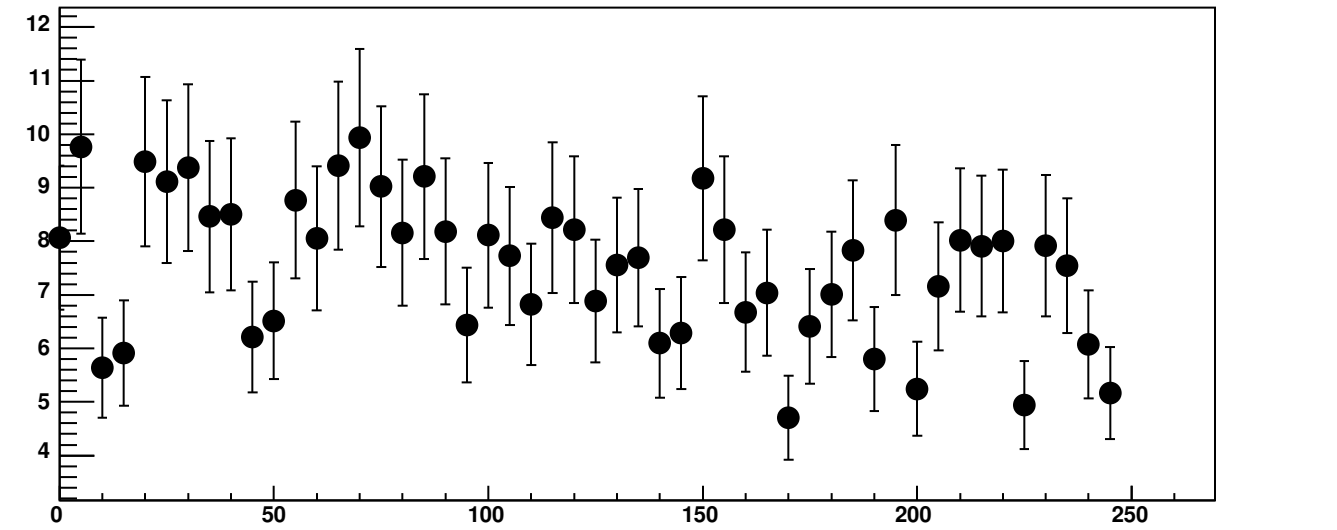

Chip 7, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 7, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250

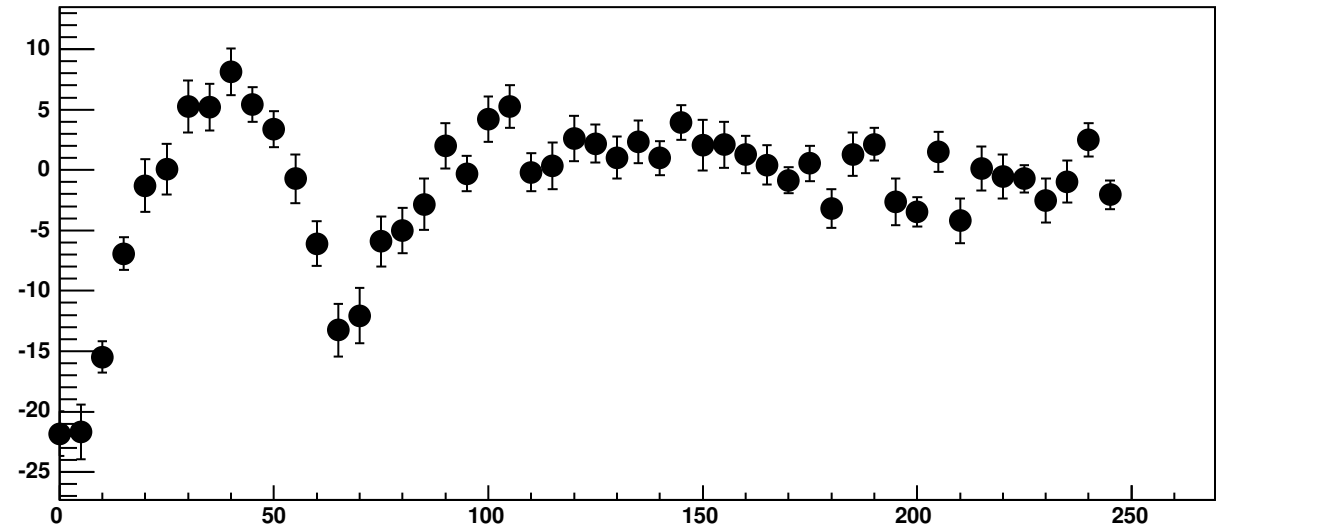
Chip 7, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold



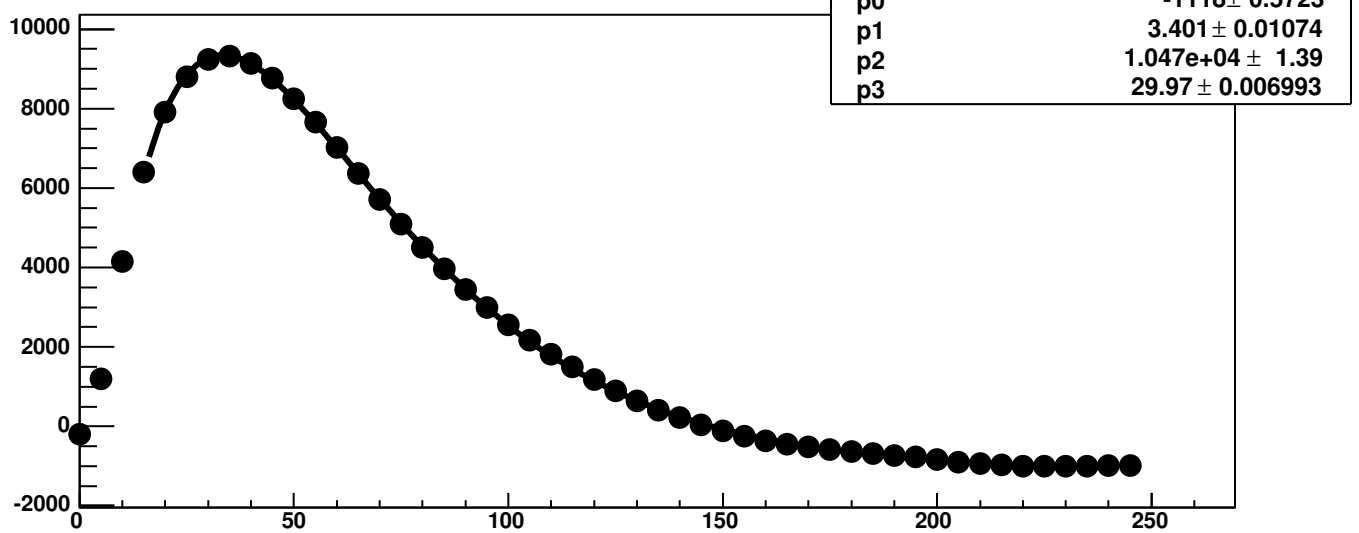
Chip 7, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



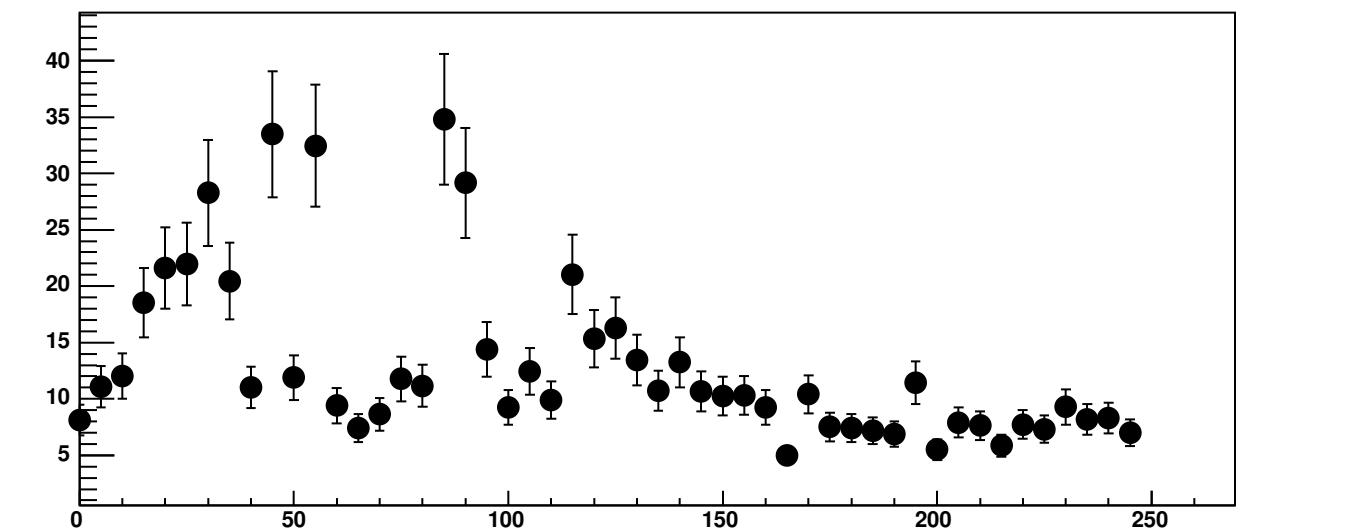
Chip 7, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold



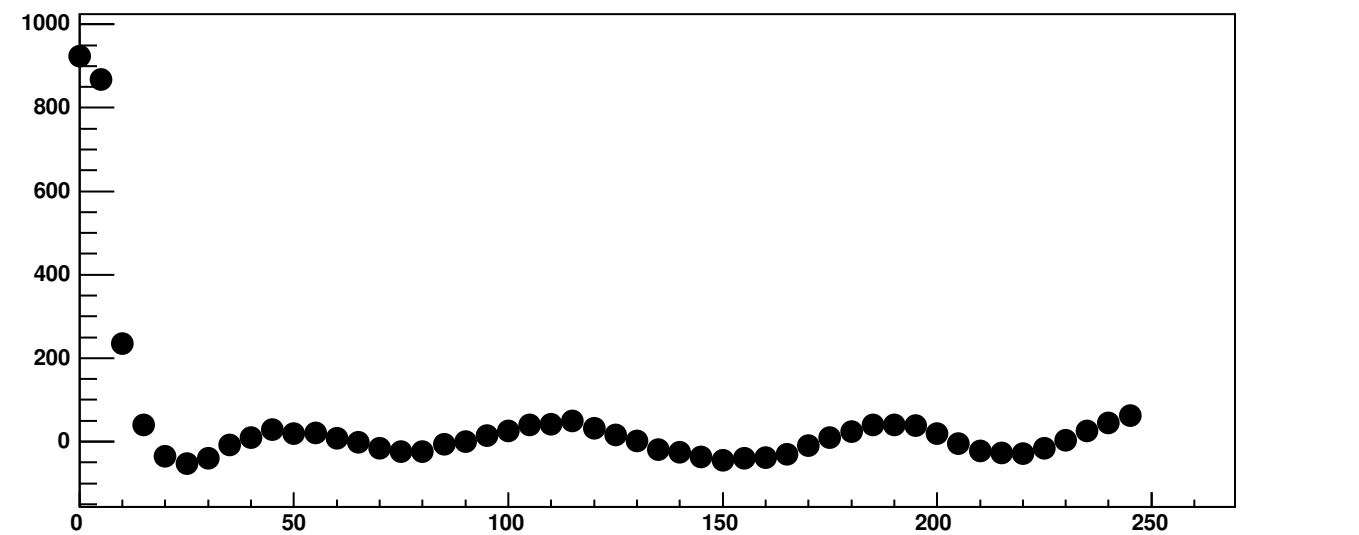
Chip 7, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold



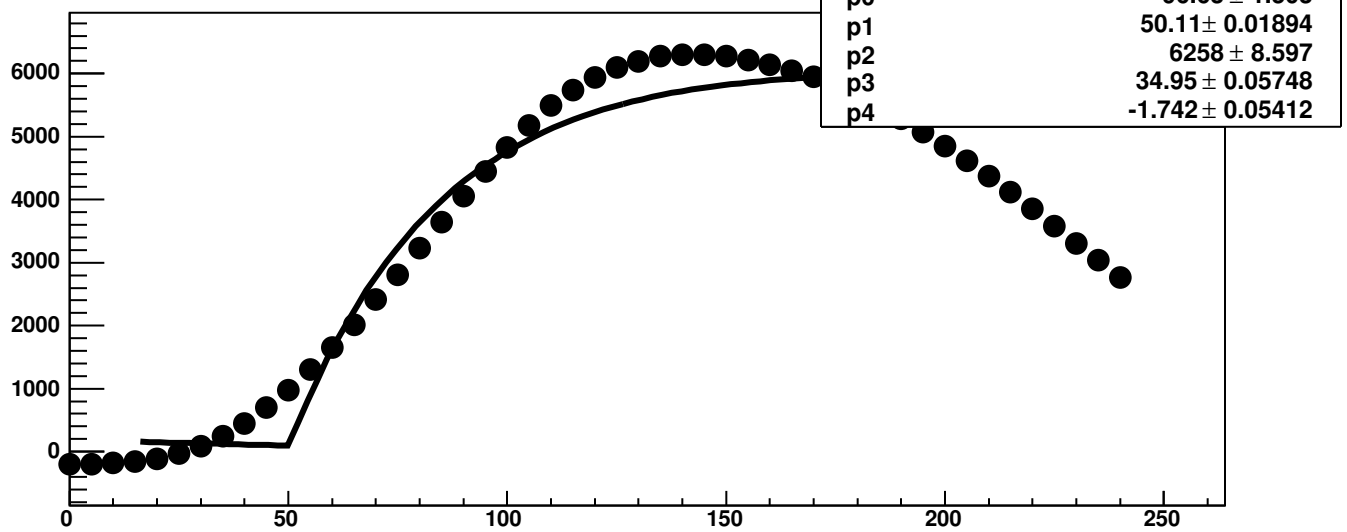
Chip 7, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold



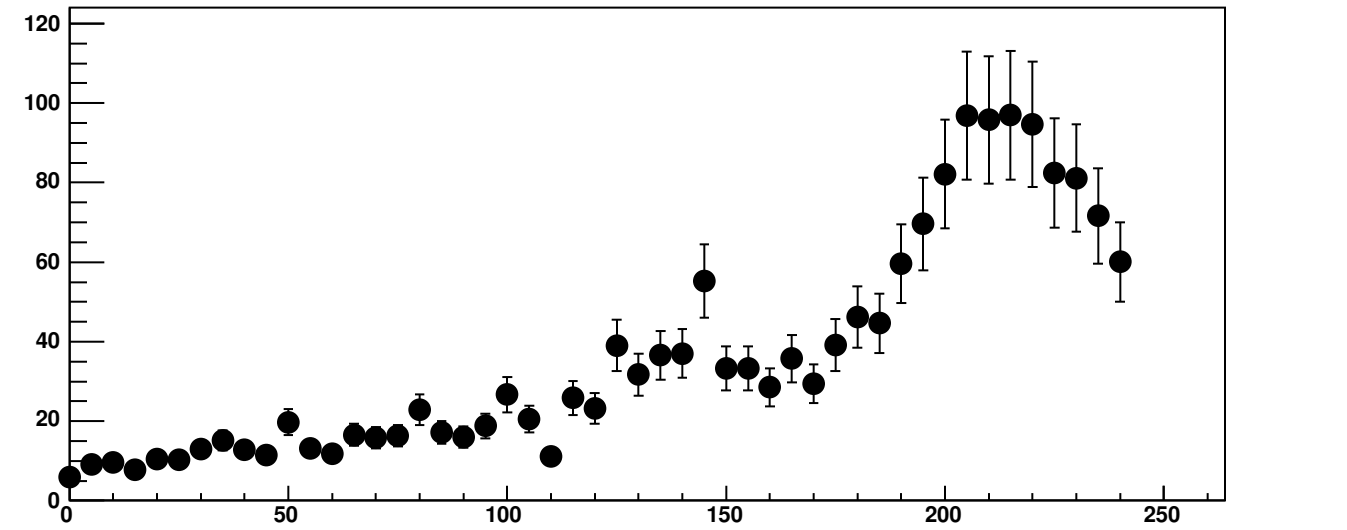
Chip 7, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold



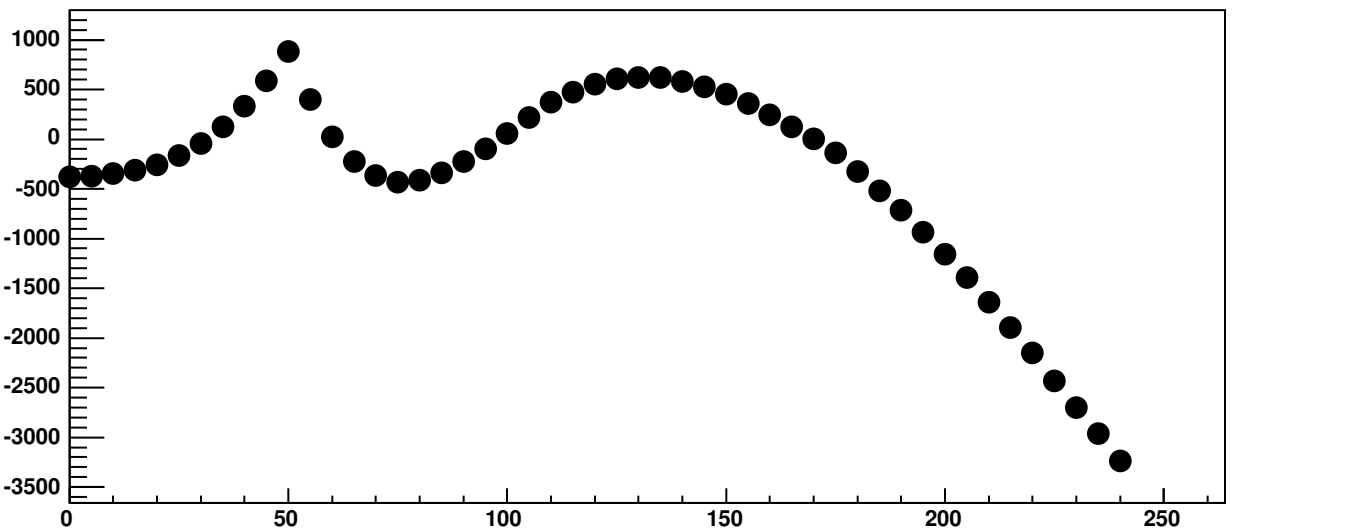
Chip 7, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold



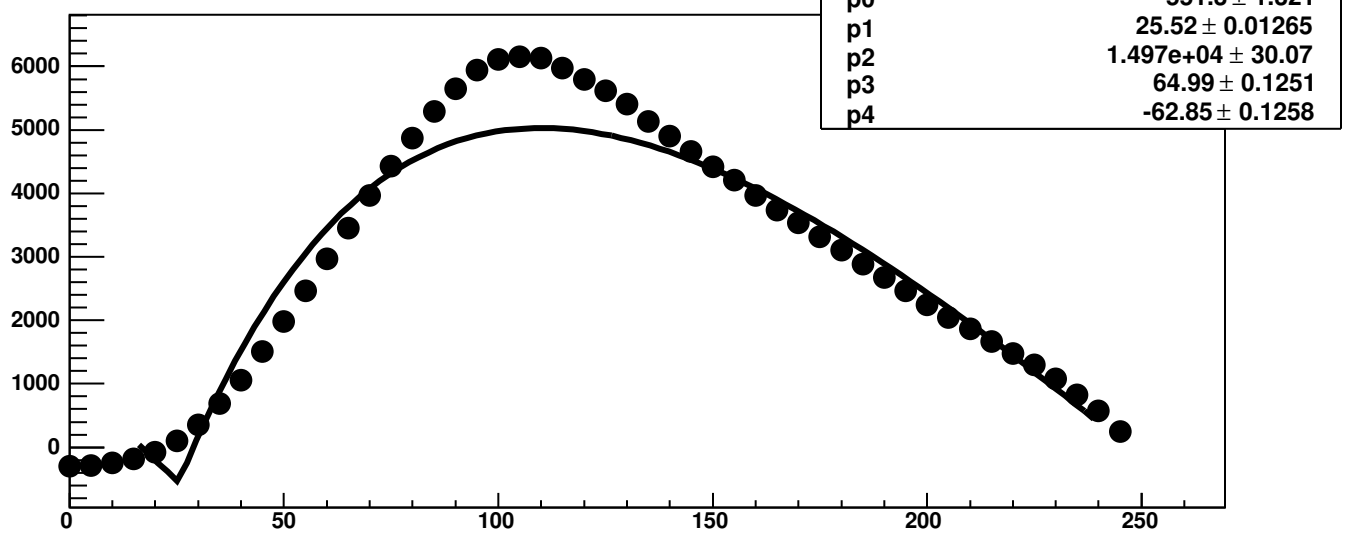
Chip 7, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



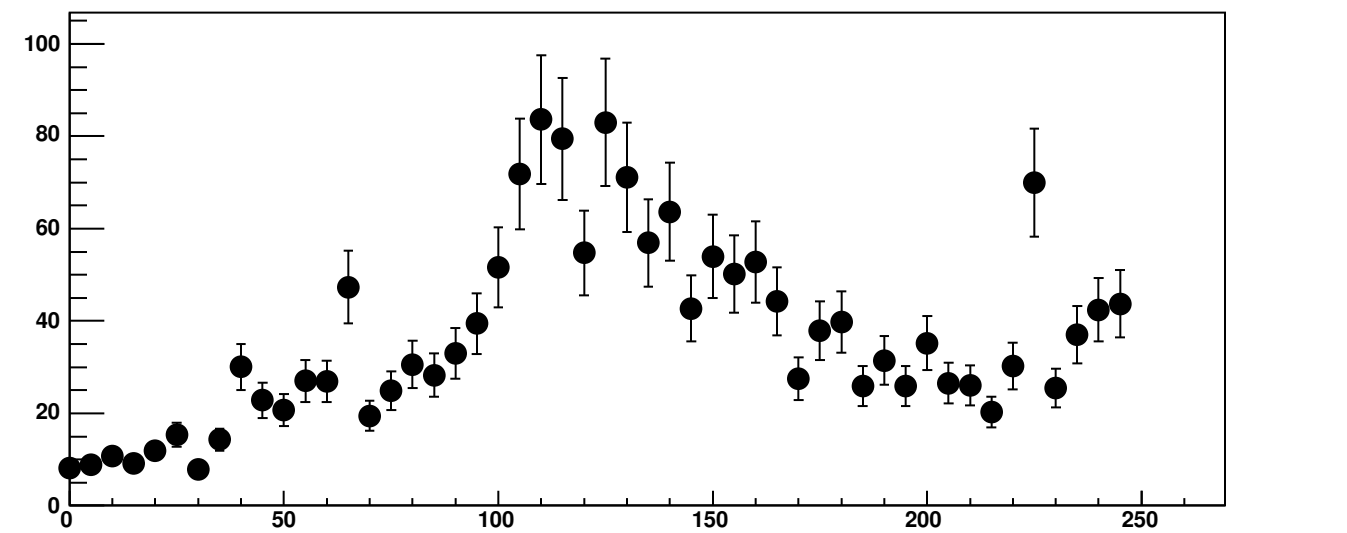
Chip 7, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold



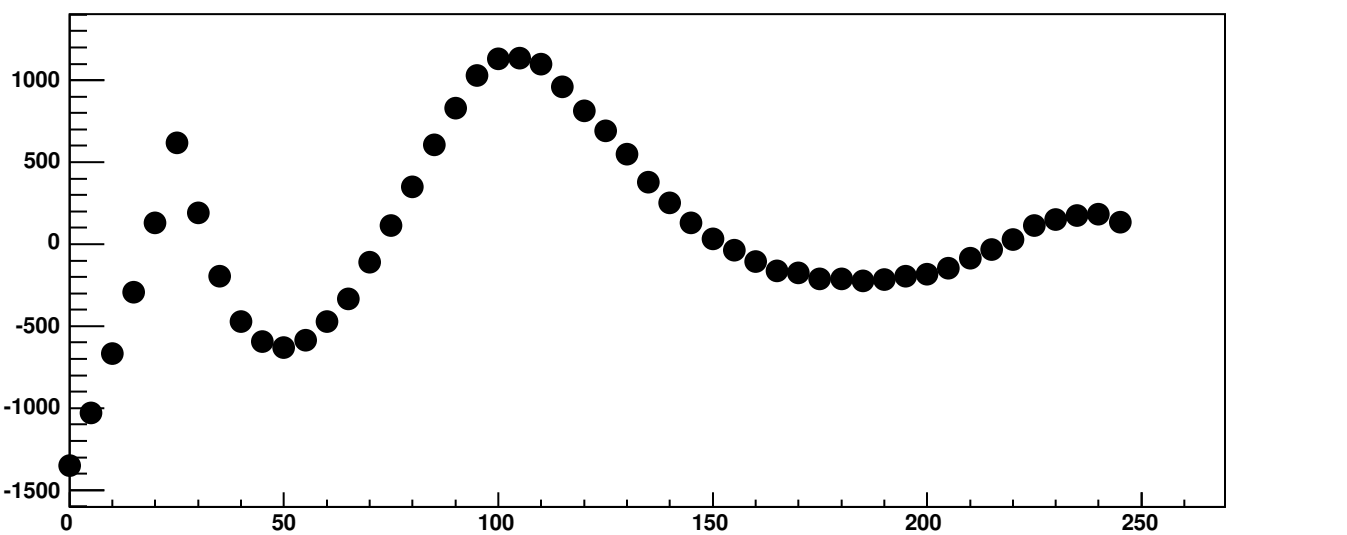
Chip 7, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold



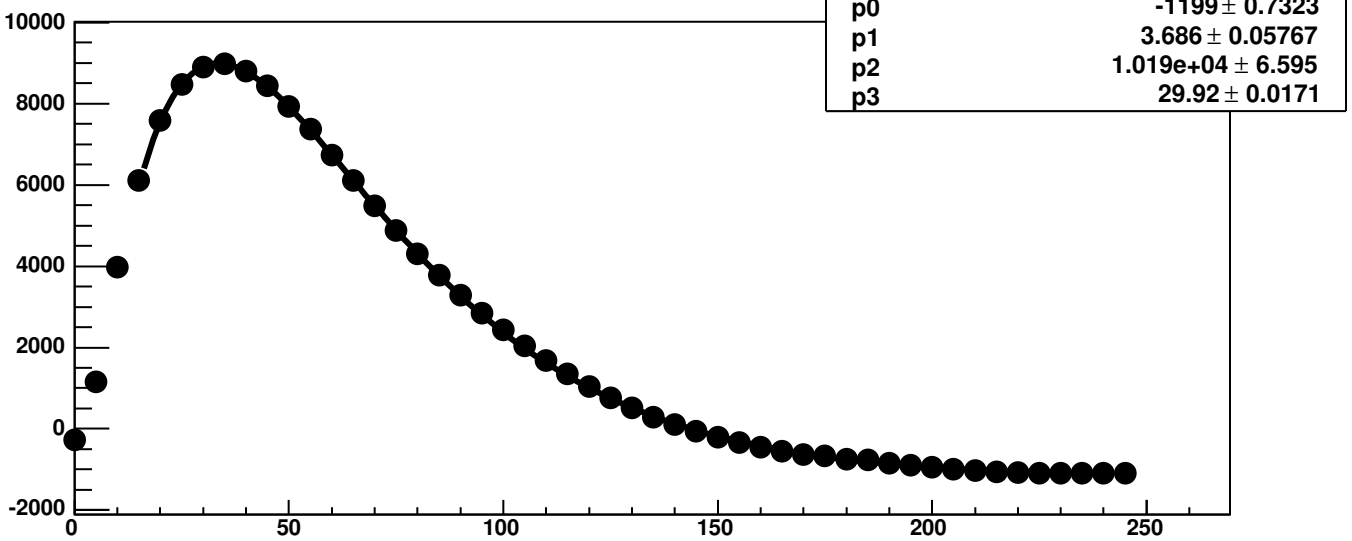
Chip 7, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



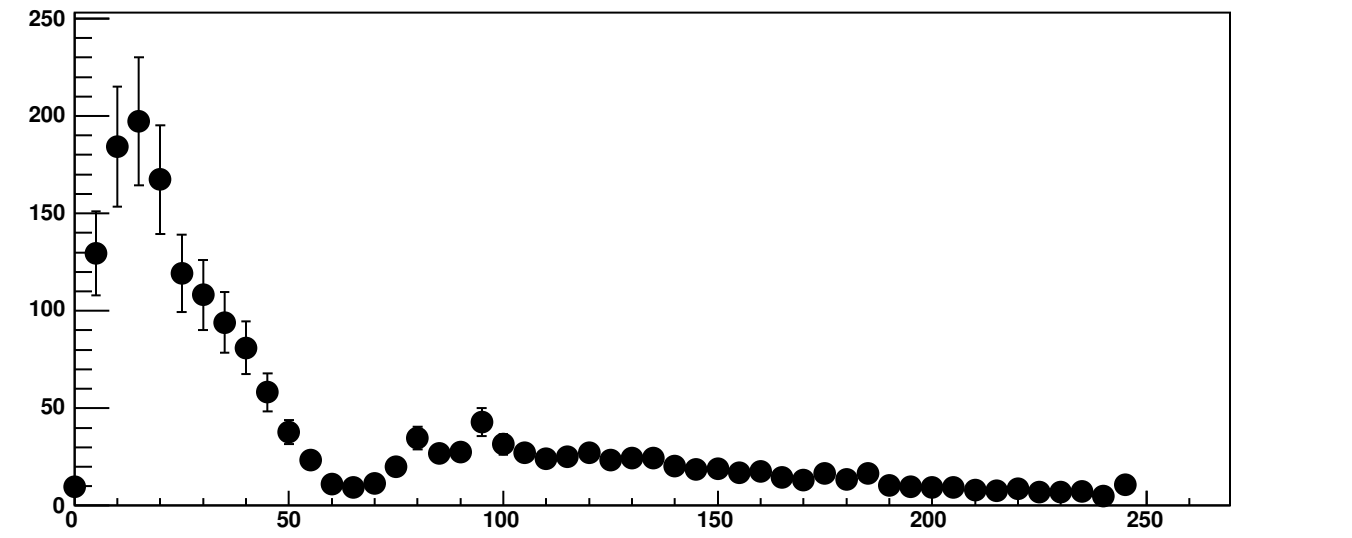
Chip 7, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold



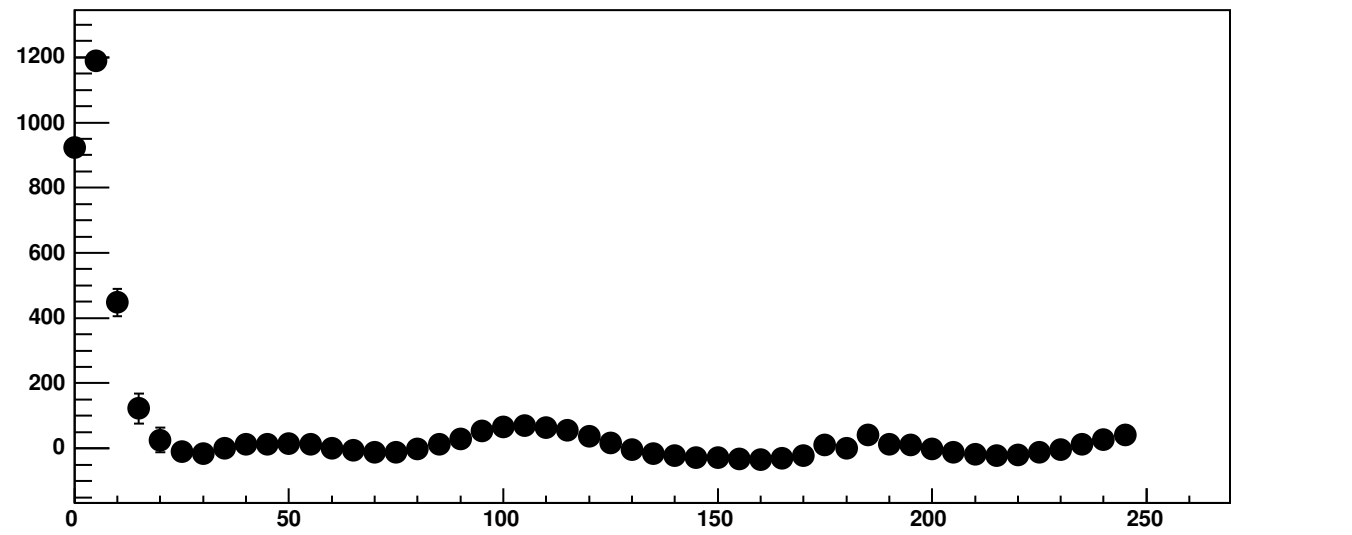
Chip 7, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold



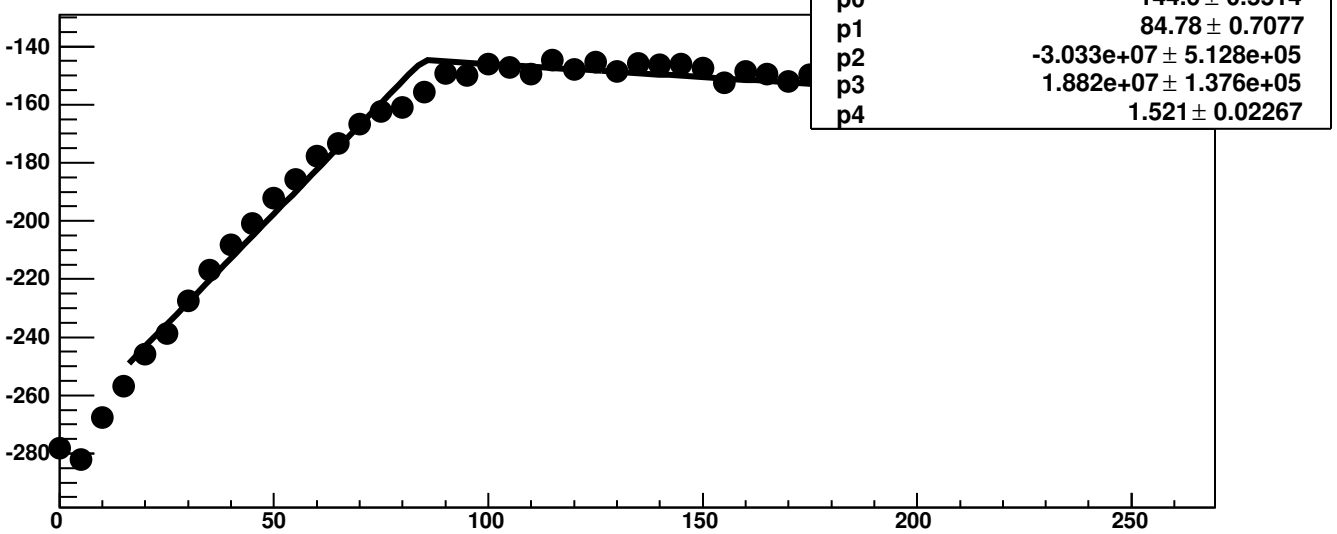
Chip 7, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold



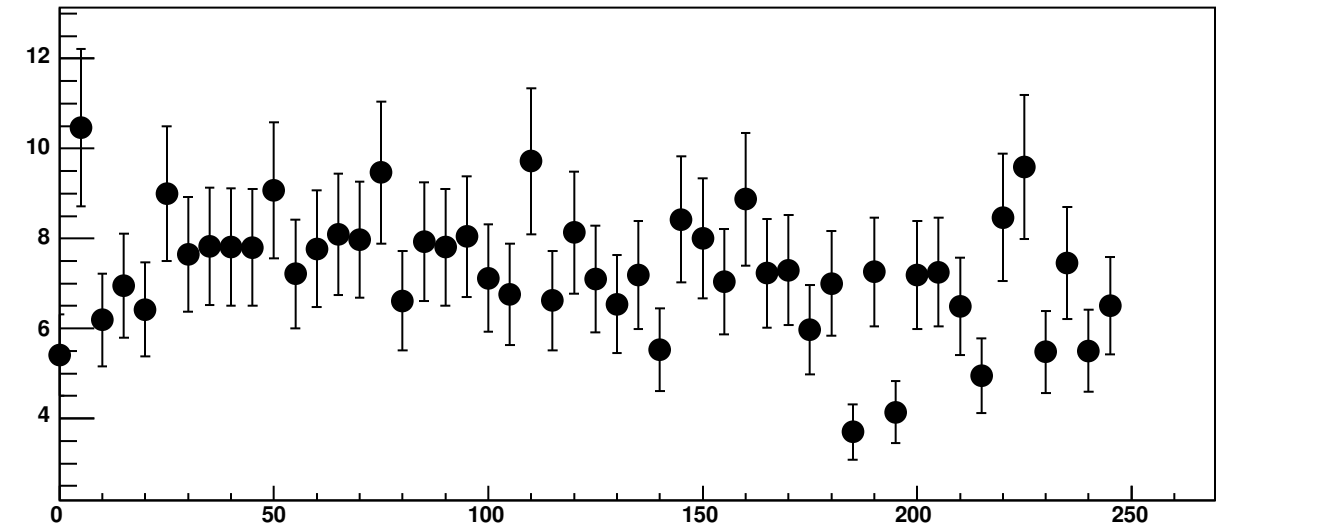
Chip 7, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold



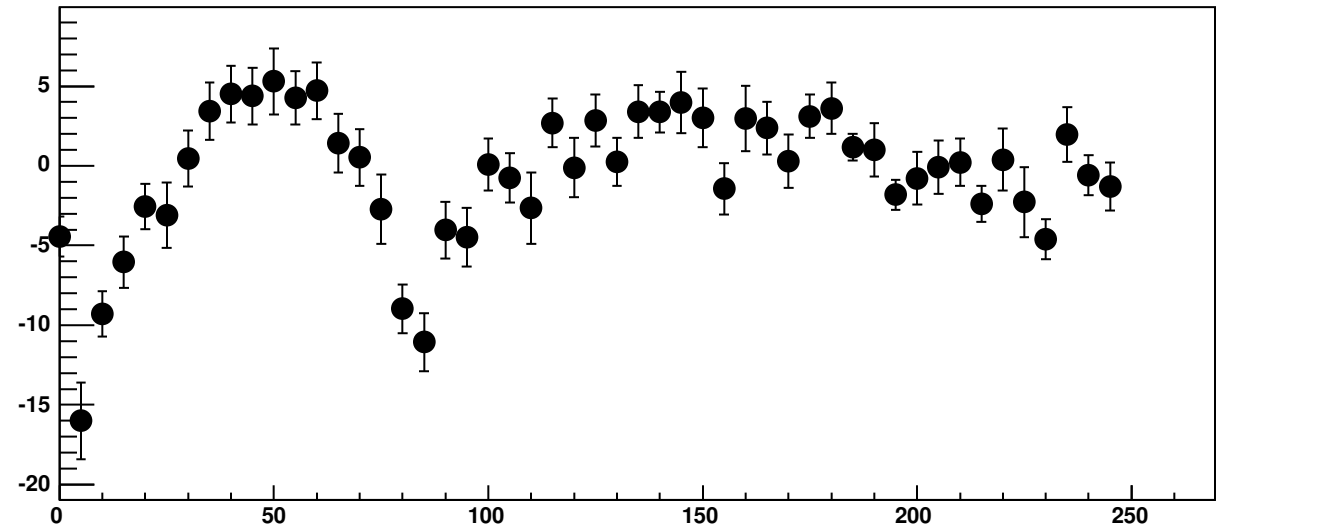
Chip 7, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

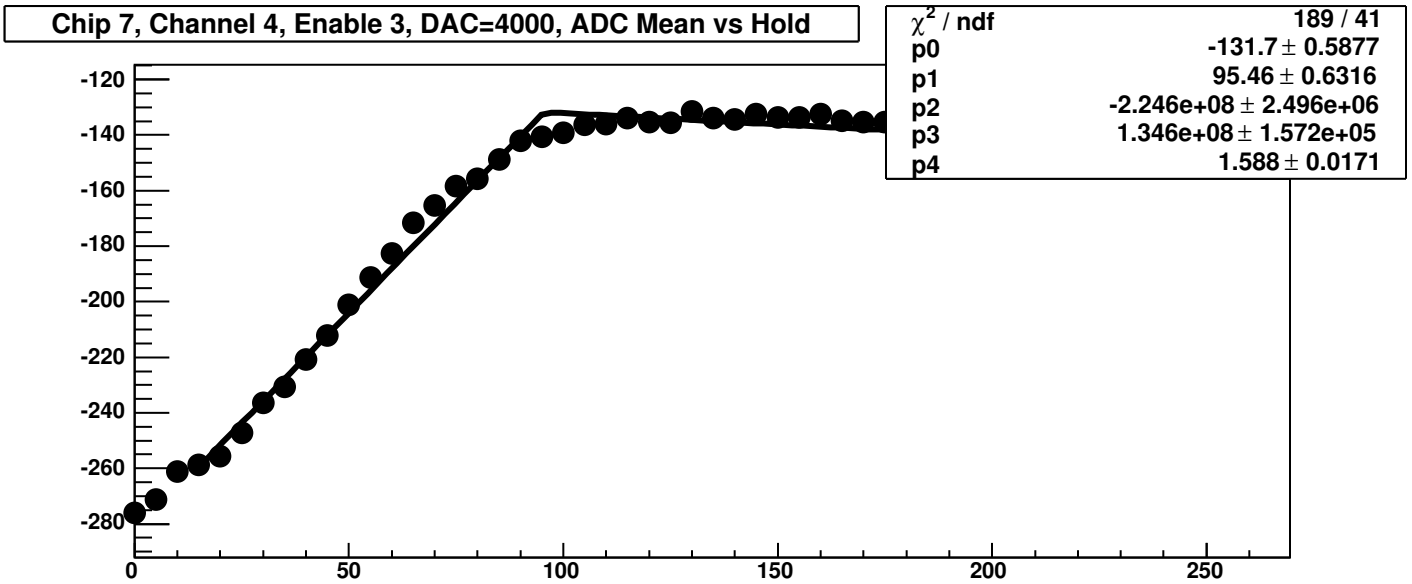


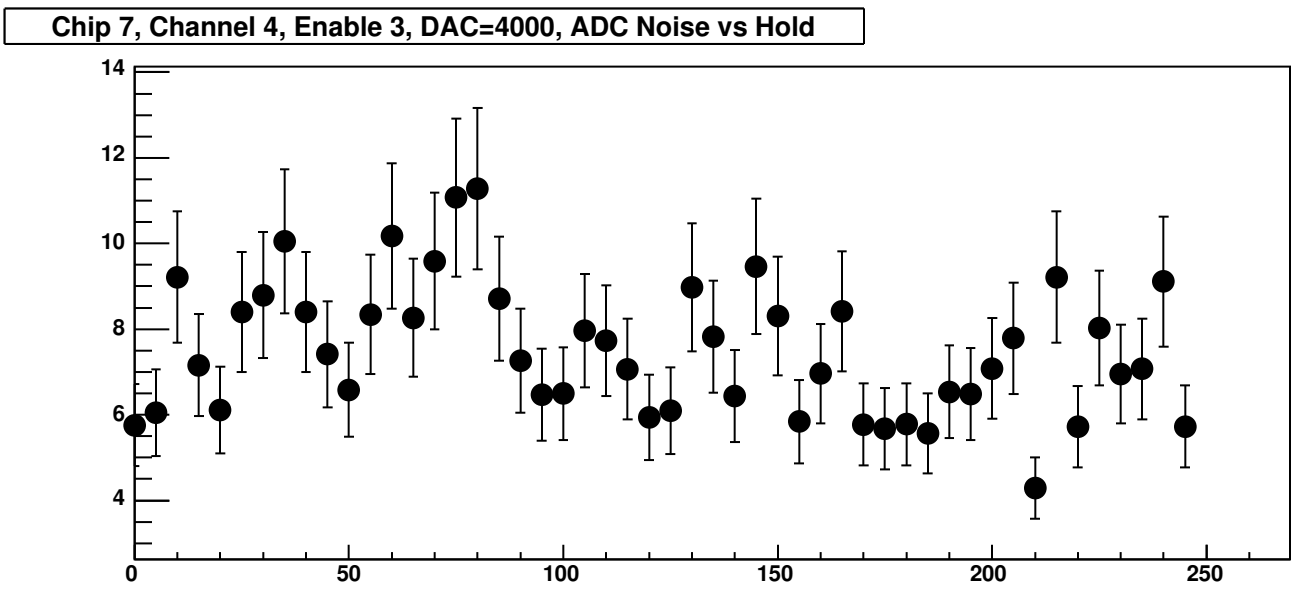
Chip 7, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold

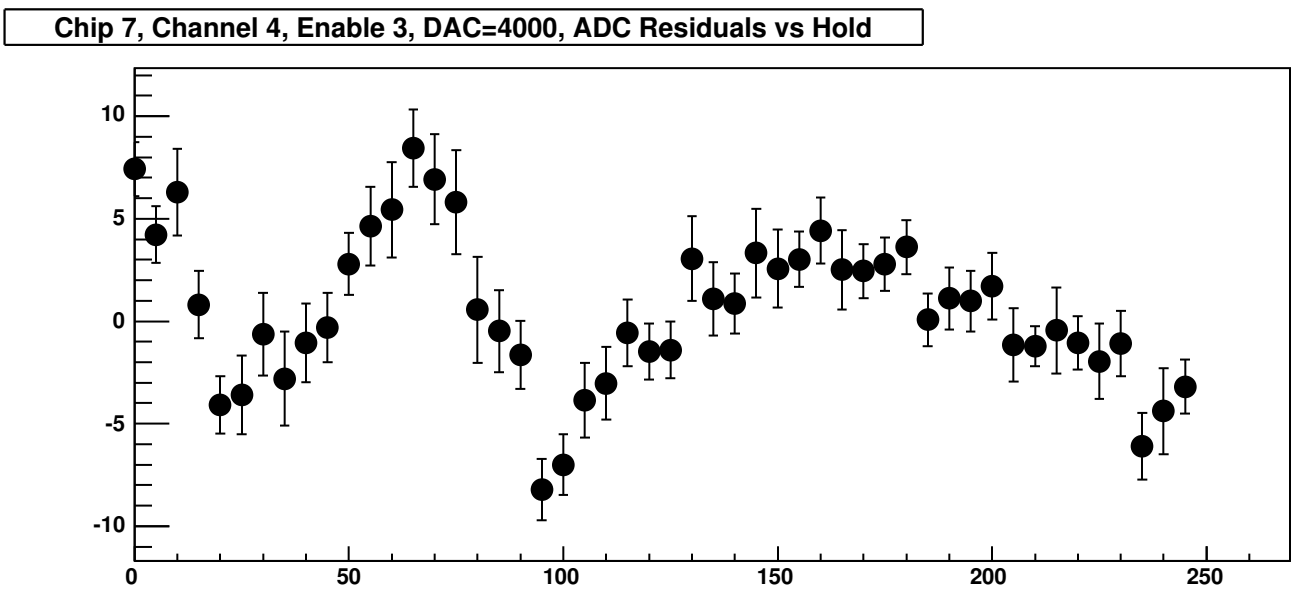


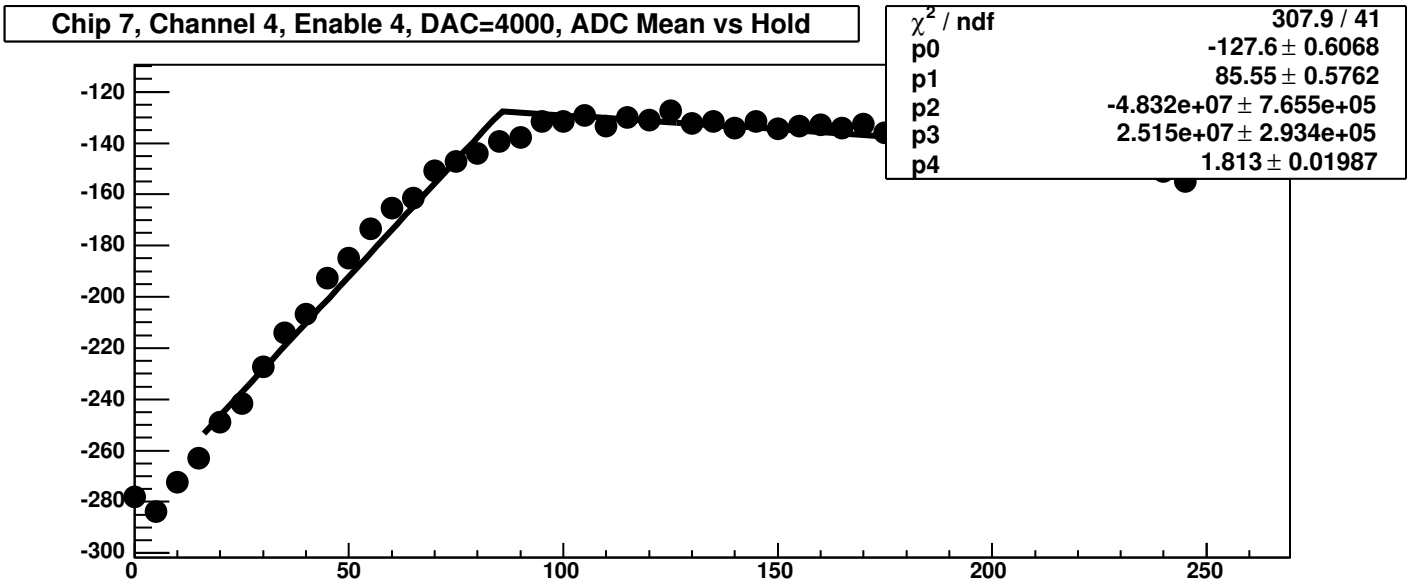
Chip 7, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold

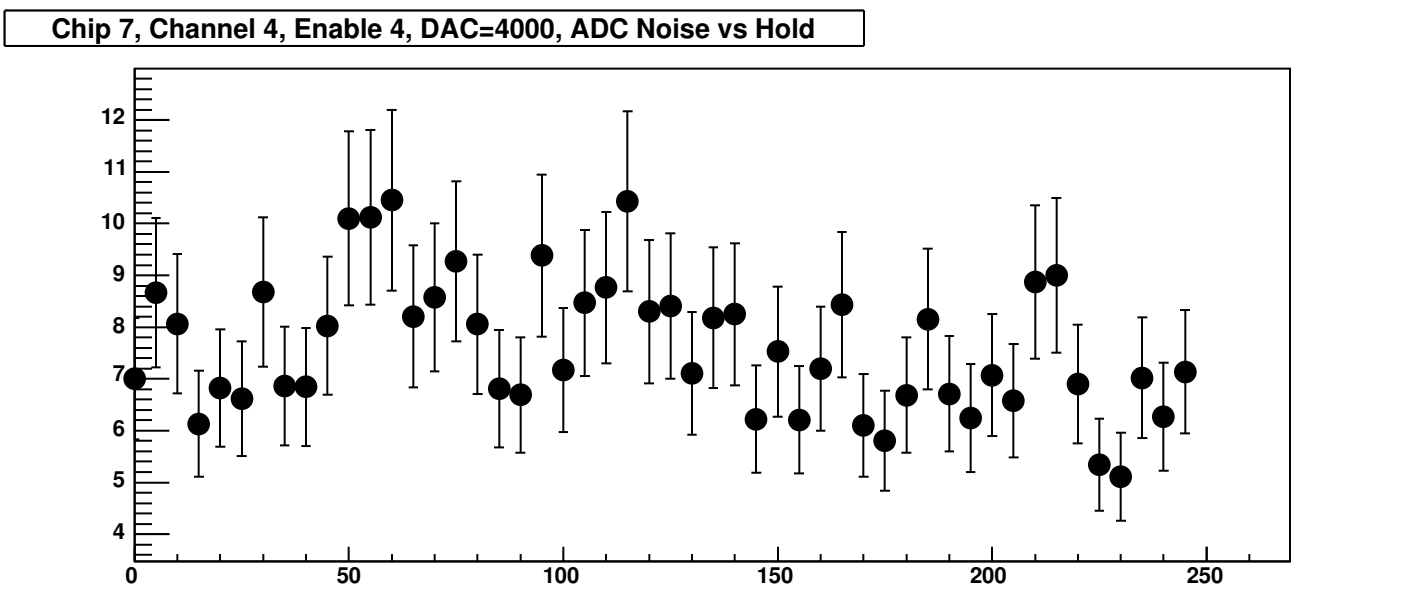


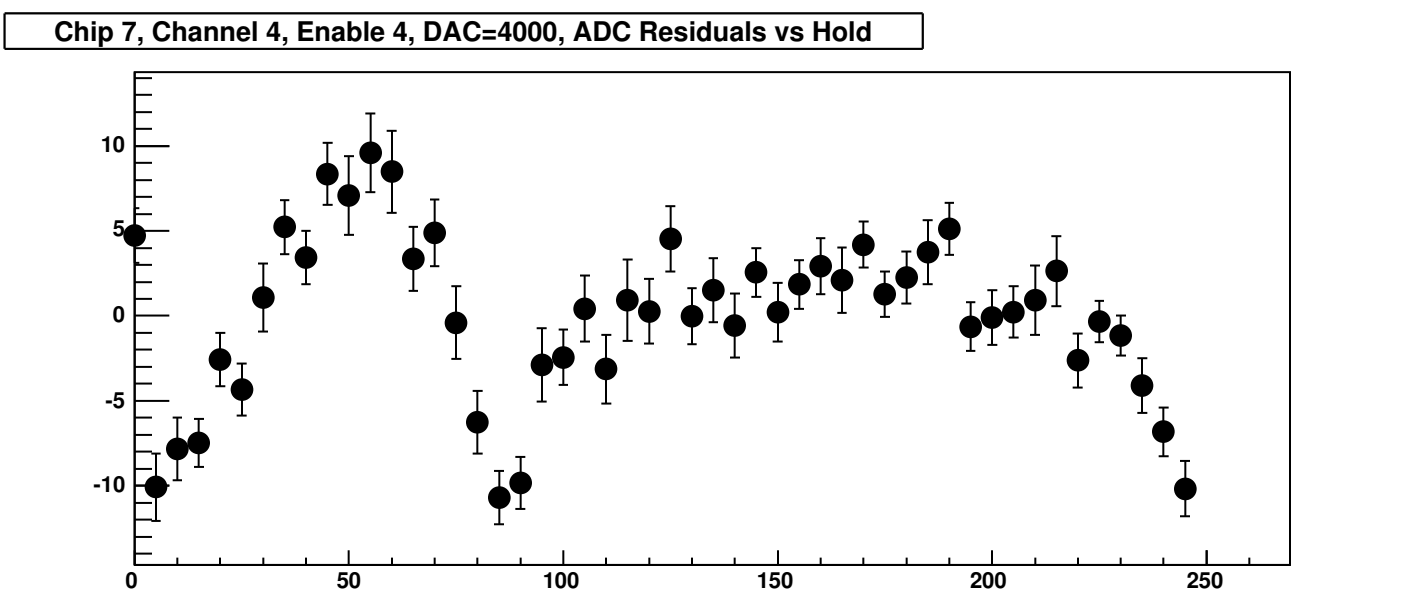

Chip 7, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 189 / 41
p0 -131.7 ± 0.5877
p1 95.46 ± 0.6316
p2 -2.246e+08 ± 2.496e+06
p3 1.346e+08 ± 1.572e+05
p4 1.588 ± 0.0171
-120
-140
-160
-180
-200
-220
-240
-260
-280
0
50
100
150
200
250

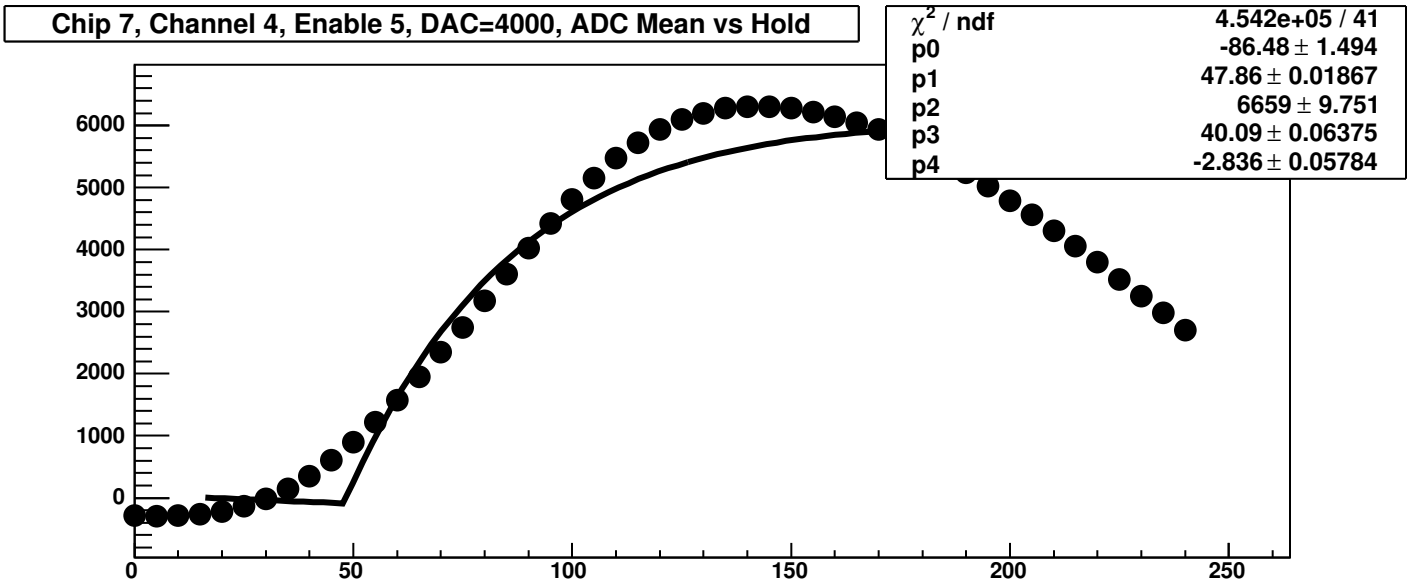

Chip 7, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

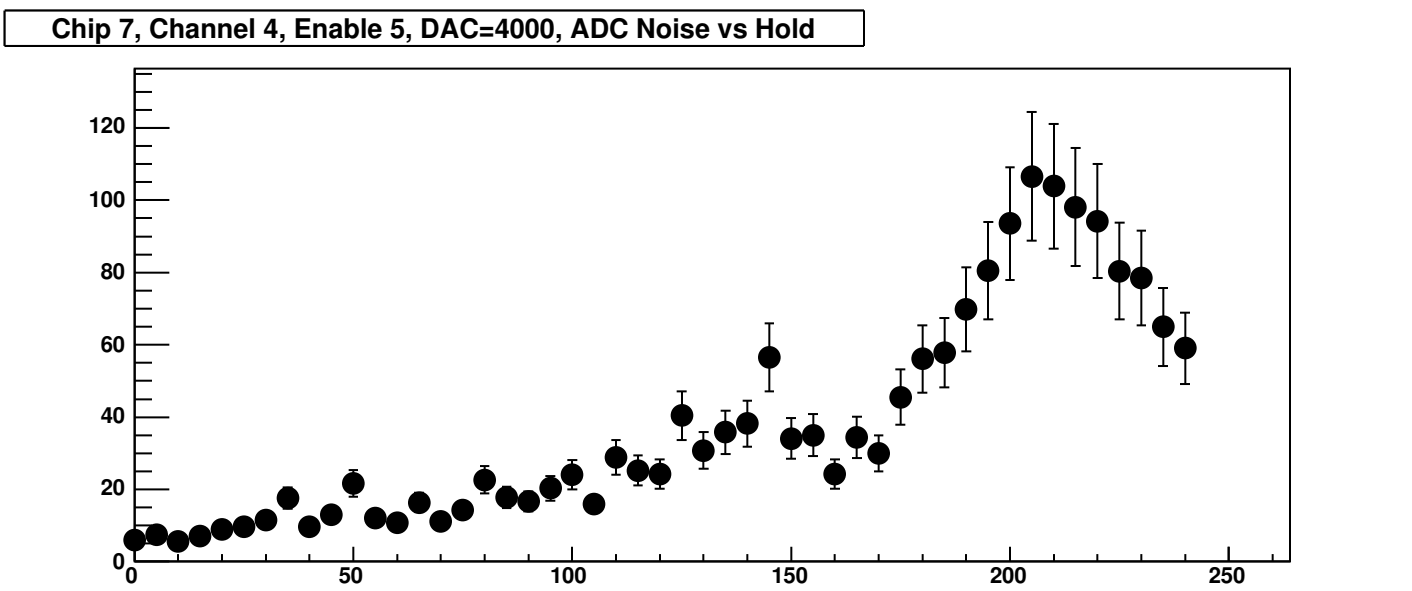

Chip 7, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250

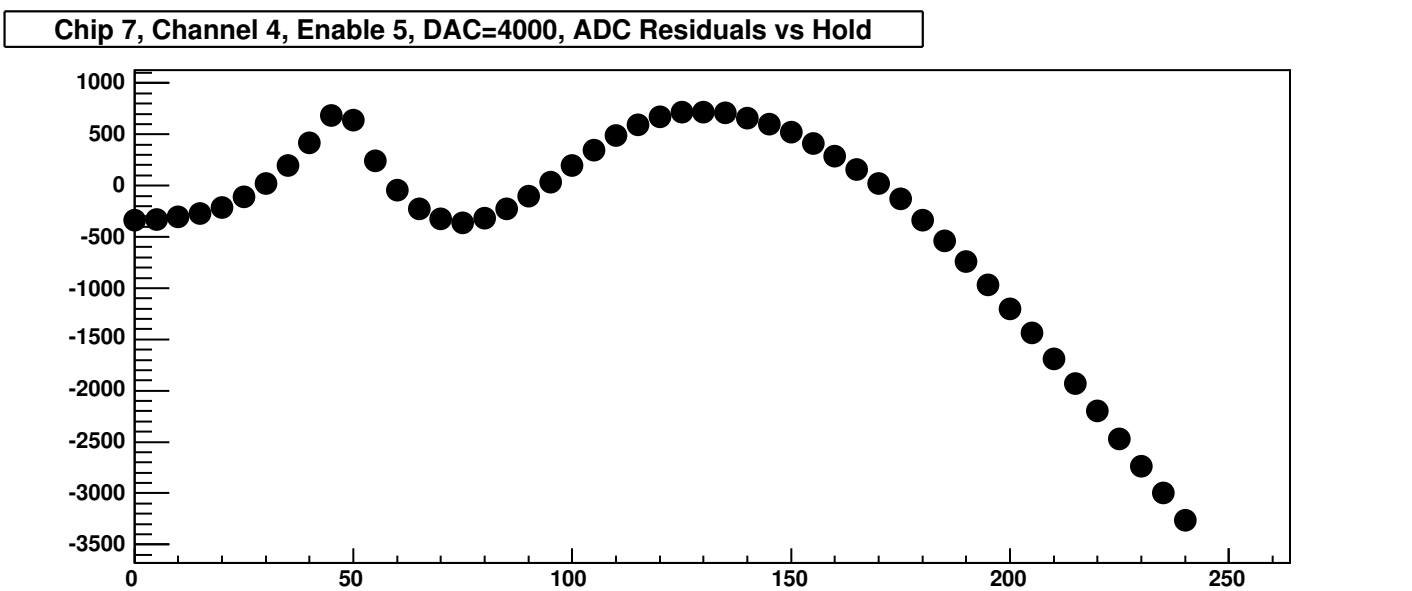

Chip 7, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 307.9 / 41
p0 -127.6 ± 0.6068
p1 85.55 ± 0.5762
p2 -4.832e+07 ± 7.655e+05
p3 2.515e+07 ± 2.934e+05
p4 1.813 ± 0.01987
-120
-140
-160
-180
-200
-220
-240
-260
-280
-300
0
50
100
150
200
250

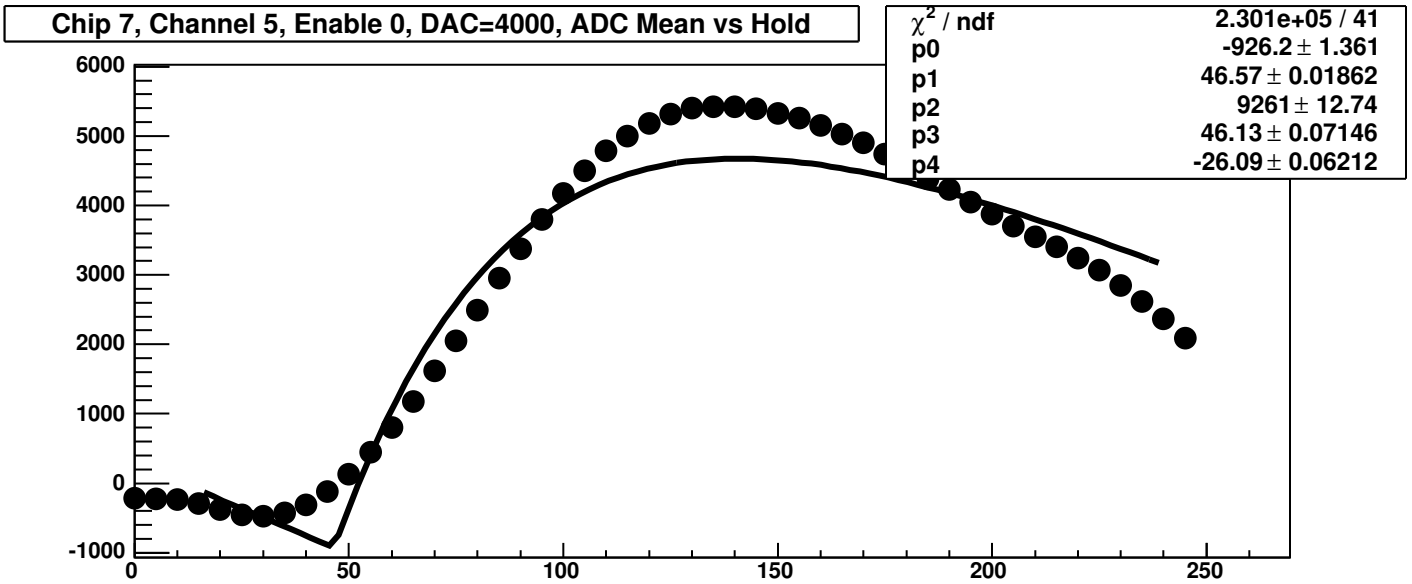

Chip 7, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

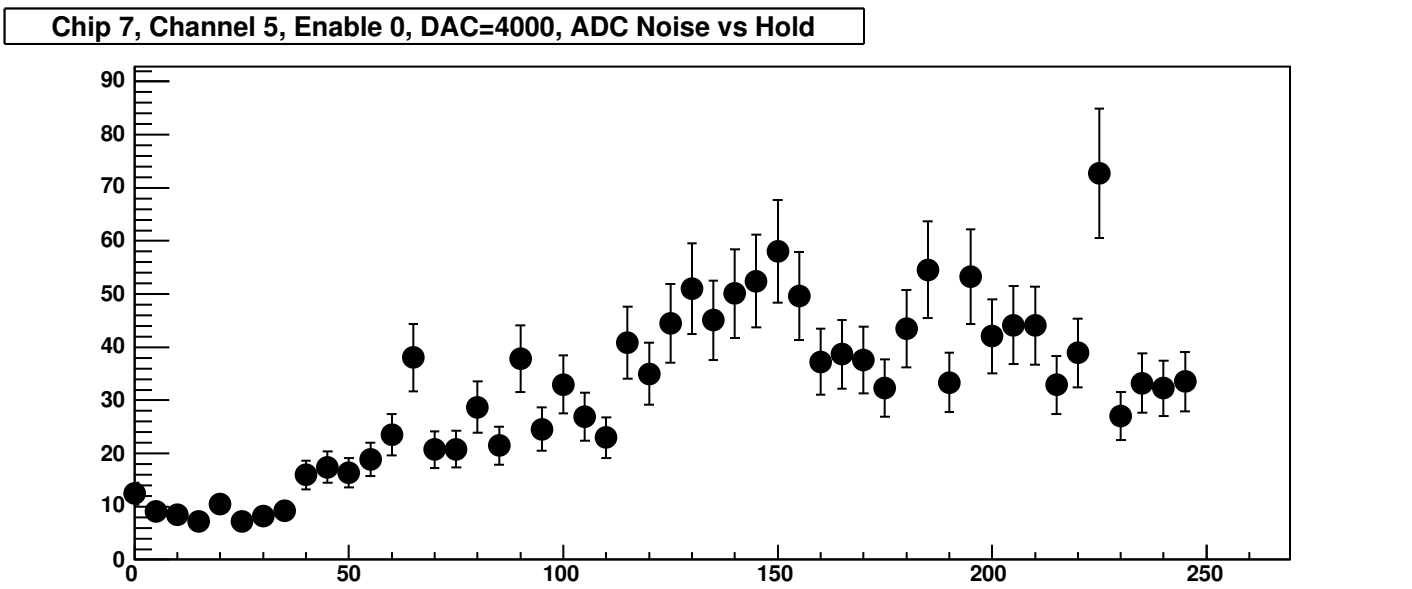

Chip 7, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250

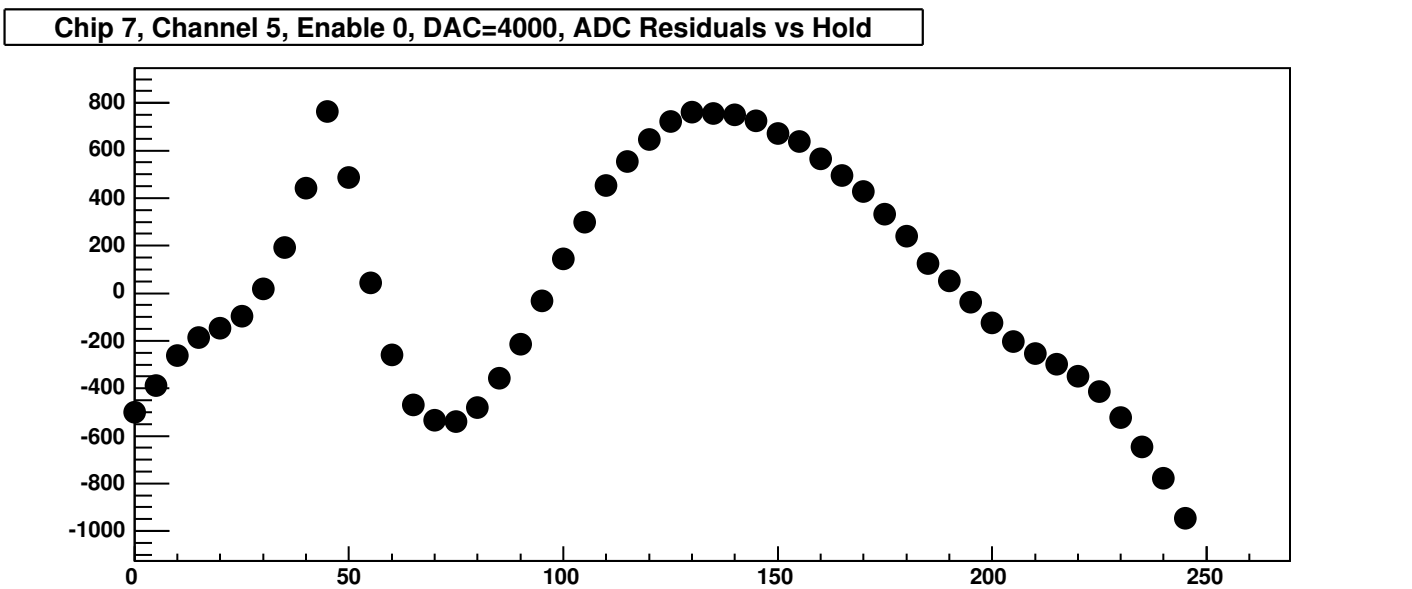

Chip 7, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.542e+05 / 41
p0 -86.48 ± 1.494
p1 47.86 ± 0.01867
p2 6659 ± 9.751
p3 40.09 ± 0.06375
p4 -2.836 ± 0.05784
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

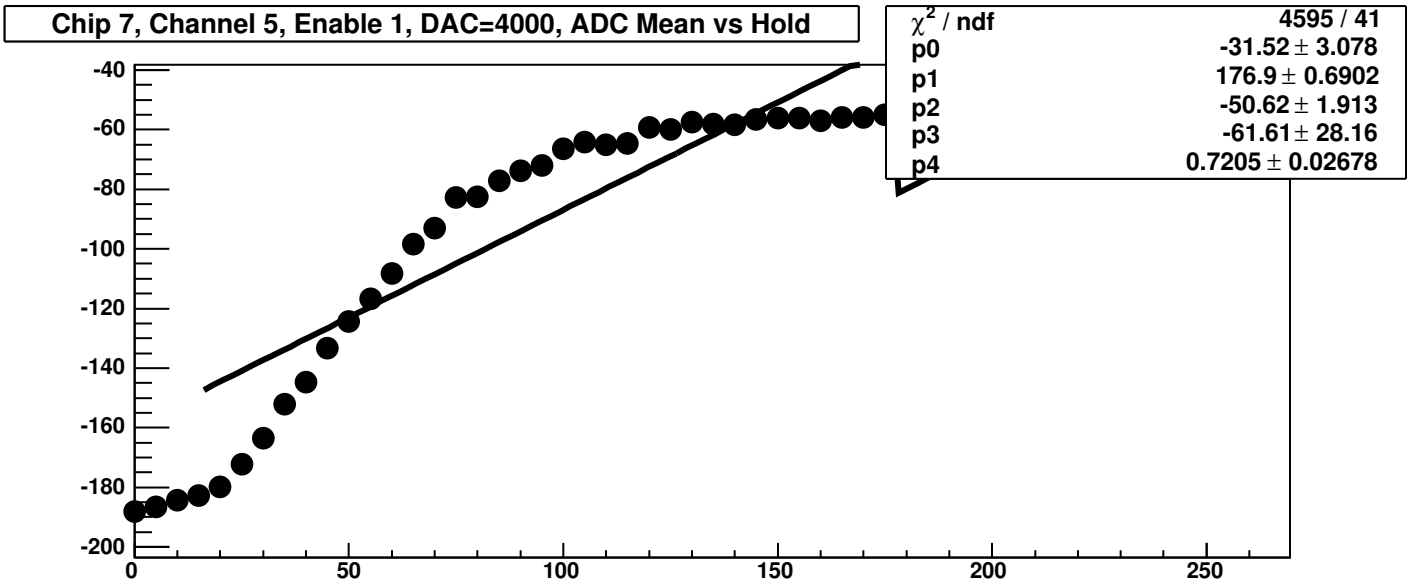

Chip 7, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250

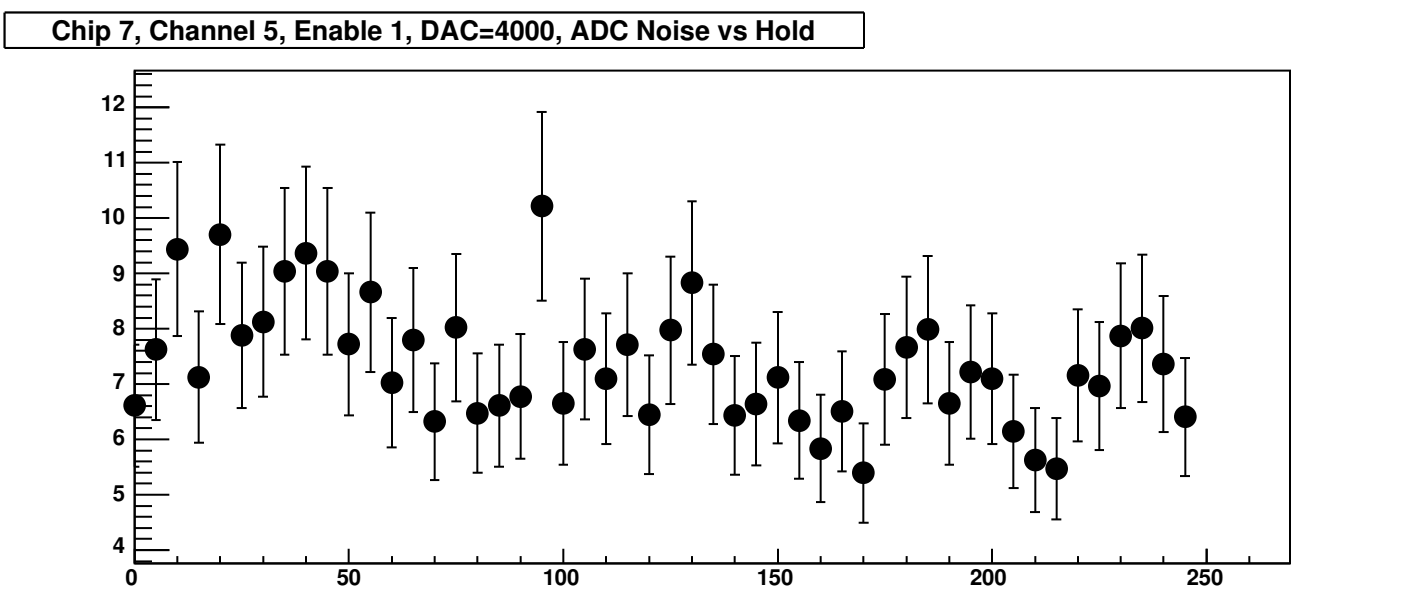

Chip 7, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
0
50
100
150
200
250

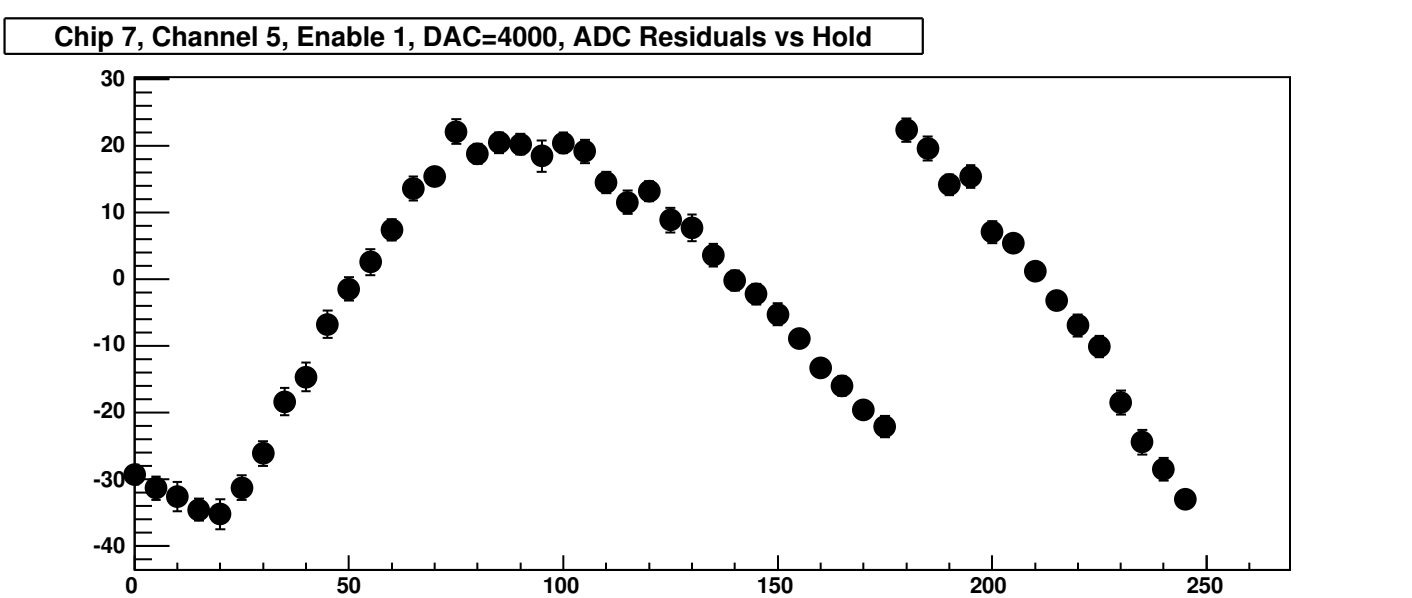

Chip 7, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.301e+05 / 41
p0 -926.2 ± 1.361
p1 46.57 ± 0.01862
p2 9261 ± 12.74
p3 46.13 ± 0.07146
p4 -26.09 ± 0.06212
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

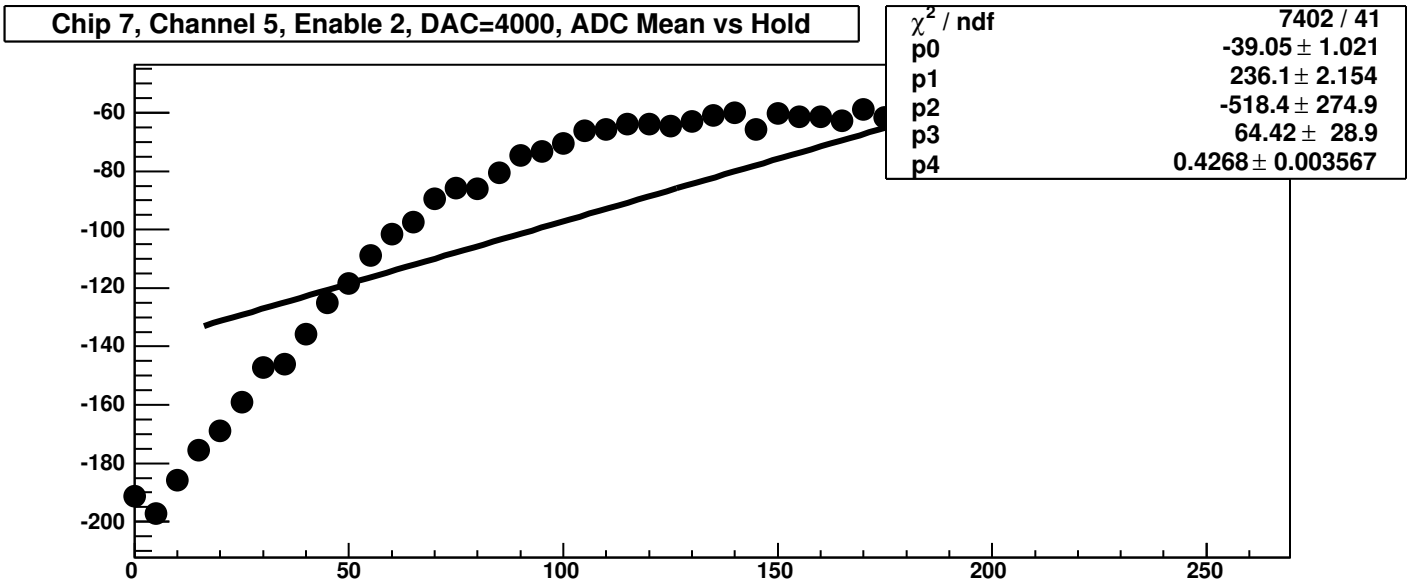

Chip 7, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold
90
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250

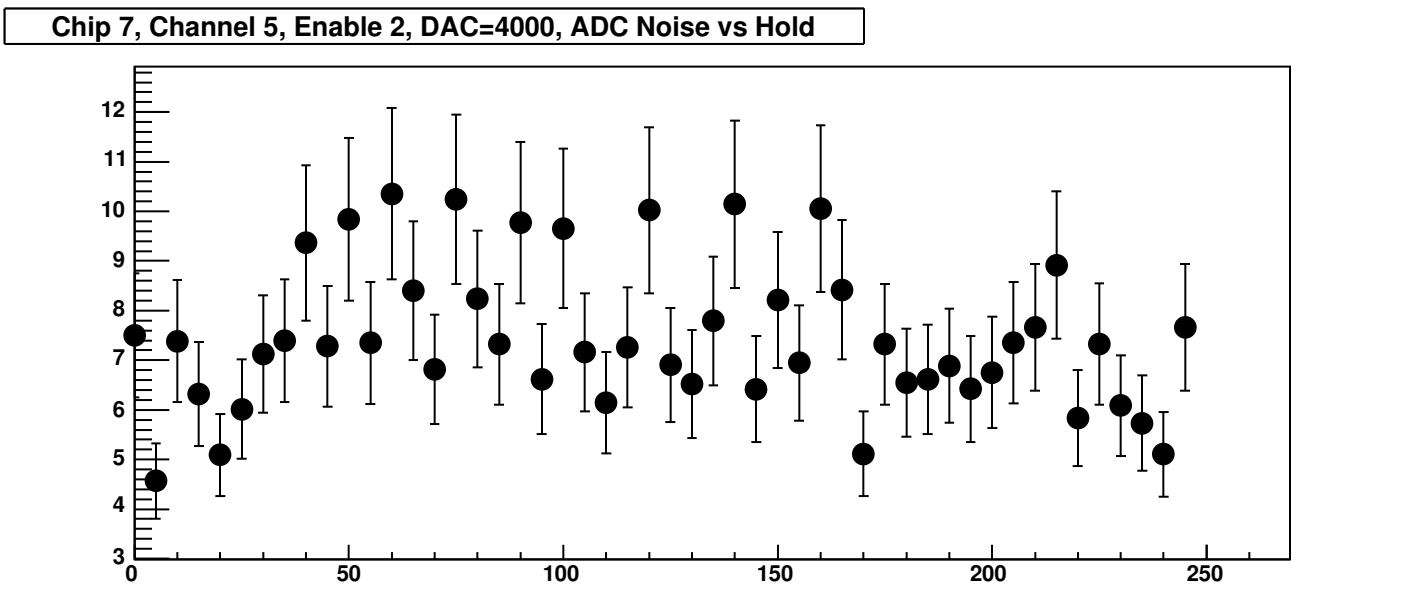

Chip 7, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold
800
600
400
200
0
-200
-400
-600
-800
-1000
0
50
100
150
200
250

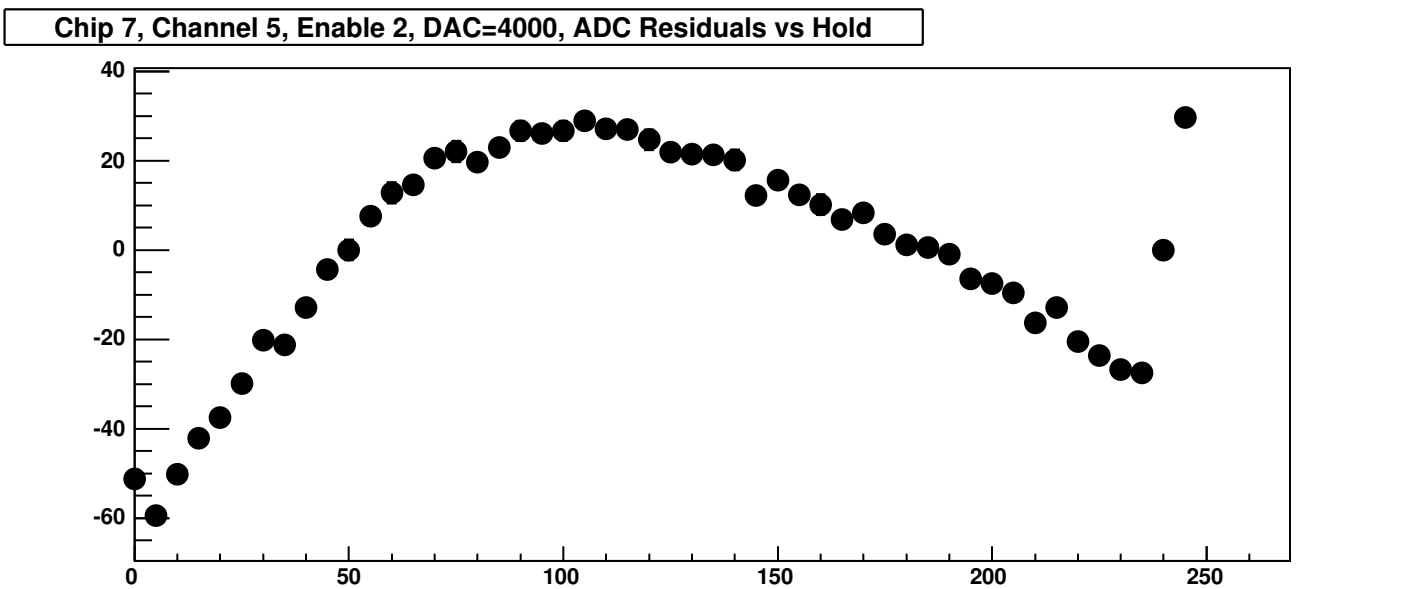

Chip 7, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 4595 / 41
p0 -31.52 ± 3.078
p1 176.9 ± 0.6902
p2 -50.62 ± 1.913
p3 -61.61 ± 28.16
p4 0.7205 ± 0.02678
-40
-60
-80
-100
-120
-140
-160
-180
-200
0
50
100
150
200
250

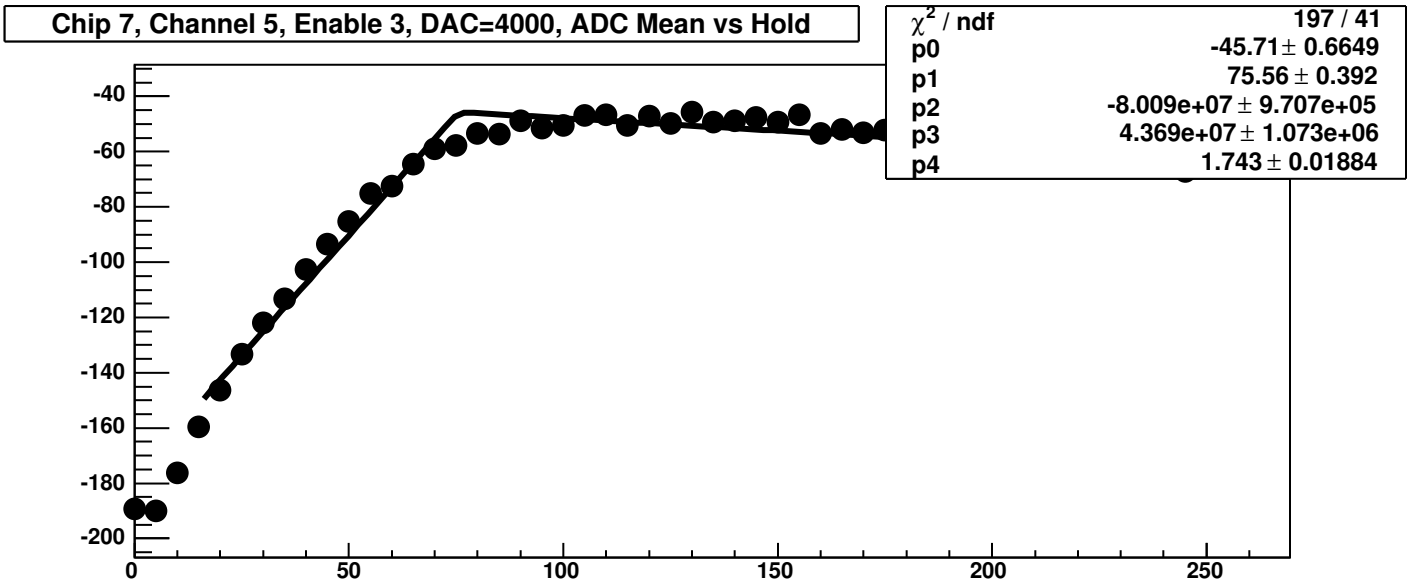

Chip 7, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

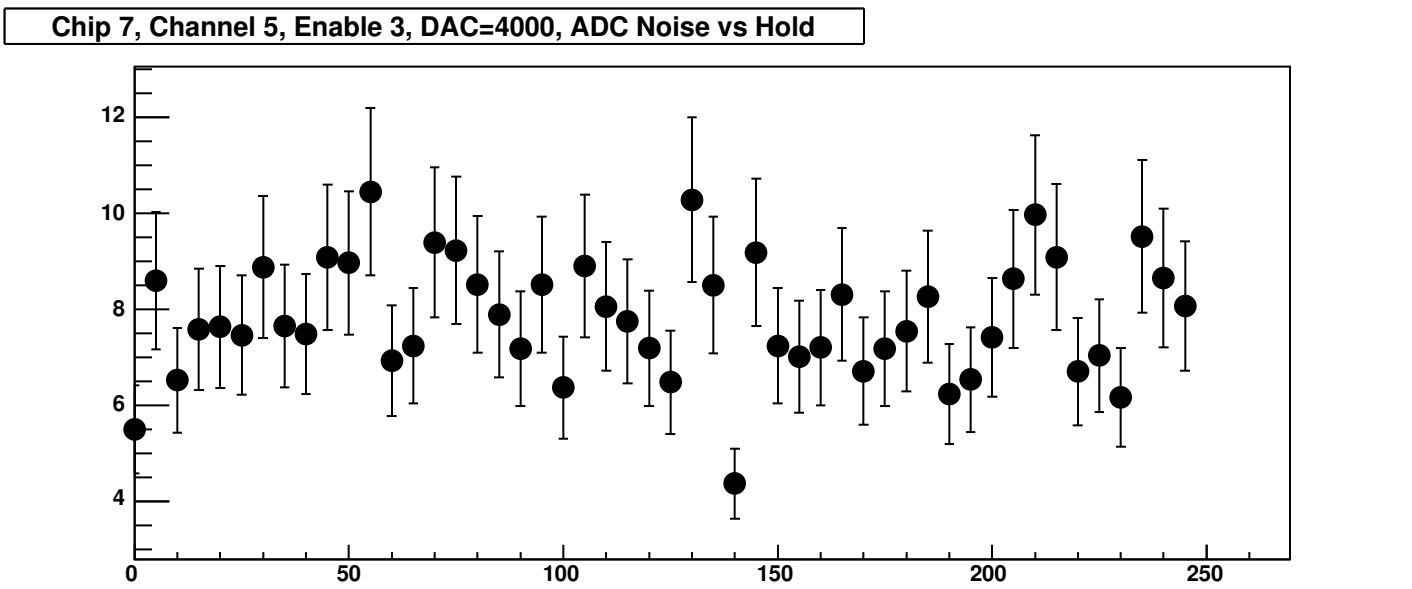

Chip 7, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
-20
-30
-40
0
50
100
150
200
250

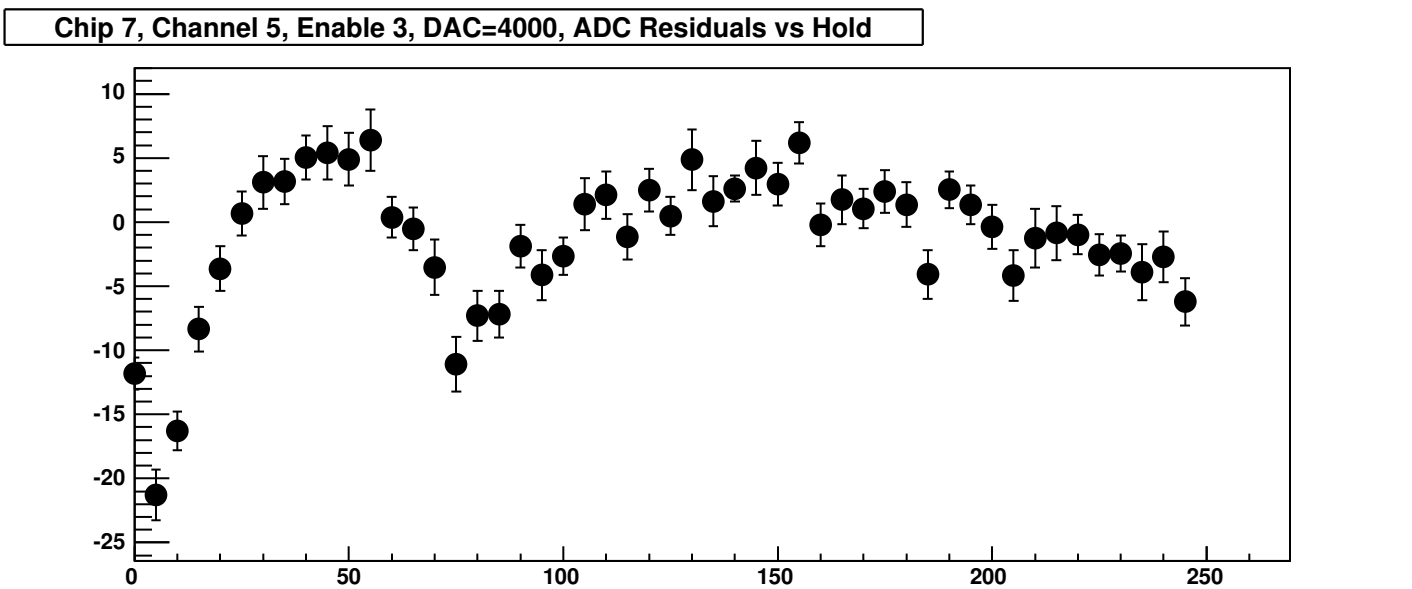

Chip 7, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 7402 / 41
p0 -39.05 ± 1.021
p1 236.1 ± 2.154
p2 -518.4 ± 274.9
p3 64.42 ± 28.9
p4 0.4268 ± 0.003567
-60
-80
-100
-120
-140
-160
-180
-200
0
50
100
150
200
250


Chip 7, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

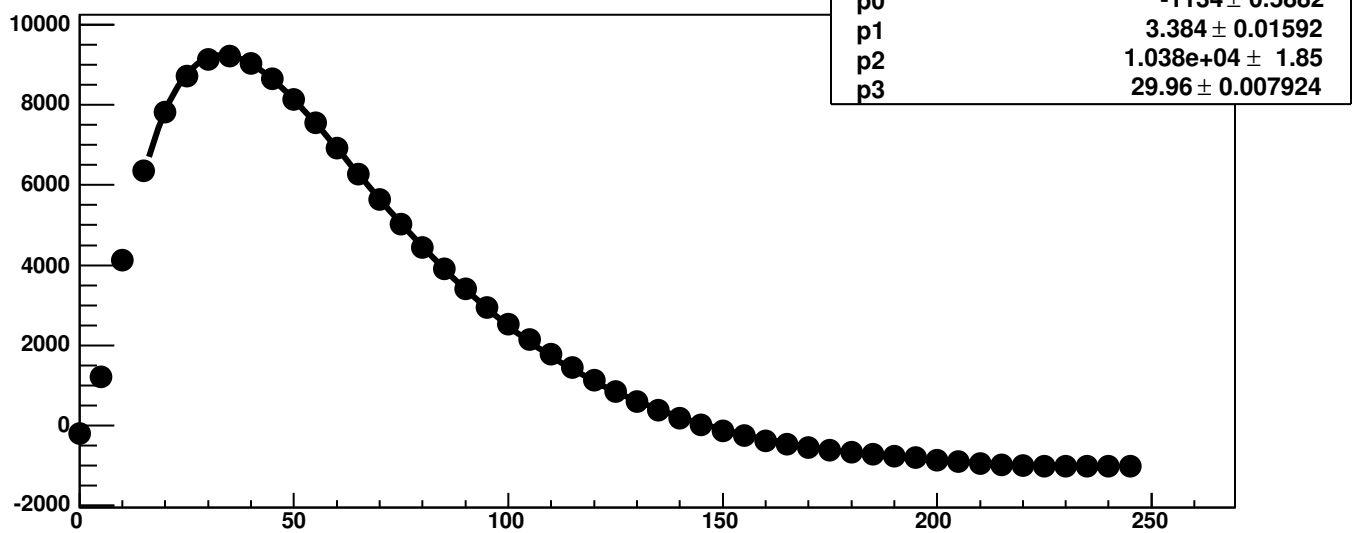

Chip 7, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
0
50
100
150
200
250


Chip 7, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 197 / 41
p0 -45.71 ± 0.6649
p1 75.56 ± 0.392
p2 -8.009e+07 ± 9.707e+05
p3 4.369e+07 ± 1.073e+06
p4 1.743 ± 0.01884
-40
-60
-80
-100
-120
-140
-160
-180
-200
0
50
100
150
200
250

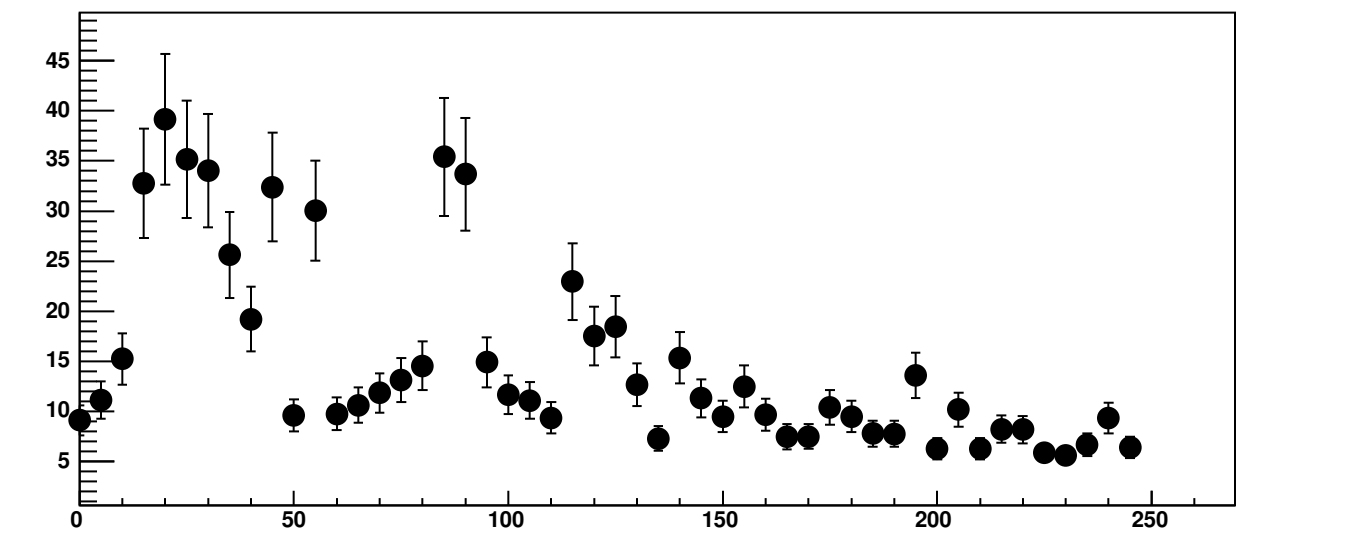

Chip 7, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 7, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
-20
-25
0
50
100
150
200
250

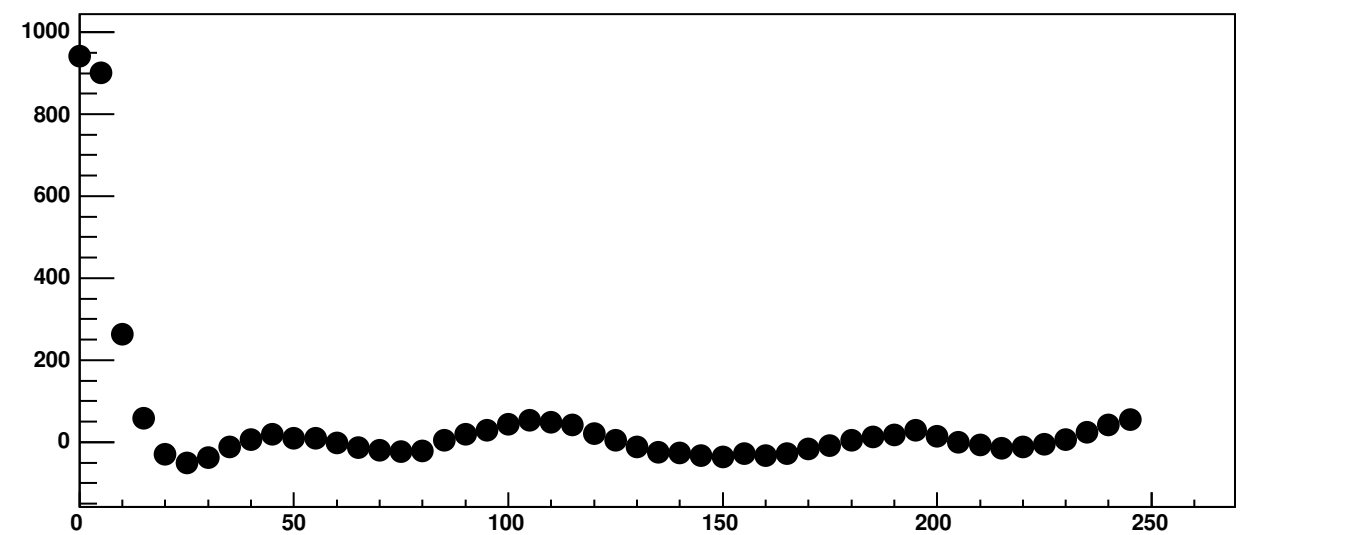
Chip 7, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold

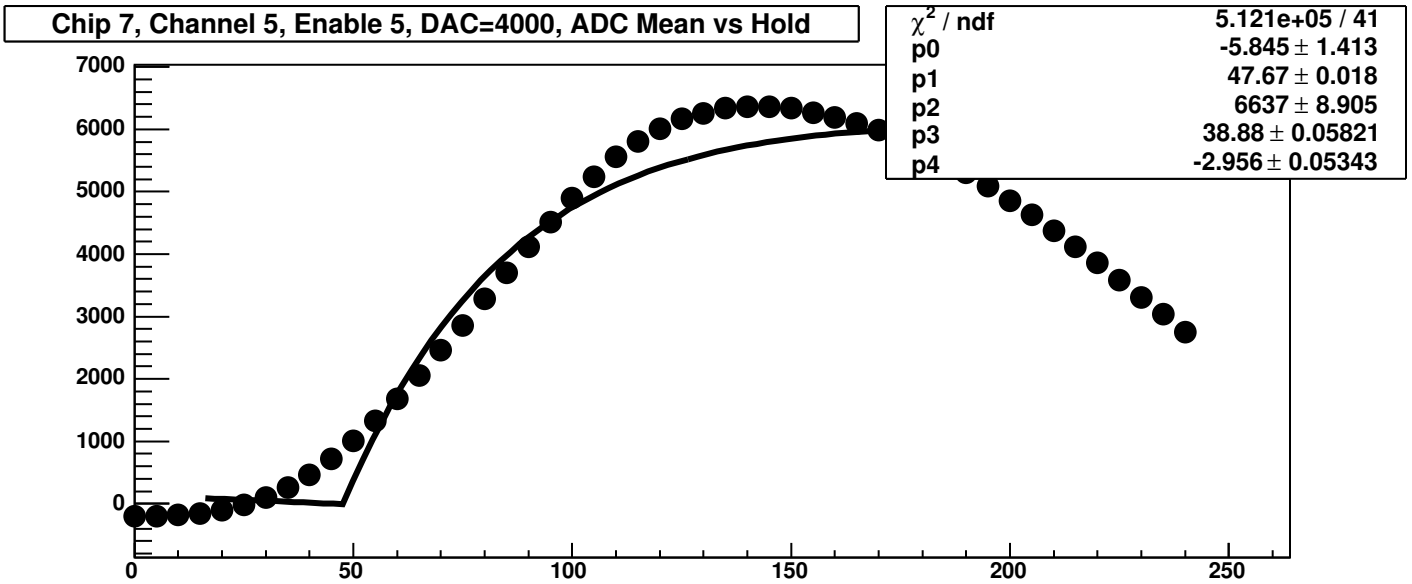


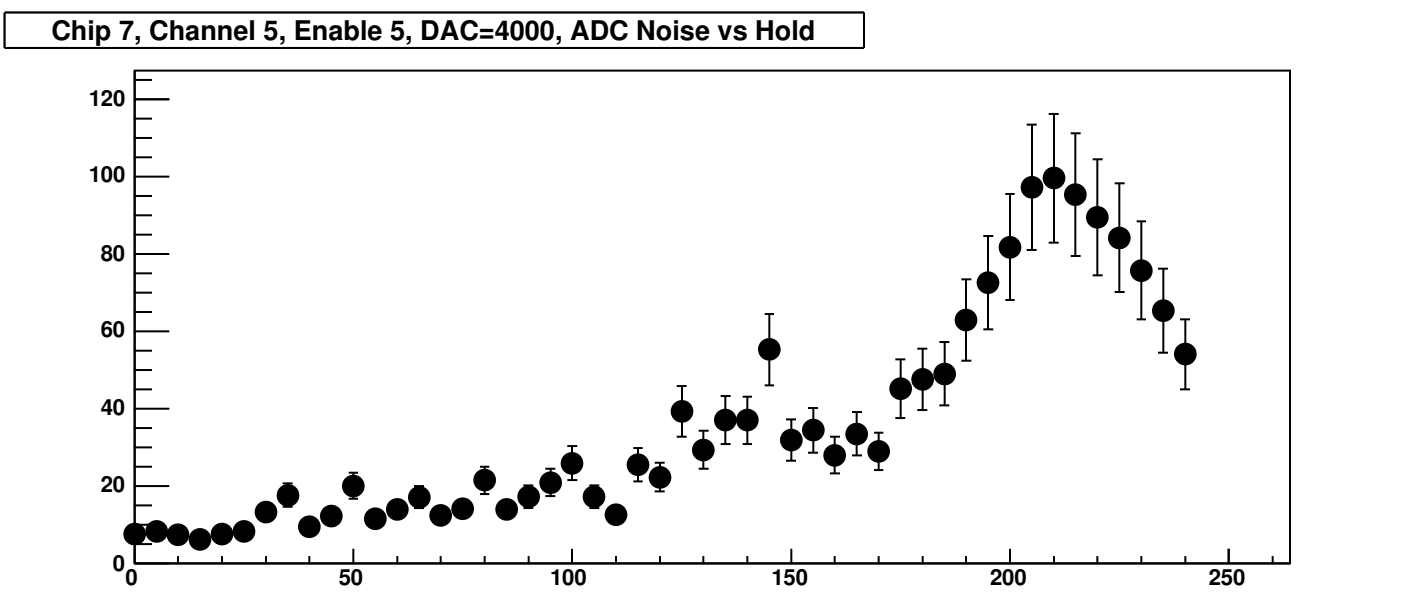
Chip 7, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold

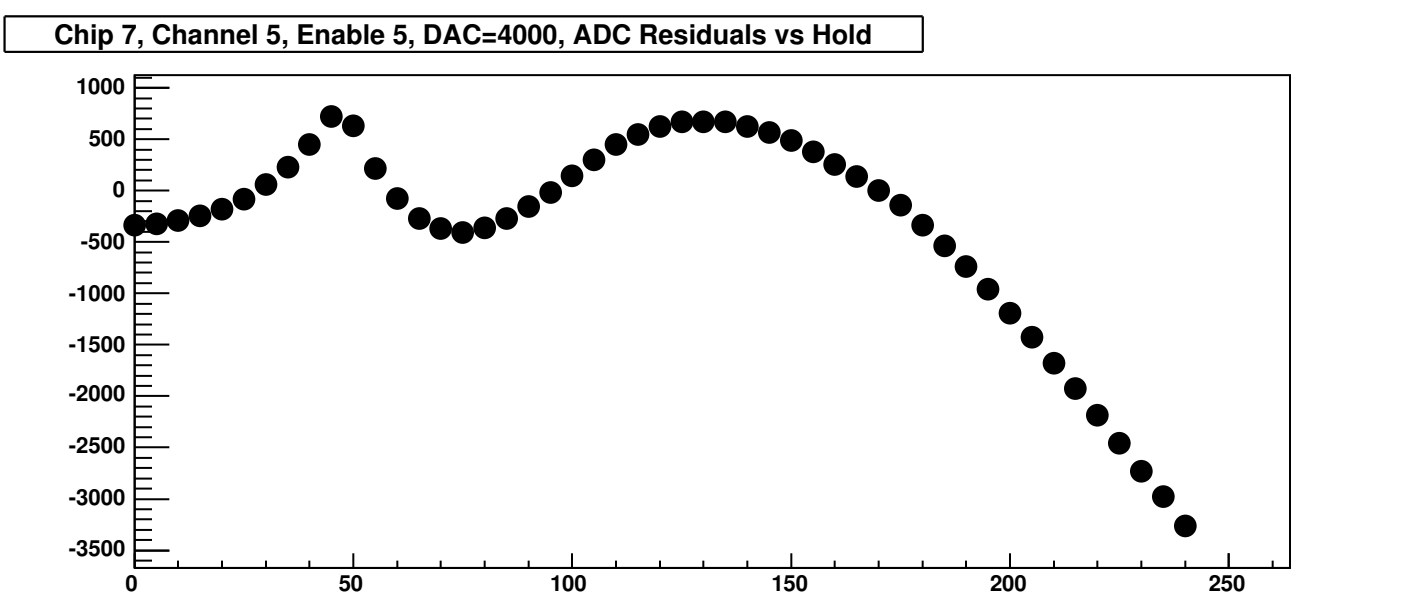


Chip 7, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold

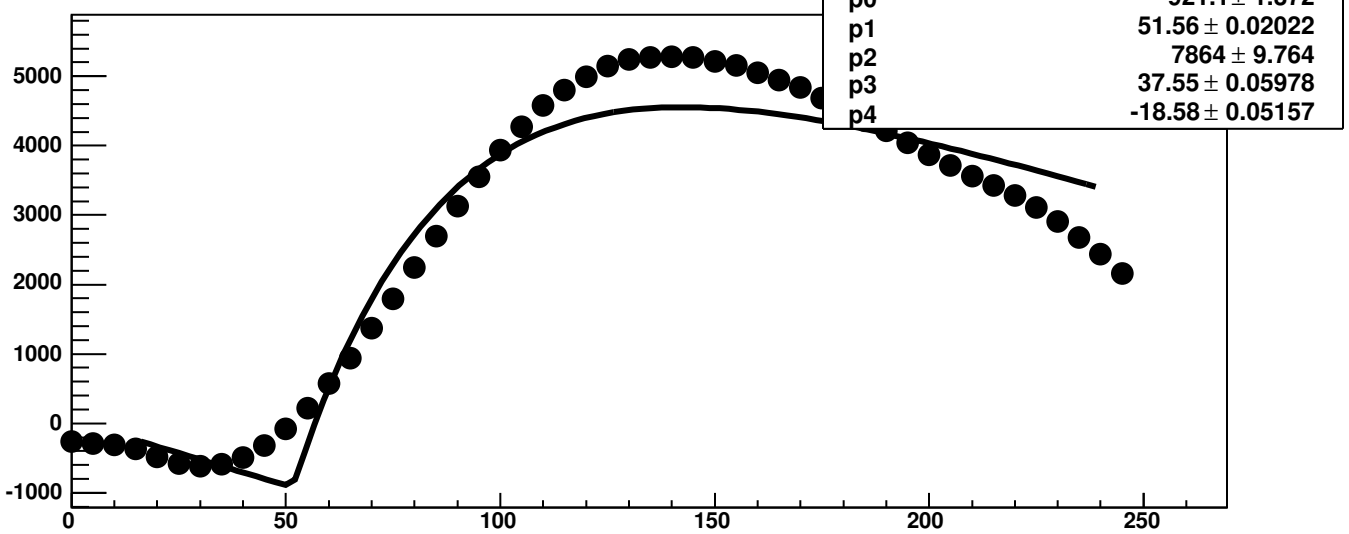



Chip 7, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.121e+05 / 41
p0 -5.845 ± 1.413
p1 47.67 ± 0.018
p2 6637 ± 8.905
p3 38.88 ± 0.05821
p4 -2.956 ± 0.05343
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

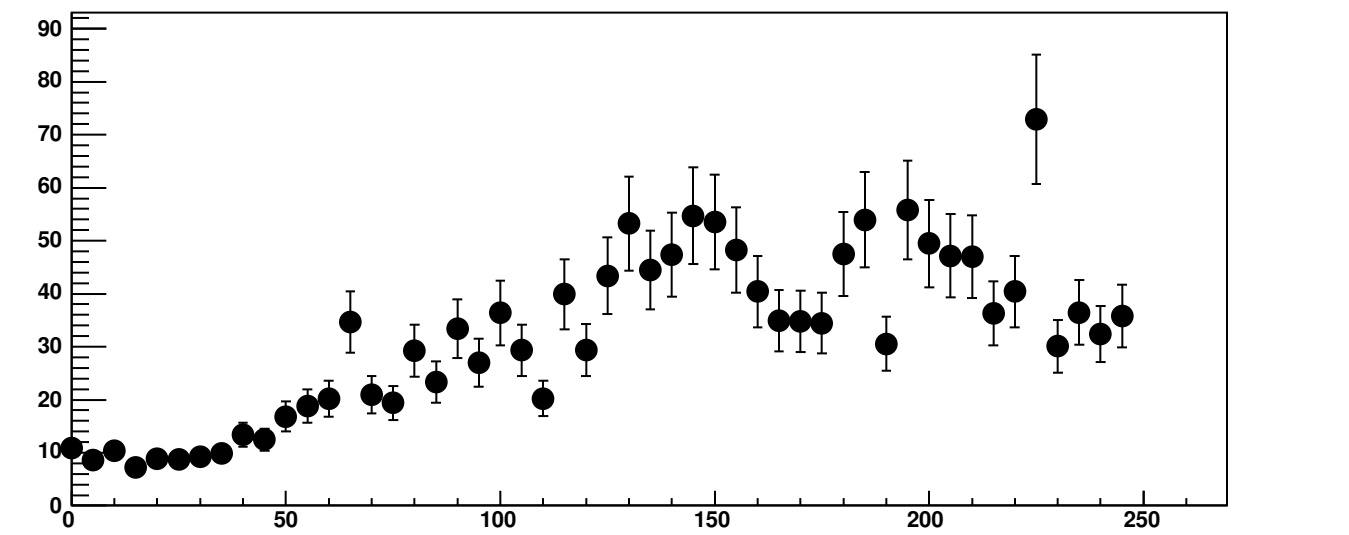

Chip 7, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 7, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
0
50
100
150
200
250

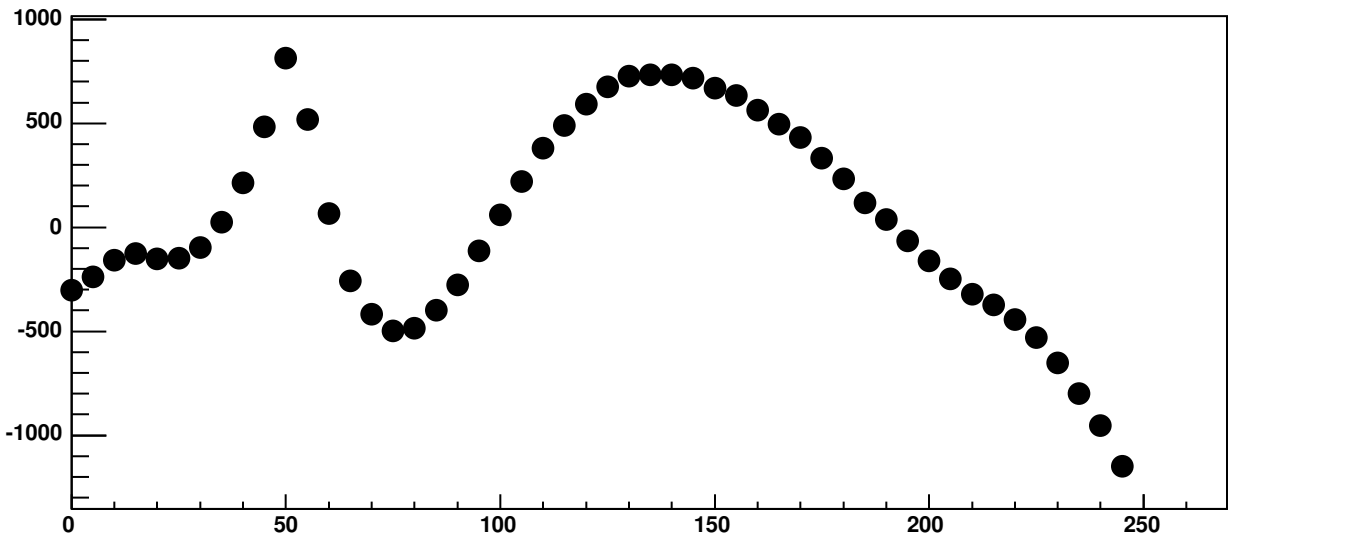
Chip 7, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold



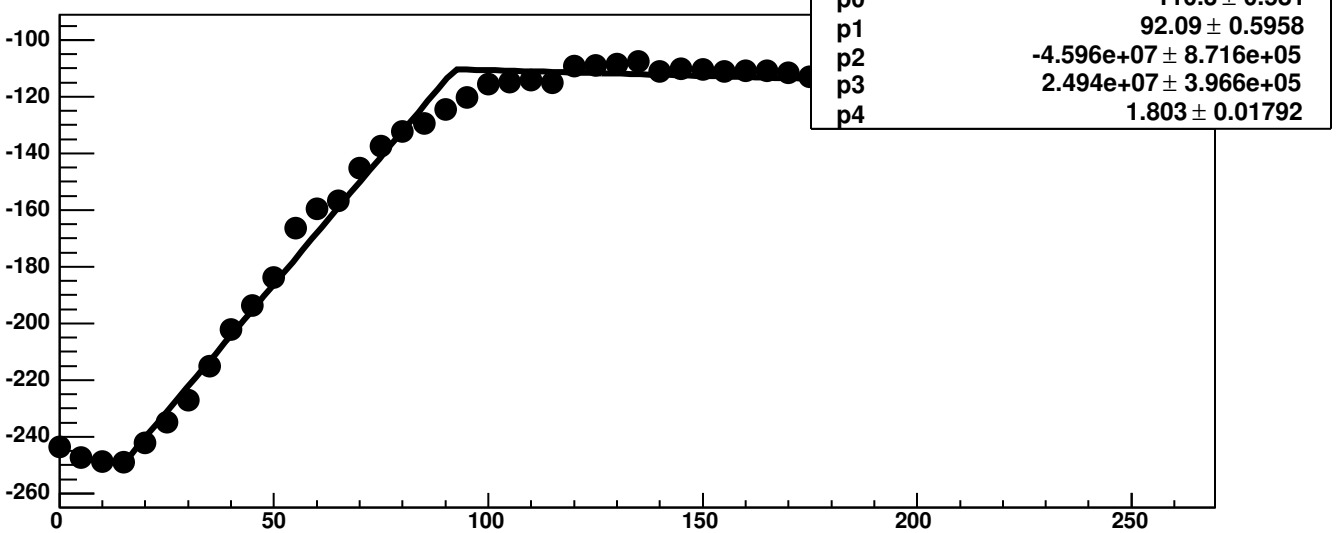
Chip 7, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



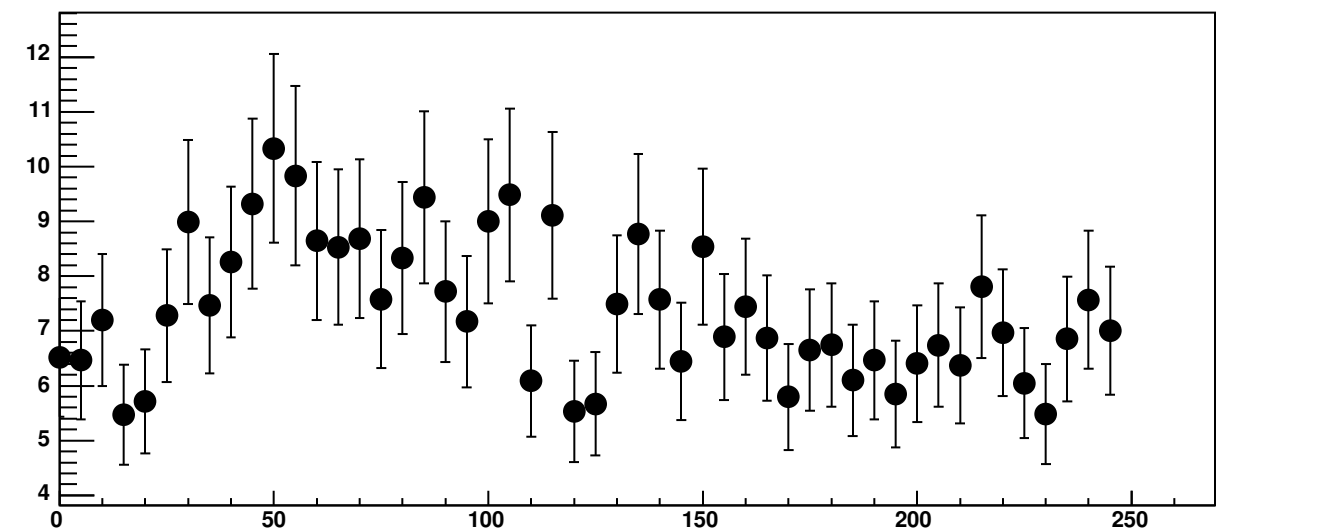
Chip 7, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold



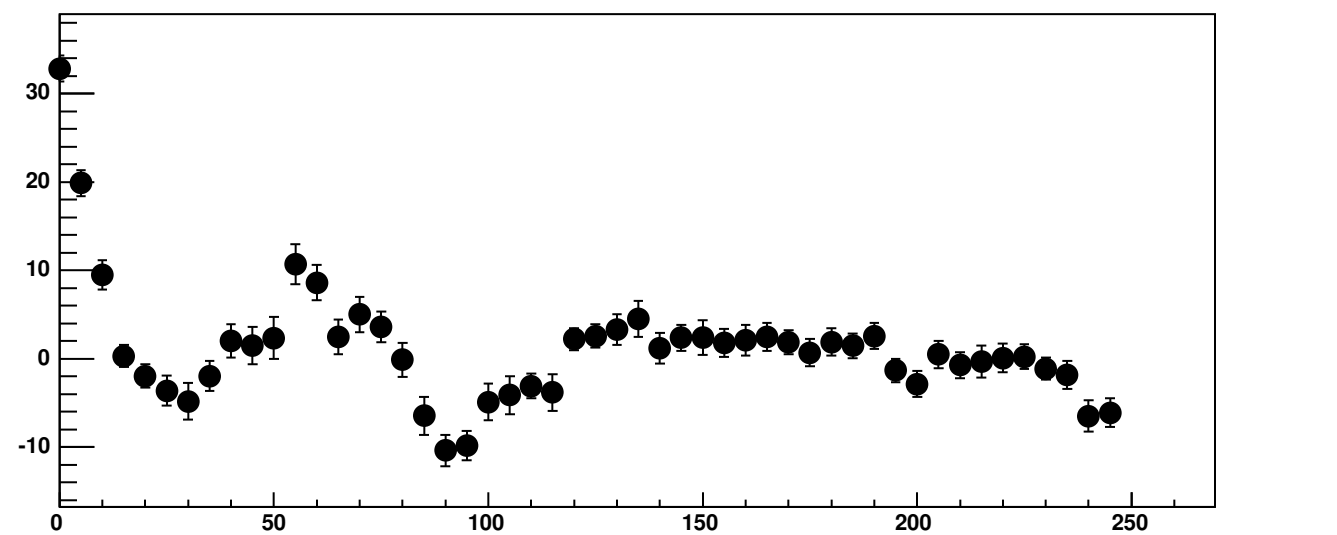
Chip 7, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold



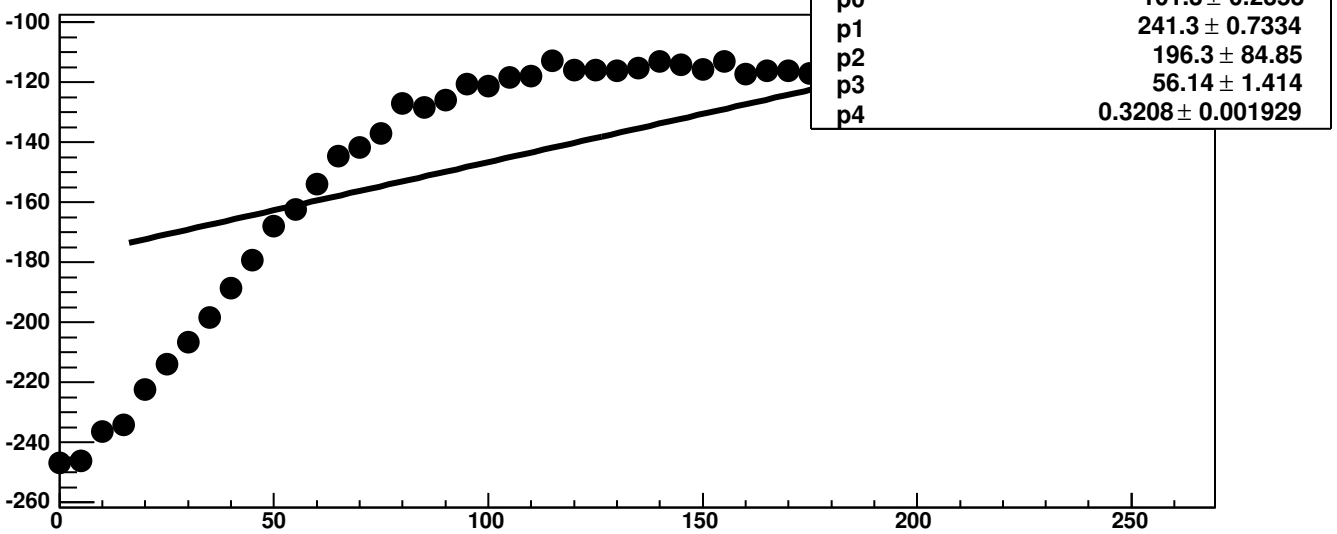
Chip 7, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



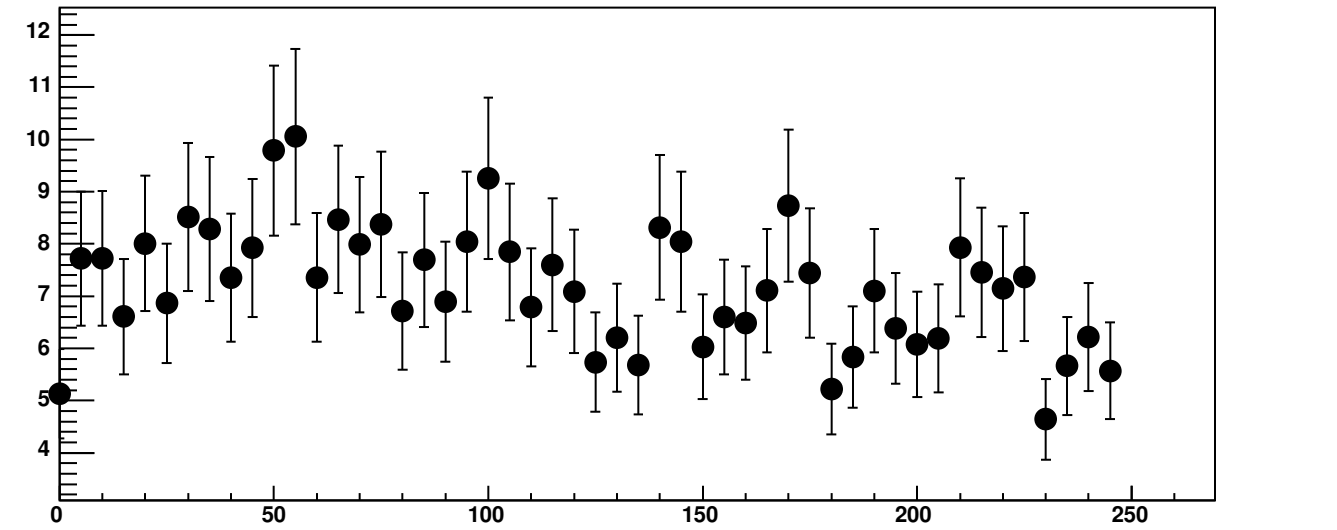
Chip 7, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold



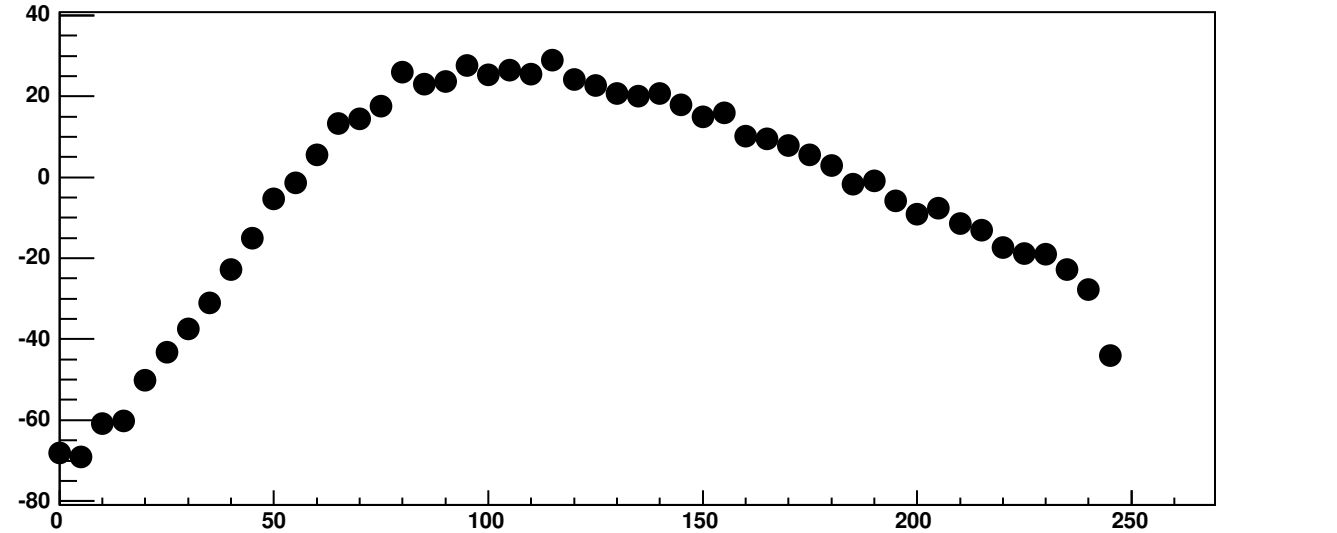
Chip 7, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold

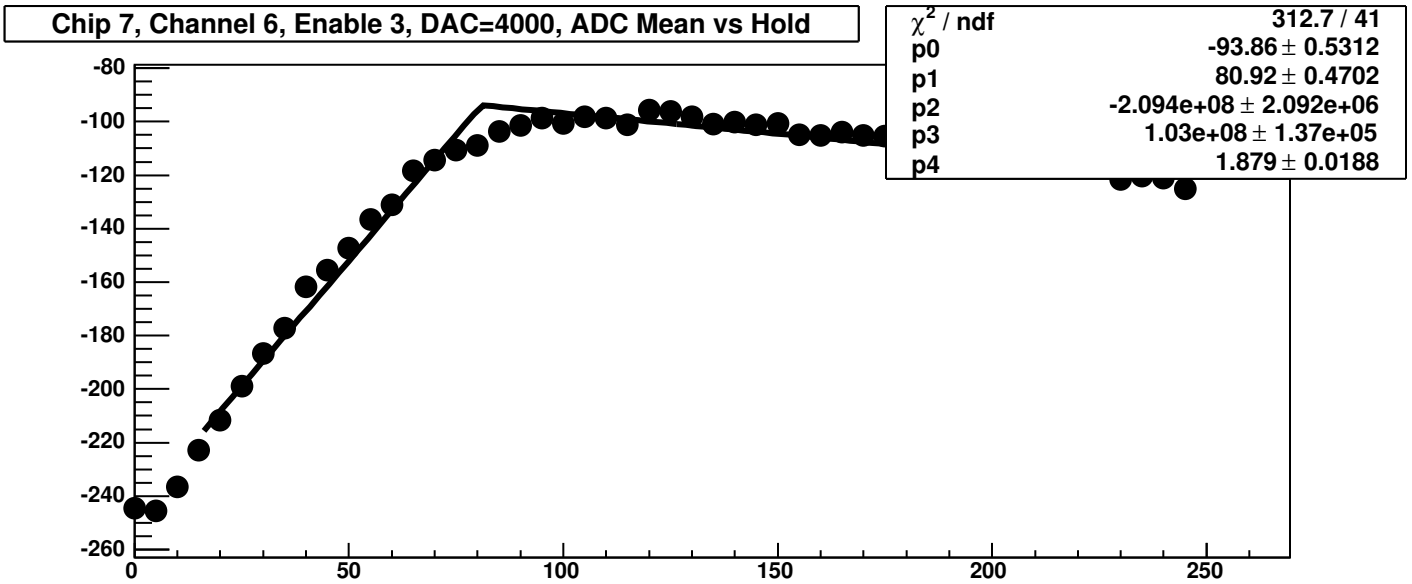


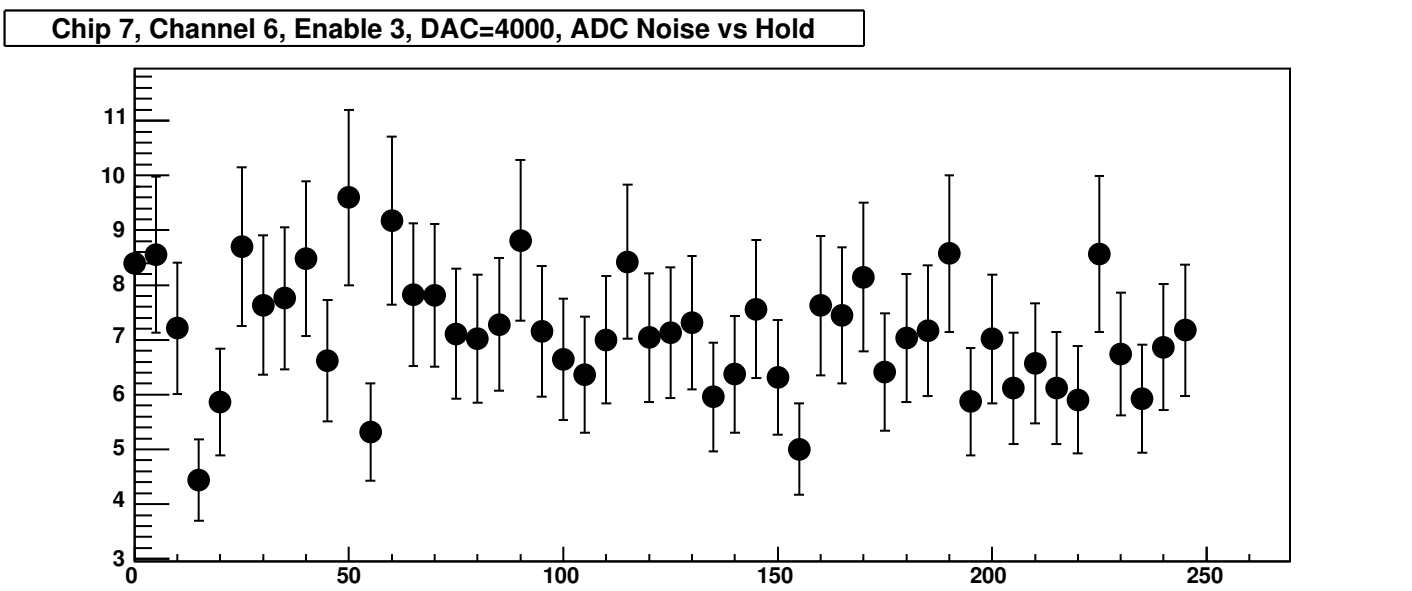
Chip 7, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold

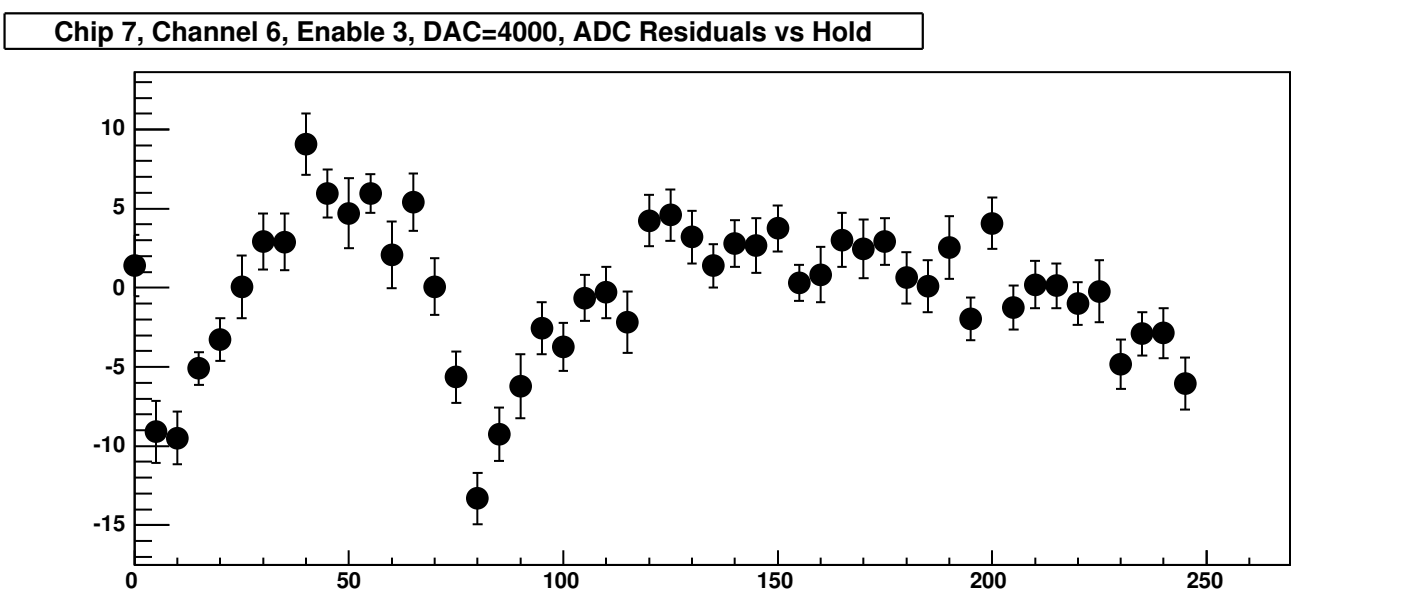


Chip 7, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold

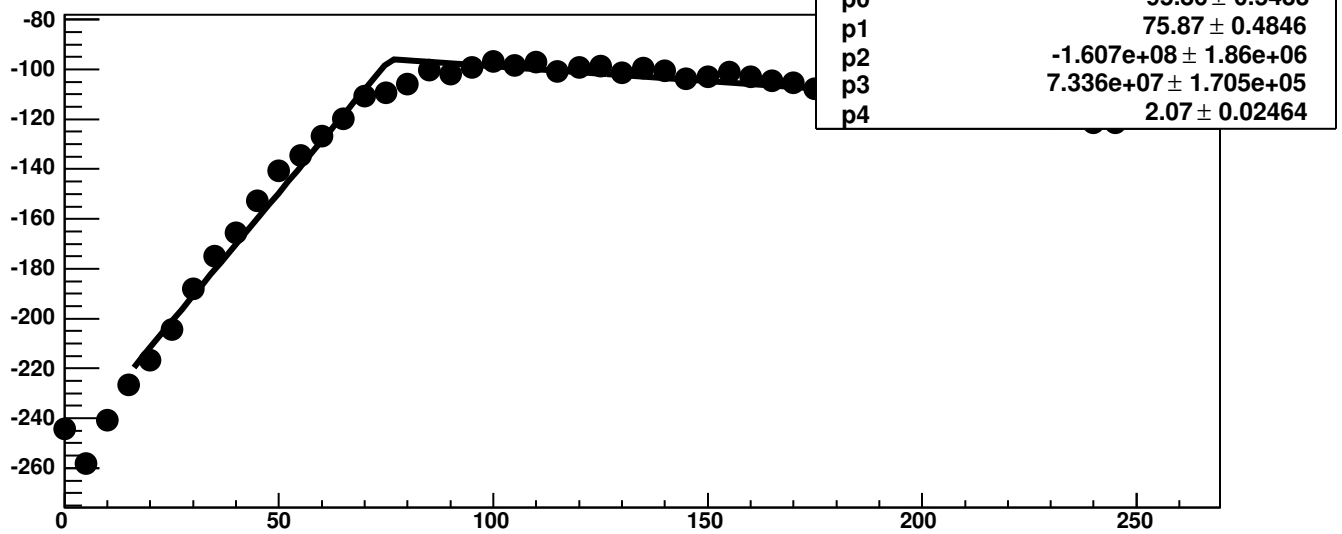



Chip 7, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 312.7 / 41
p0 -93.86 ± 0.5312
p1 80.92 ± 0.4702
p2 -2.094e+08 ± 2.092e+06
p3 1.03e+08 ± 1.37e+05
p4 1.879 ± 0.0188
-80
-100
-120
-140
-160
-180
-200
-220
-240
-260
0
50
100
150
200
250

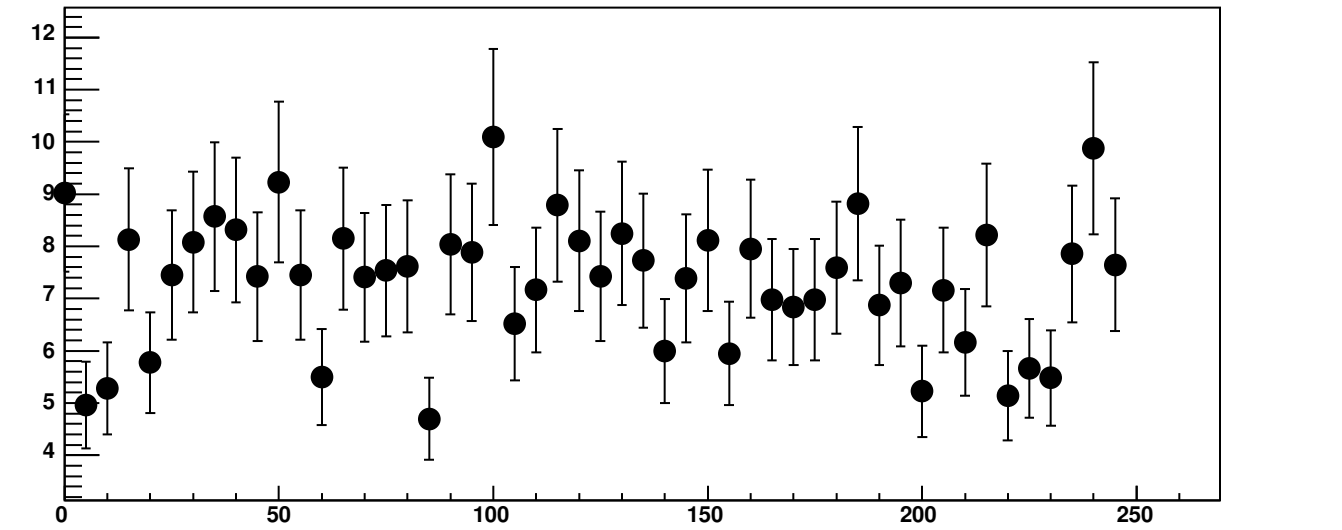

Chip 7, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 7, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
0
50
100
150
200
250

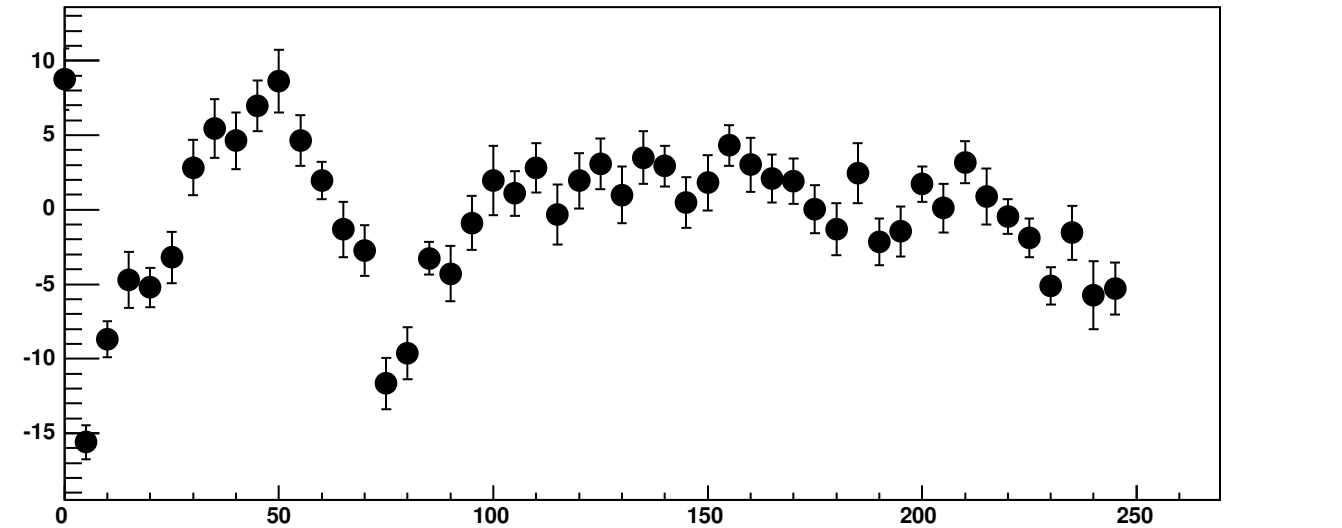
Chip 7, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

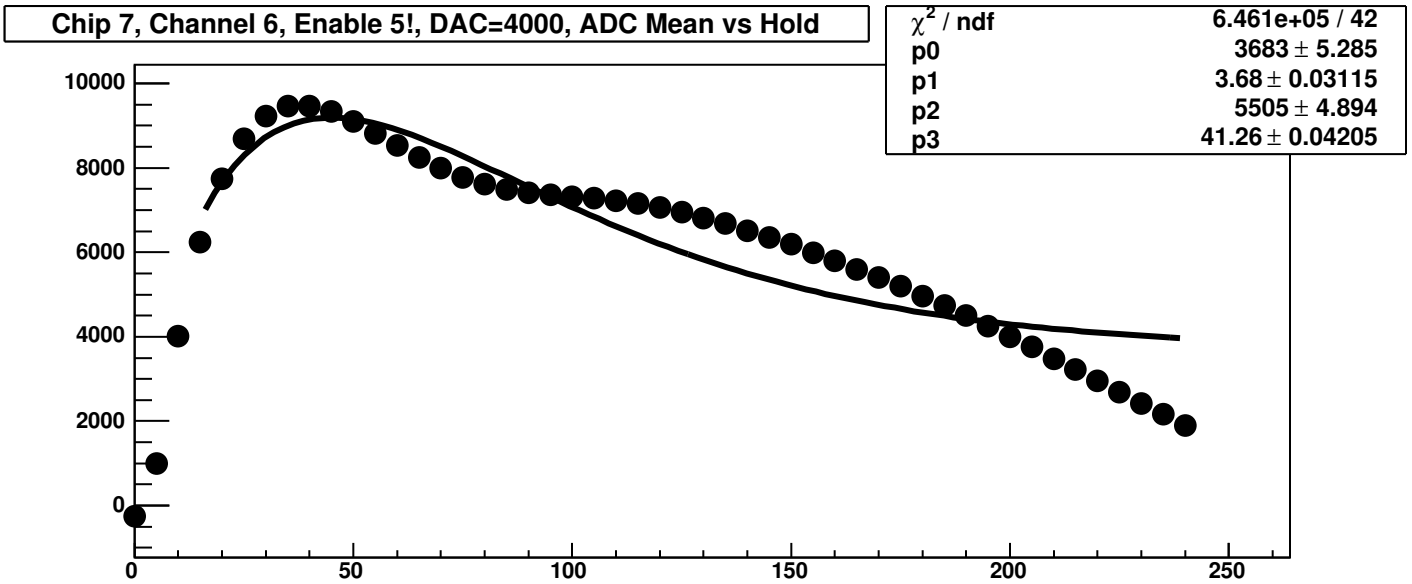


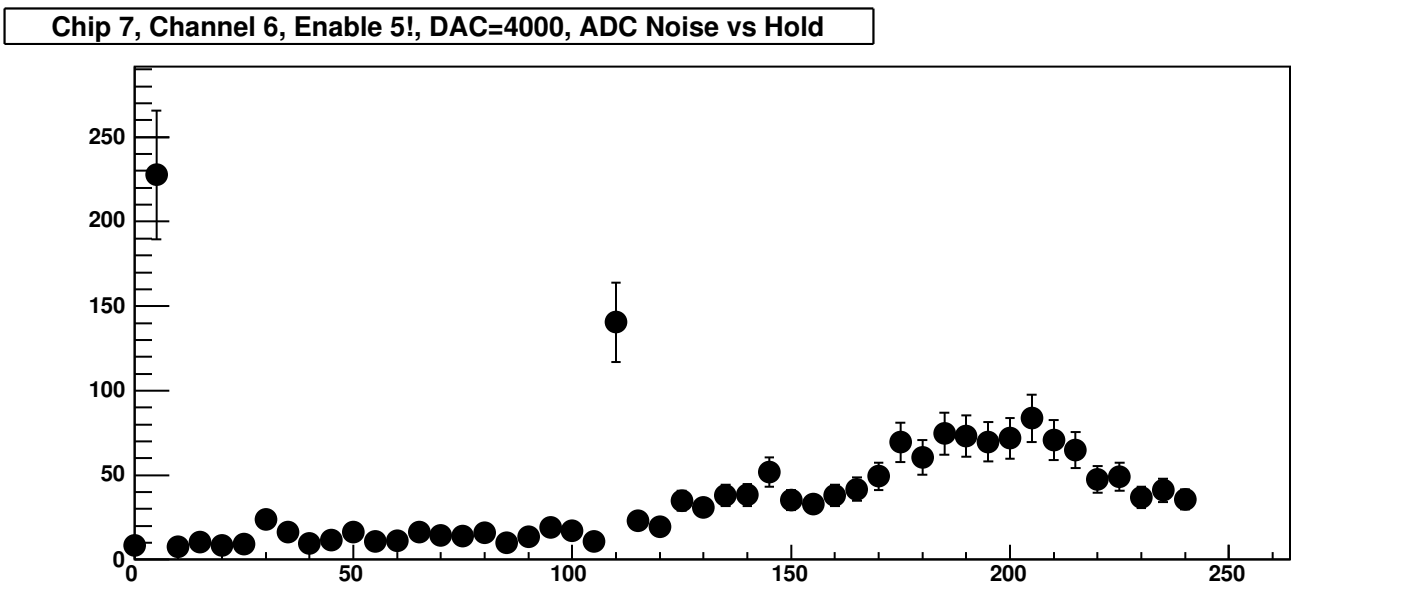
Chip 7, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold

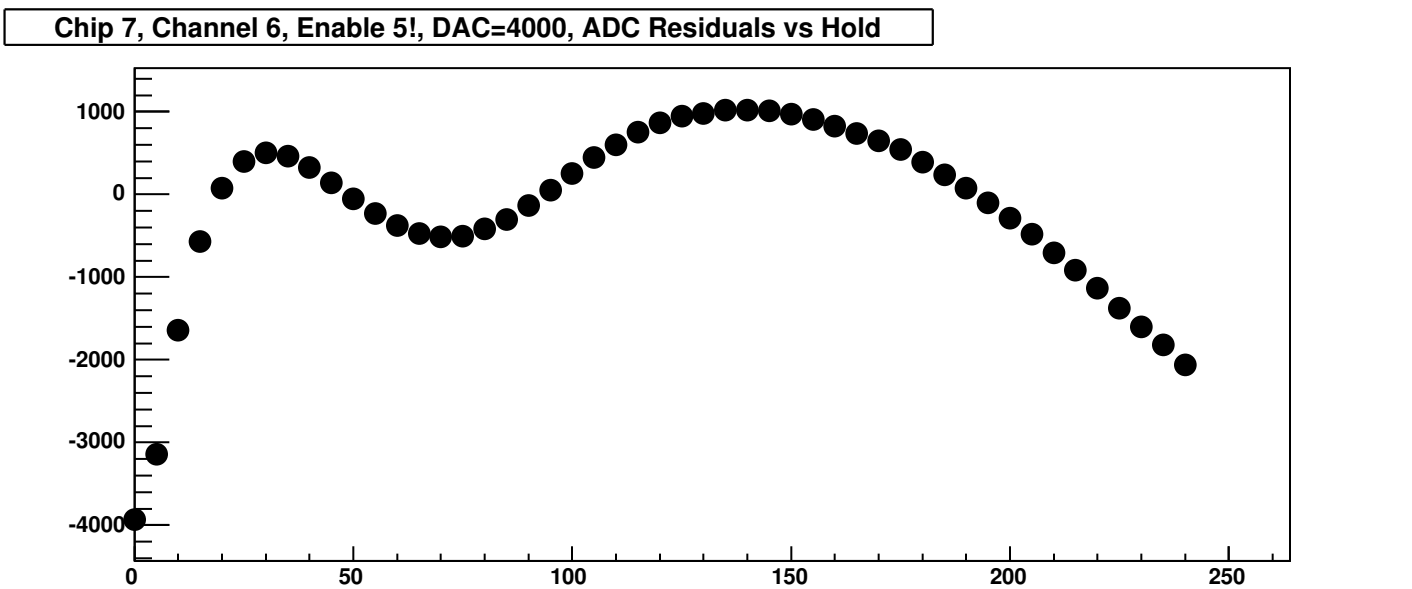


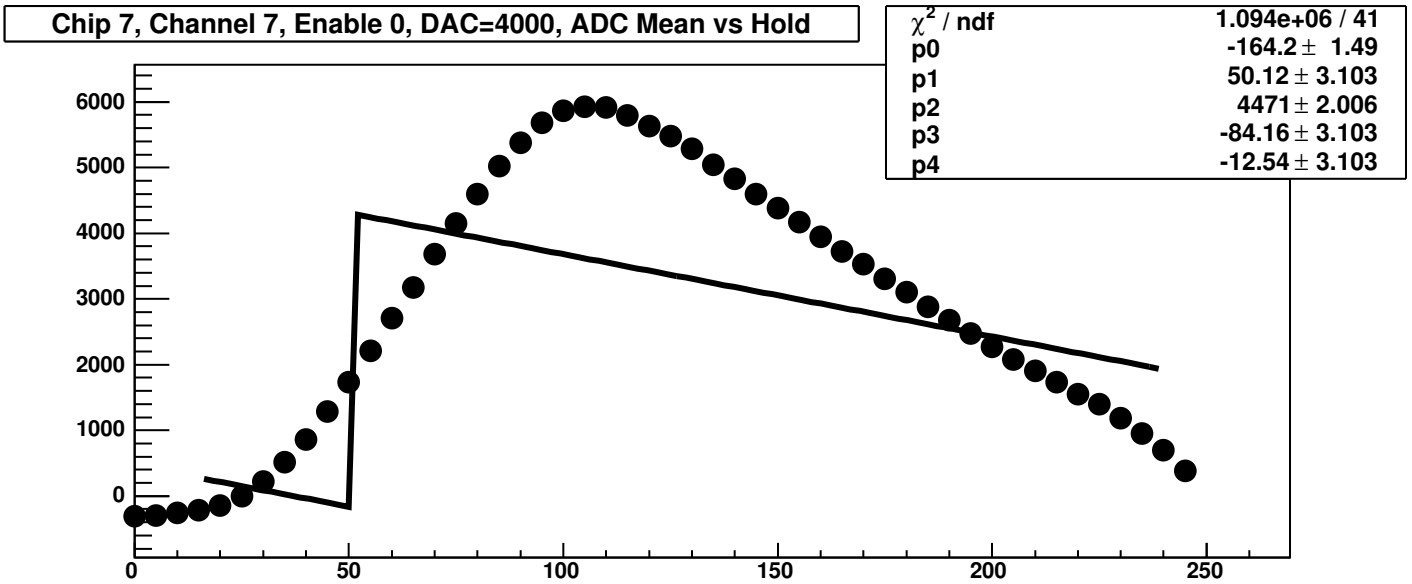
Chip 7, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

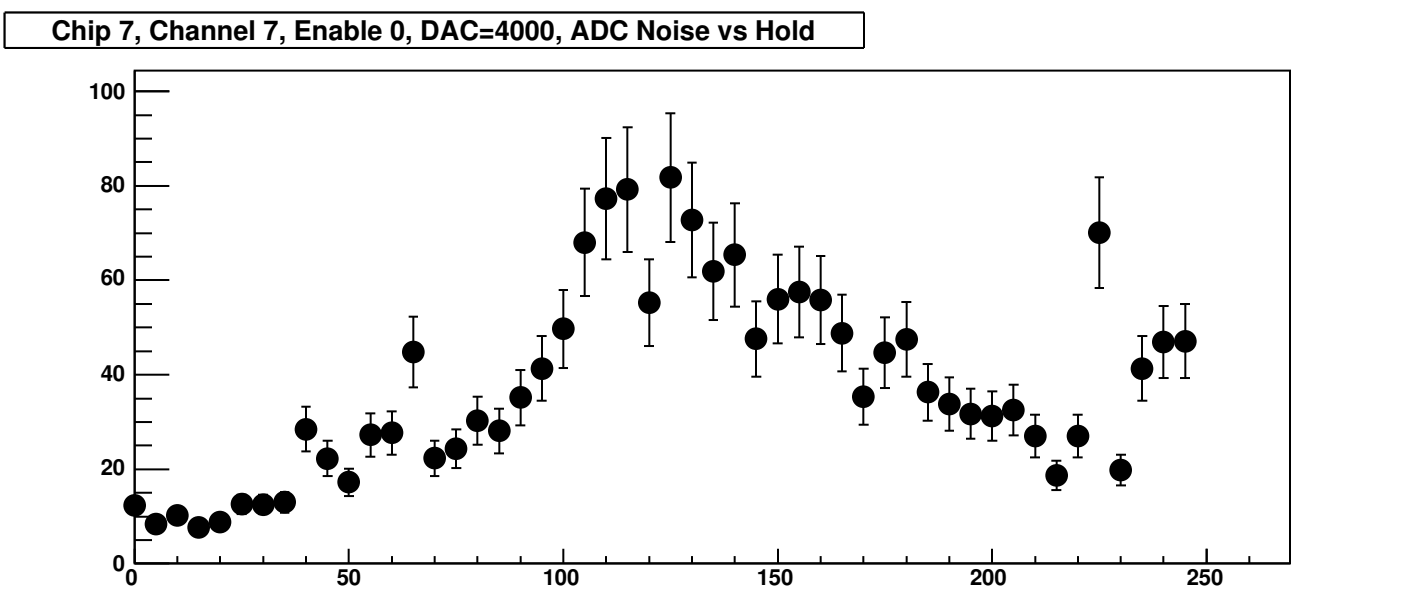


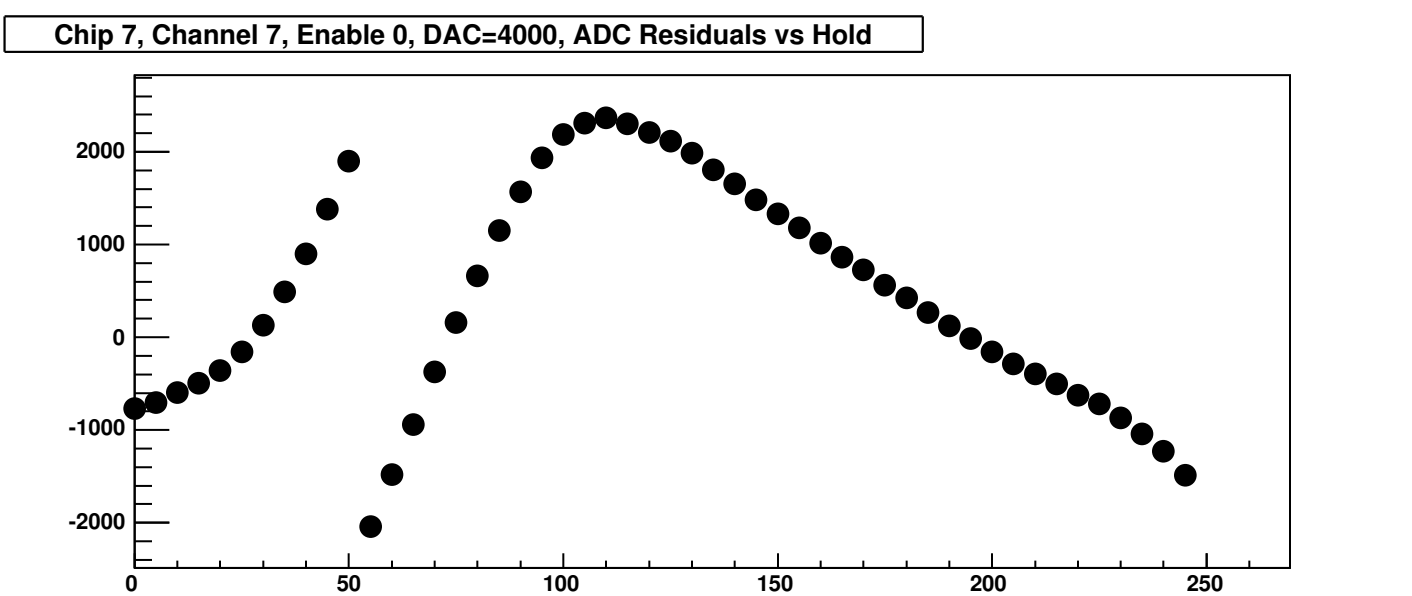

Chip 7, Channel 6, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 6.461e+05 / 42
p0 3683 ± 5.285
p1 3.68 ± 0.03115
p2 5505 ± 4.894
p3 41.26 ± 0.04205
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 7, Channel 6, Enable 5!, DAC=4000, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250

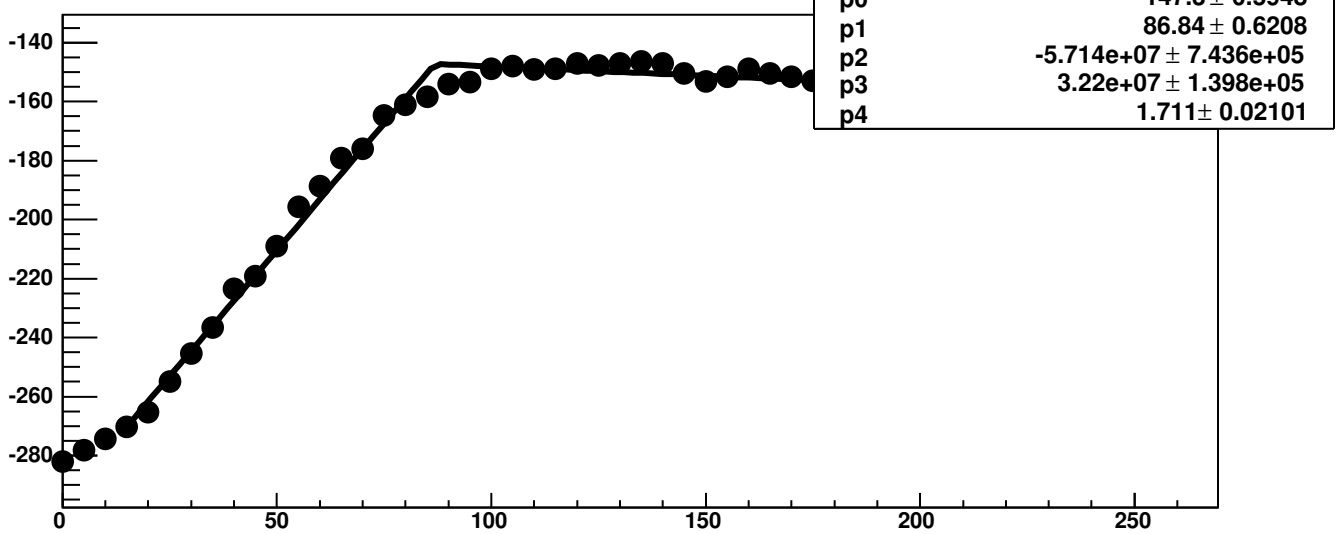

Chip 7, Channel 6, Enable 5!, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250


Chip 7, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.094e+06 / 41
p0 -164.2 ± 1.49
p1 50.12 ± 3.103
p2 4471 ± 2.006
p3 -84.16 ± 3.103
p4 -12.54 ± 3.103
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

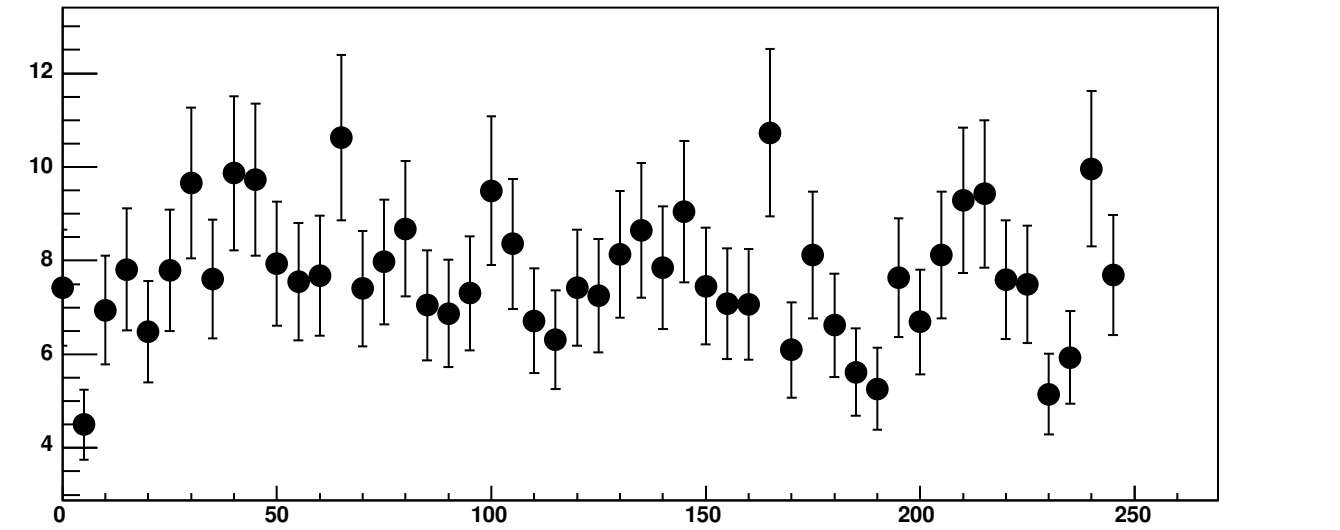

Chip 7, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250


Chip 7, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
0
50
100
150
200
250

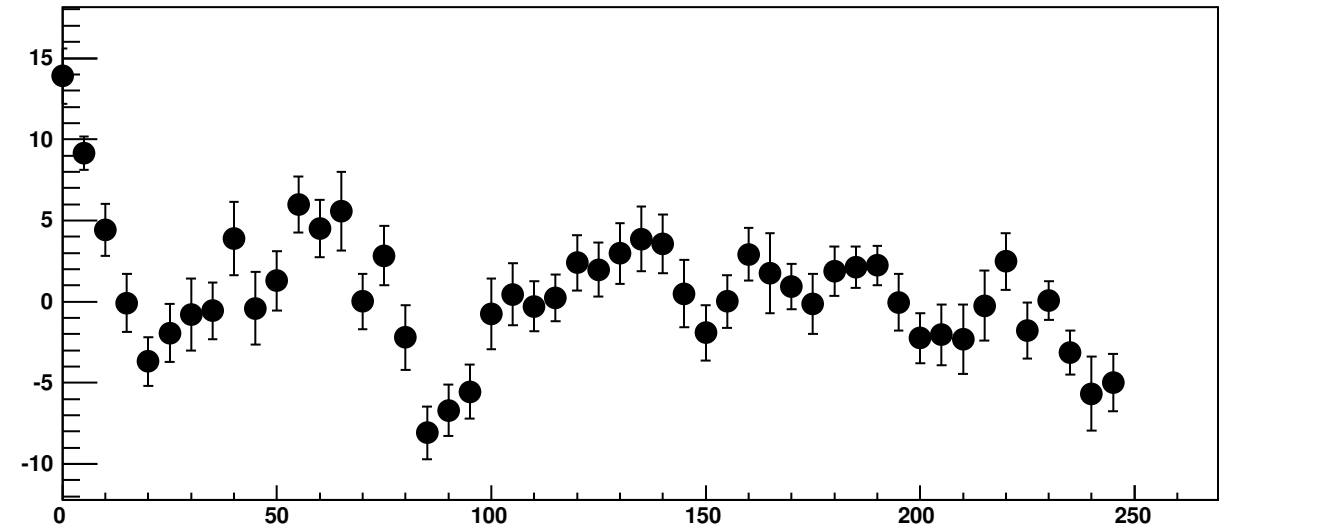
Chip 7, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 7, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold

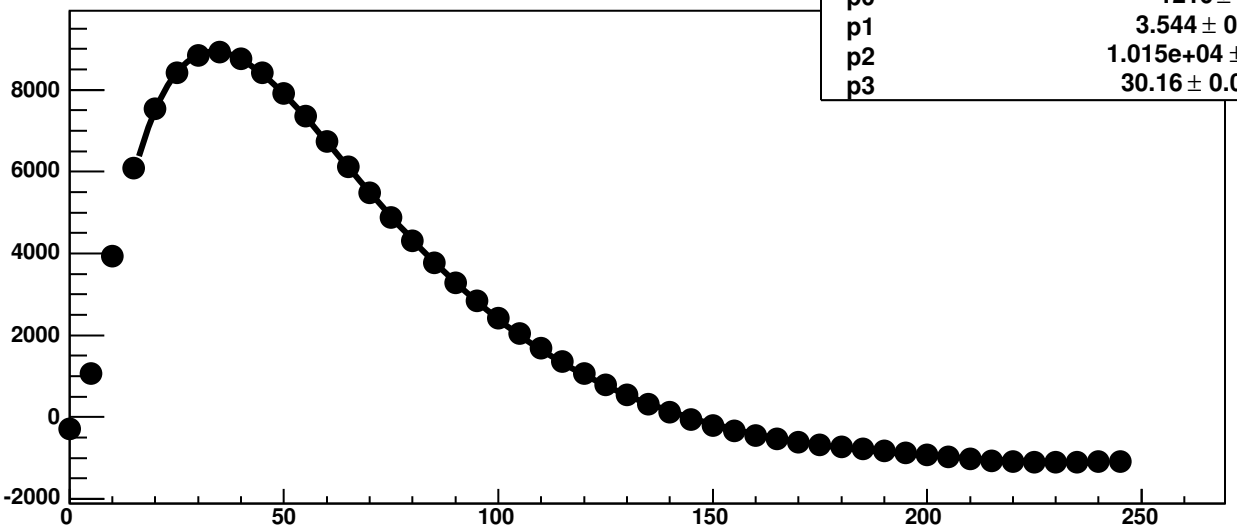


Chip 7, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

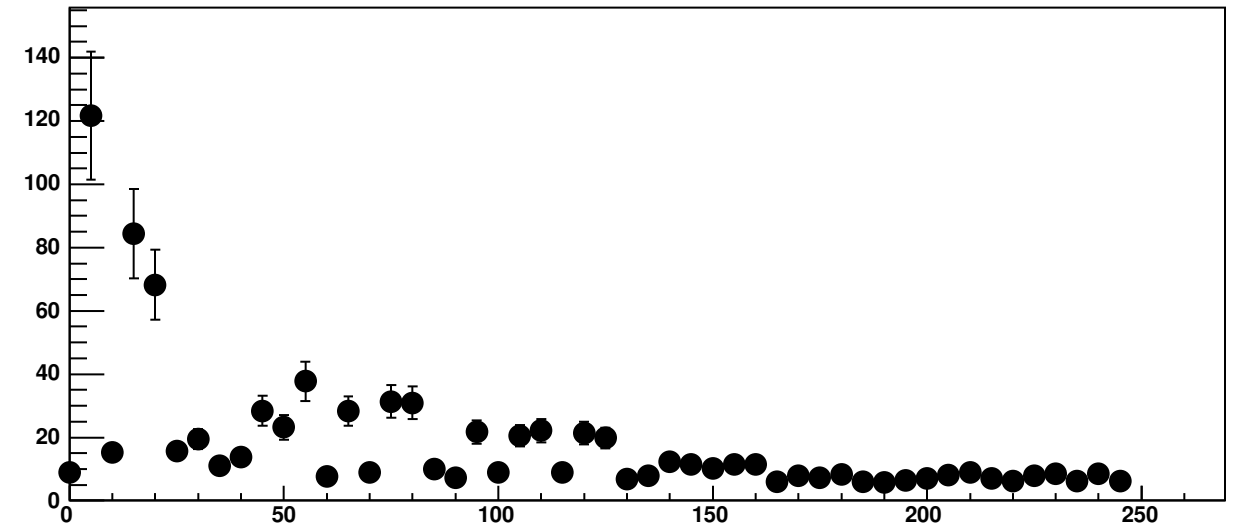


Chip 7, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold

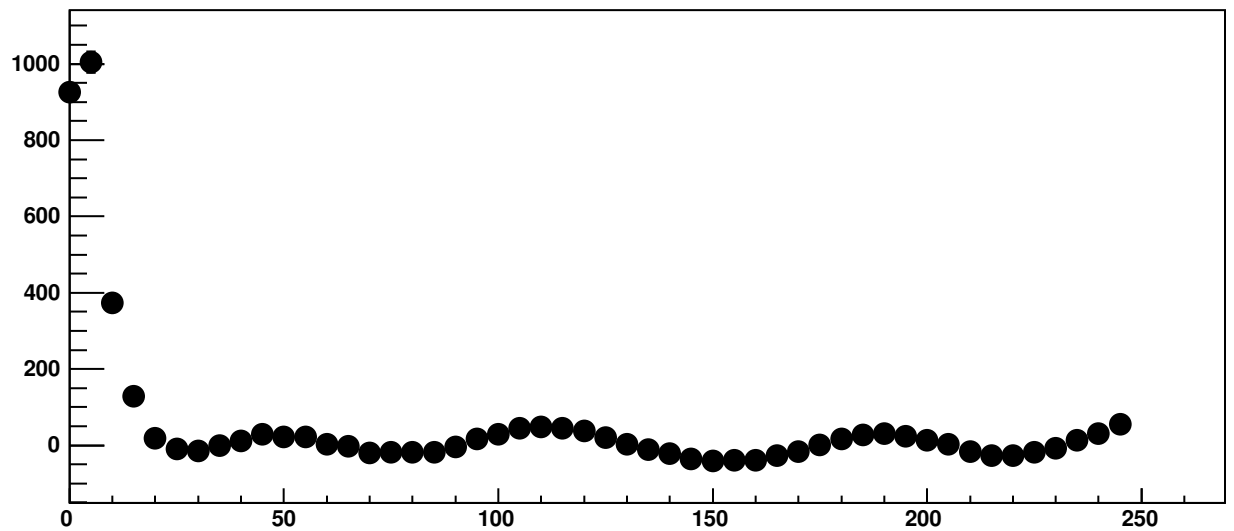
χ^2 / ndf	5312 / 42
p0	-1210 ± 0.5893
p1	3.544 ± 0.01999
p2	1.015e+04 ± 1.452
p3	30.16 ± 0.008723

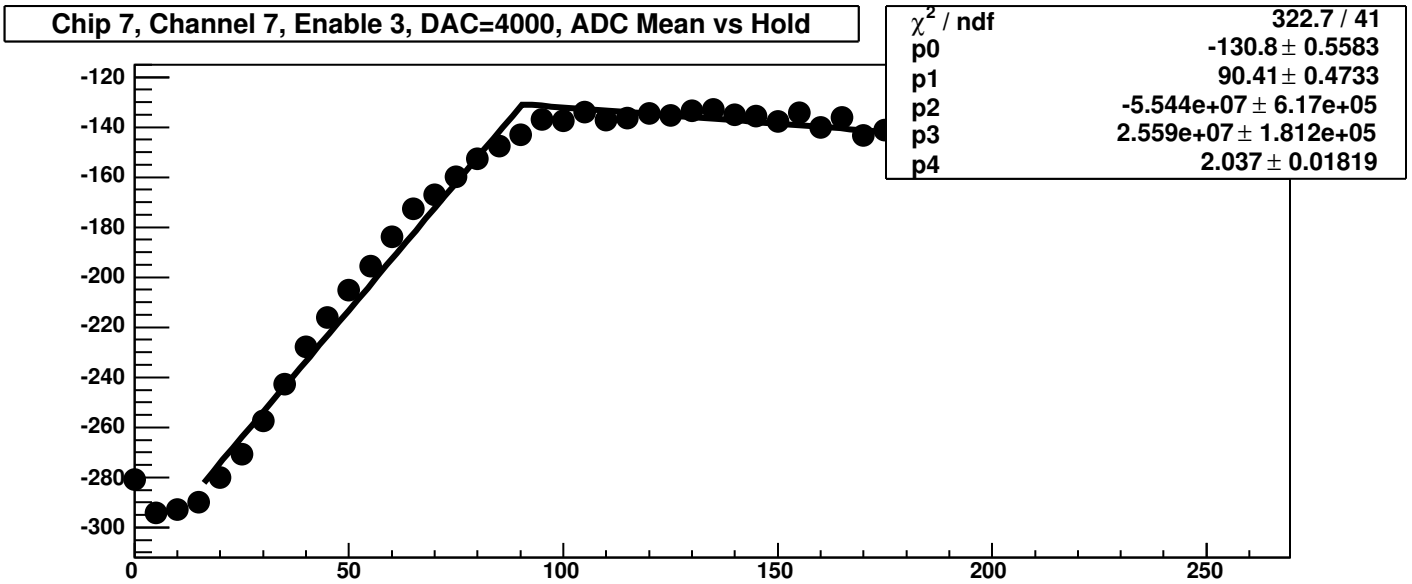


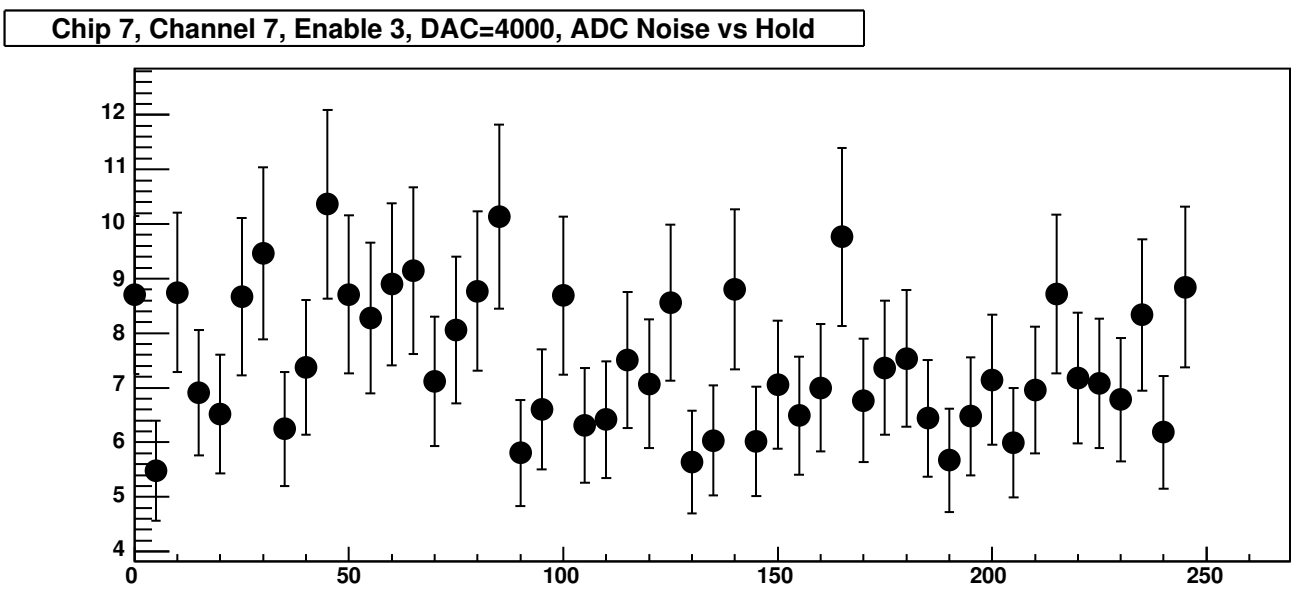
Chip 7, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold

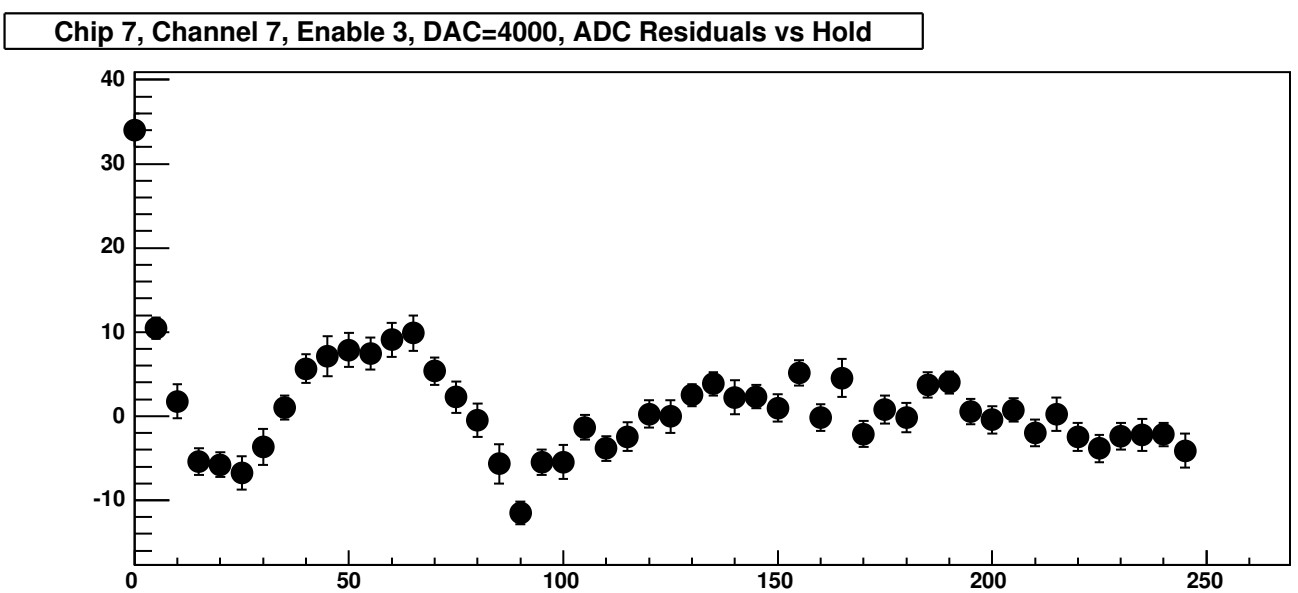


Chip 7, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold

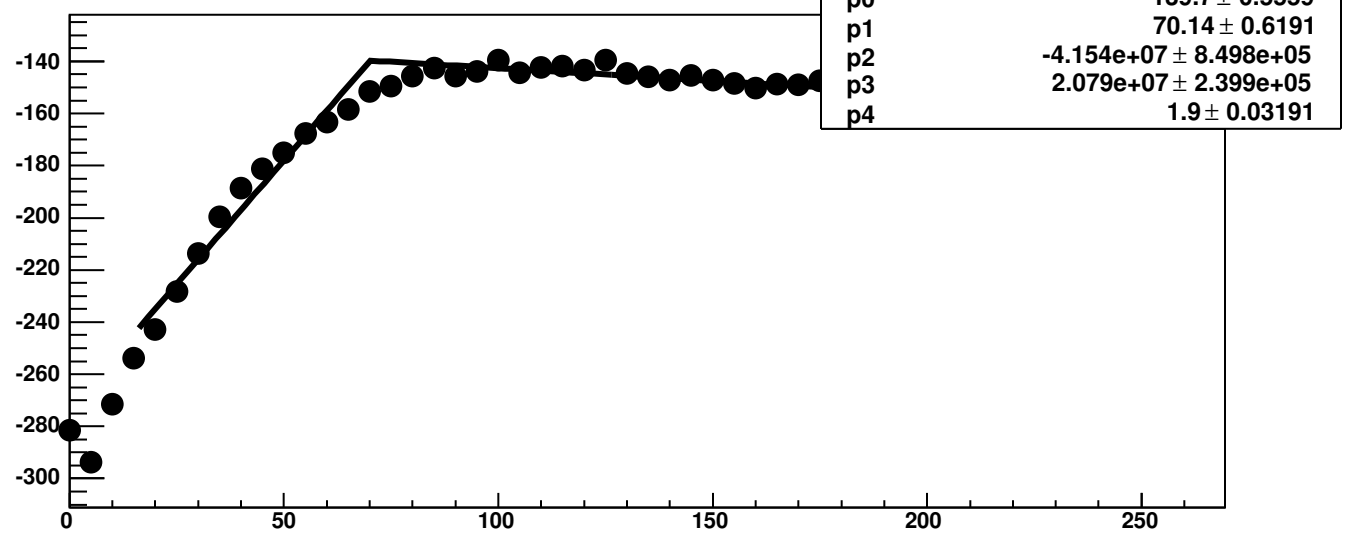



Chip 7, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 322.7 / 41
p0 -130.8 ± 0.5583
p1 90.41 ± 0.4733
p2 -5.544e+07 ± 6.17e+05
p3 2.559e+07 ± 1.812e+05
p4 2.037 ± 0.01819
-120
-140
-160
-180
-200
-220
-240
-260
-280
-300
0
50
100
150
200
250

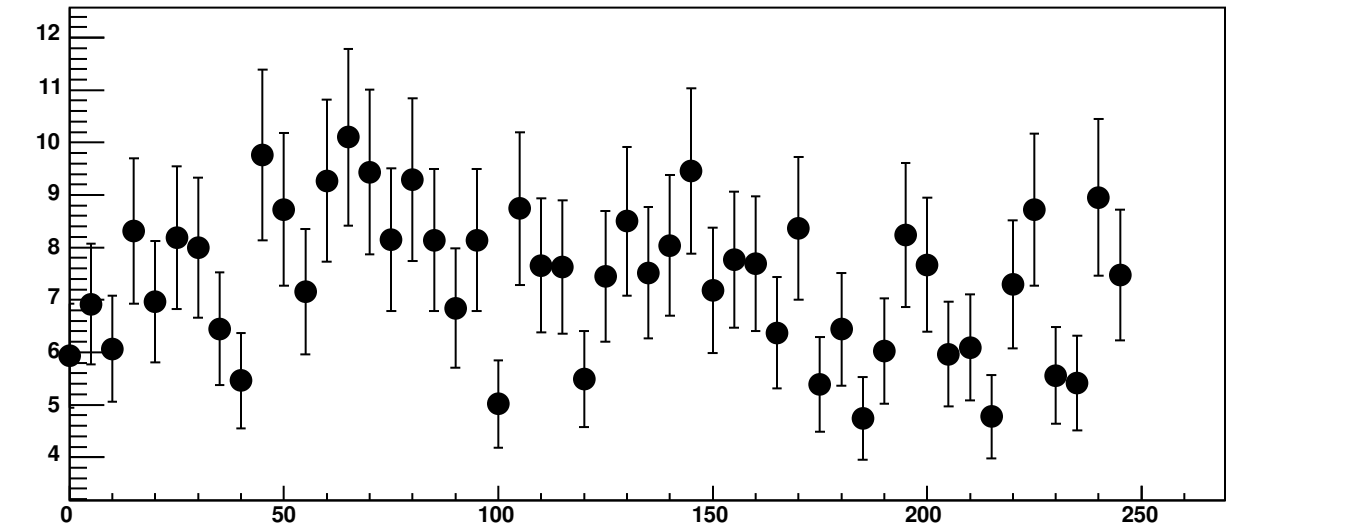

Chip 7, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 7, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

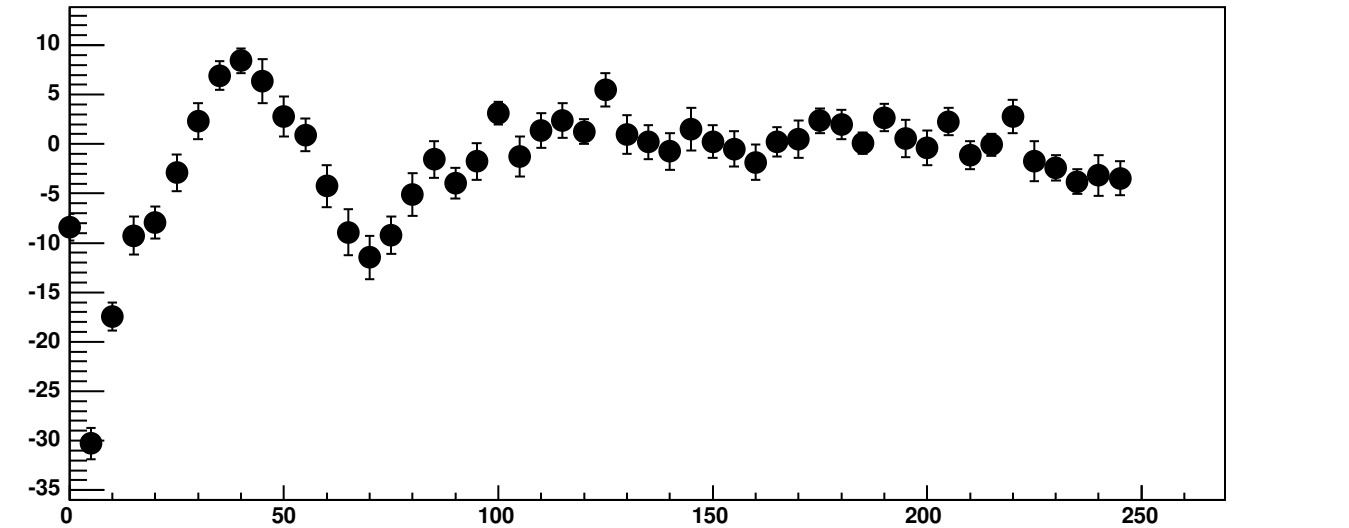
Chip 7, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

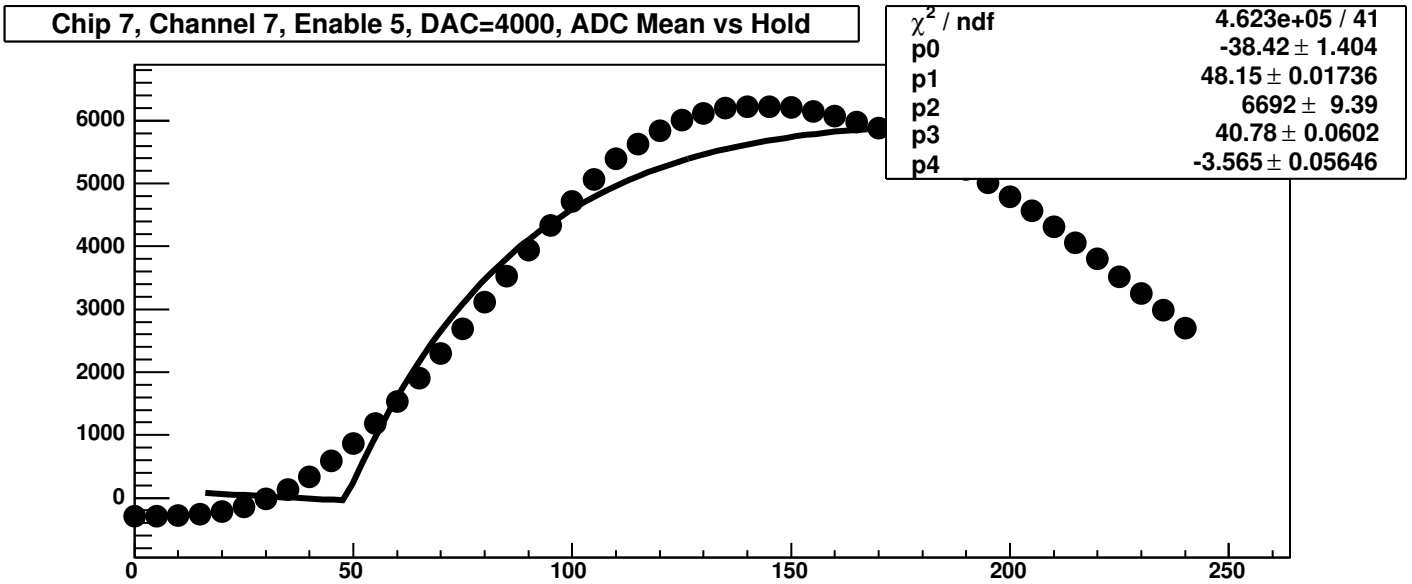


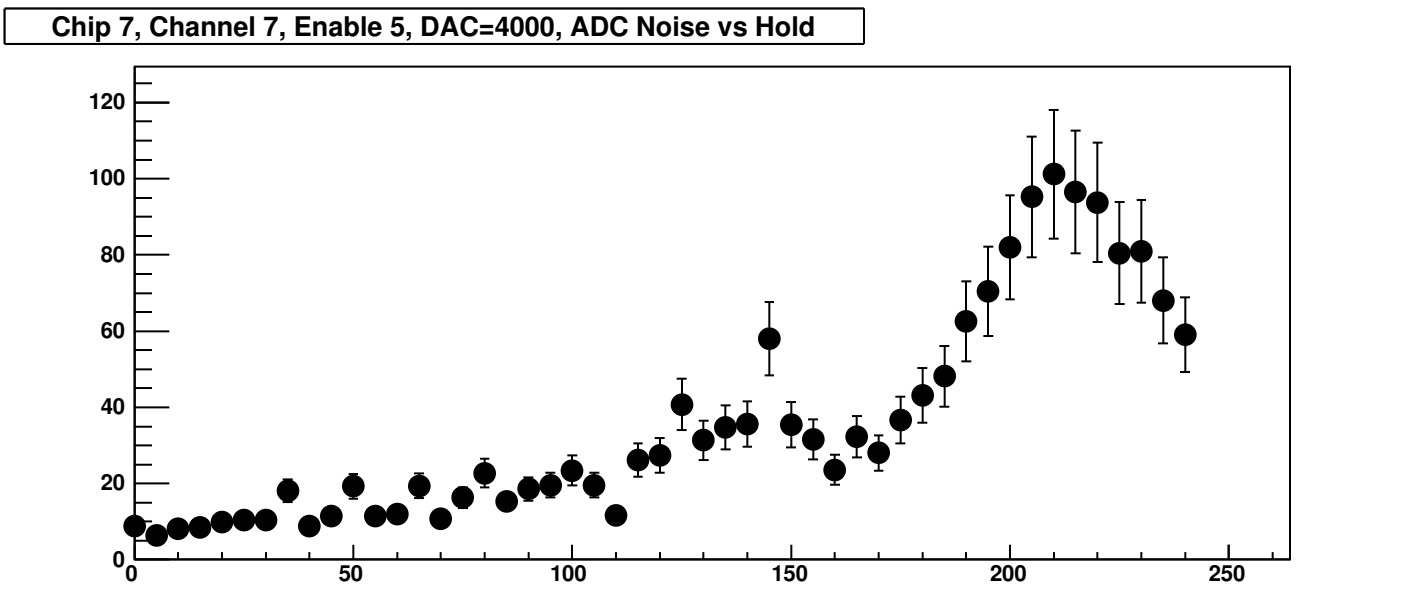
Chip 7, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold

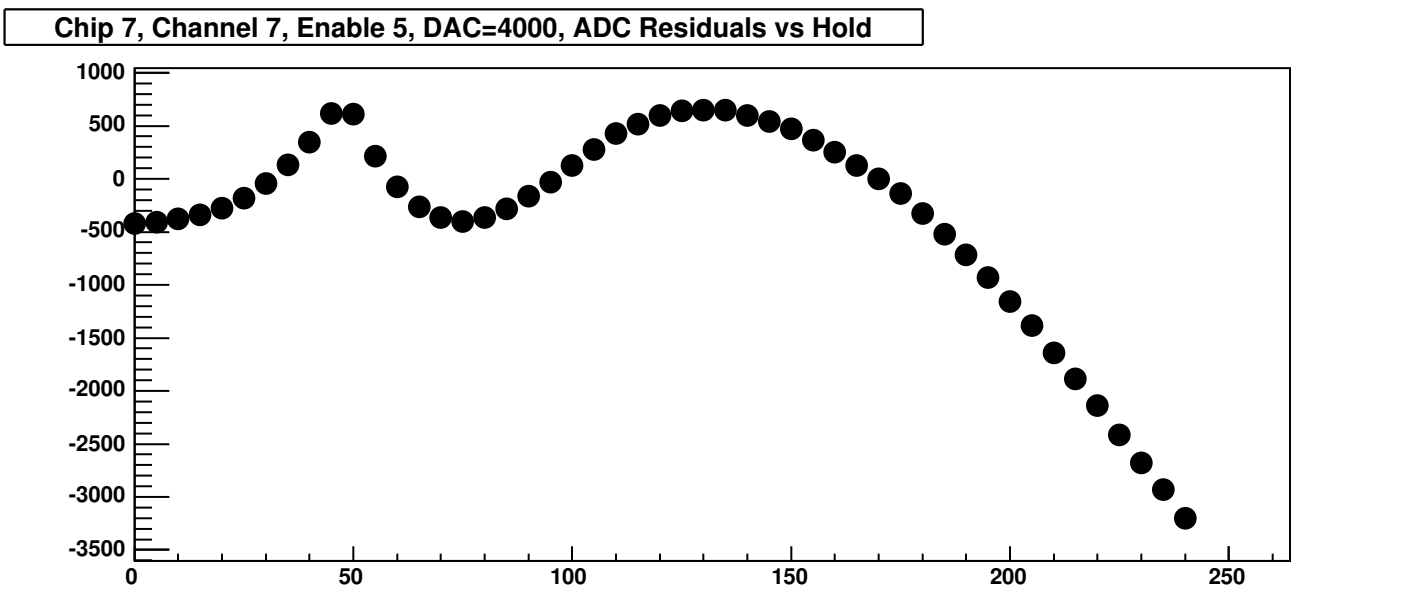


Chip 7, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

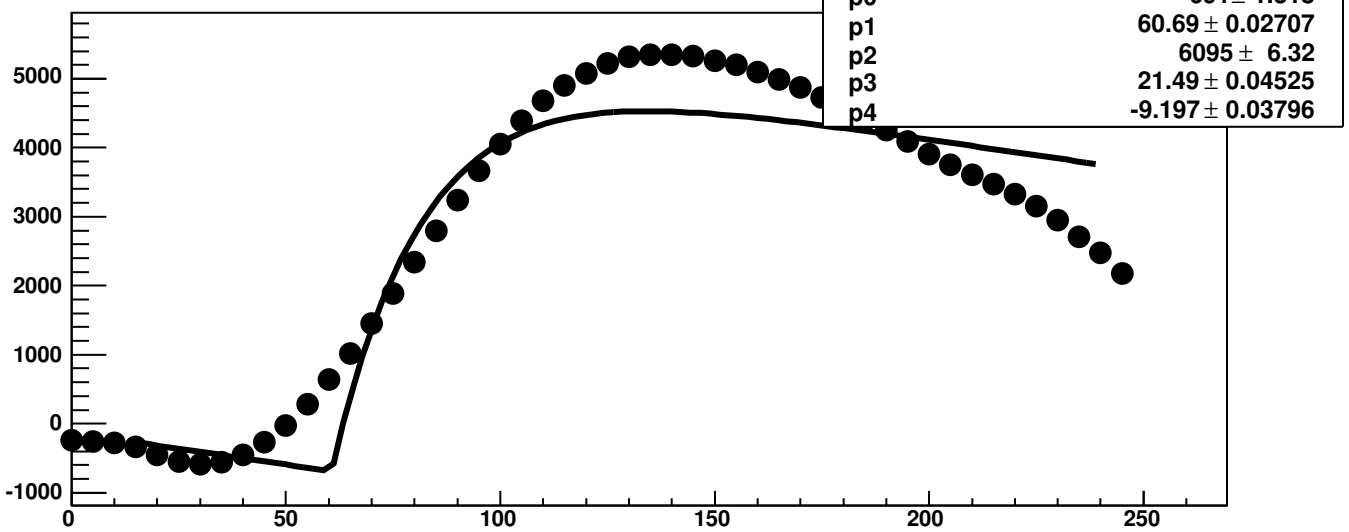



Chip 7, Channel 7, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.623e+05 / 41
p0 -38.42 ± 1.404
p1 48.15 ± 0.01736
p2 6692 ± 9.39
p3 40.78 ± 0.0602
p4 -3.565 ± 0.05646
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

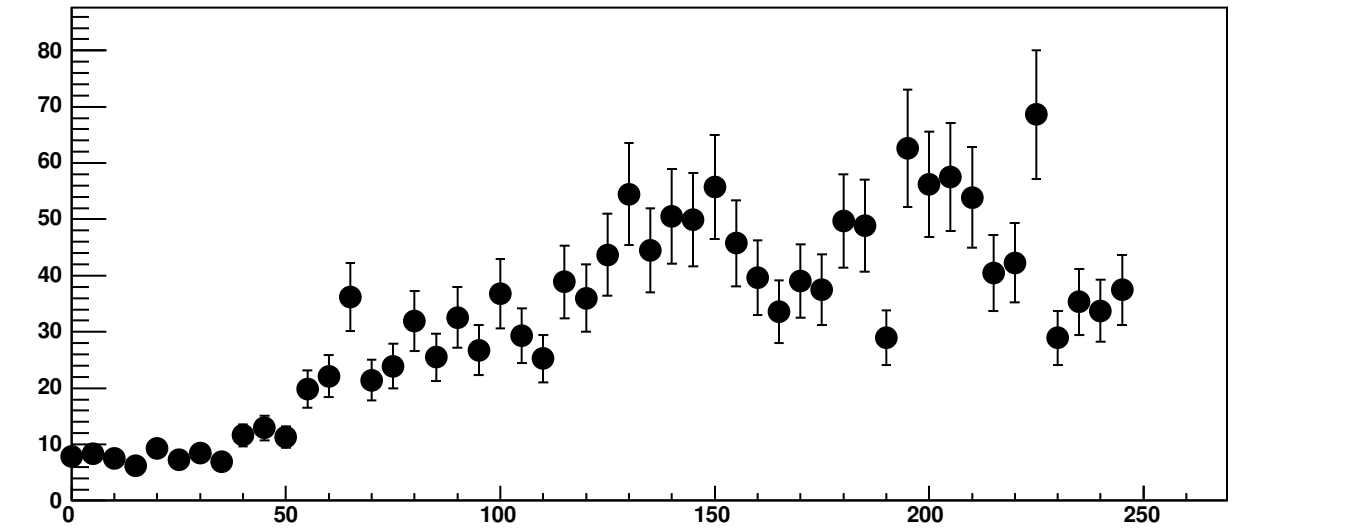

Chip 7, Channel 7, Enable 5, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 7, Channel 7, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
0
50
100
150
200
250

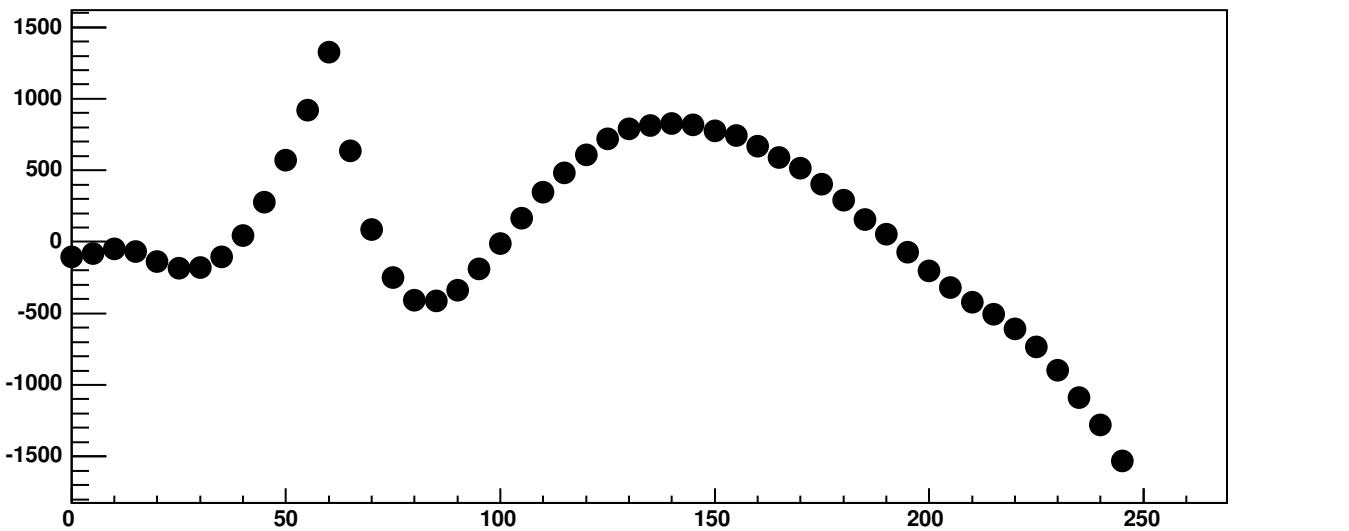
Chip 7, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold



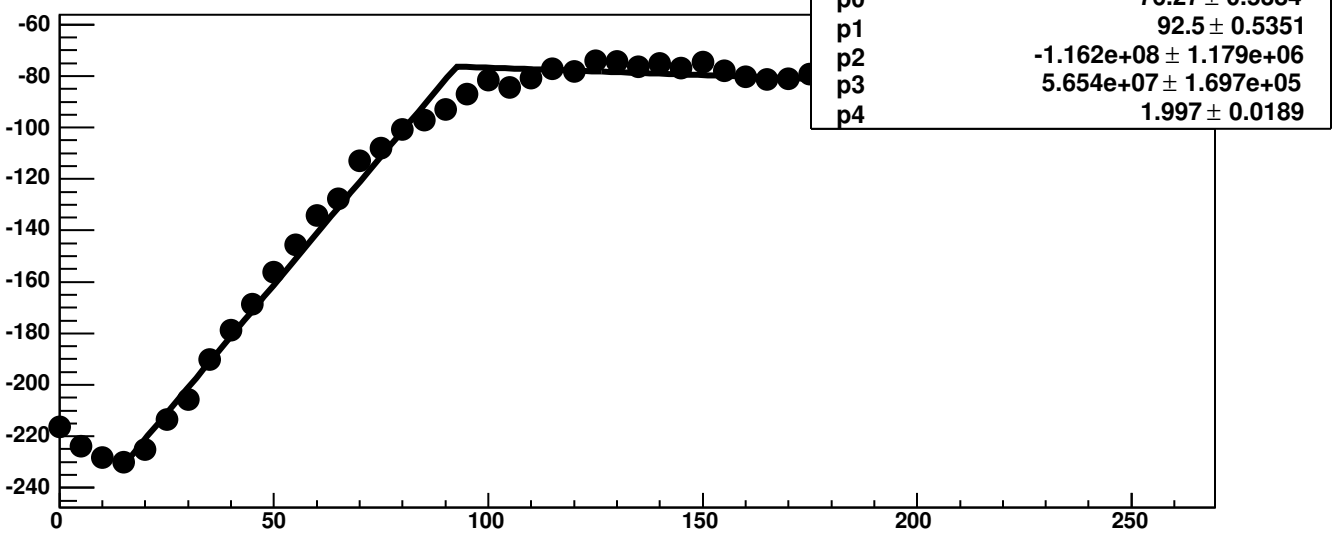
Chip 7, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



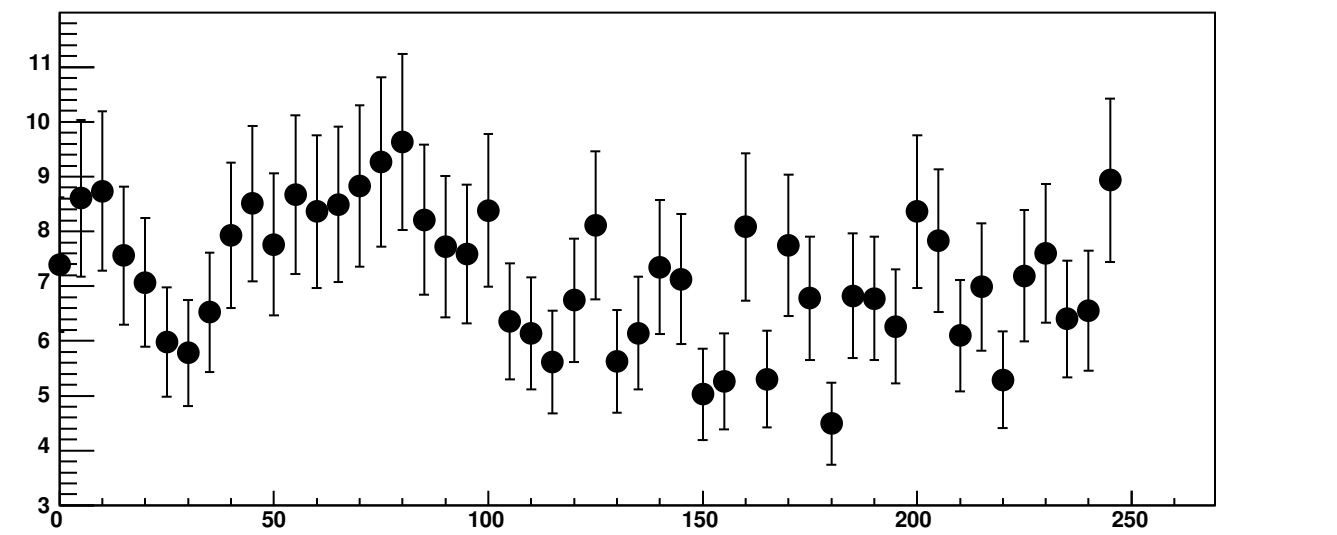
Chip 7, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold



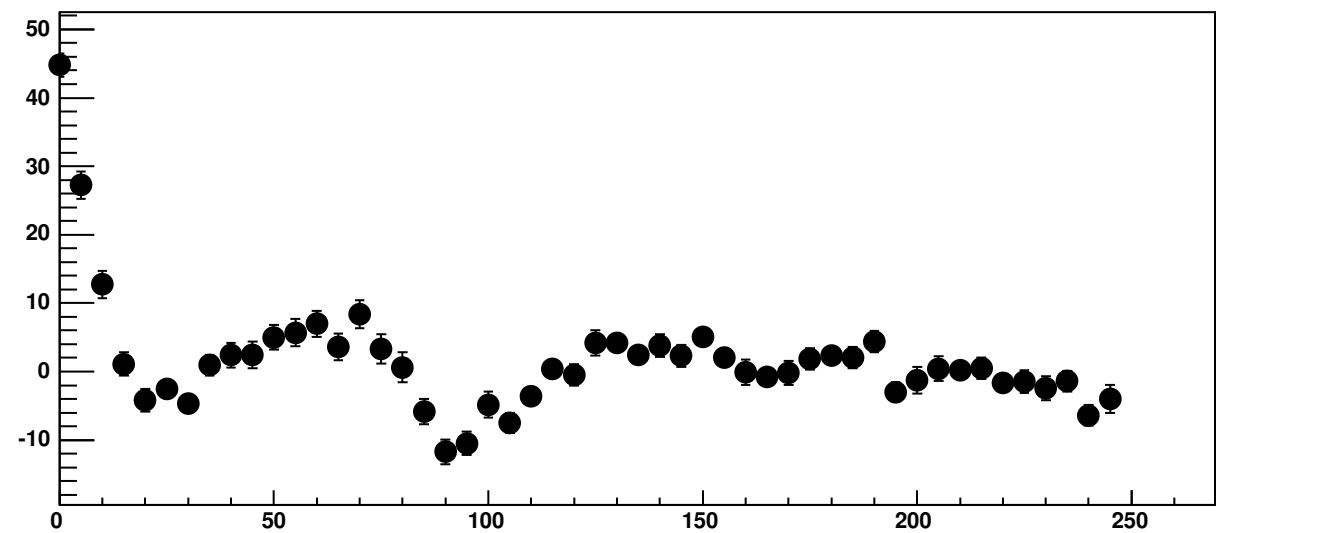
Chip 7, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

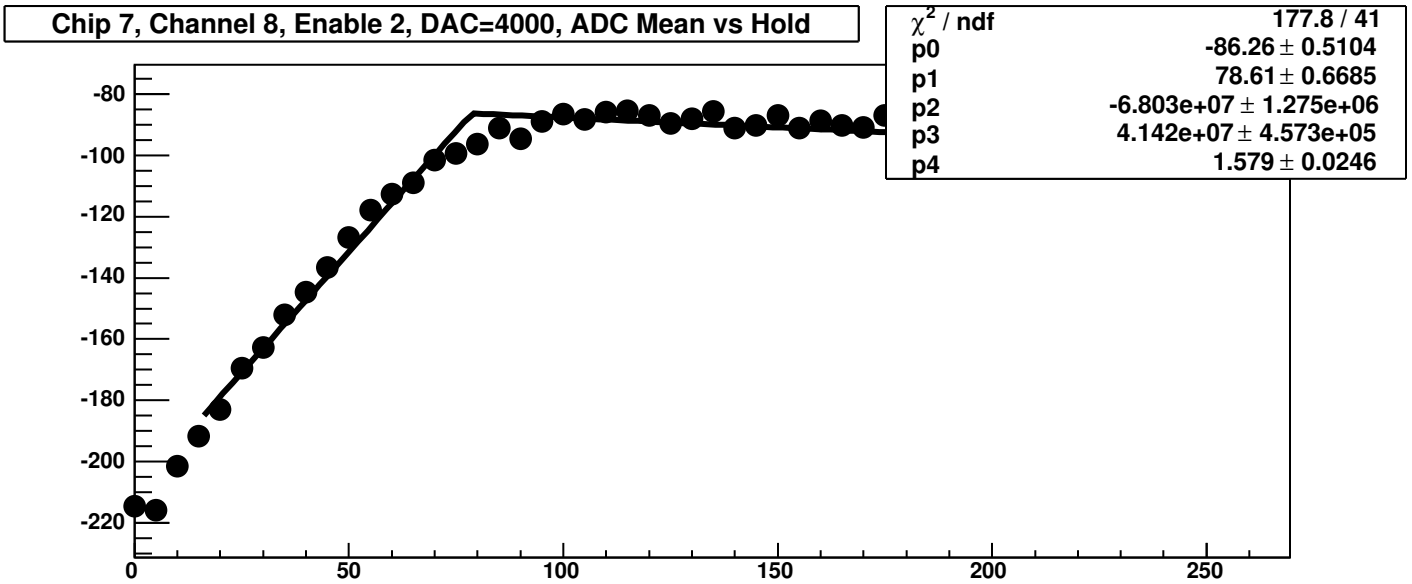


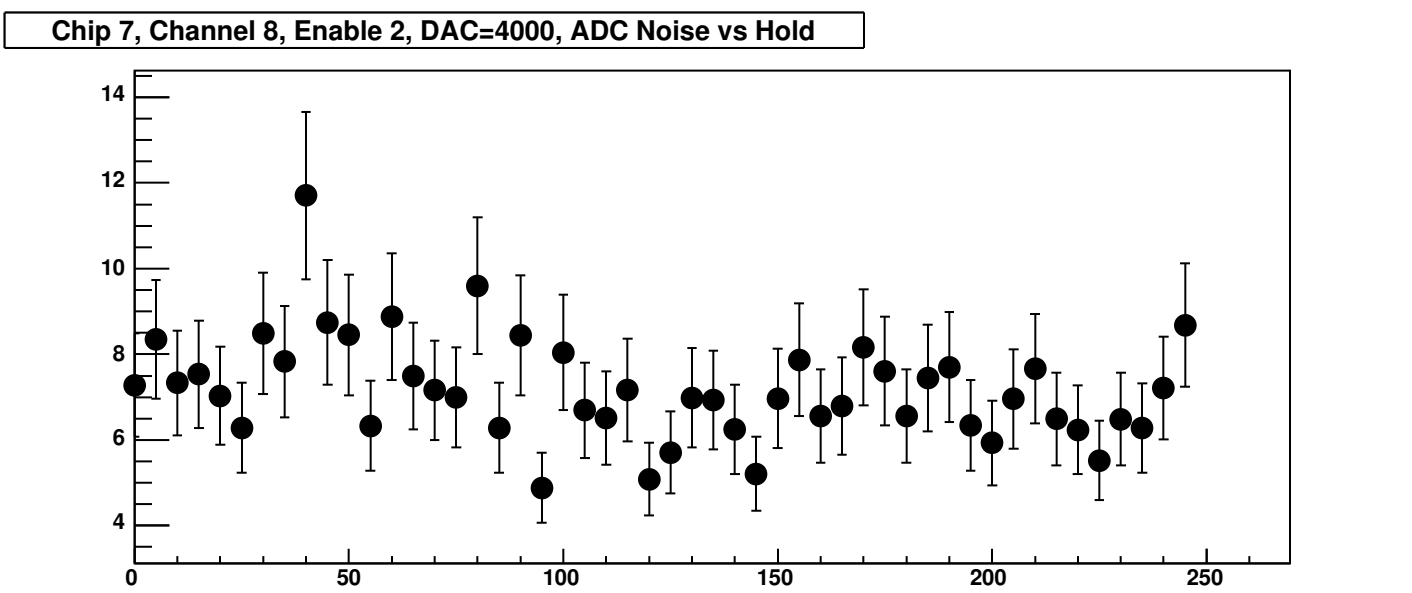
Chip 7, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold

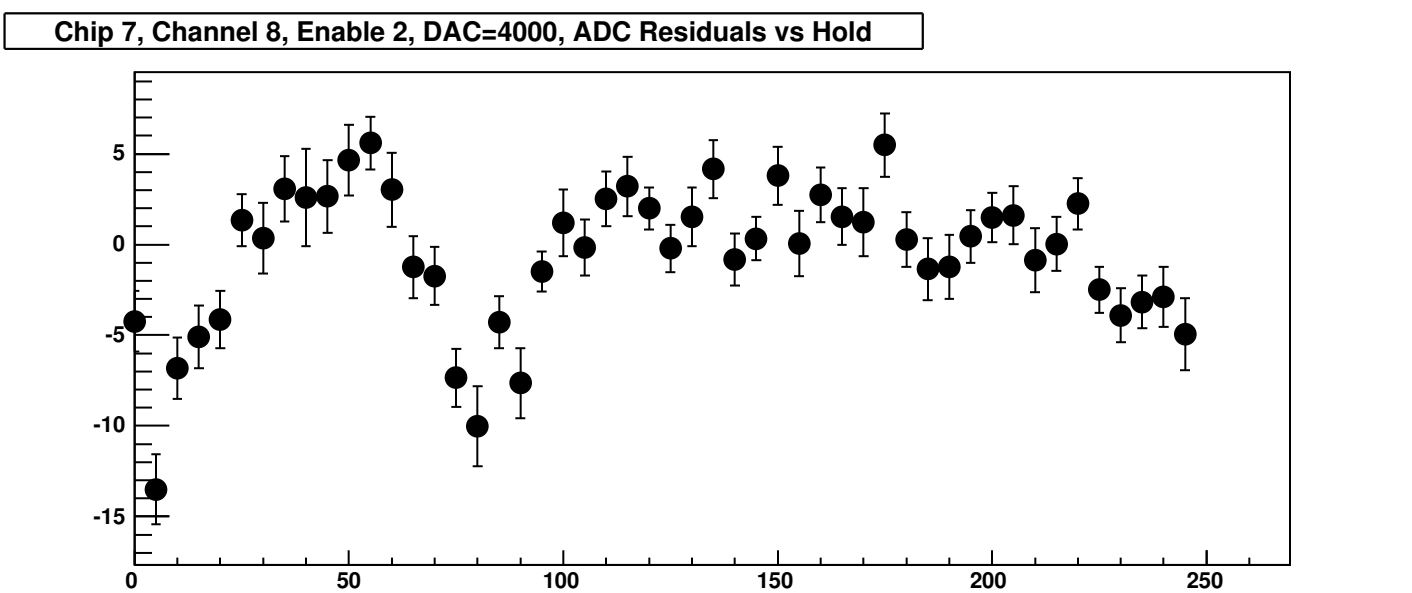


Chip 7, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

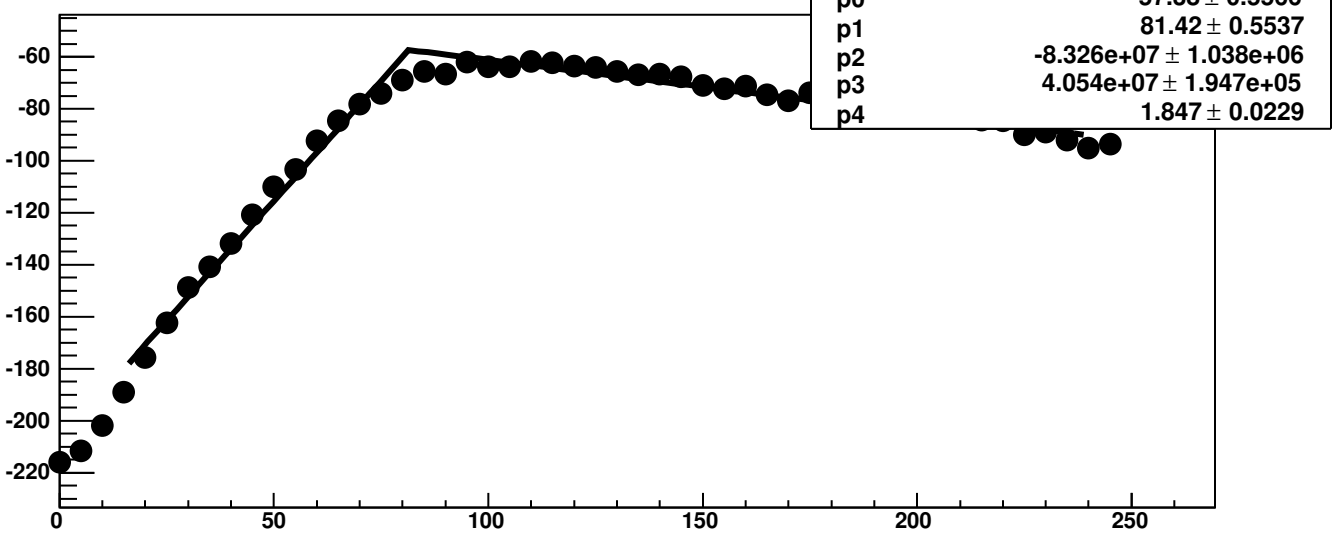



Chip 7, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 177.8 / 41
p0 -86.26 ± 0.5104
p1 78.61 ± 0.6685
p2 -6.803e+07 ± 1.275e+06
p3 4.142e+07 ± 4.573e+05
p4 1.579 ± 0.0246
-80
-100
-120
-140
-160
-180
-200
-220
0
50
100
150
200
250

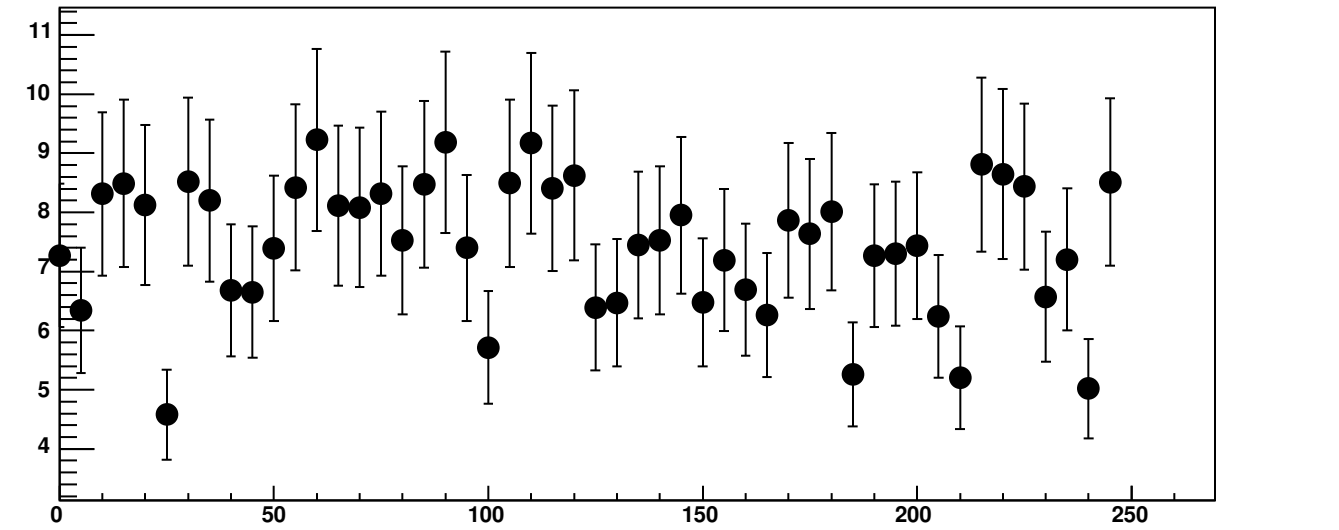

Chip 7, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 7, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold
5
0
-5
-10
-15
0
50
100
150
200
250

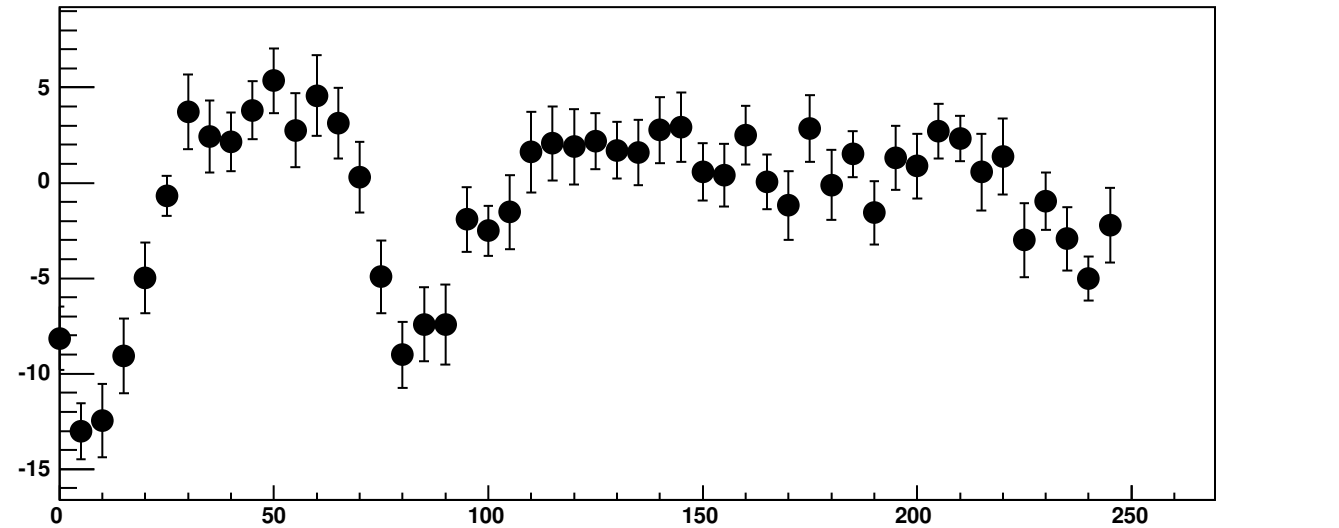
Chip 7, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold



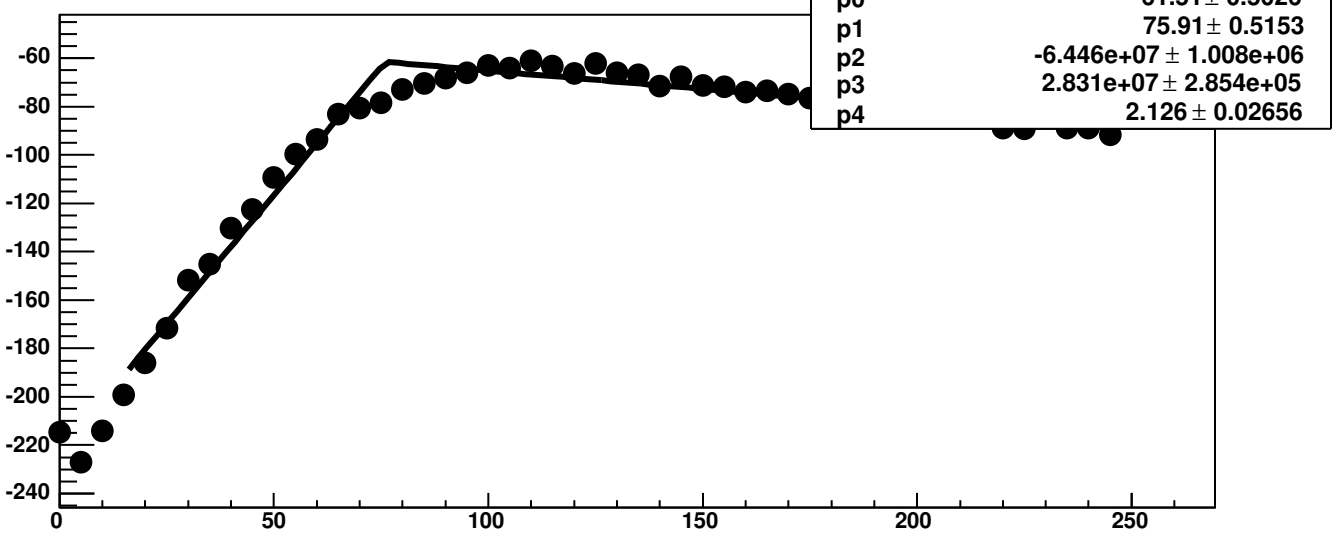
Chip 7, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



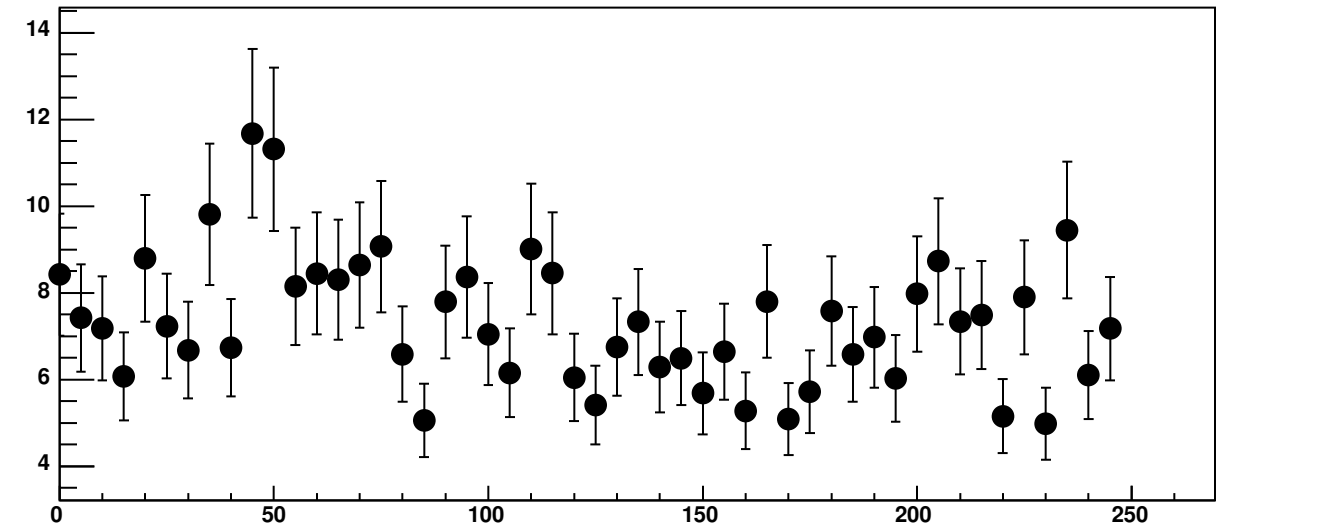
Chip 7, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold



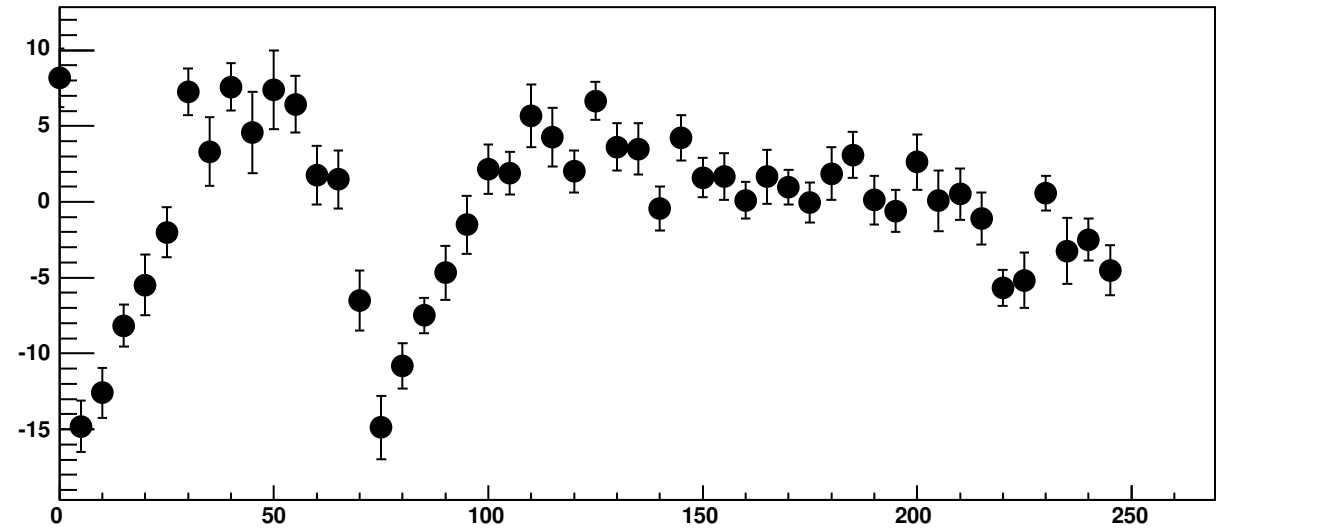
Chip 7, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold



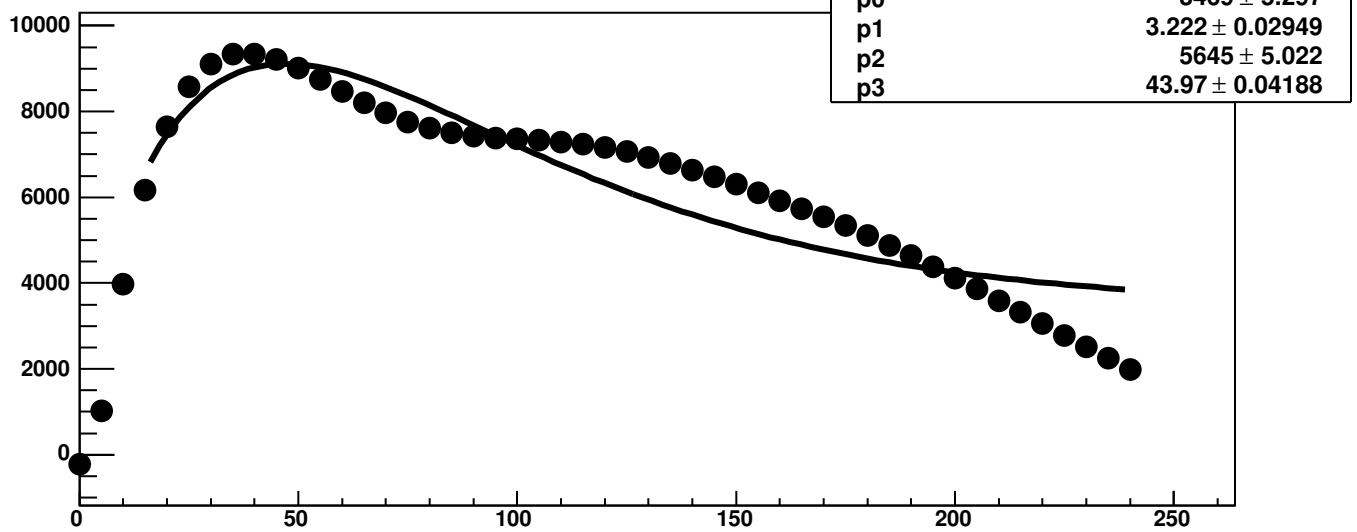
Chip 7, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



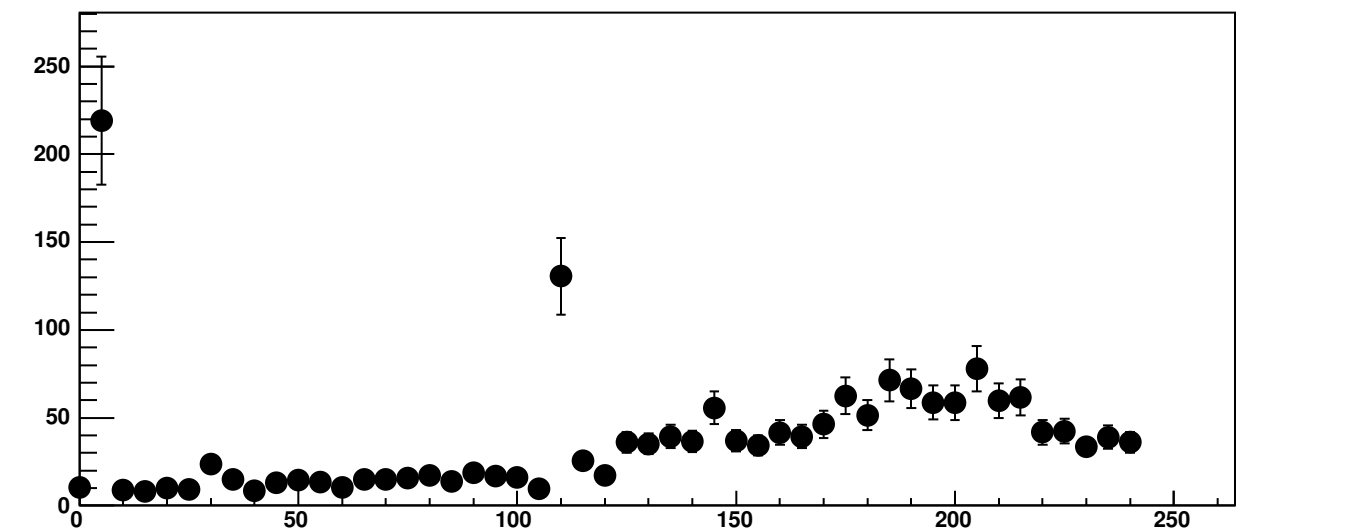
Chip 7, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold



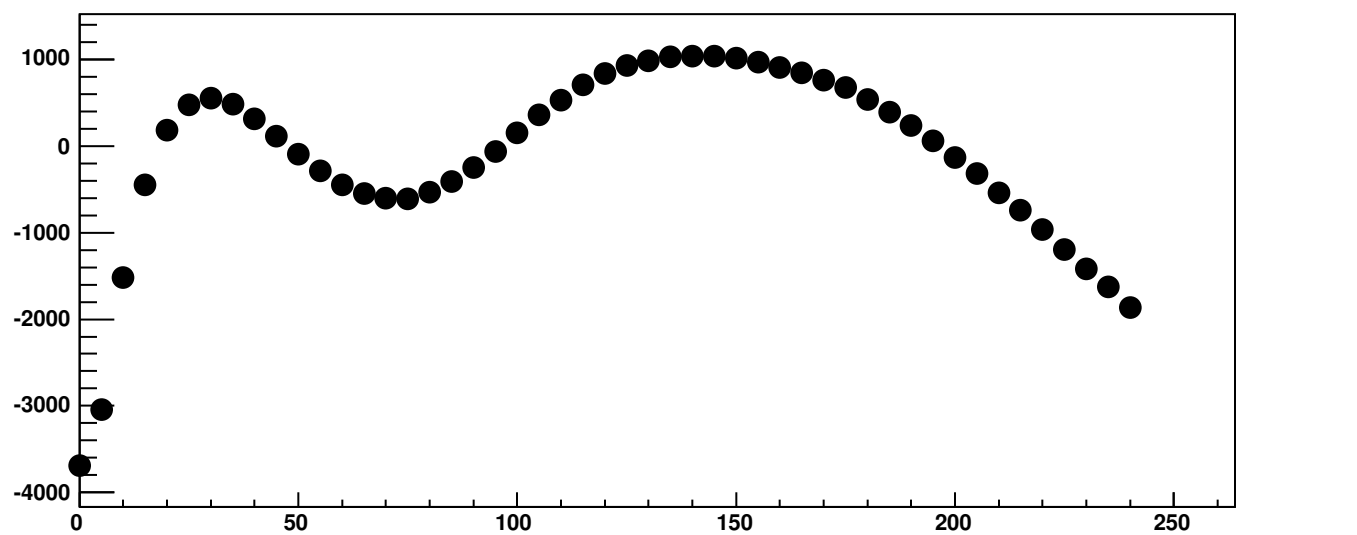
Chip 7, Channel 8, Enable 5!, DAC=4000, ADC Mean vs Hold

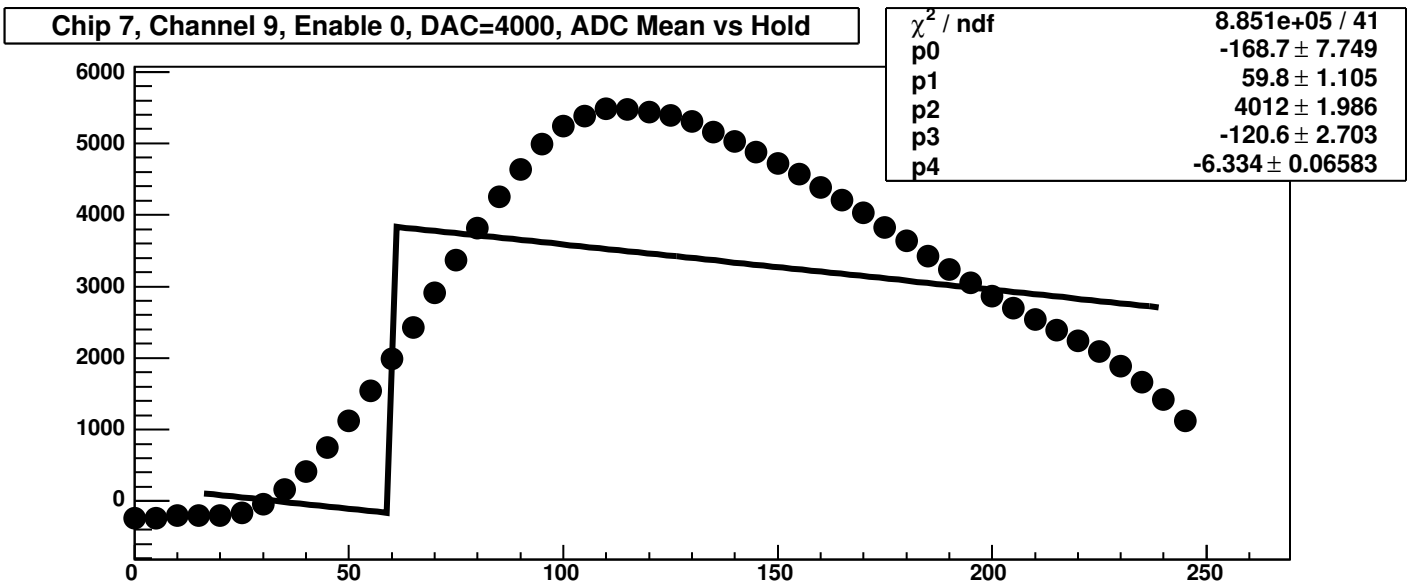


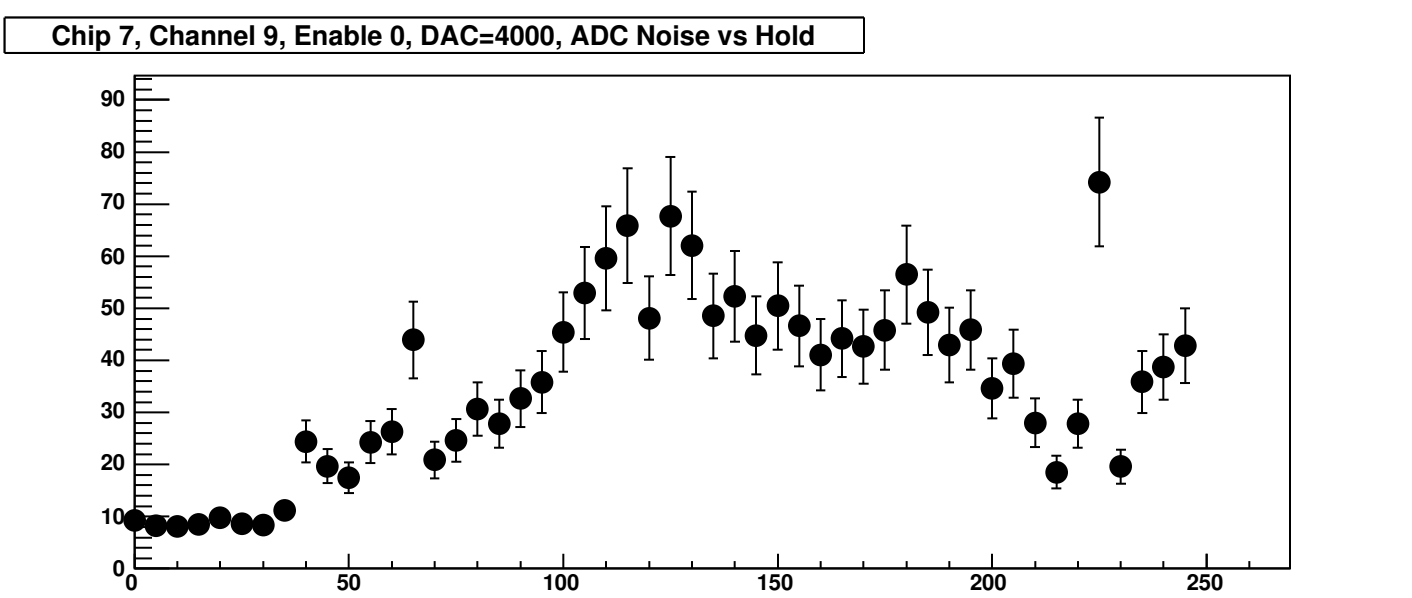
Chip 7, Channel 8, Enable 5!, DAC=4000, ADC Noise vs Hold

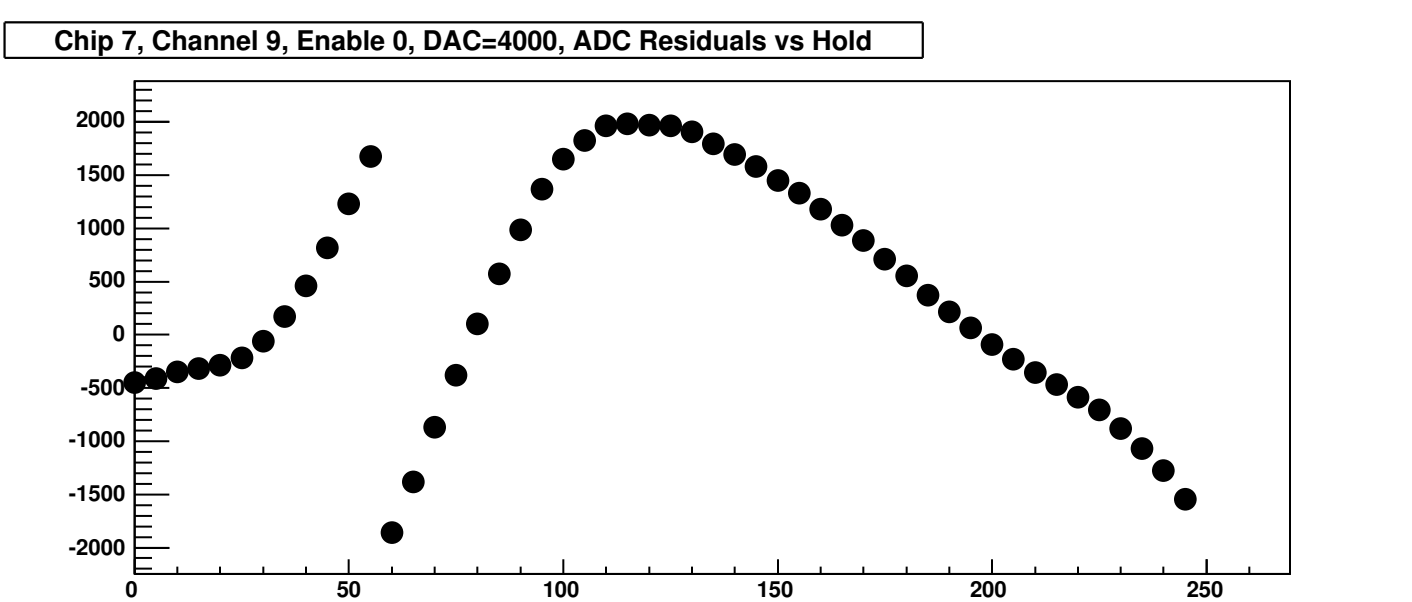


Chip 7, Channel 8, Enable 5!, DAC=4000, ADC Residuals vs Hold

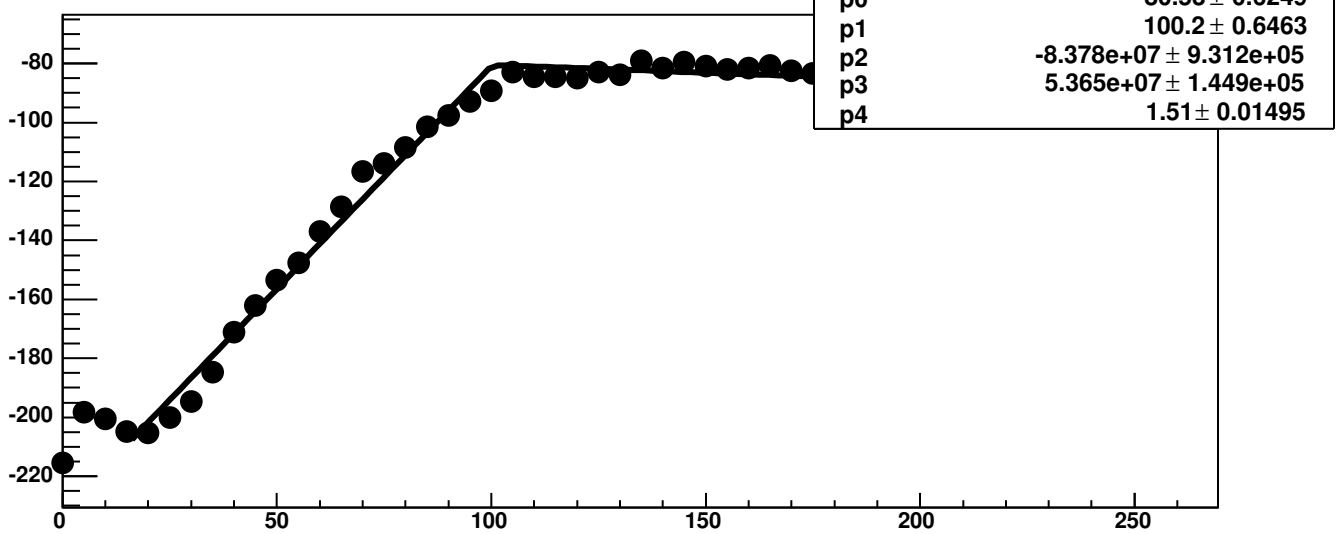



Chip 7, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 8.851e+05 / 41
p0 -168.7 ± 7.749
p1 59.8 ± 1.105
p2 4012 ± 1.986
p3 -120.6 ± 2.703
p4 -6.334 ± 0.06583
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

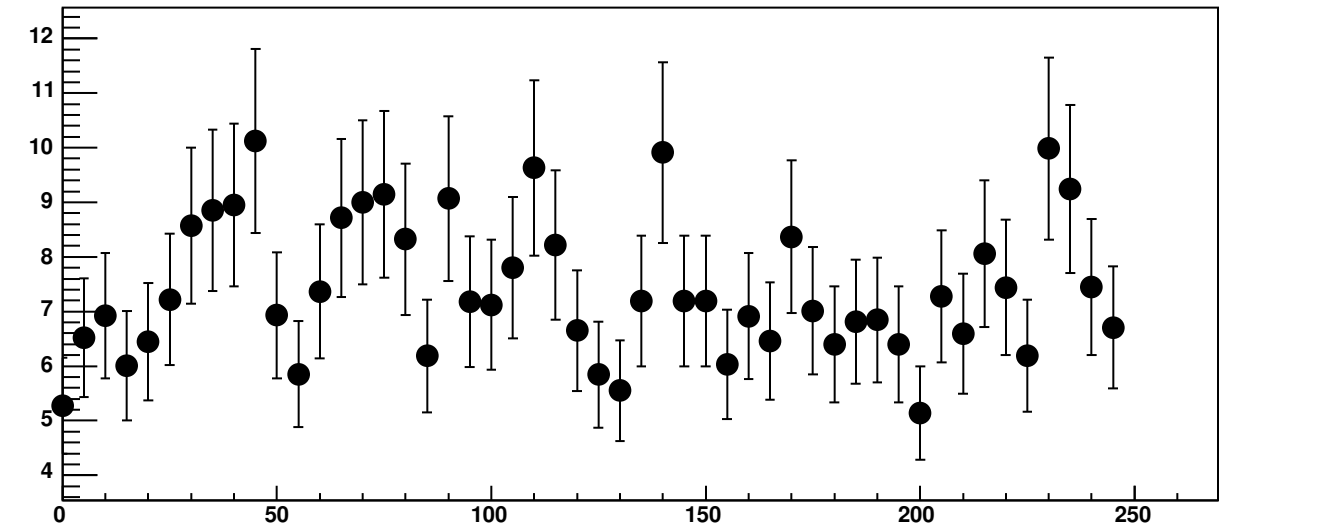

Chip 7, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold
90
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250


Chip 7, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

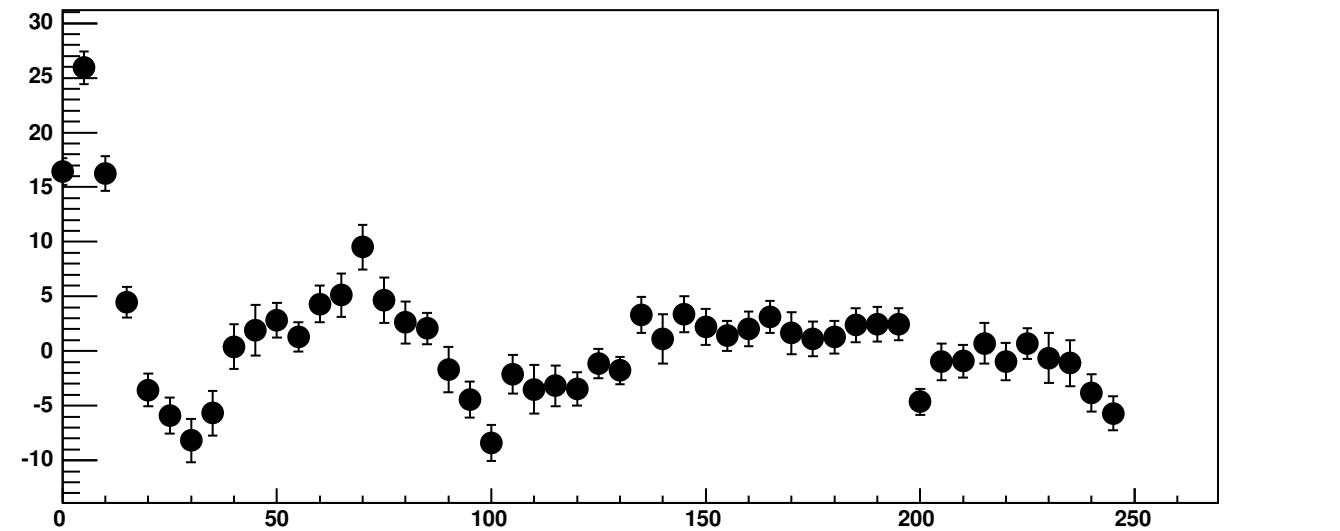
Chip 7, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold



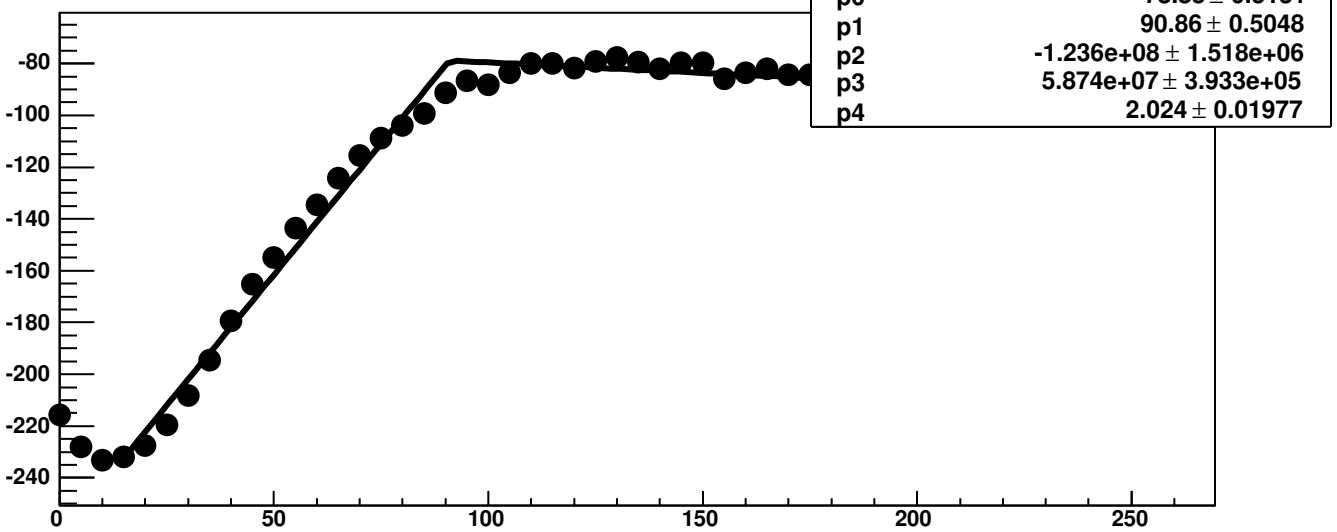
Chip 7, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



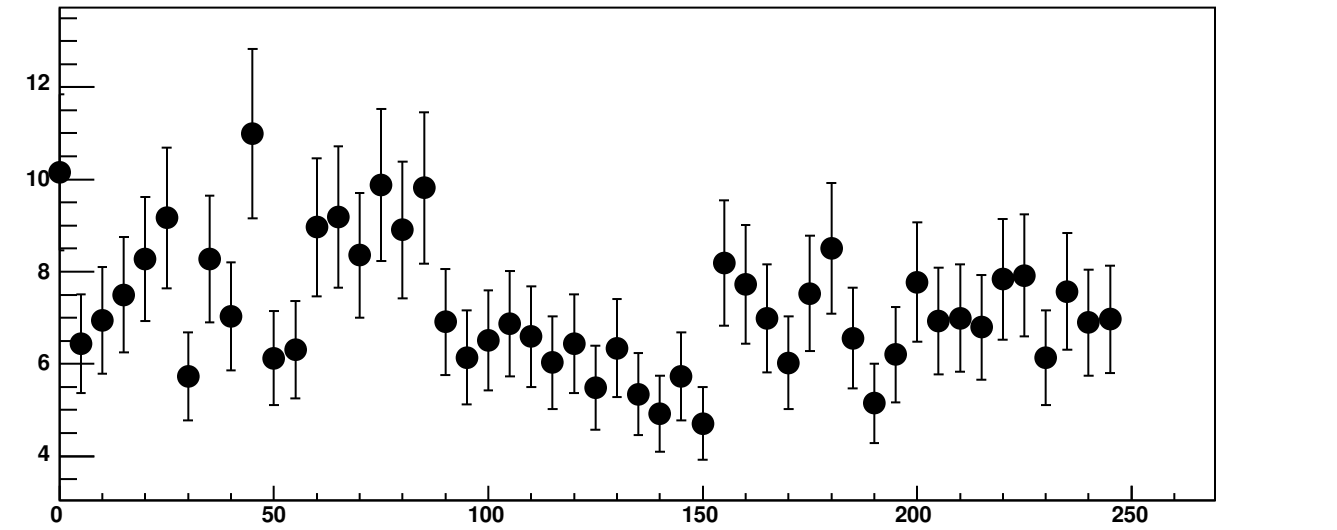
Chip 7, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold



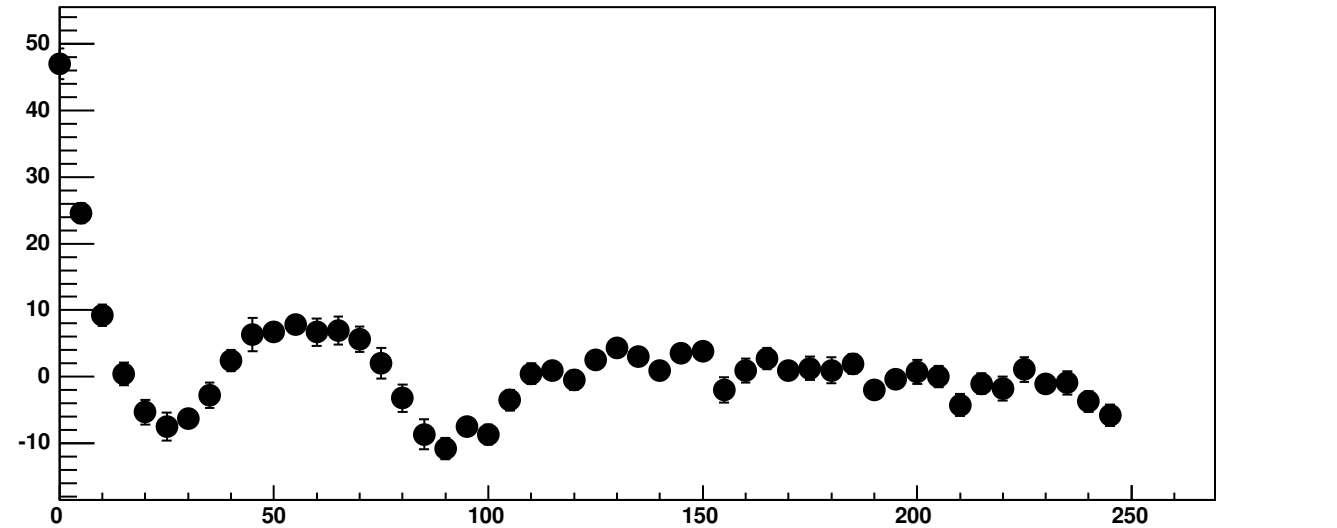
Chip 7, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold



Chip 7, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold

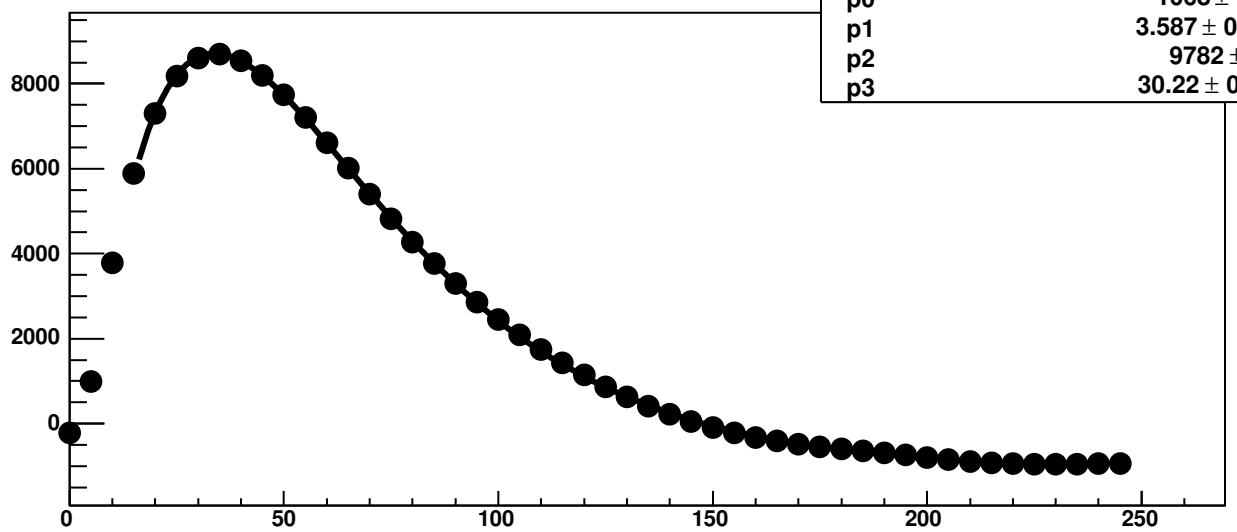


Chip 7, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold

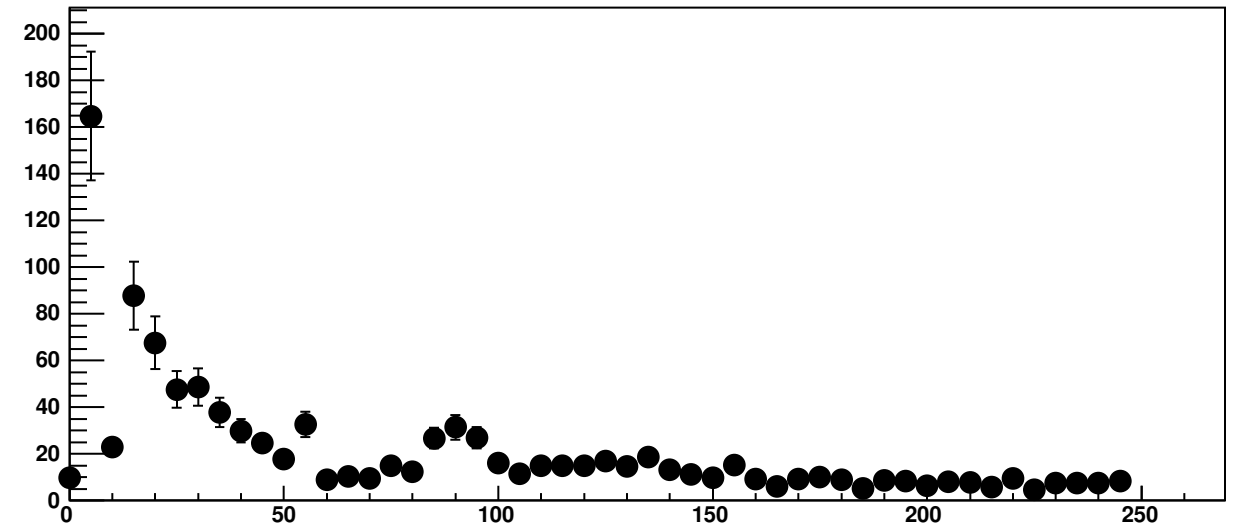


Chip 7, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold

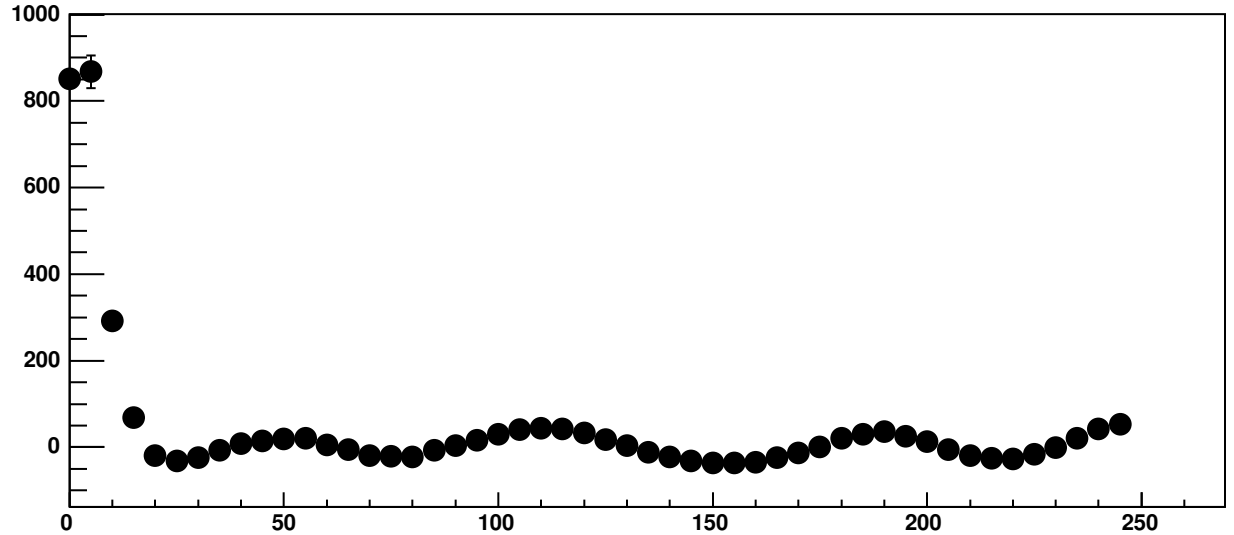
χ^2 / ndf	5010 / 42
p0	-1068 ± 0.6195
p1	3.587 ± 0.02916
p2	9782 ± 2.882
p3	30.22 ± 0.01103



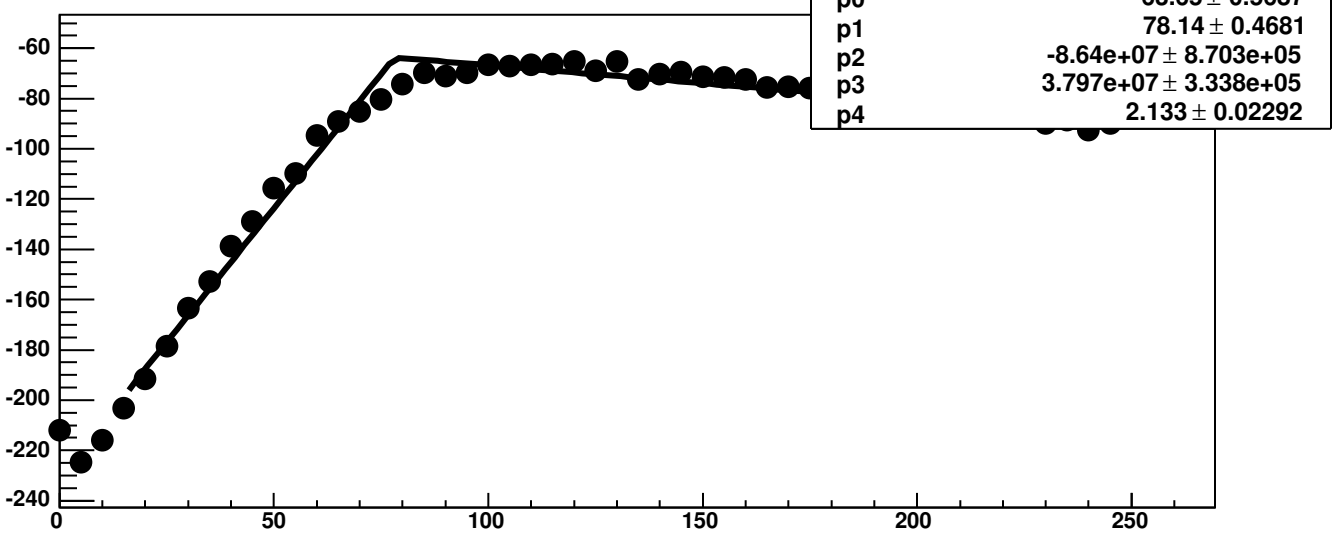
Chip 7, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold



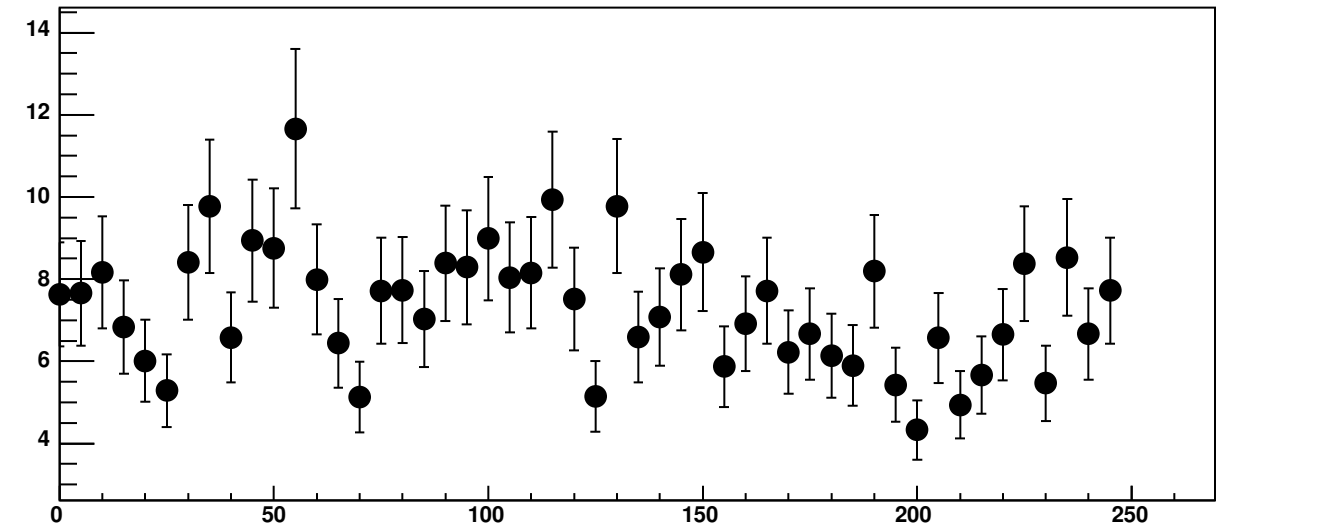
Chip 7, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold



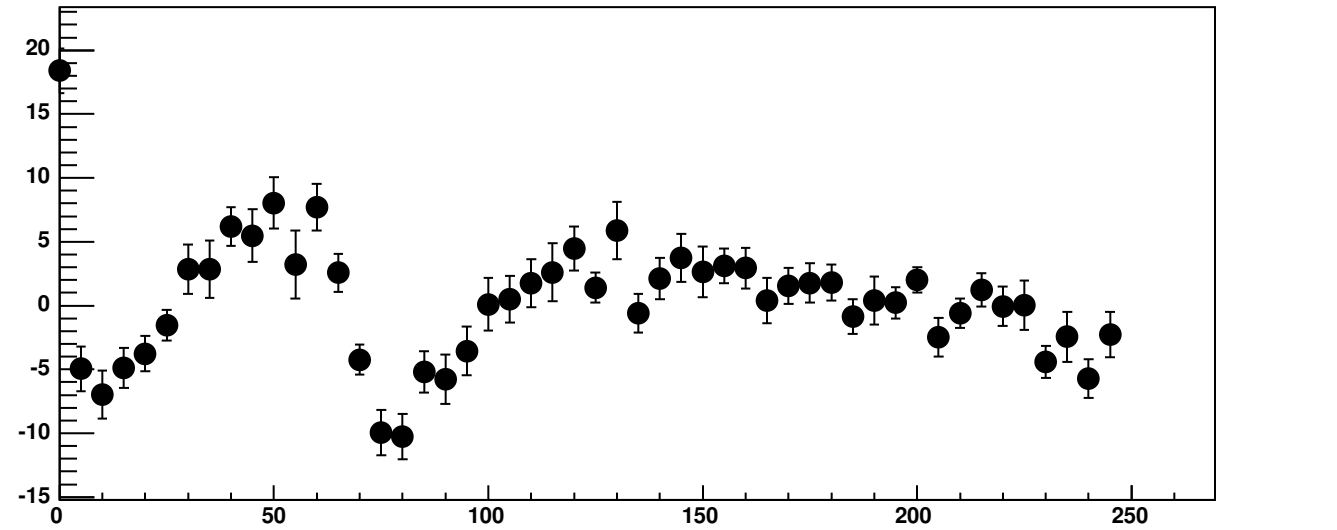
Chip 7, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

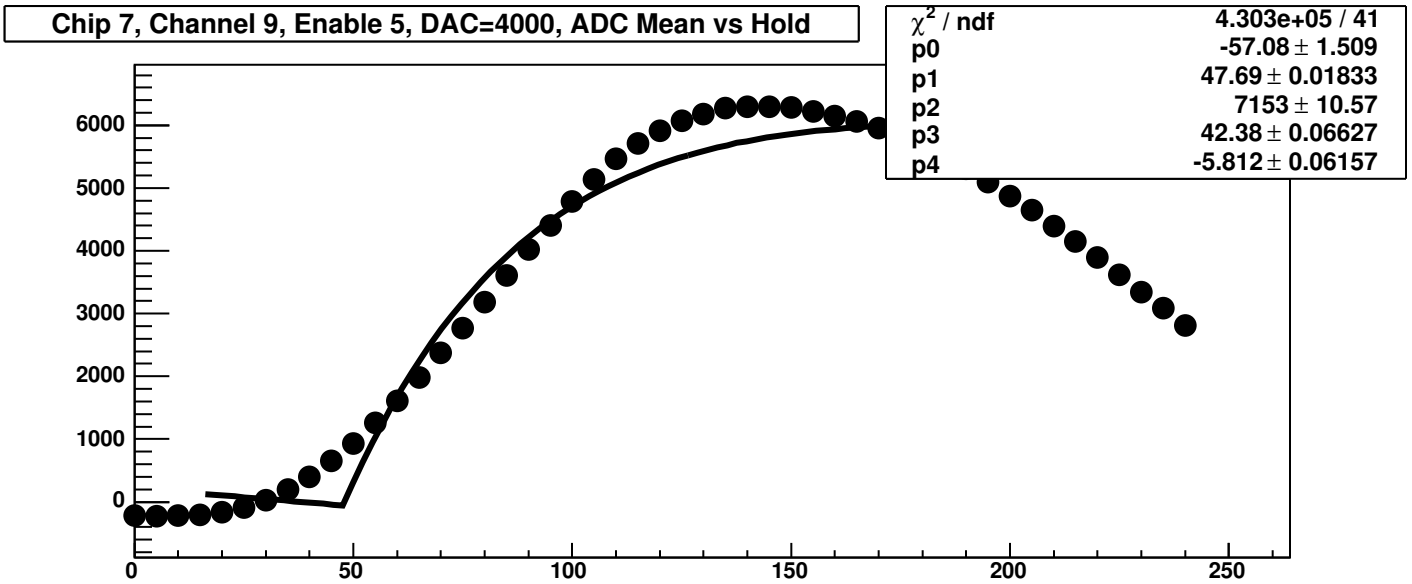


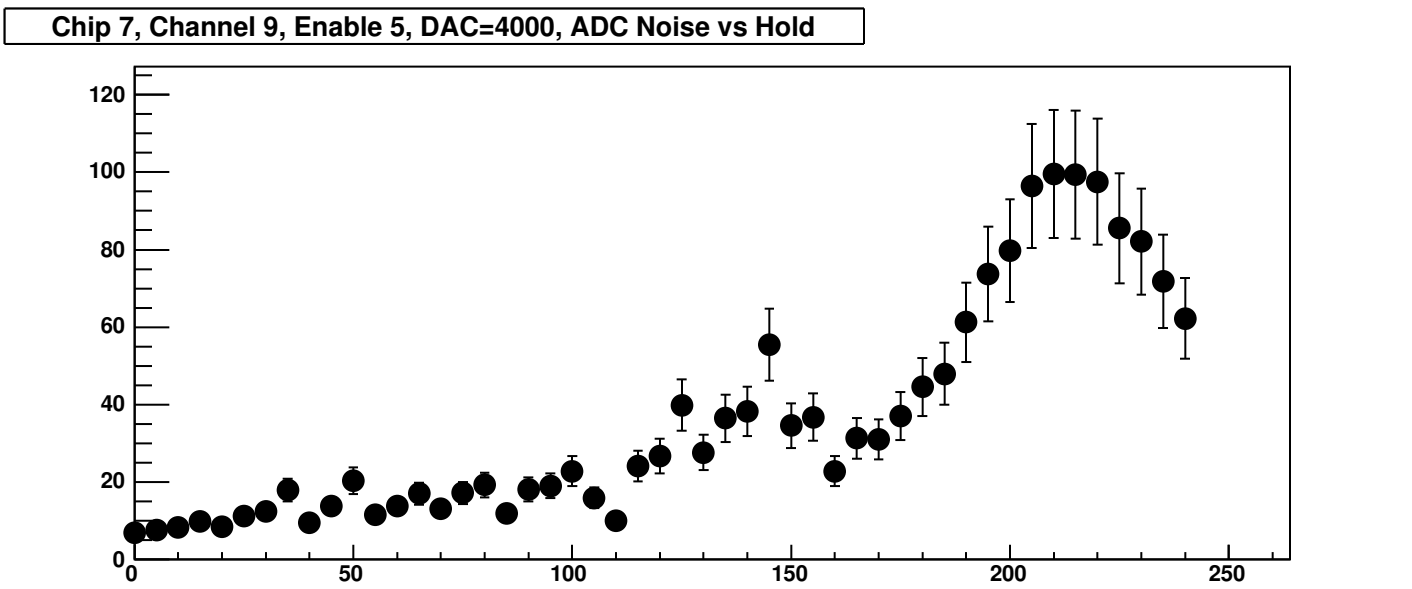
Chip 7, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold

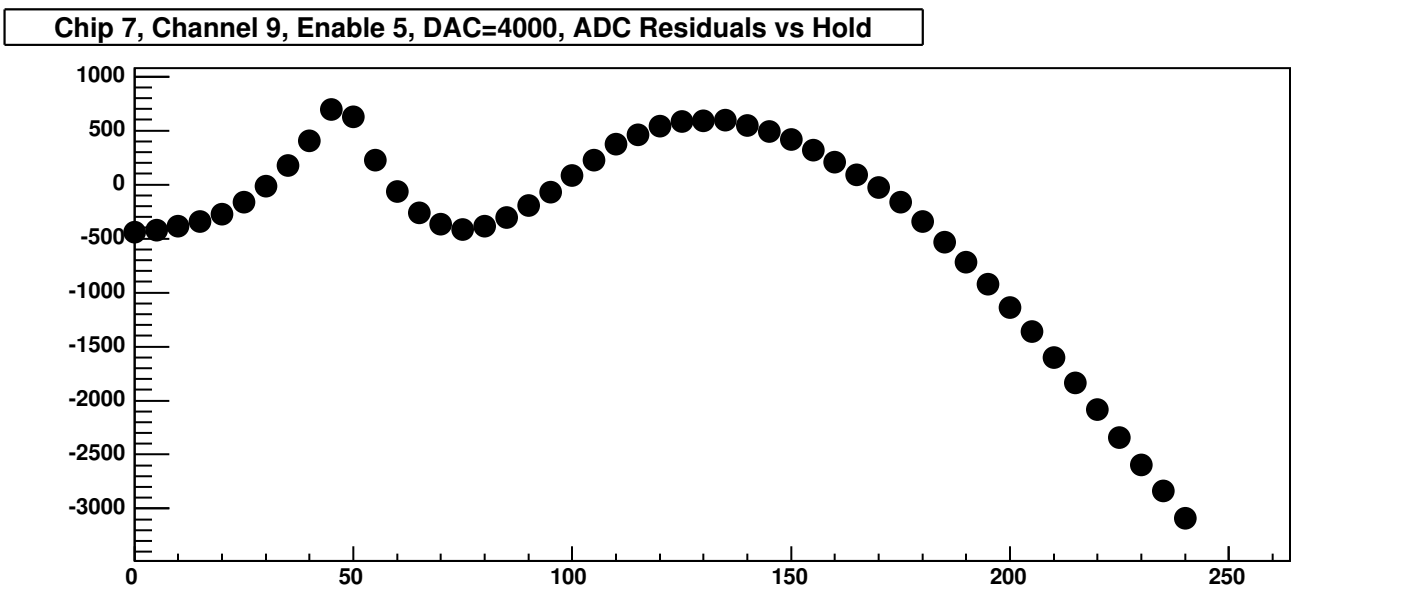


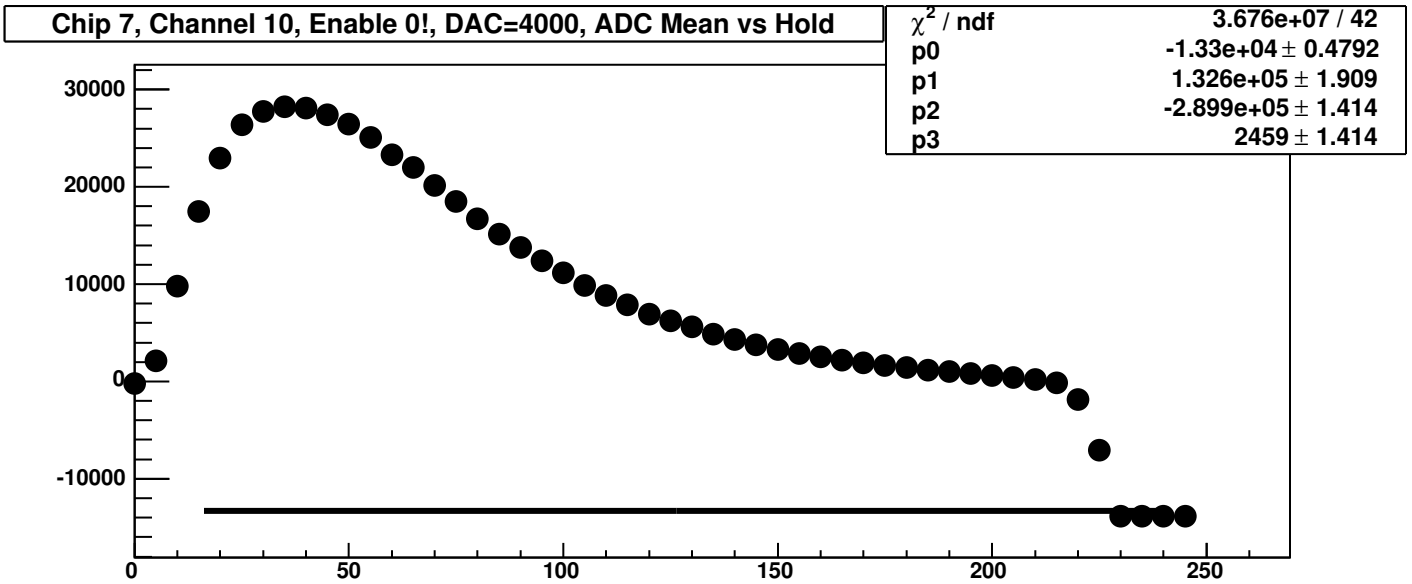
Chip 7, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

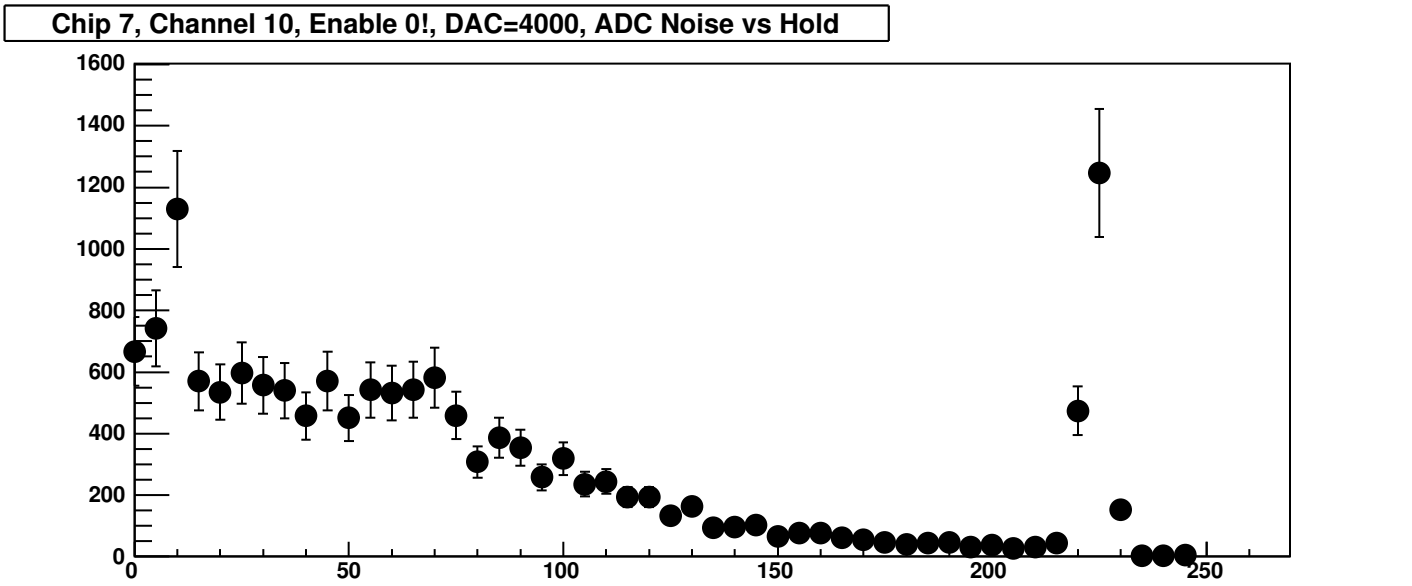


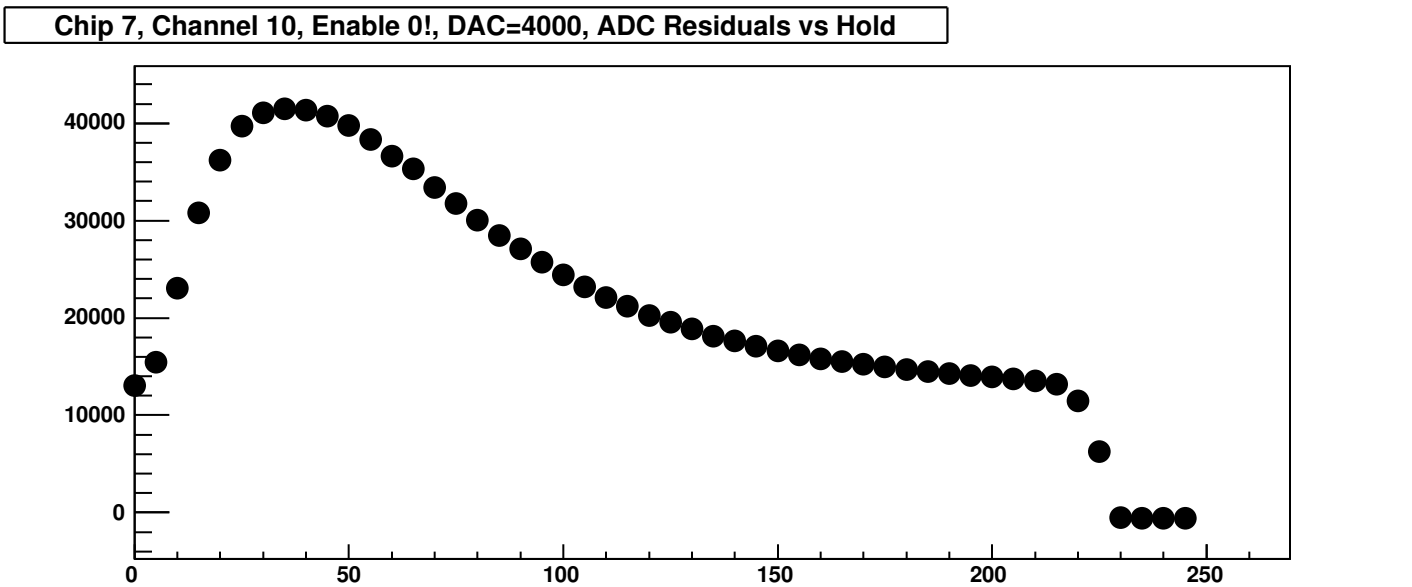

Chip 7, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.303e+05 / 41
p0 -57.08 ± 1.509
p1 47.69 ± 0.01833
p2 7153 ± 10.57
p3 42.38 ± 0.06627
p4 -5.812 ± 0.06157
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 7, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250

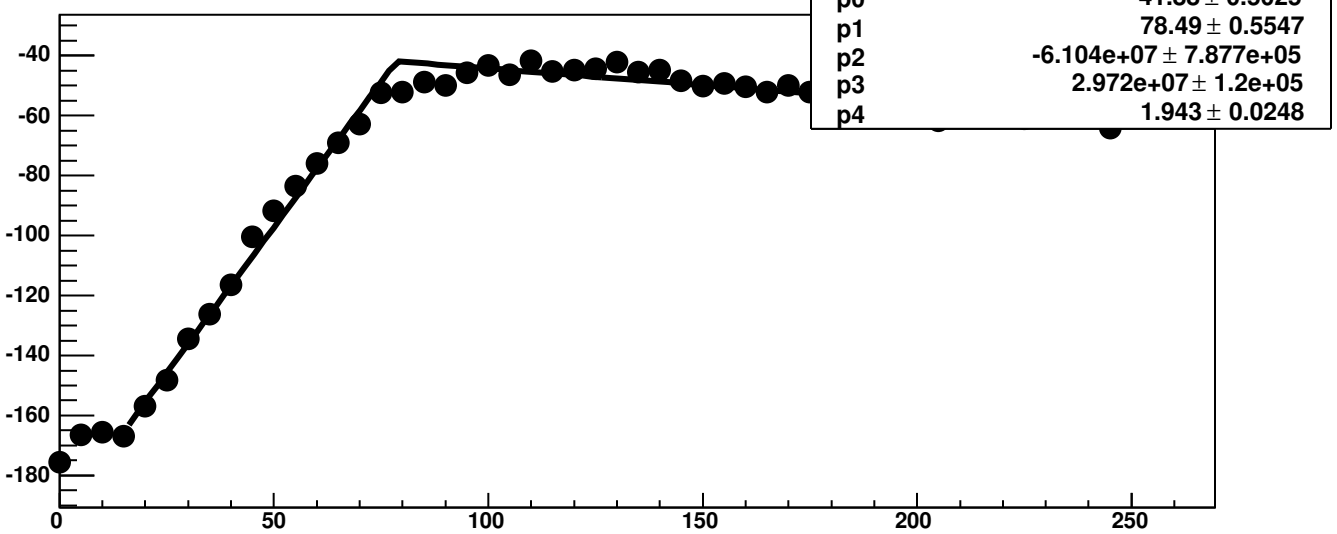

Chip 7, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250


Chip 7, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.676e+07 / 42
p0 -1.33e+04 ± 0.4792
p1 1.326e+05 ± 1.909
p2 -2.899e+05 ± 1.414
p3 2459 ± 1.414
30000
20000
10000
0
-10000
0
50
100
150
200
250

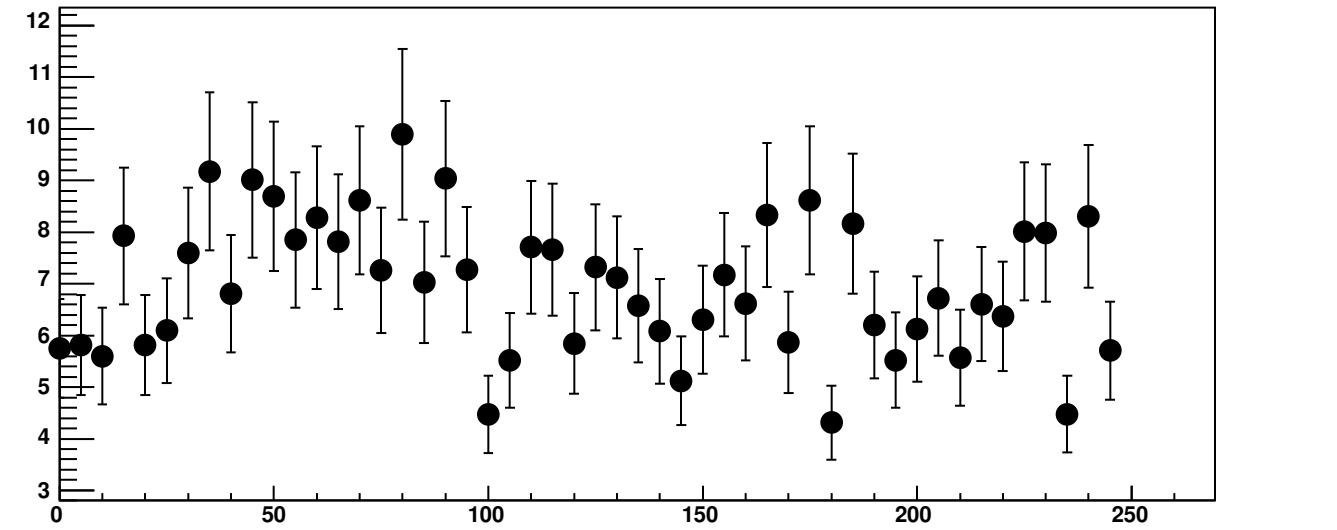

Chip 7, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 7, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold
40000
30000
20000
10000
0
0
50
100
150
200
250

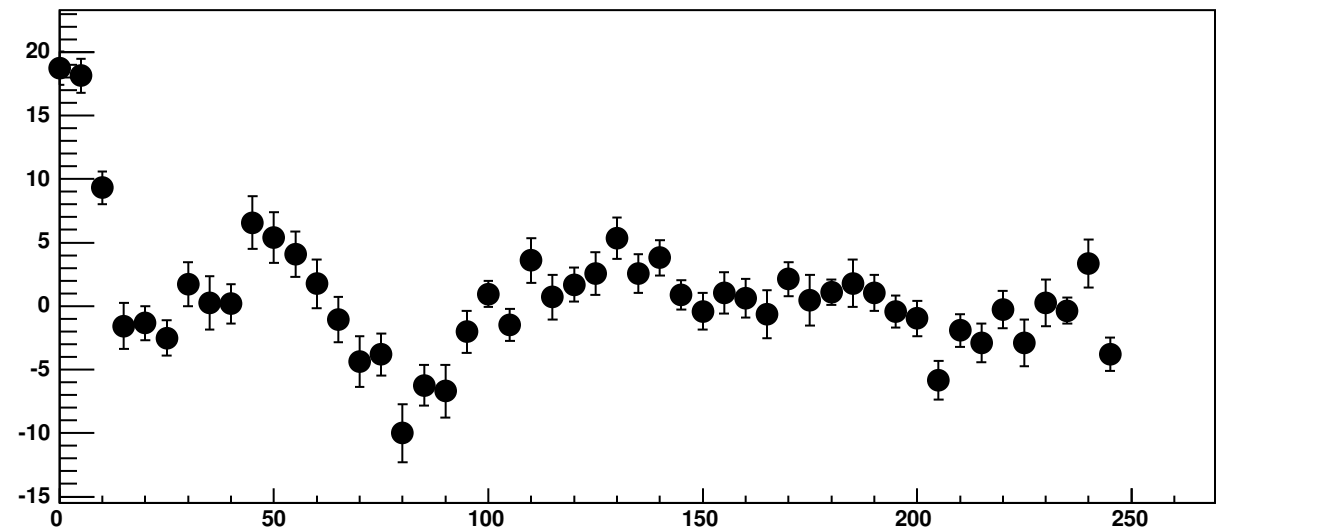
Chip 7, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold



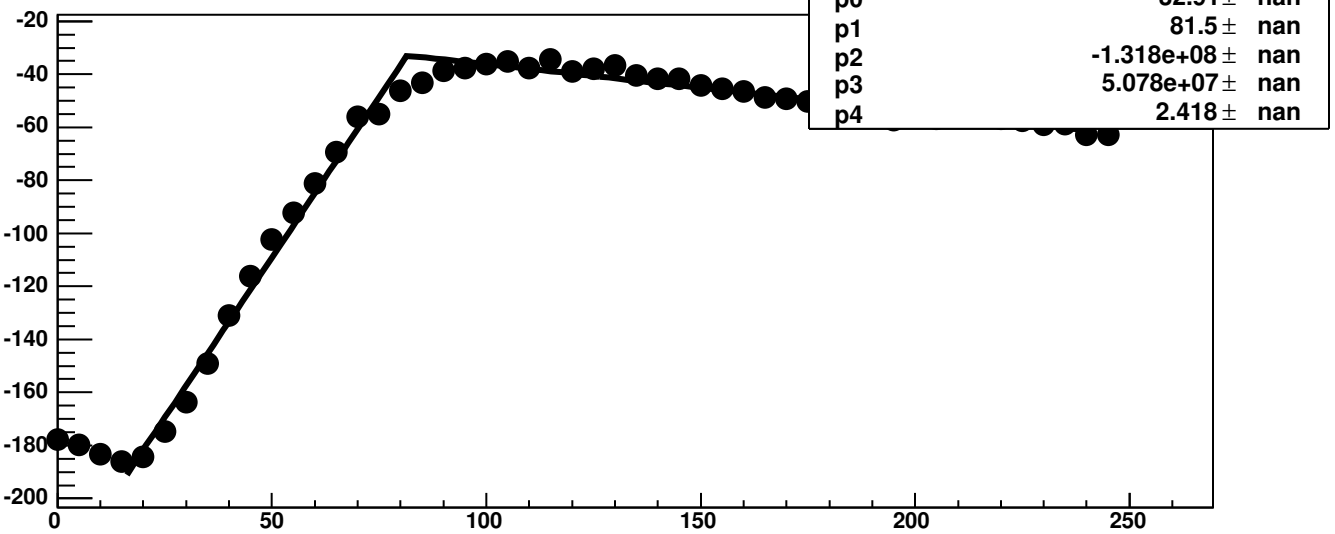
Chip 7, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



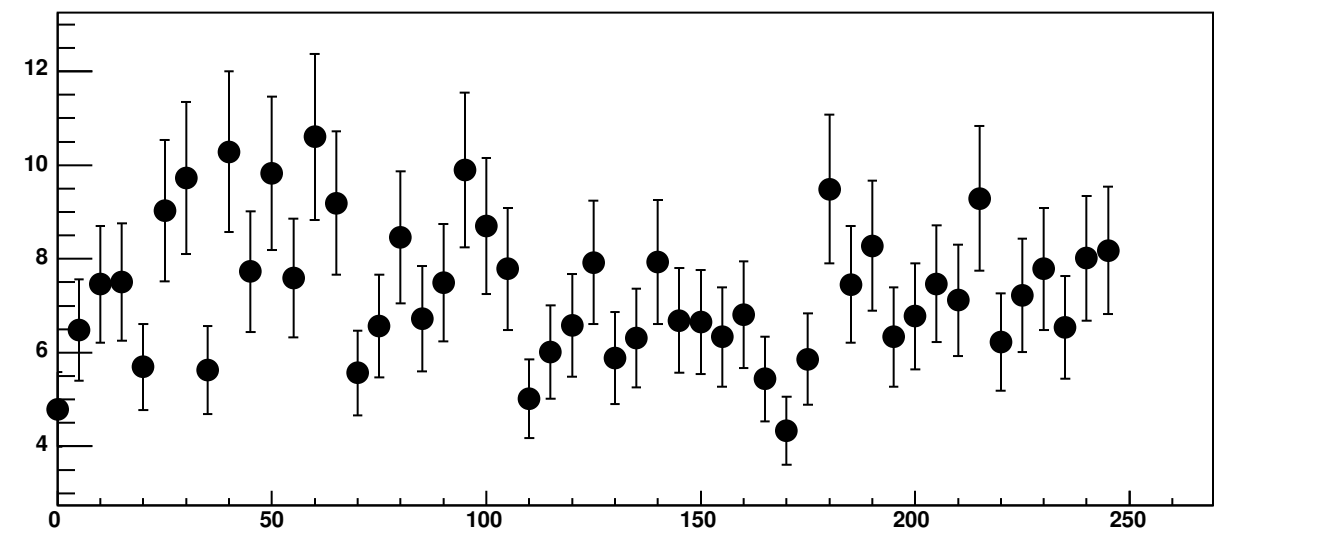
Chip 7, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold



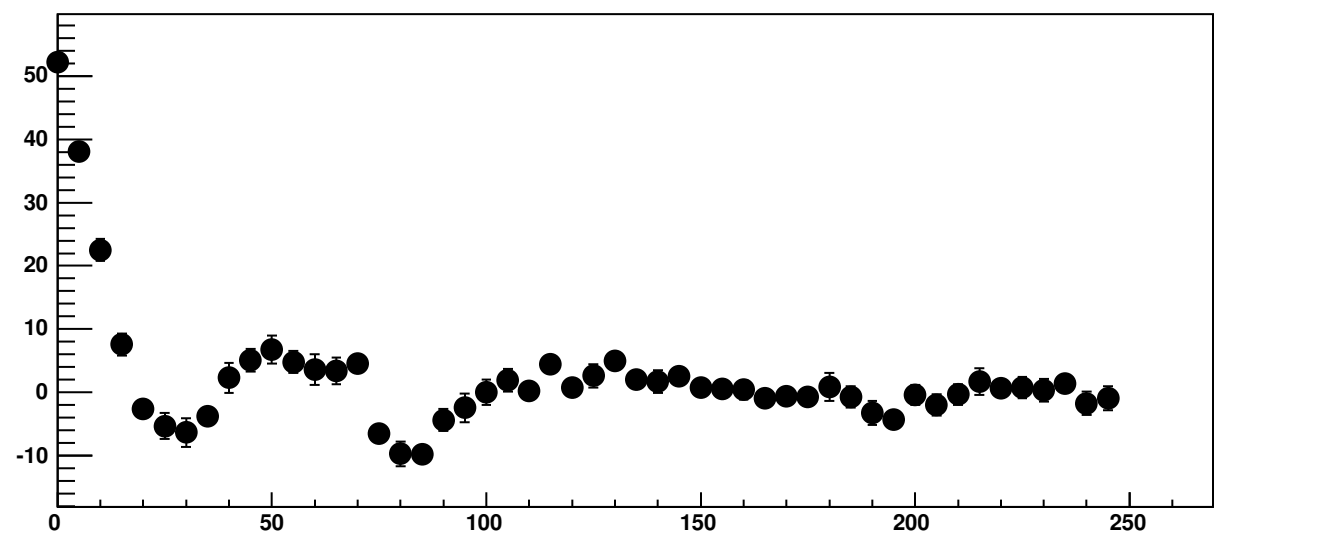
Chip 7, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold



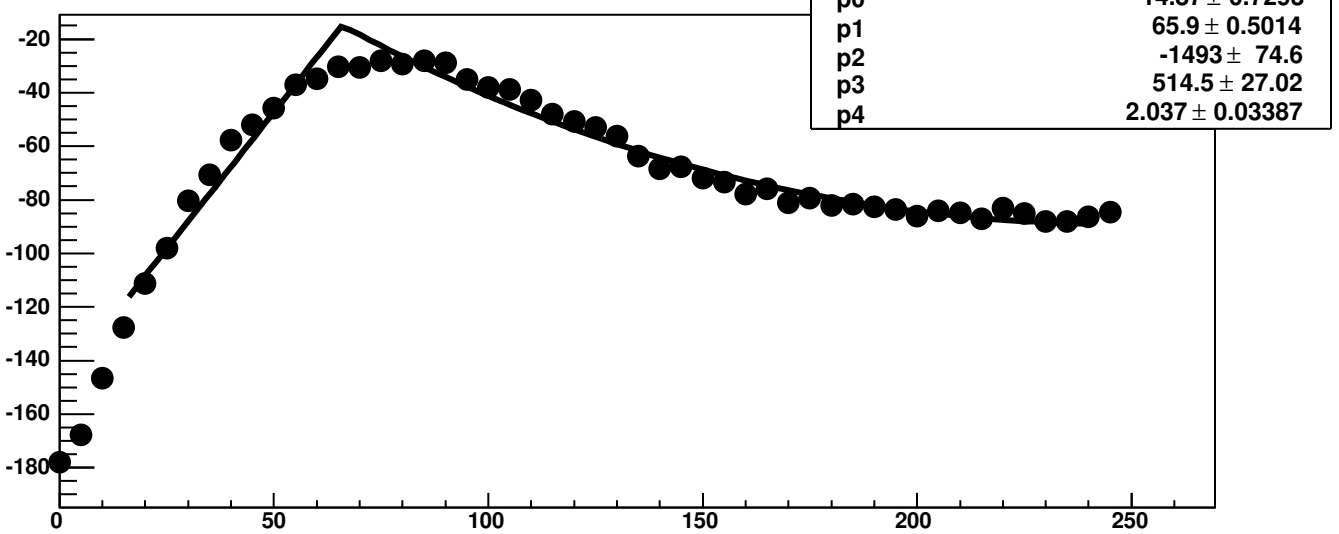
Chip 7, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



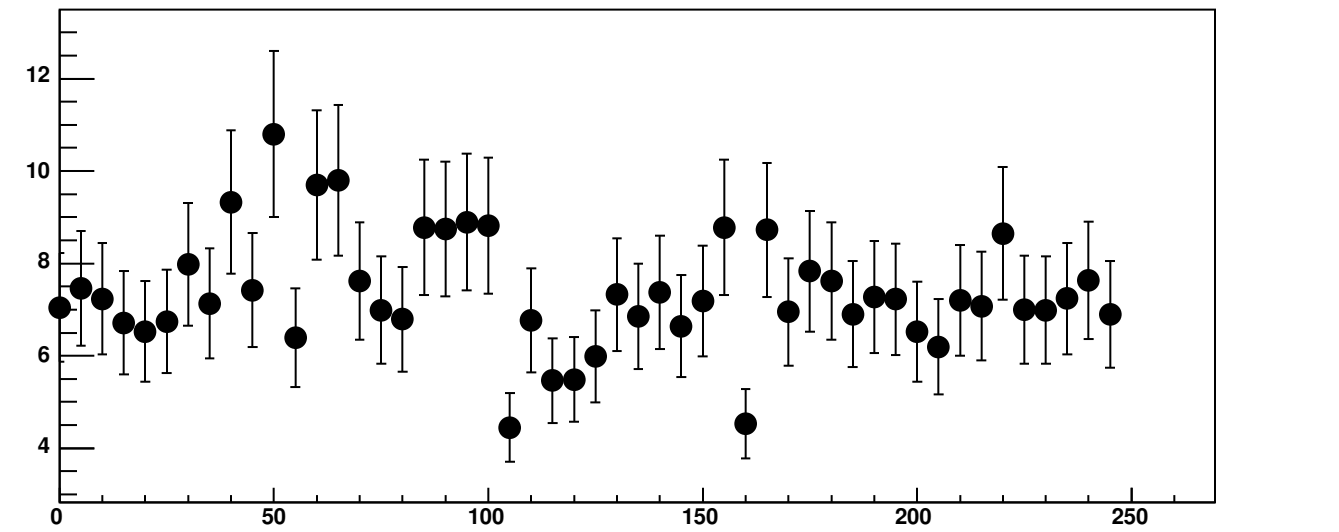
Chip 7, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold



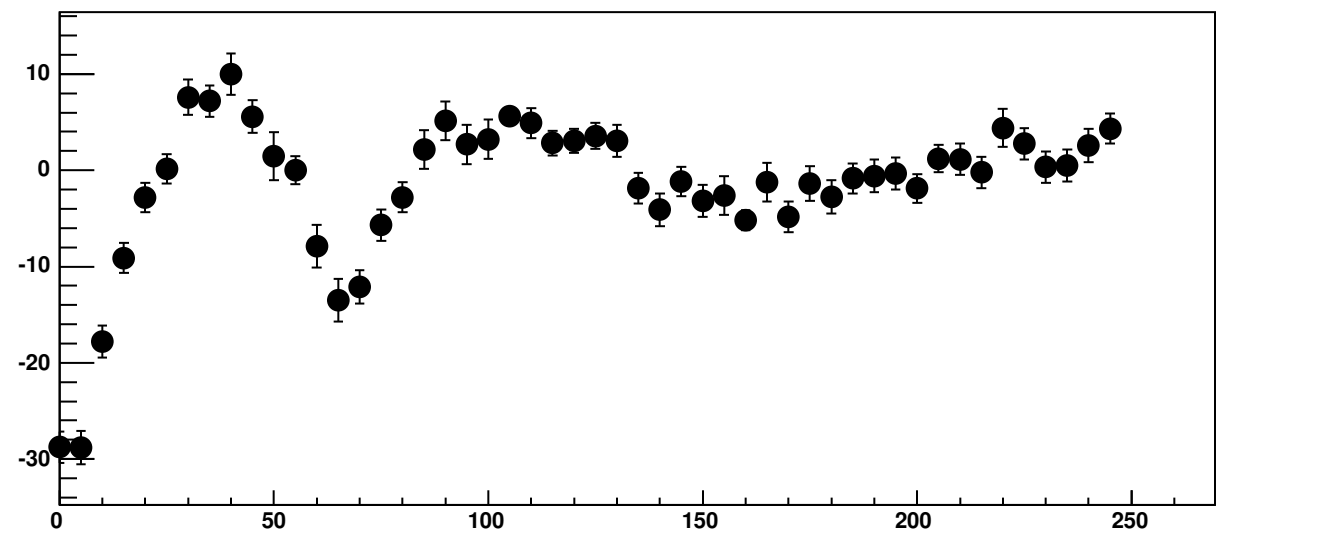
Chip 7, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold



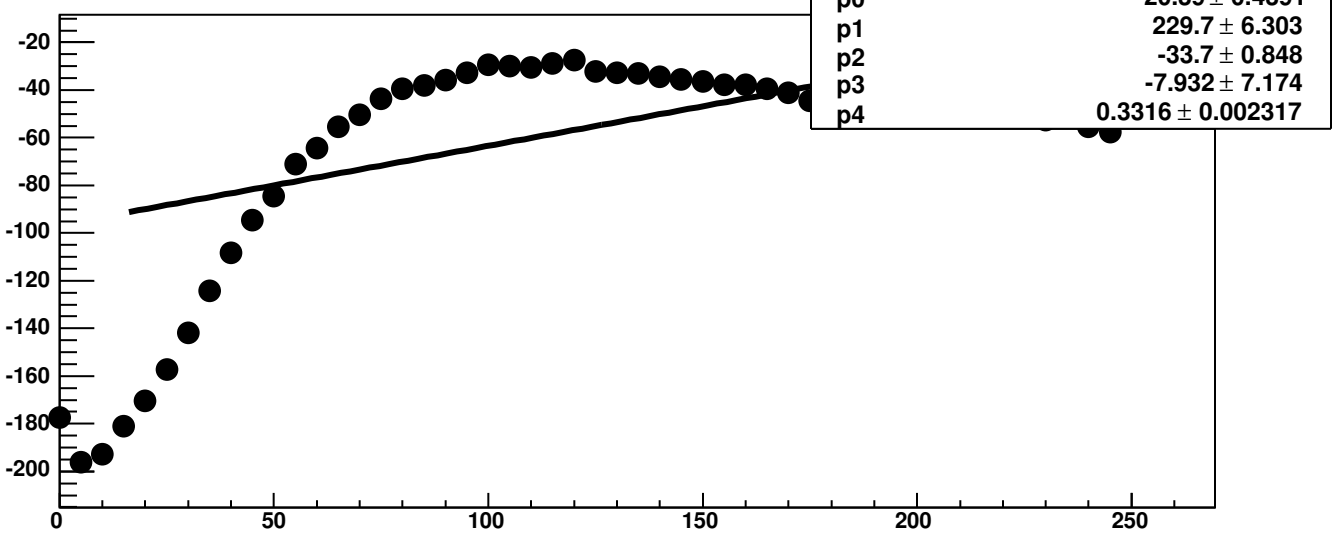
Chip 7, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold



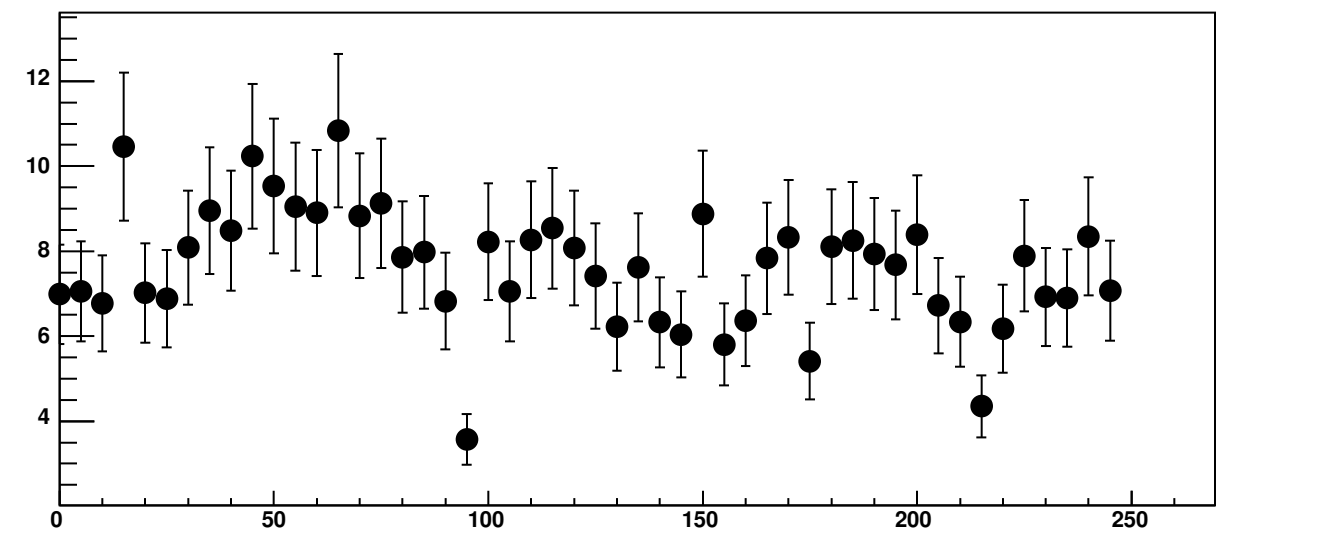
Chip 7, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold



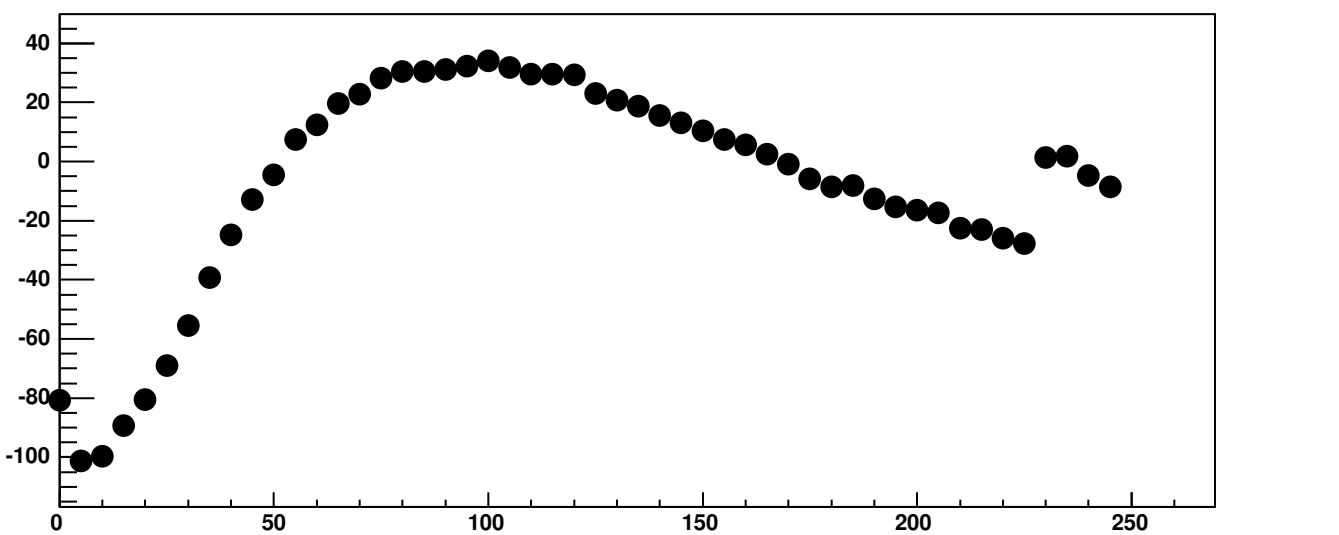
Chip 7, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

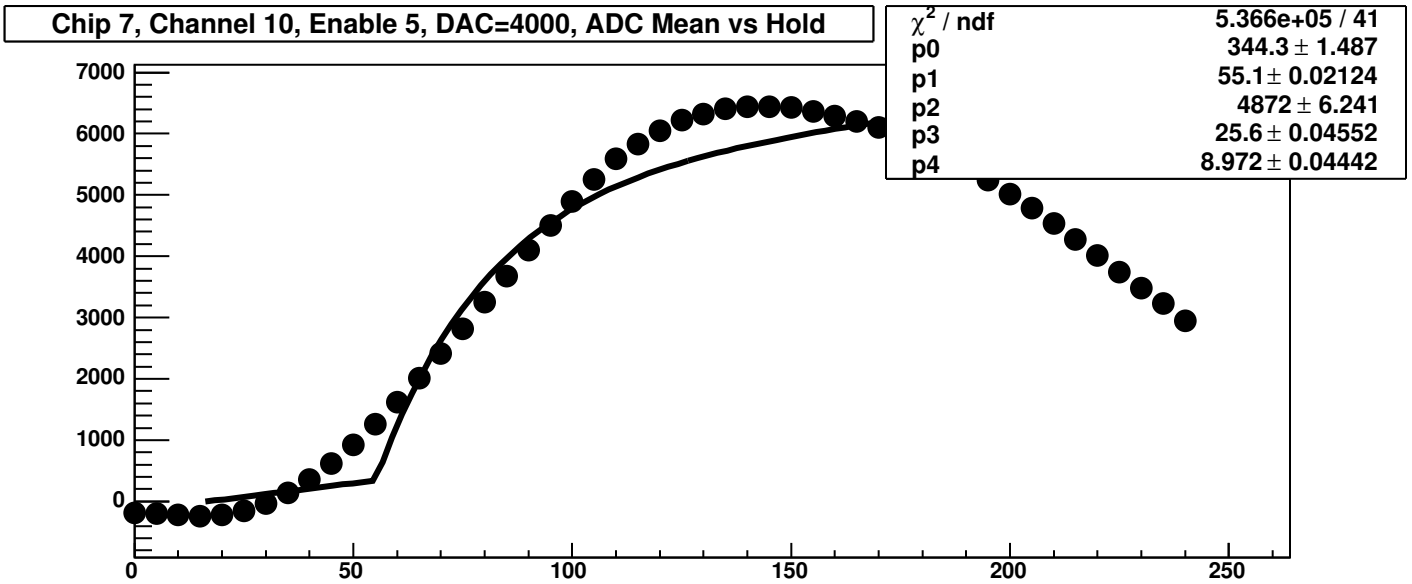


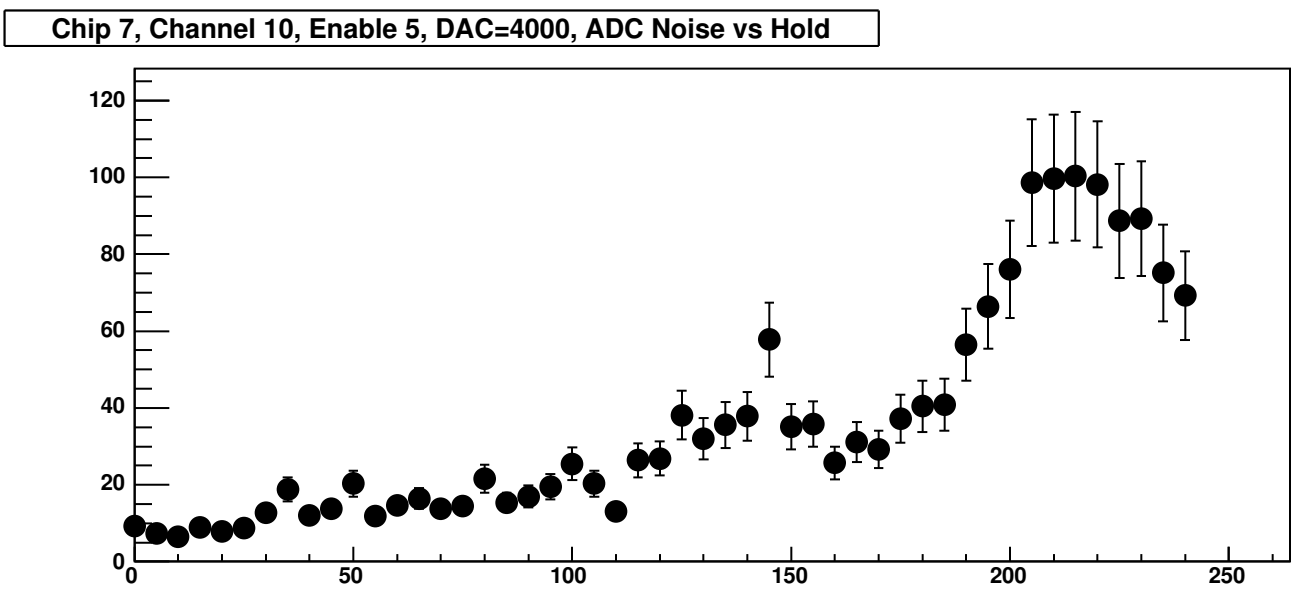
Chip 7, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold

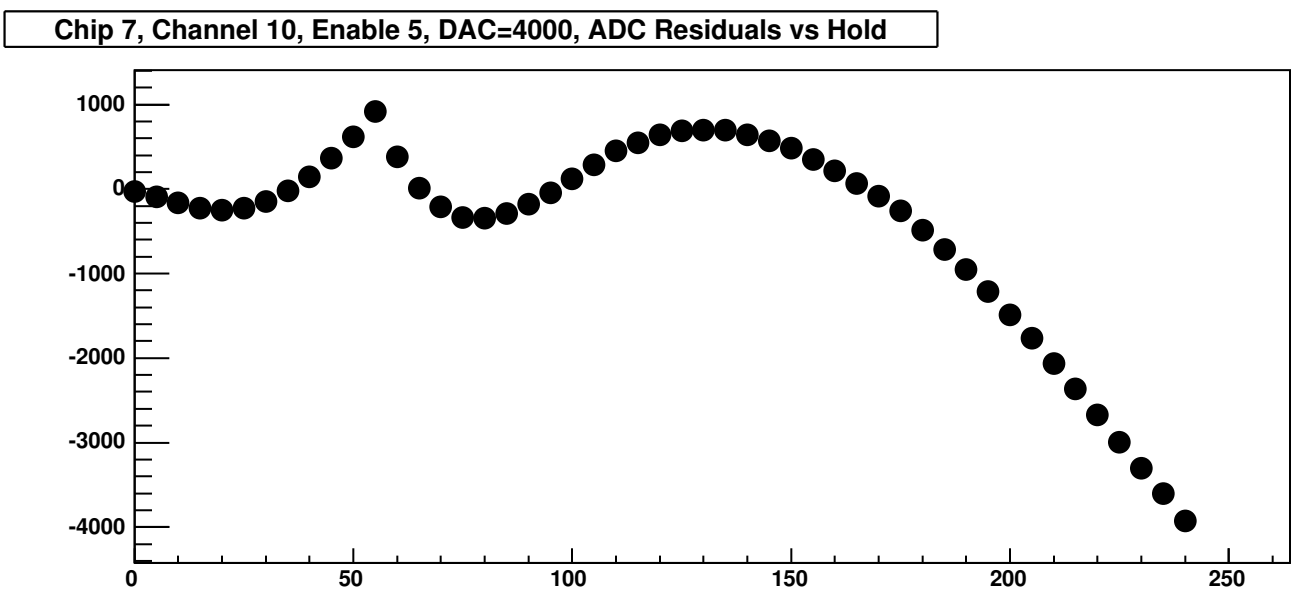


Chip 7, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

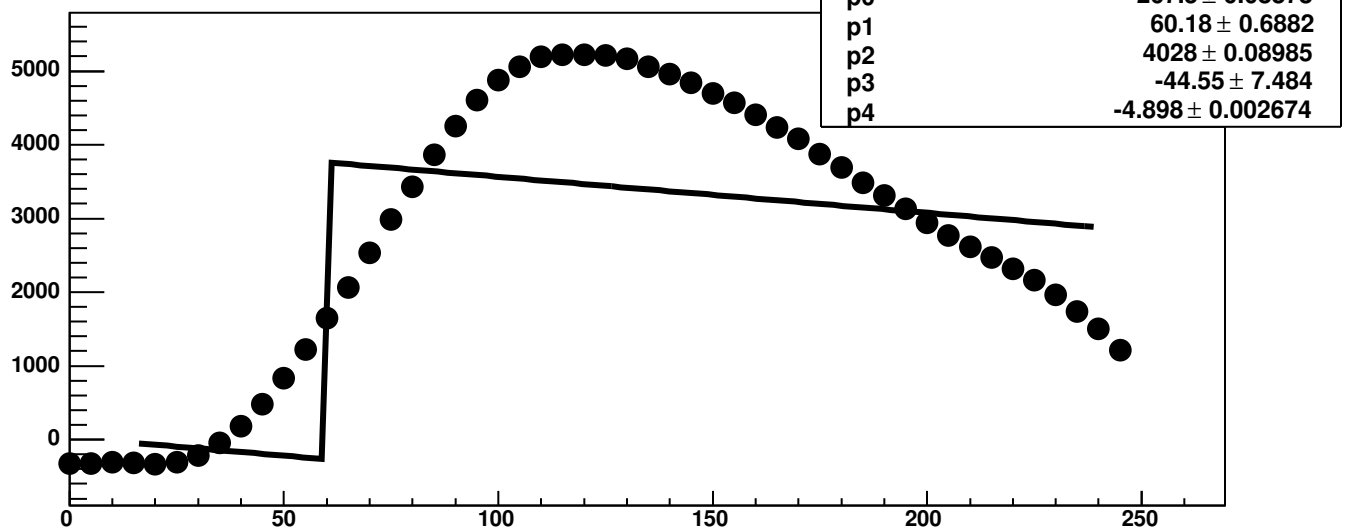



Chip 7, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.366e+05 / 41
p0 344.3 ± 1.487
p1 55.1 ± 0.02124
p2 4872 ± 6.241
p3 25.6 ± 0.04552
p4 8.972 ± 0.04442
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

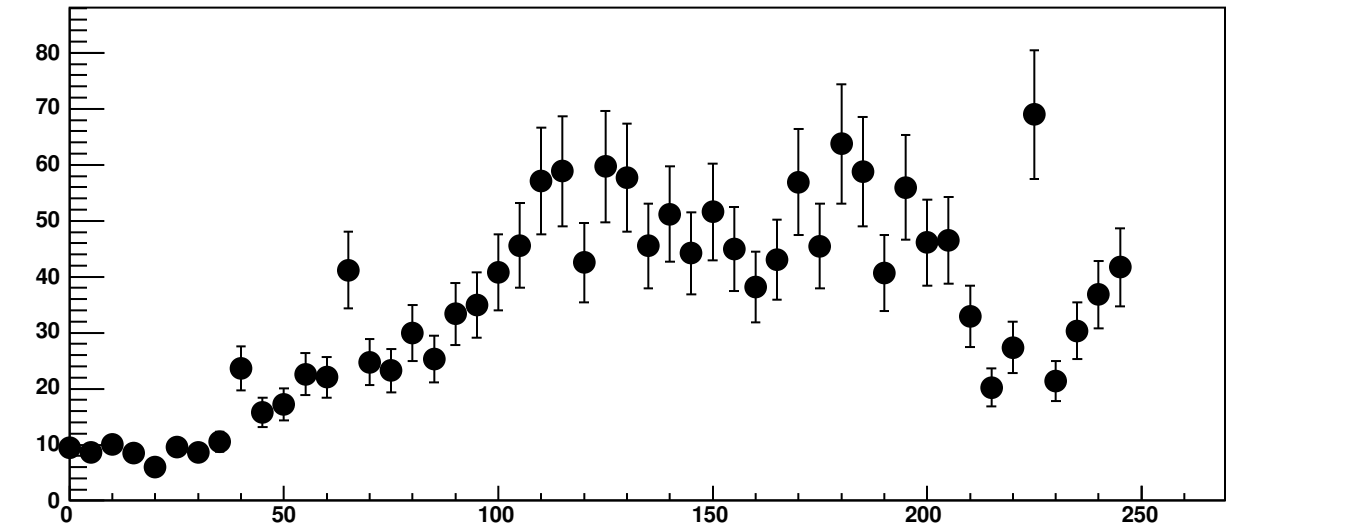

Chip 7, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 7, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

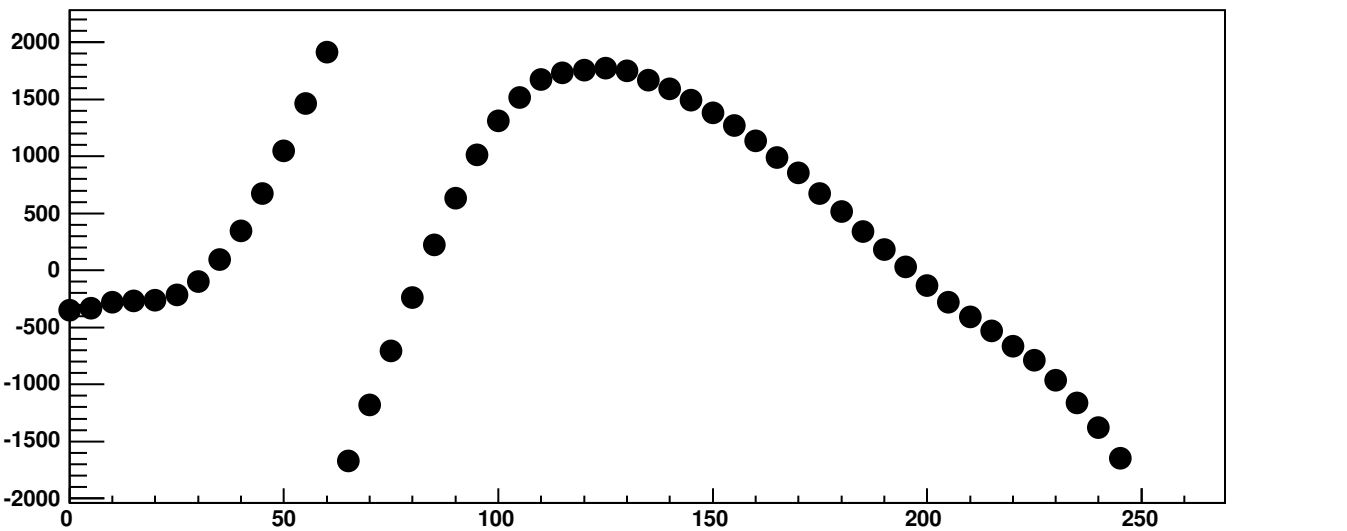
Chip 7, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold



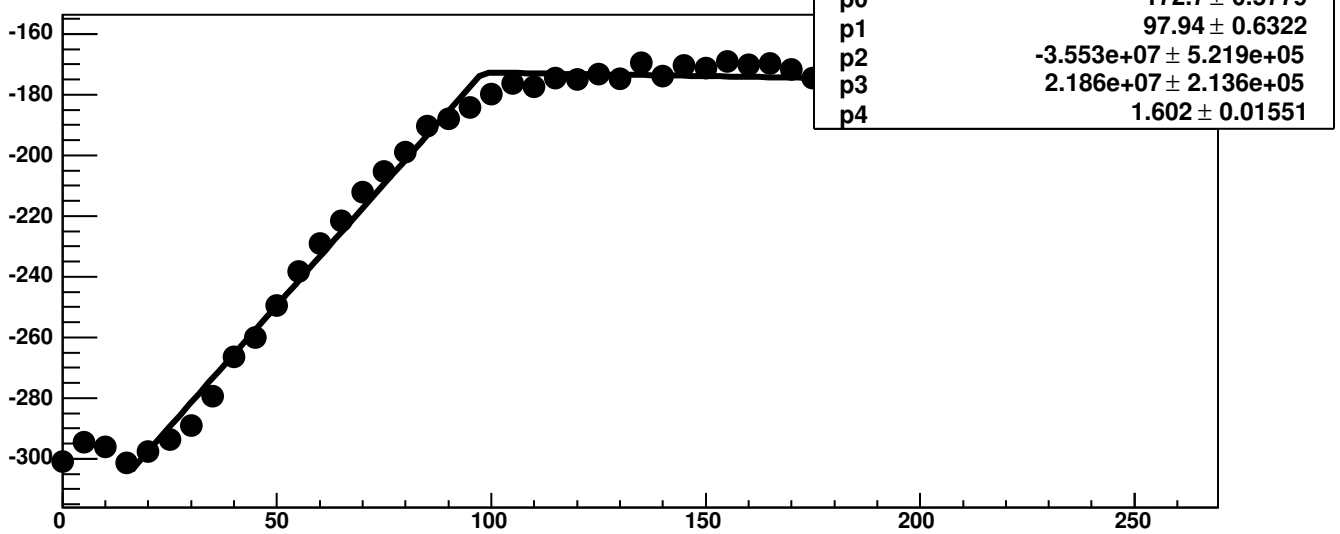
Chip 7, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold



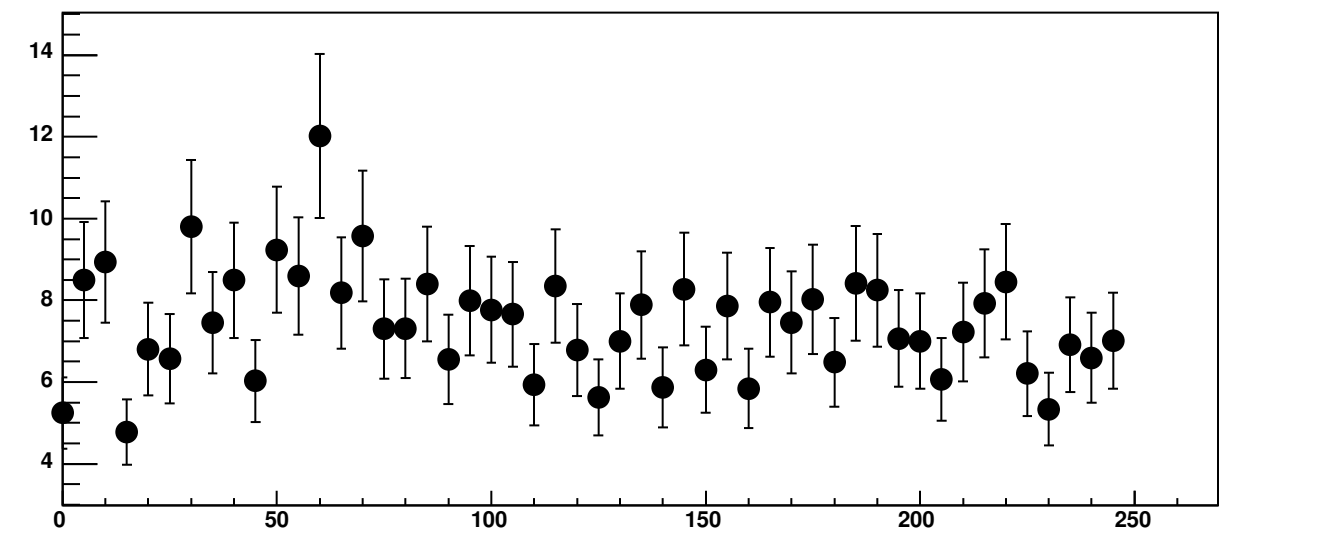
Chip 7, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold



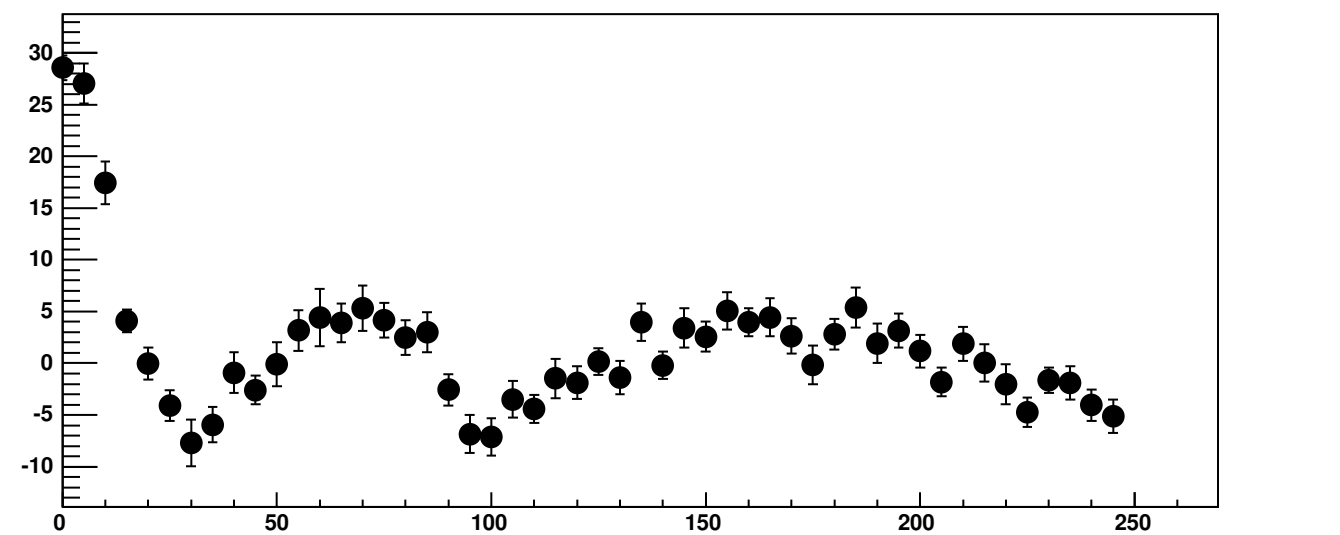
Chip 7, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

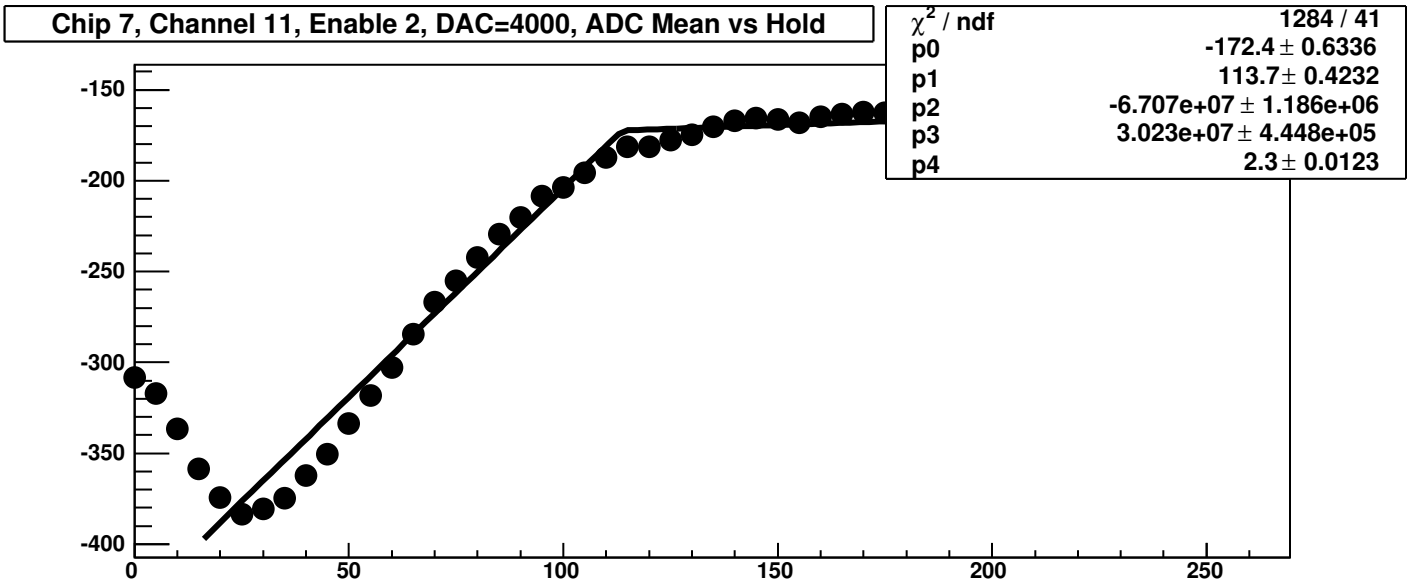


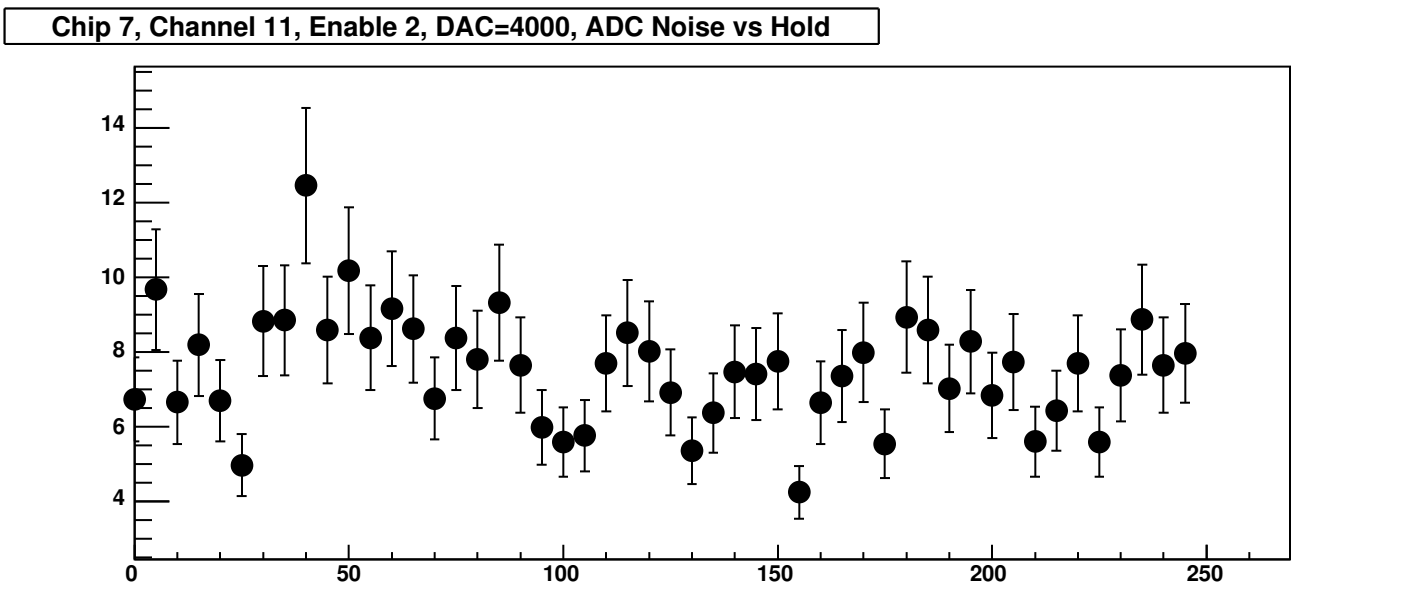
Chip 7, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold

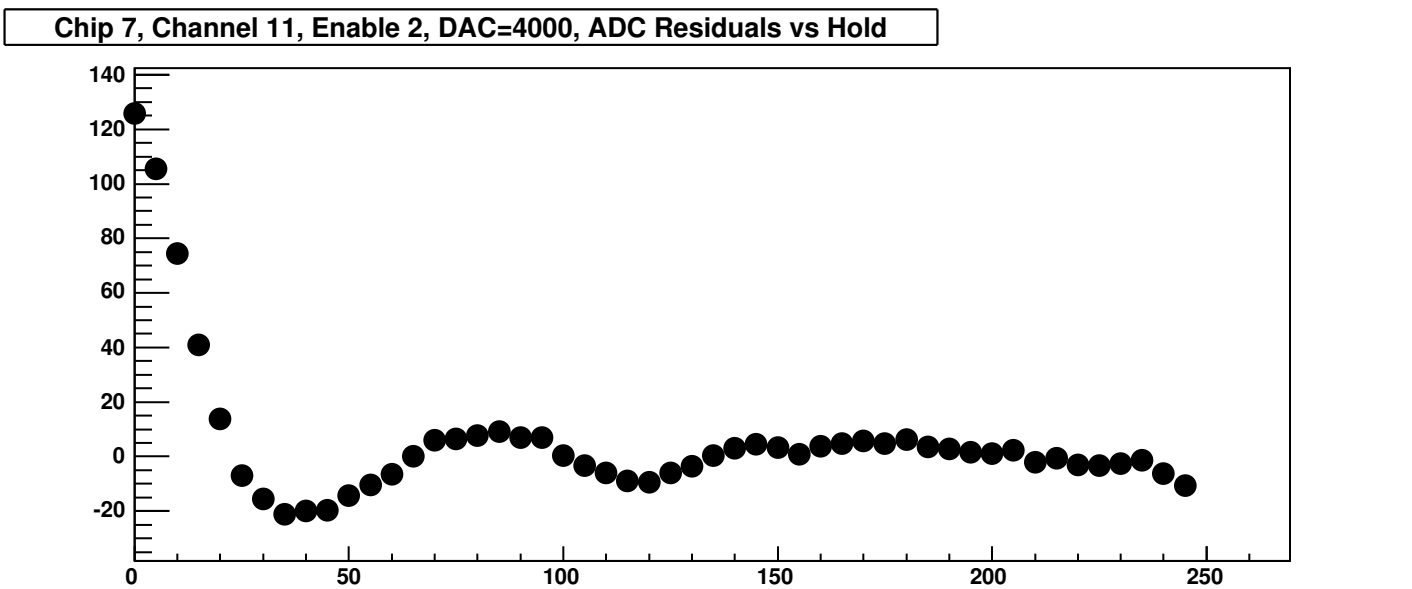


Chip 7, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold



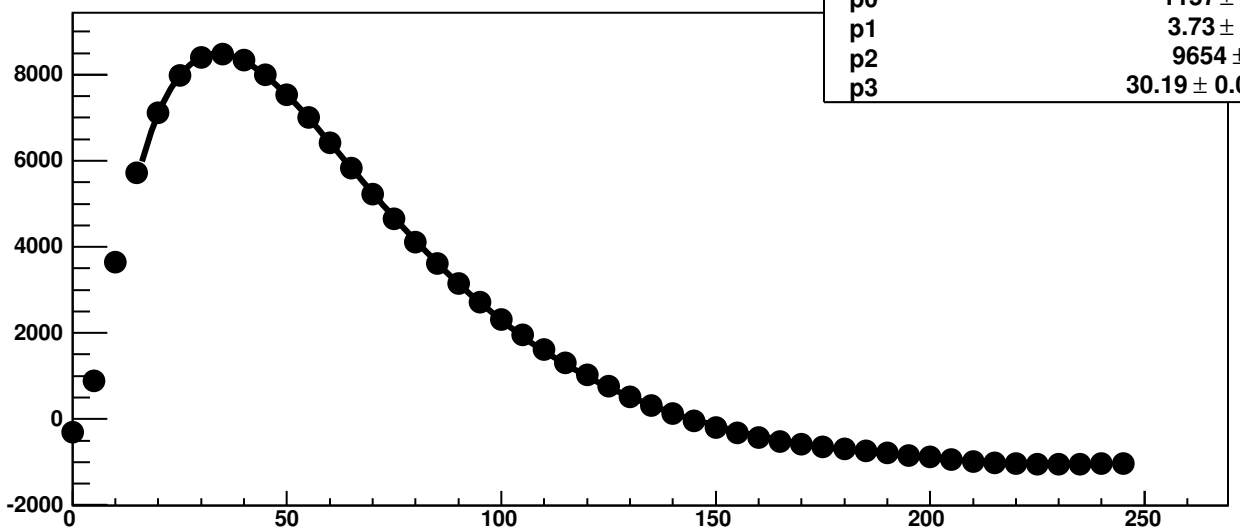

Chip 7, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 1284 / 41
p0 -172.4 ± 0.6336
p1 113.7 ± 0.4232
p2 -6.707e+07 ± 1.186e+06
p3 3.023e+07 ± 4.448e+05
p4 2.3 ± 0.0123
-150
-200
-250
-300
-350
-400
0
50
100
150
200
250


Chip 7, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

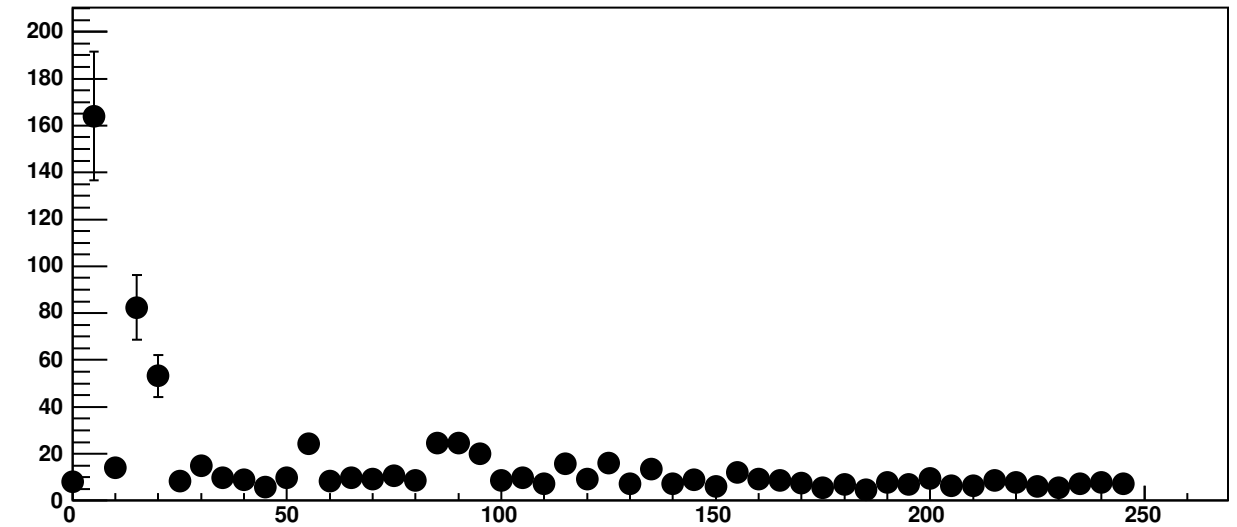

Chip 7, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold
140
120
100
80
60
40
20
0
-20
0
50
100
150
200
250

Chip 7, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold

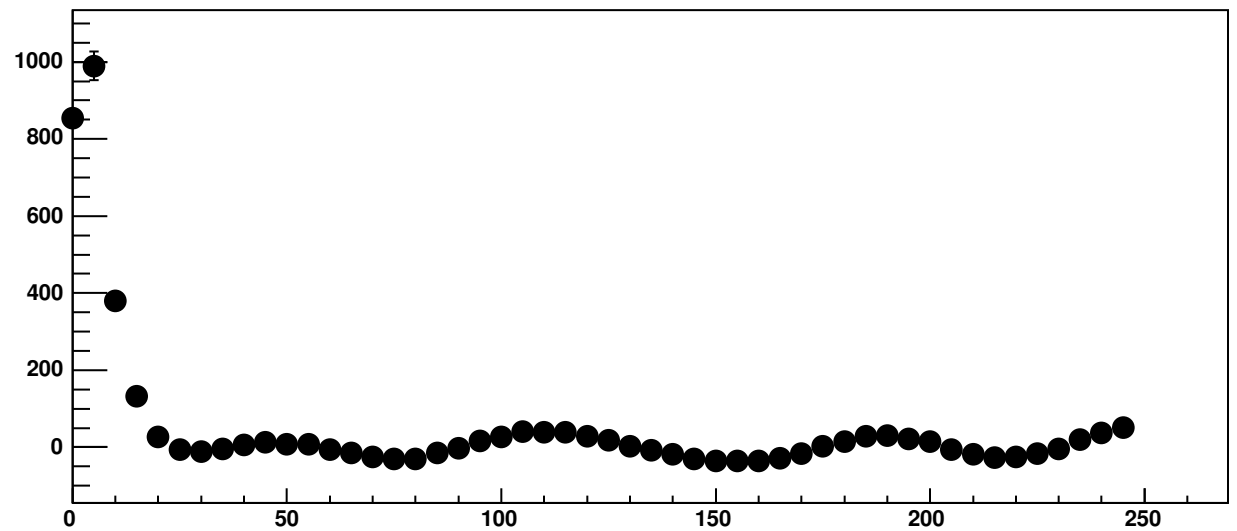
χ^2 / ndf	6429 / 42
p0	-1157 ± 0.5372
p1	3.73 ± 0.0139
p2	9654 ± 0.949
p3	30.19 ± 0.006992

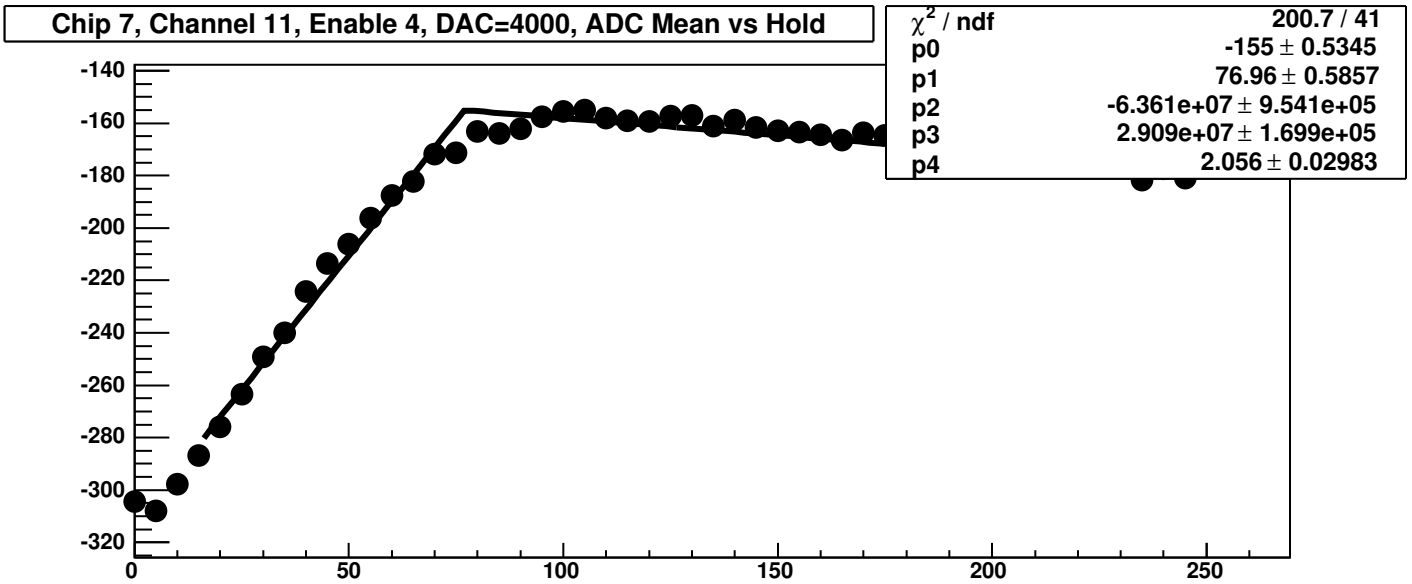


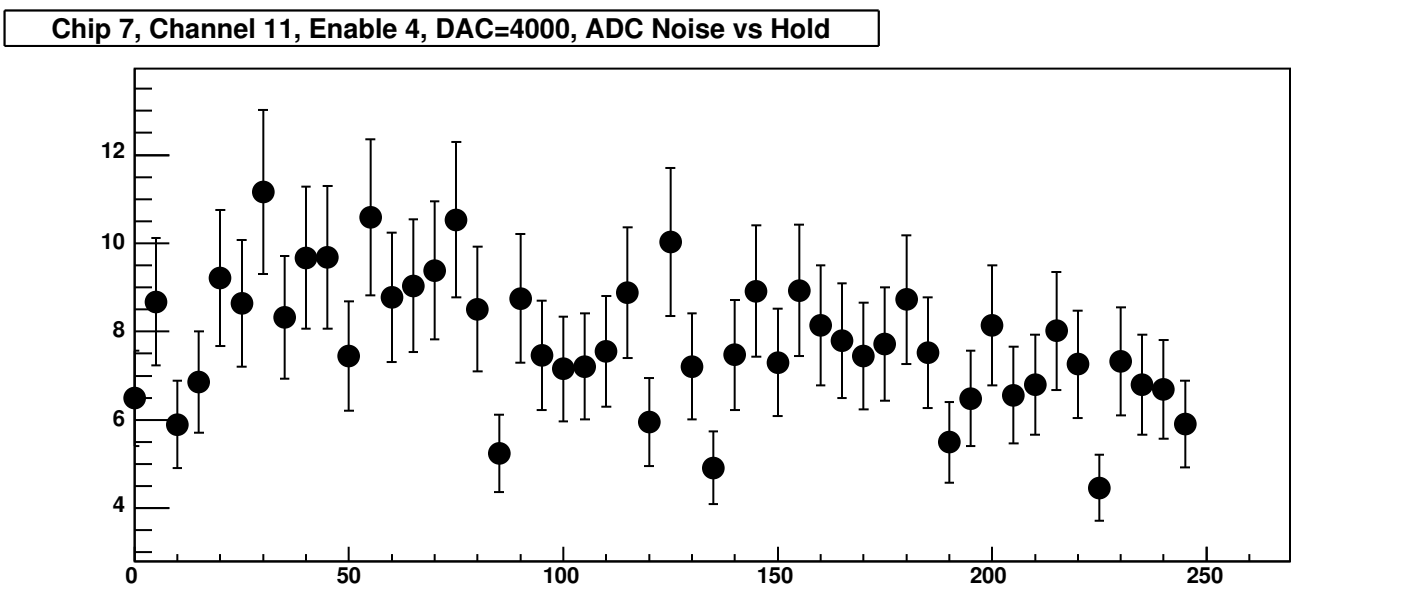
Chip 7, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold

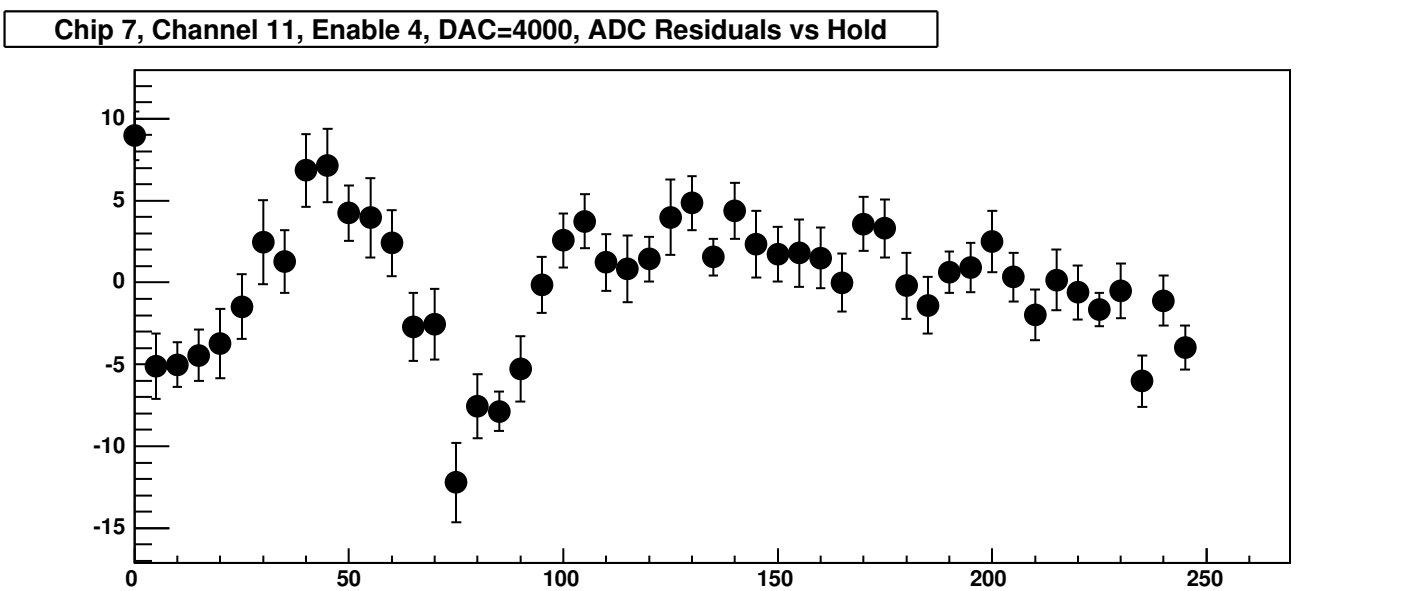


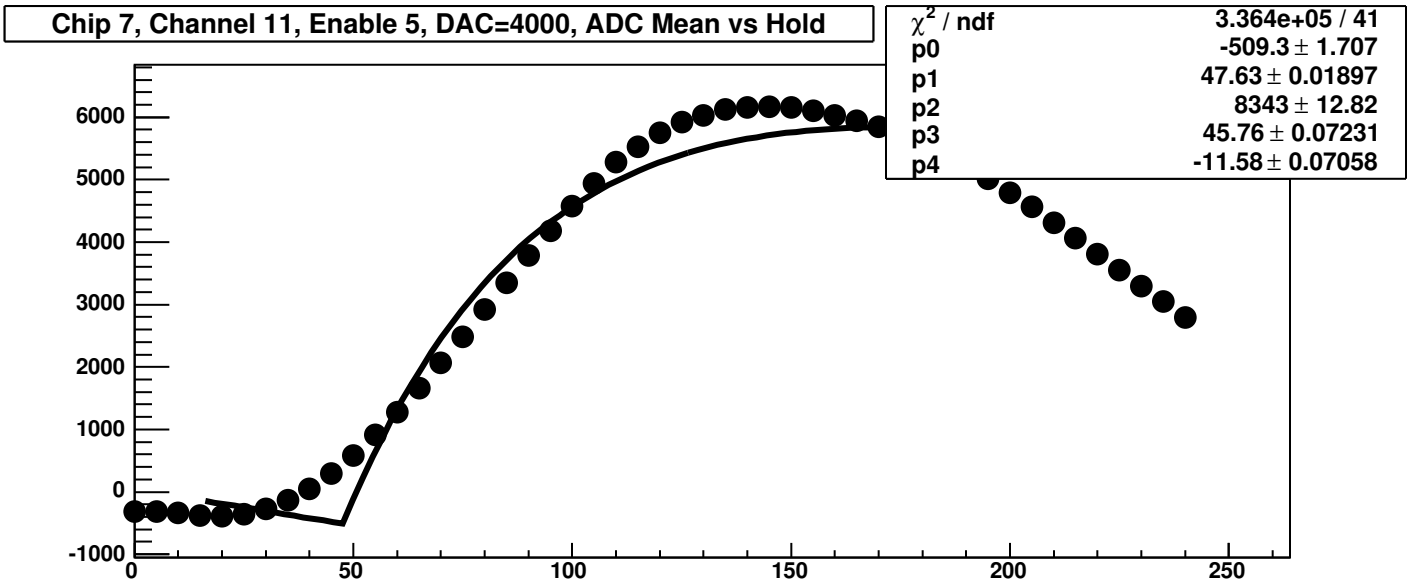
Chip 7, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold

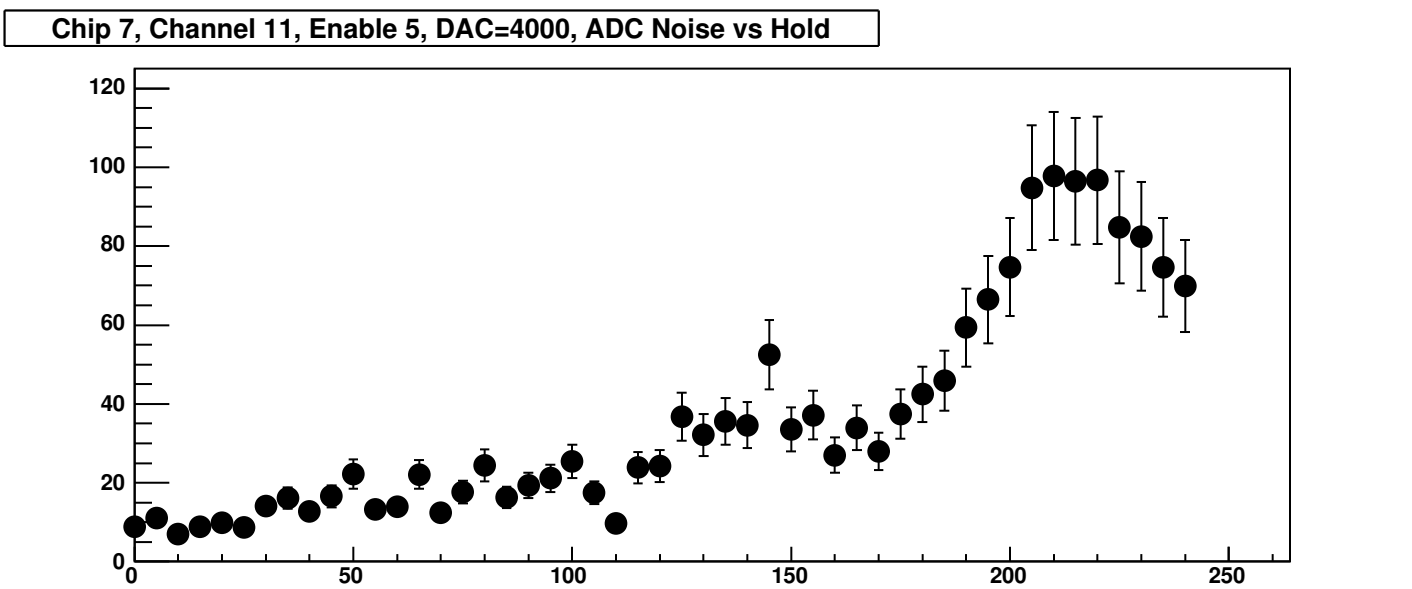


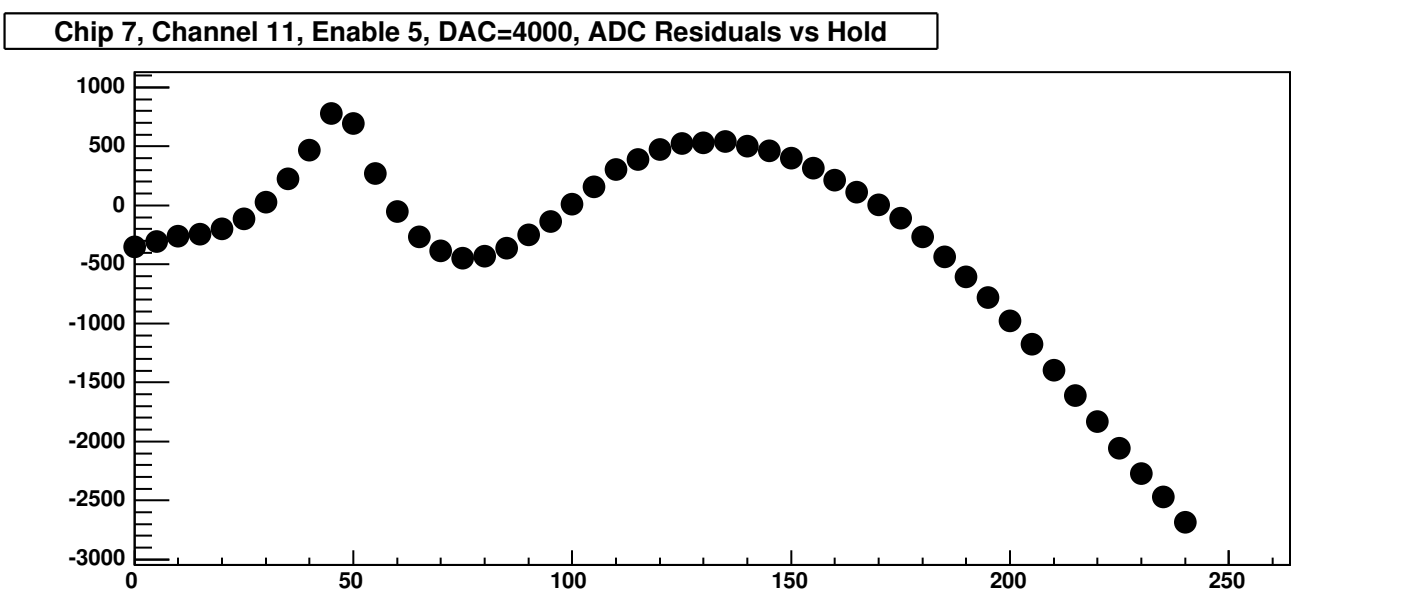

Chip 7, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 200.7 / 41
p0 -155 ± 0.5345
p1 76.96 ± 0.5857
p2 -6.361e+07 ± 9.541e+05
p3 2.909e+07 ± 1.699e+05
p4 2.056 ± 0.02983
-140
-160
-180
-200
-220
-240
-260
-280
-300
-320
0
50
100
150
200
250


Chip 7, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

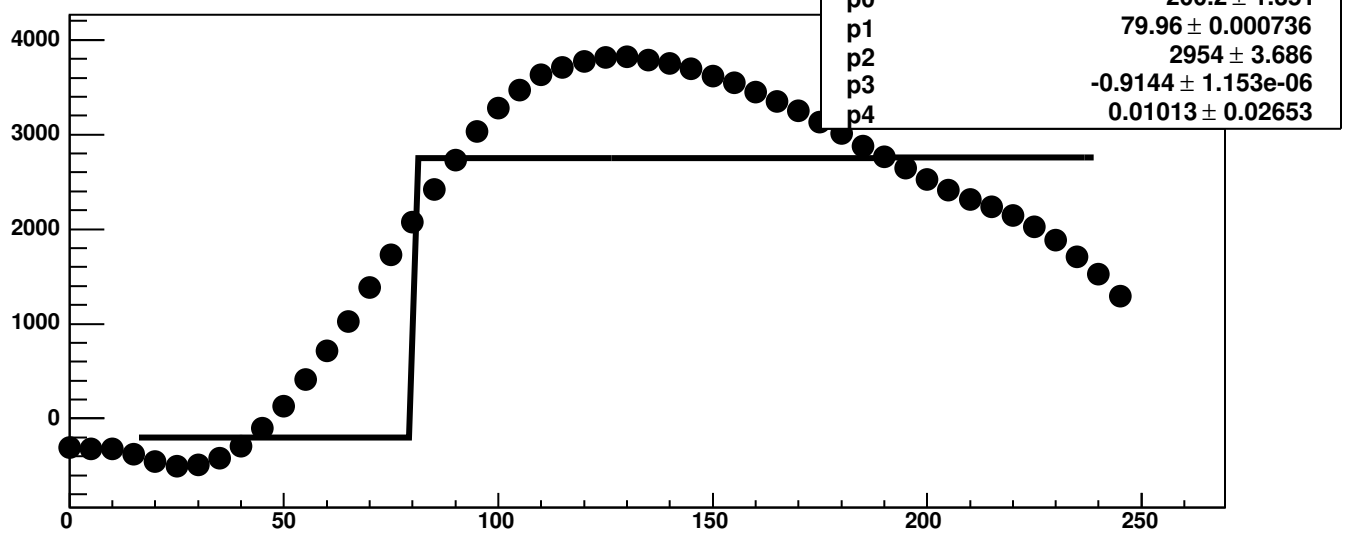

Chip 7, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
0
50
100
150
200
250


Chip 7, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.364e+05 / 41
p0 -509.3 ± 1.707
p1 47.63 ± 0.01897
p2 8343 ± 12.82
p3 45.76 ± 0.07231
p4 -11.58 ± 0.07058
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

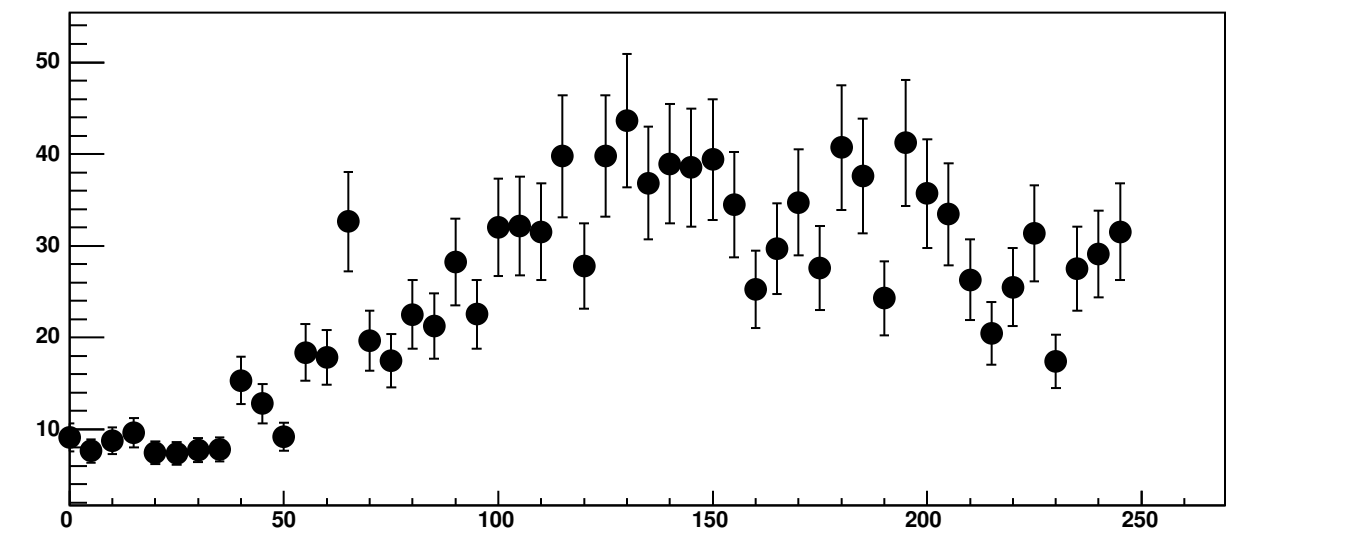

Chip 7, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 7, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250

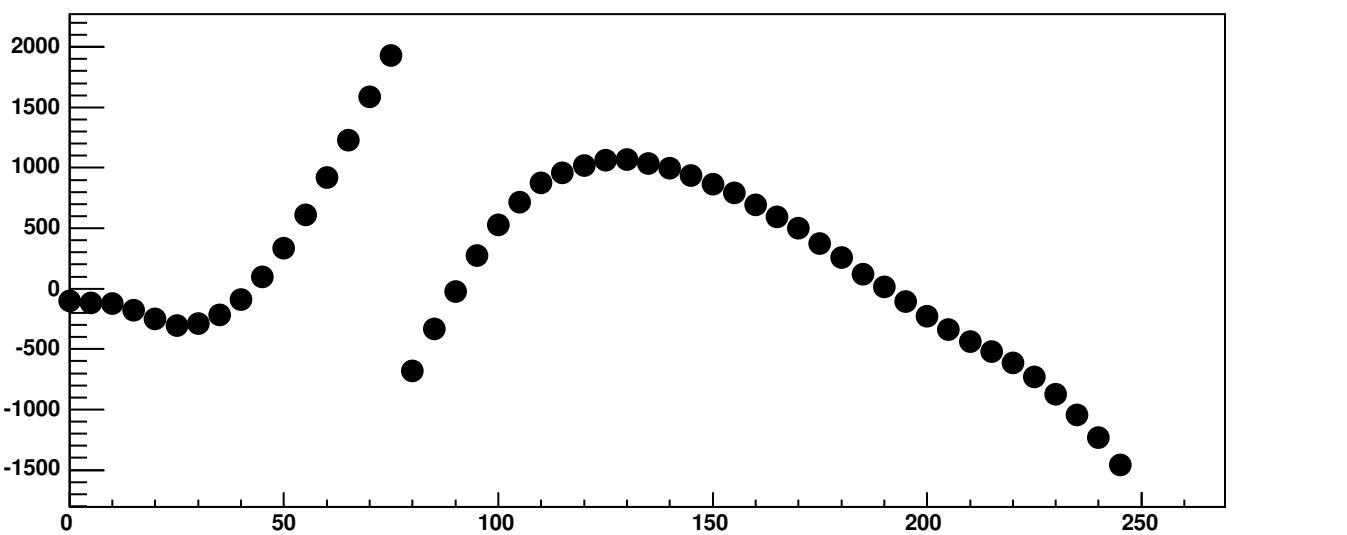
Chip 7, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold



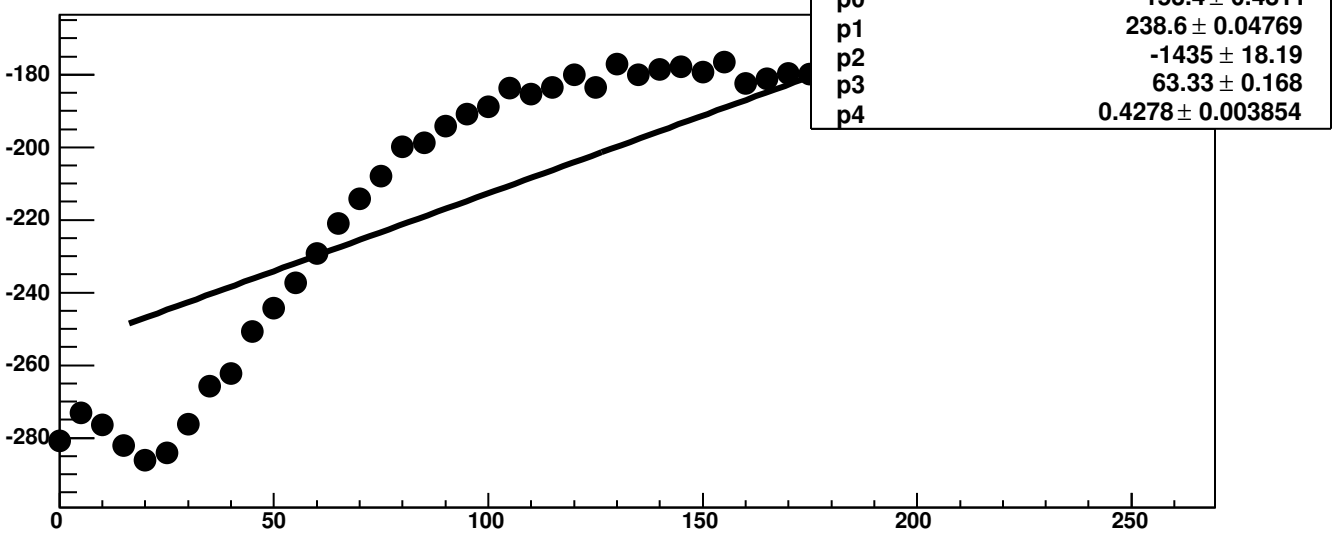
Chip 7, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



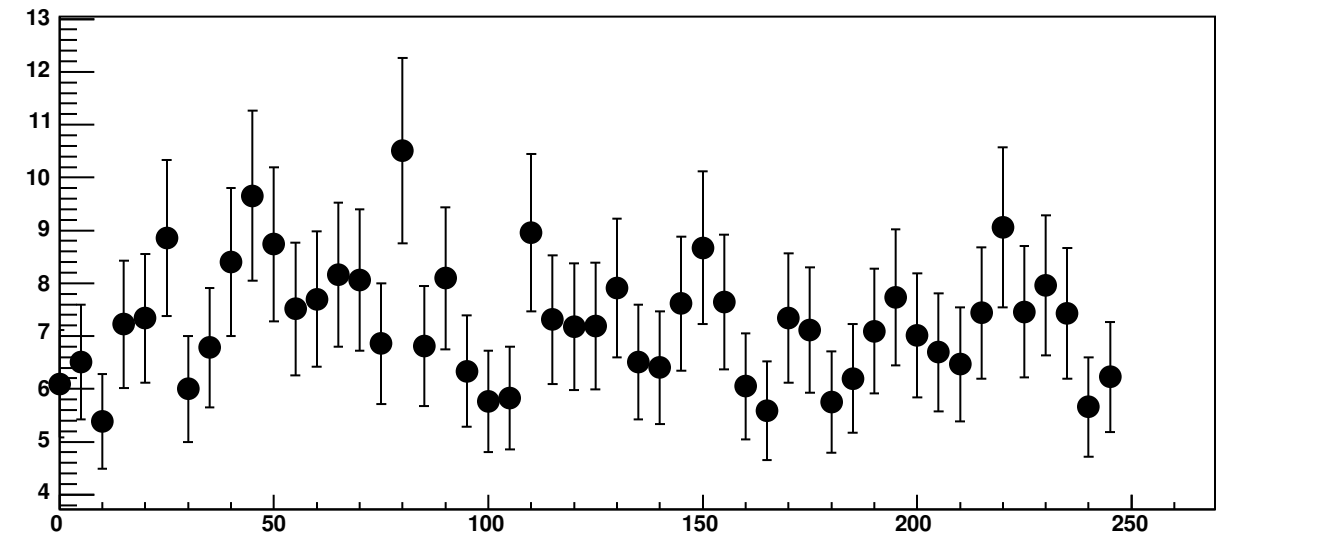
Chip 7, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold



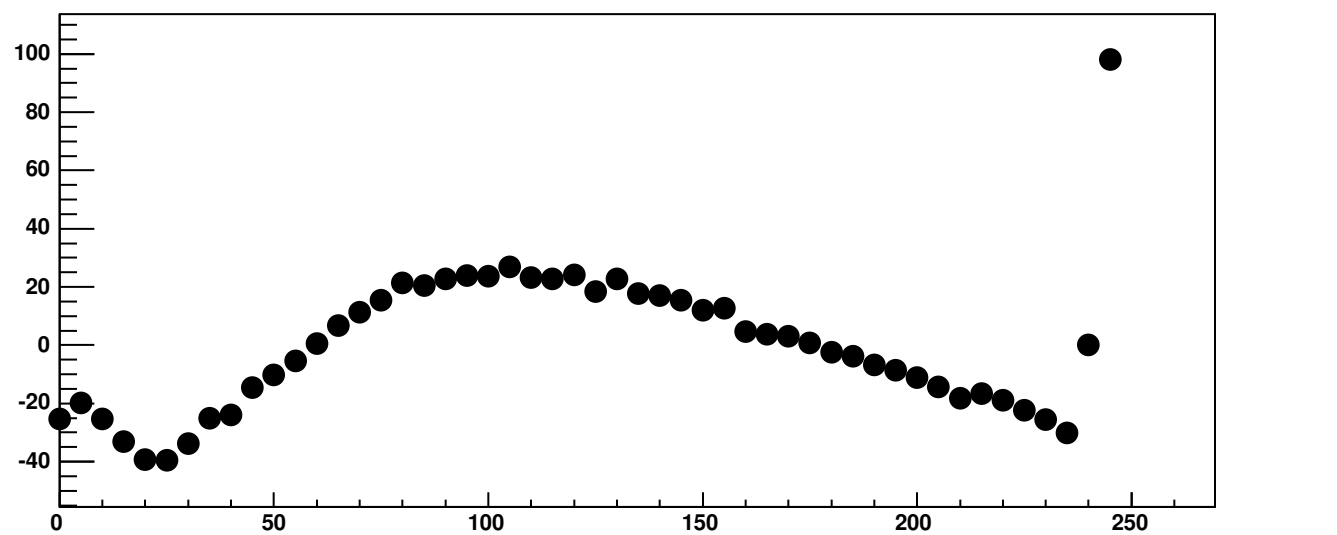
Chip 7, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold



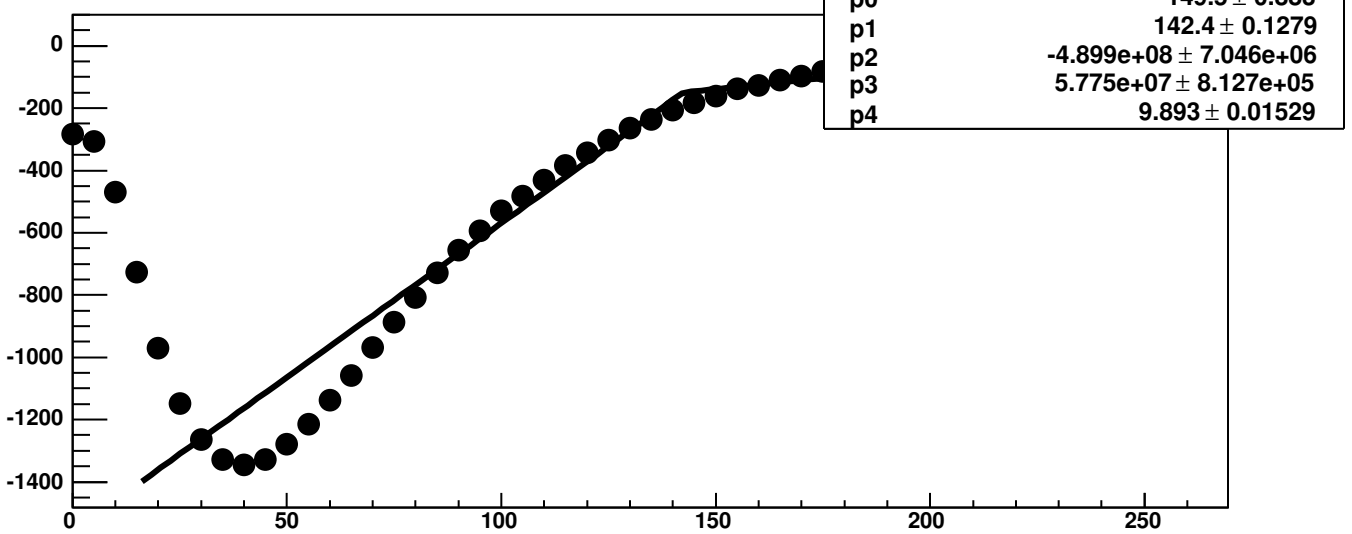
Chip 7, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold



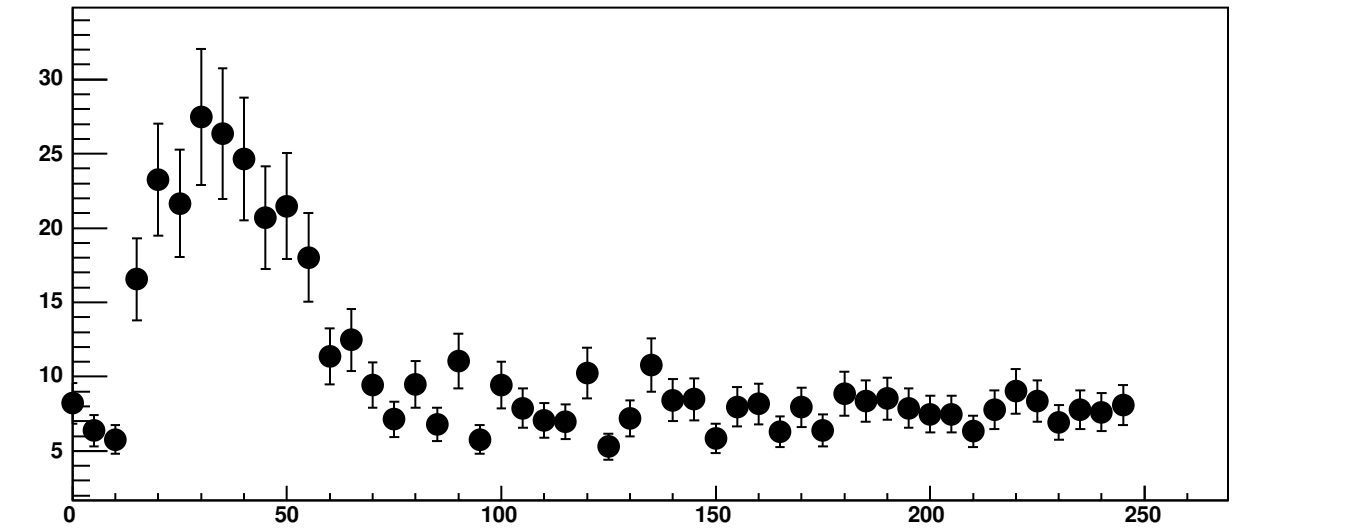
Chip 7, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold



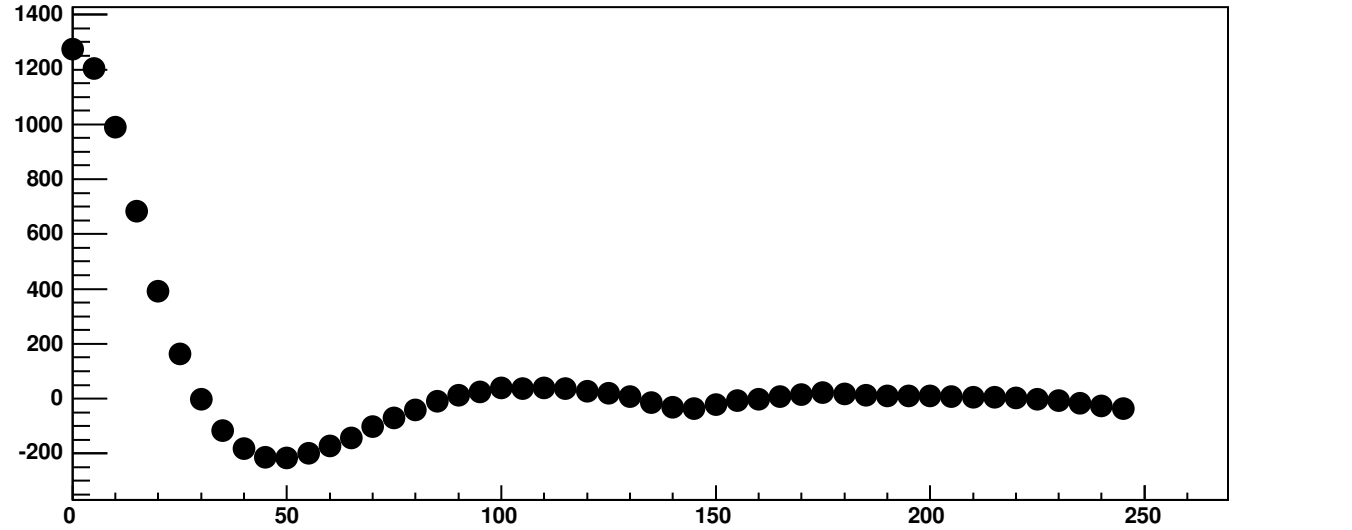
Chip 7, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

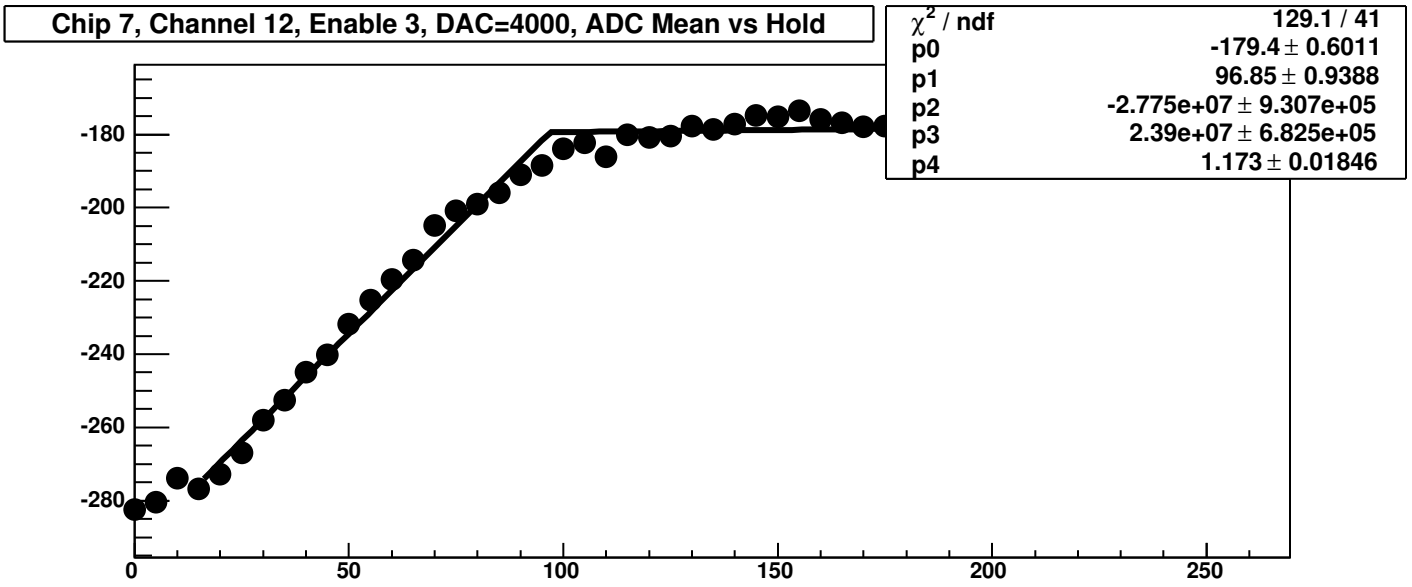


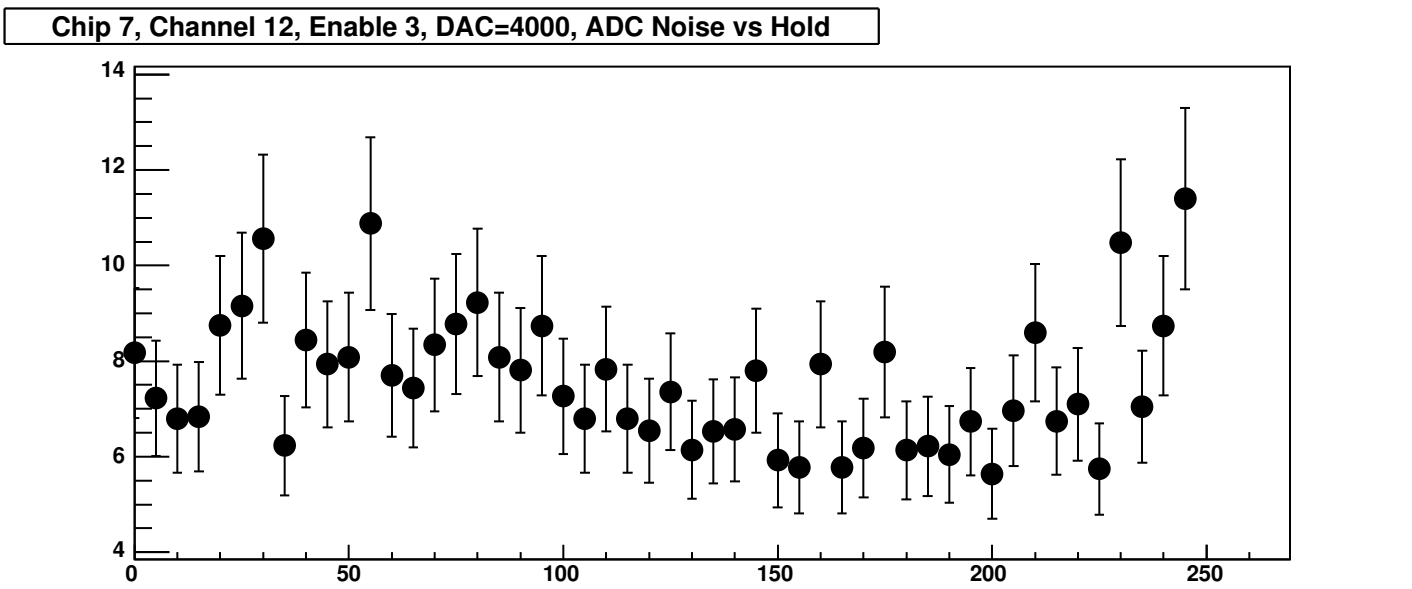
Chip 7, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold

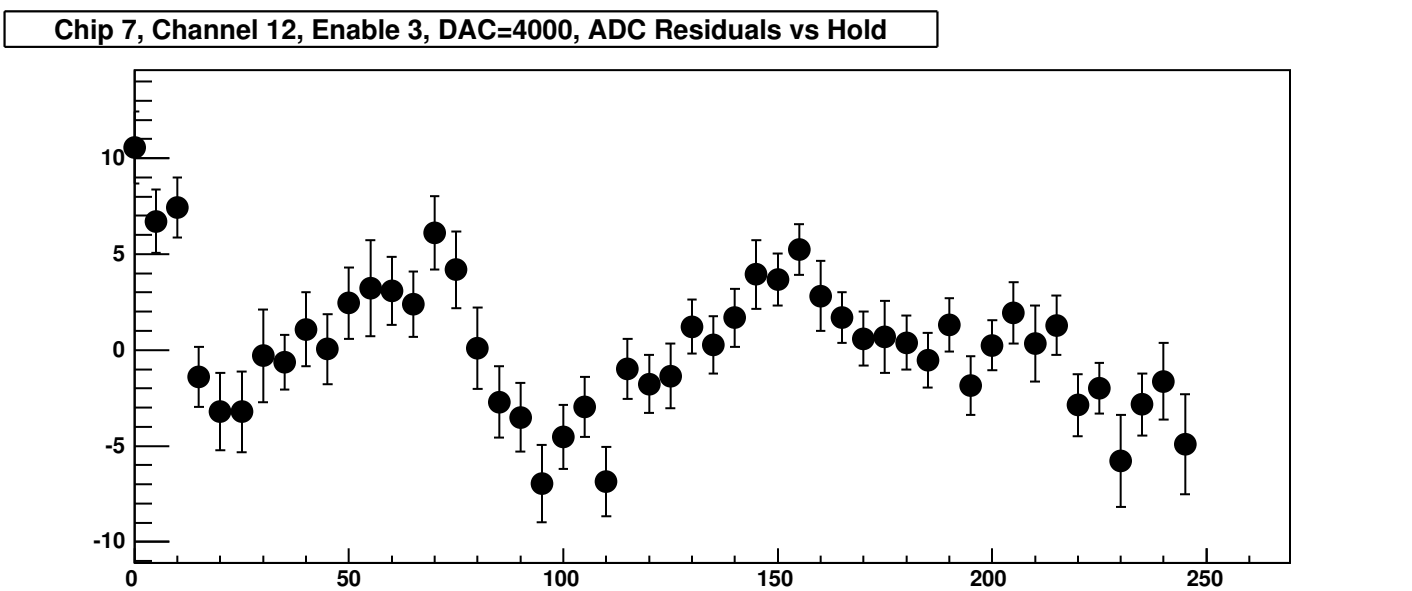


Chip 7, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold

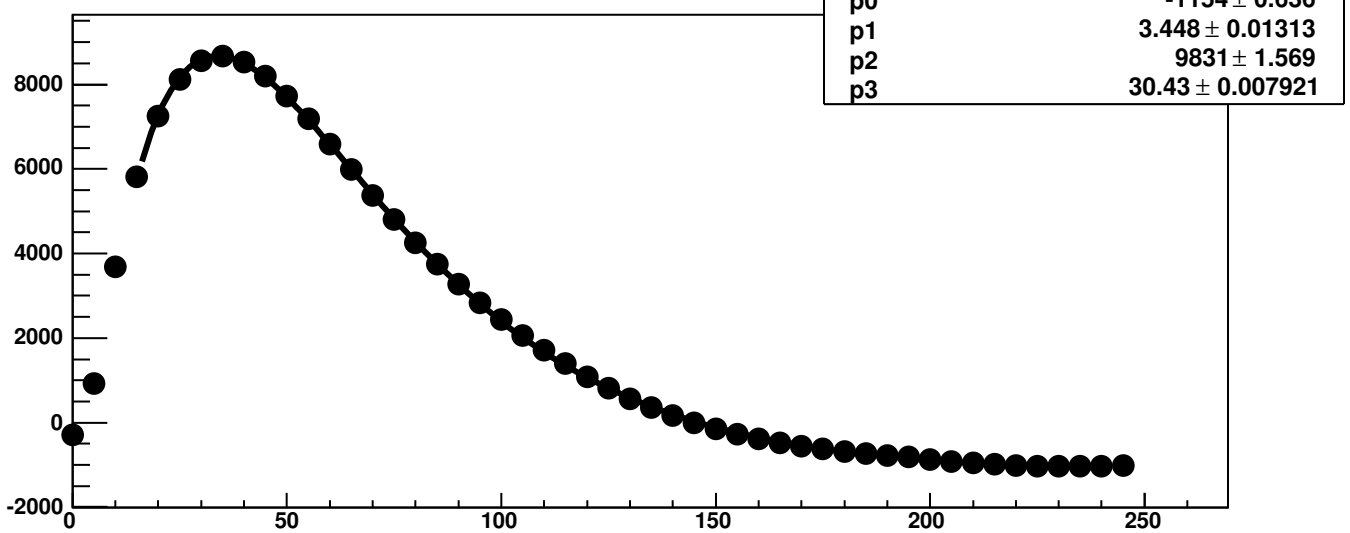



Chip 7, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 129.1 / 41
p0 -179.4 ± 0.6011
p1 96.85 ± 0.9388
p2 -2.775e+07 ± 9.307e+05
p3 2.39e+07 ± 6.825e+05
p4 1.173 ± 0.01846
-180
-200
-220
-240
-260
-280
0
50
100
150
200
250

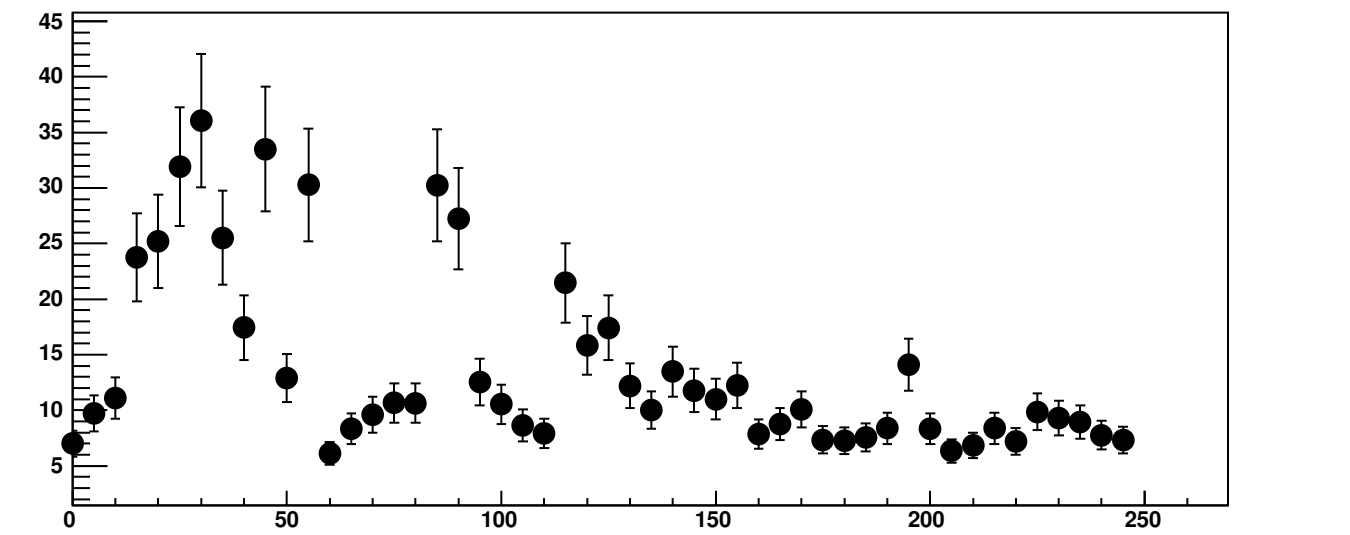

Chip 7, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 7, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250

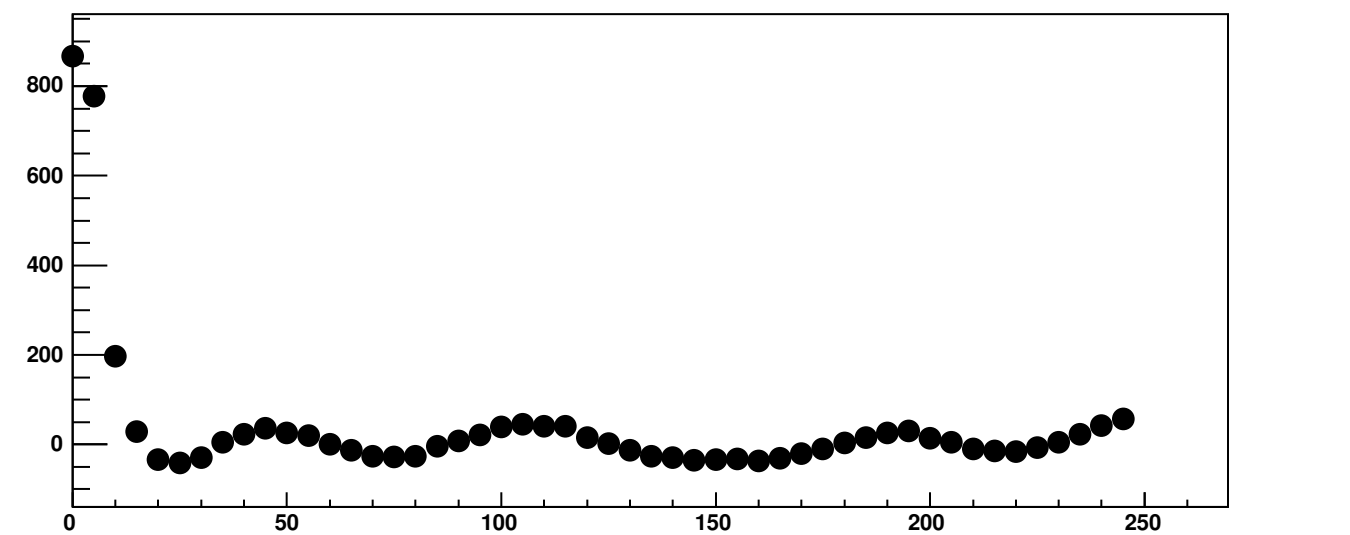
Chip 7, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold



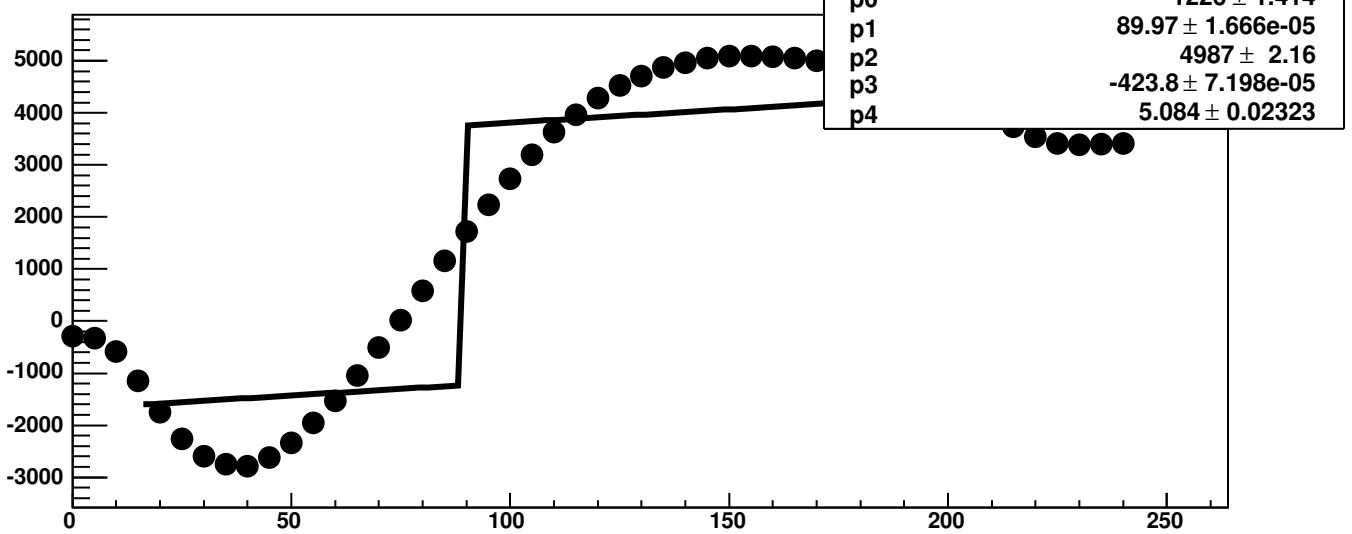
Chip 7, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold



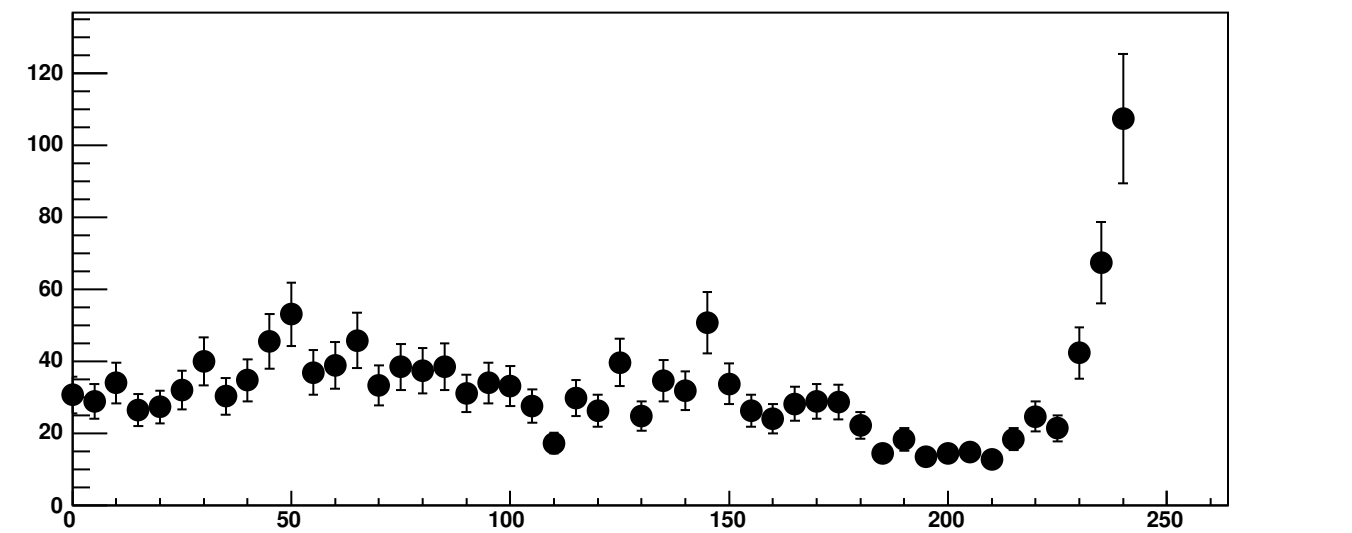
Chip 7, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold



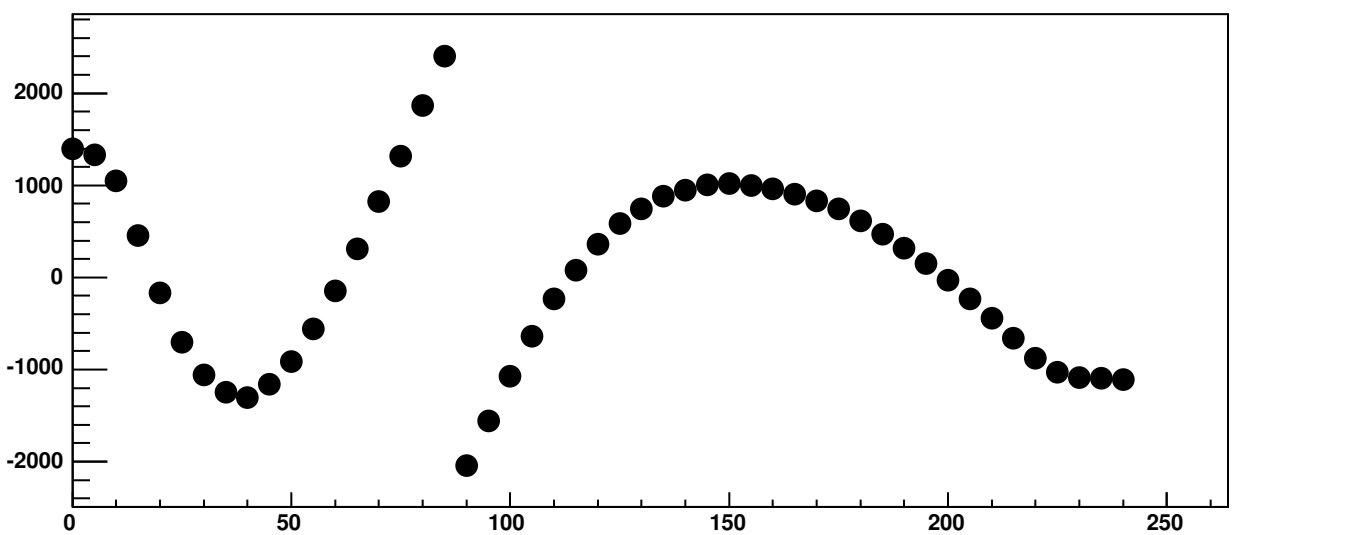
Chip 7, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold



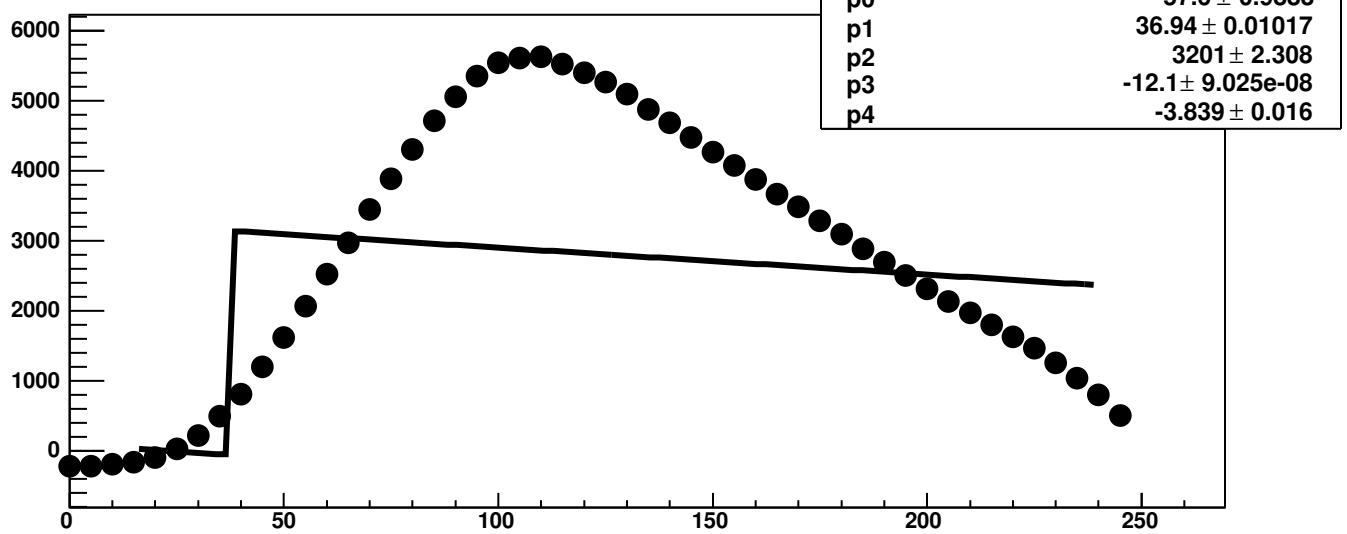
Chip 7, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold



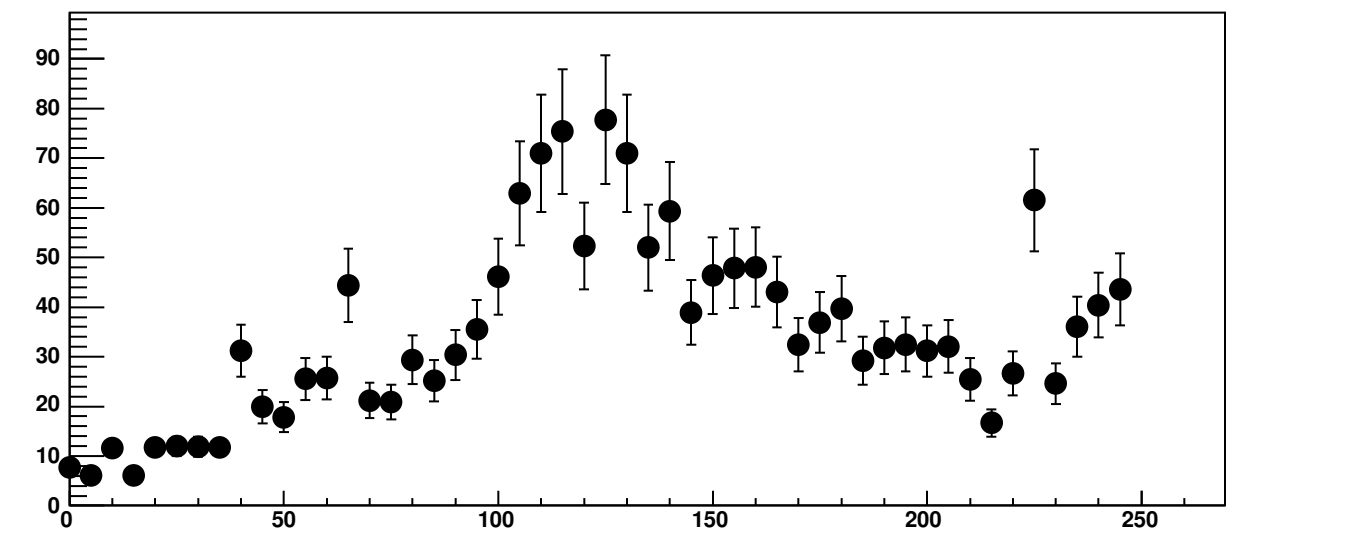
Chip 7, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold



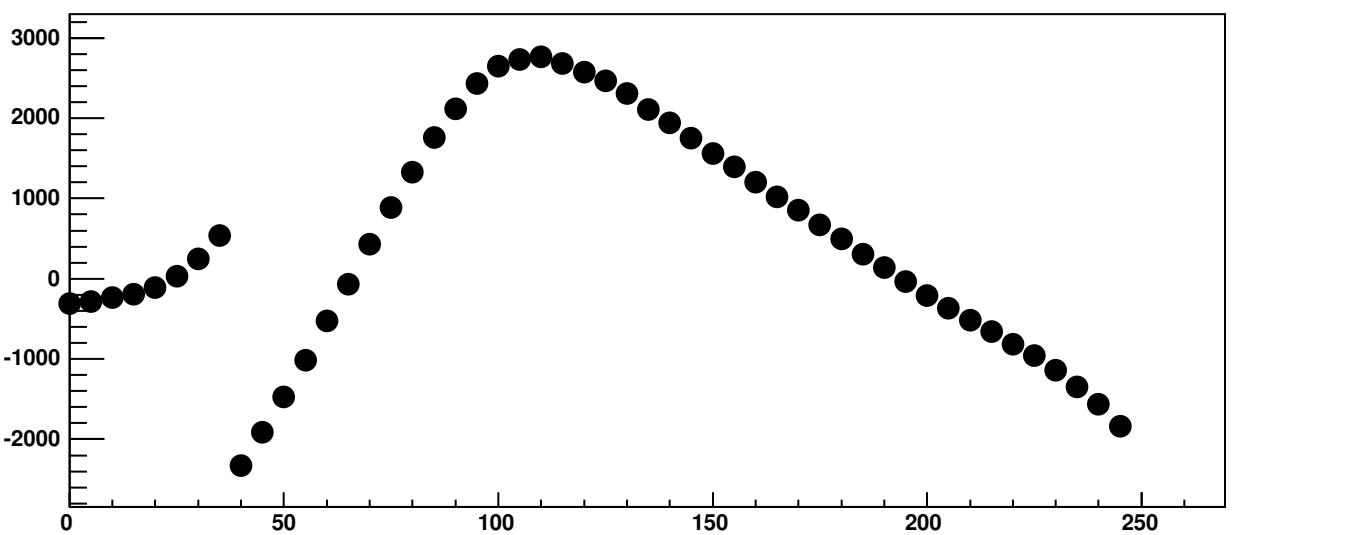
Chip 7, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold



Chip 7, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold

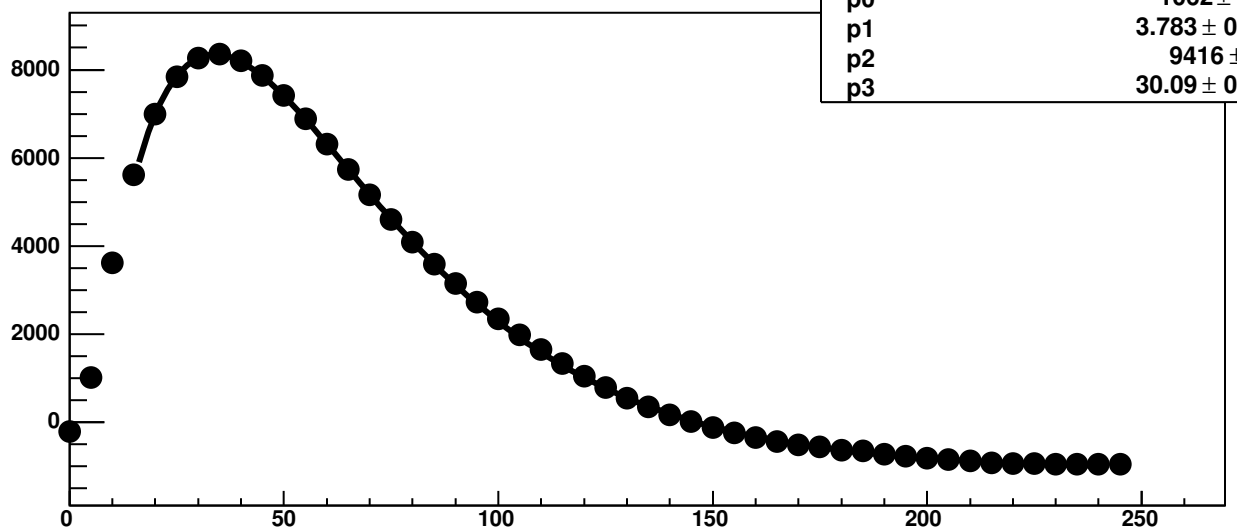


Chip 7, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold

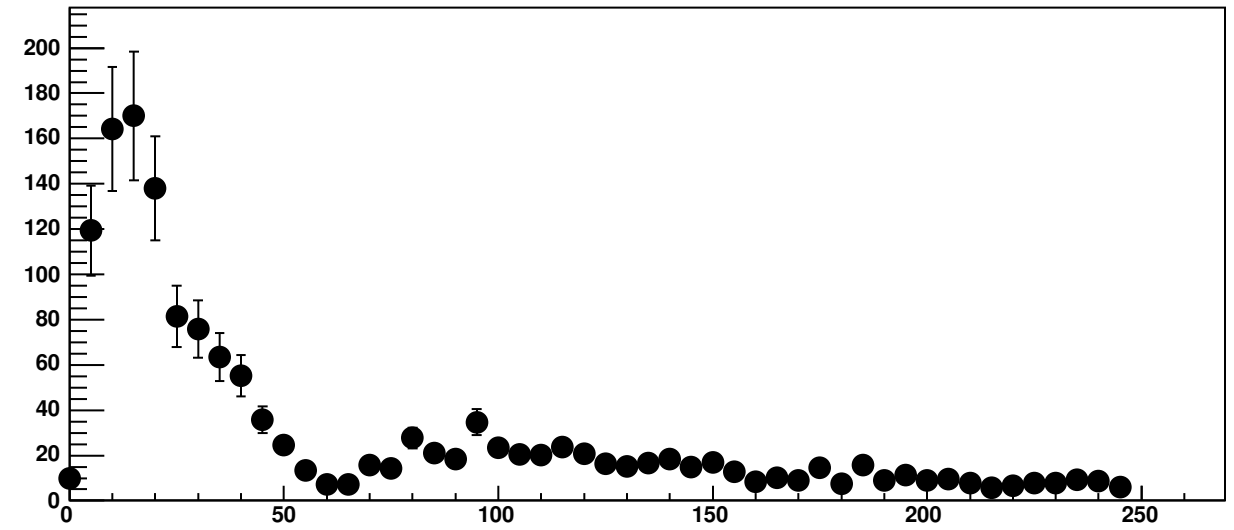


Chip 7, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold

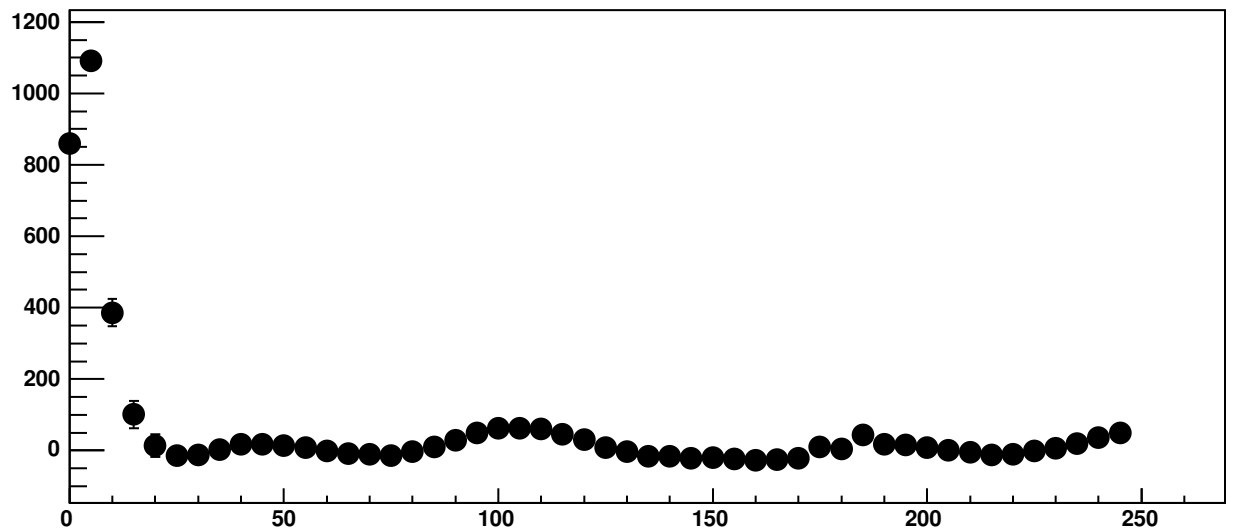
χ^2 / ndf	2212 / 42
p0	-1062 ± 0.7357
p1	3.783 ± 0.04837
p2	9416 ± 4.787
p3	30.09 ± 0.01484



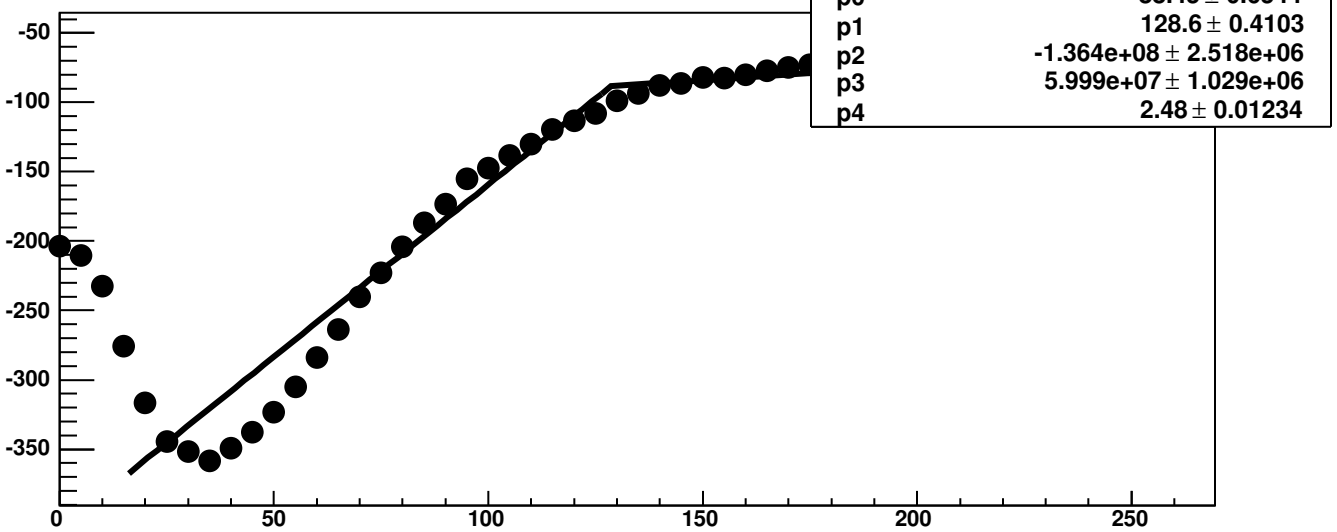
Chip 7, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold



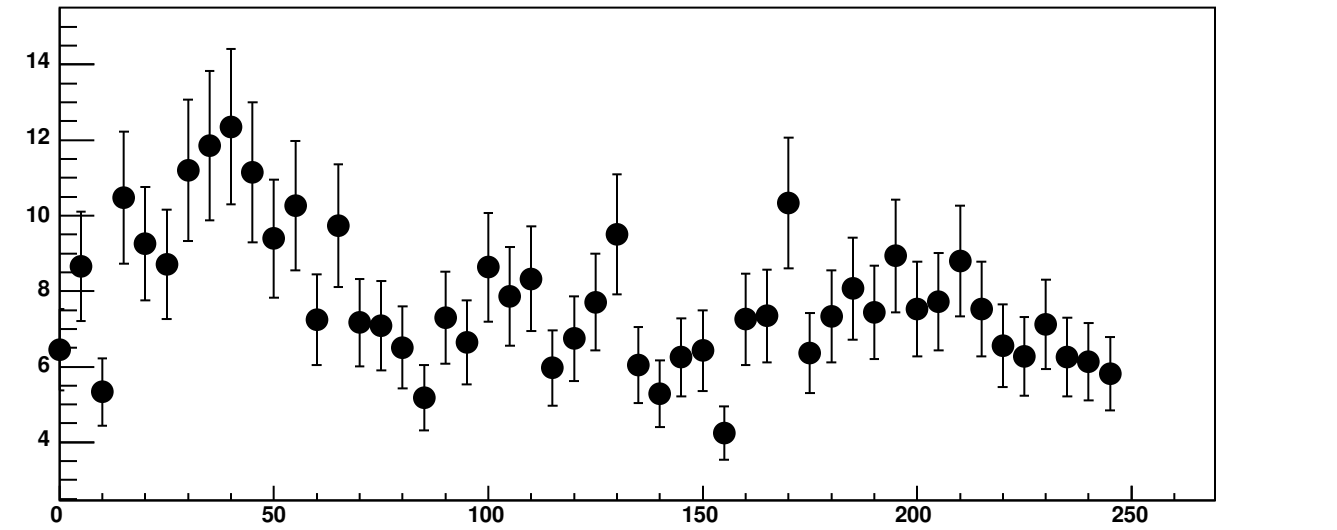
Chip 7, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold



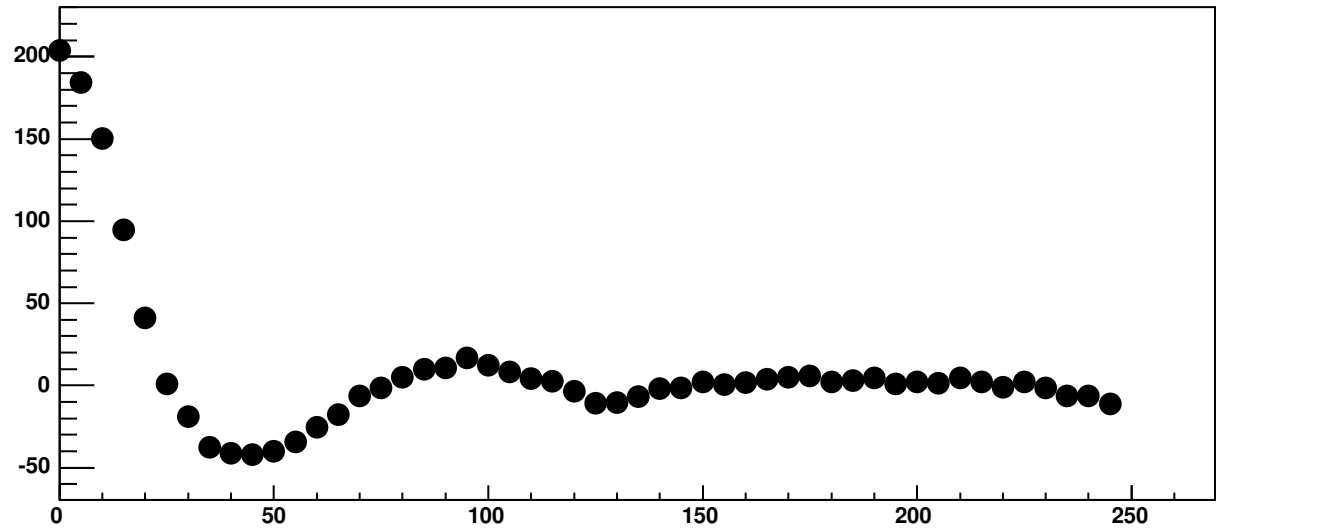
Chip 7, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold



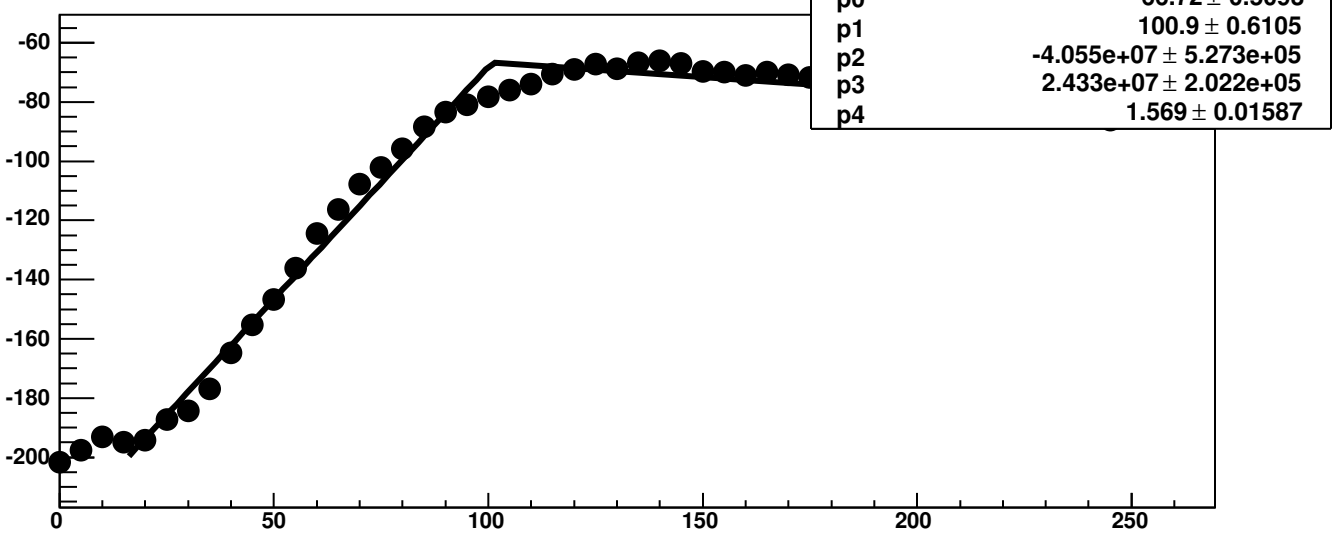
Chip 7, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



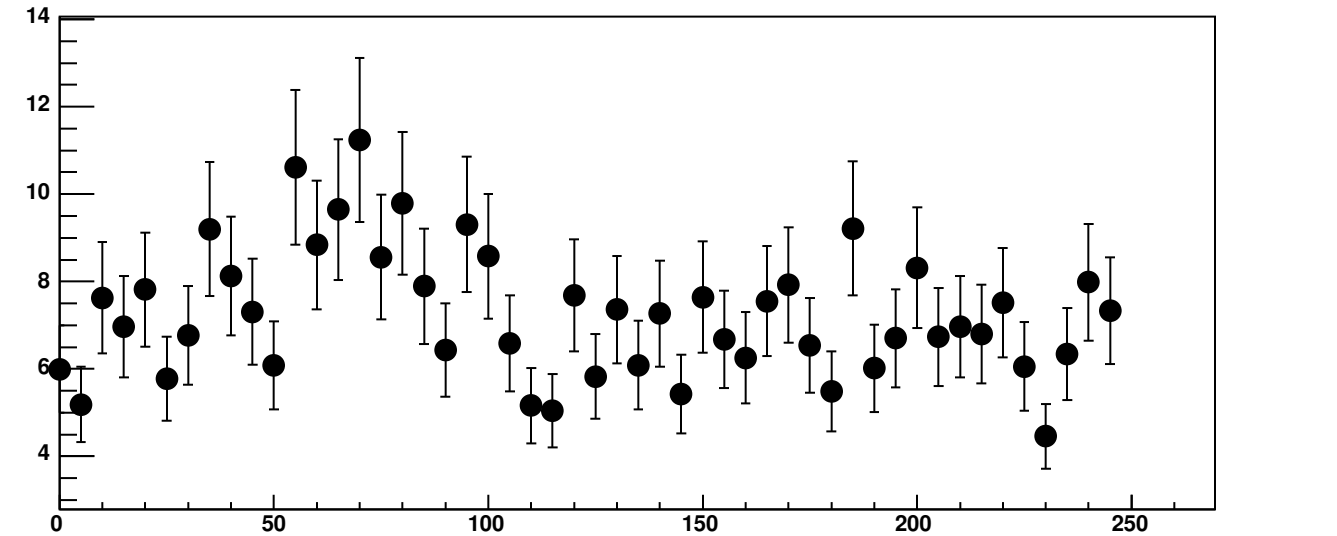
Chip 7, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold



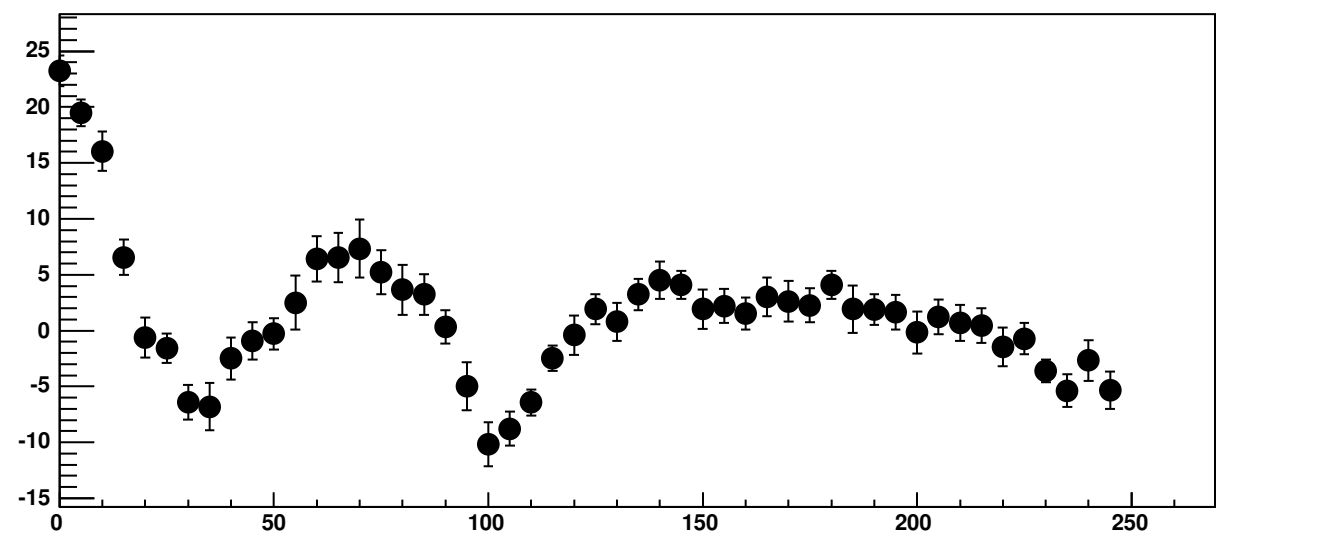
Chip 7, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold



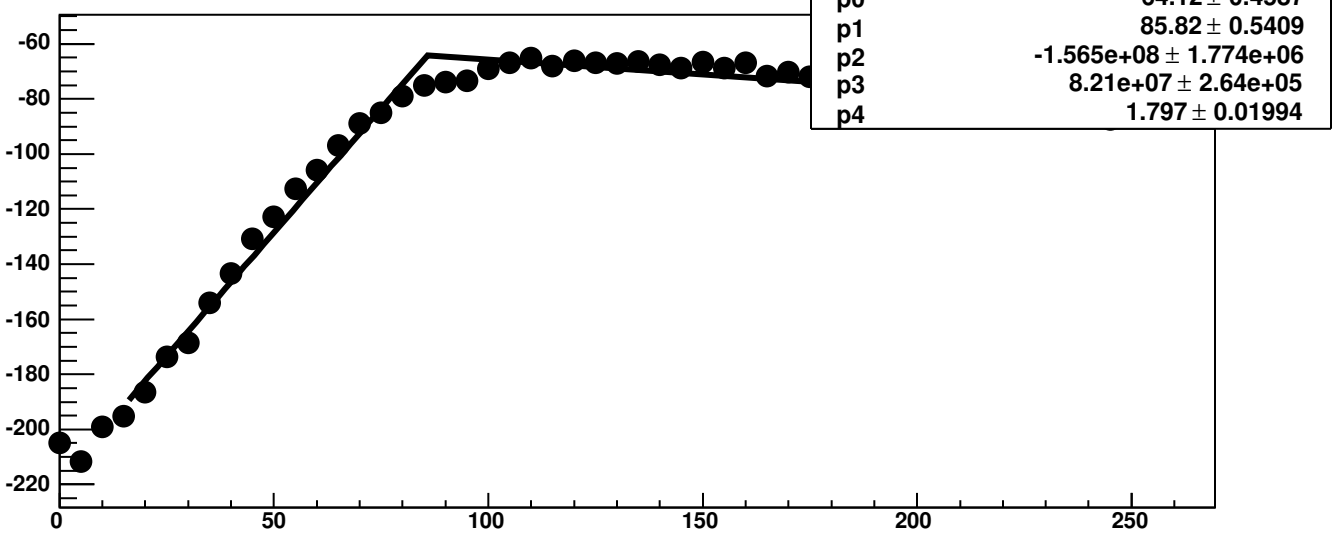
Chip 7, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



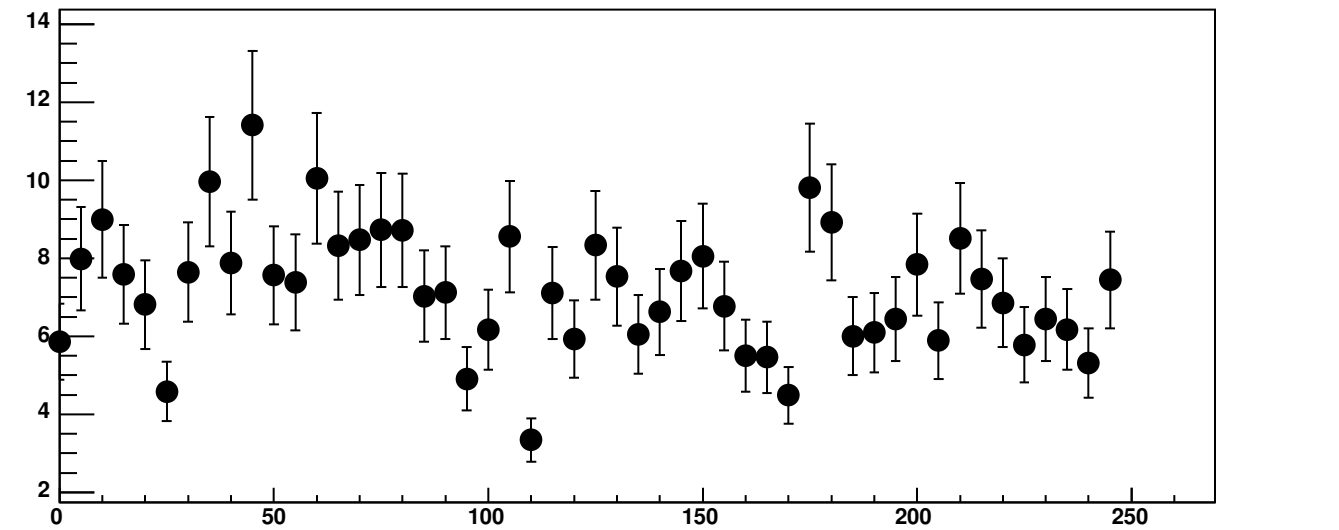
Chip 7, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold



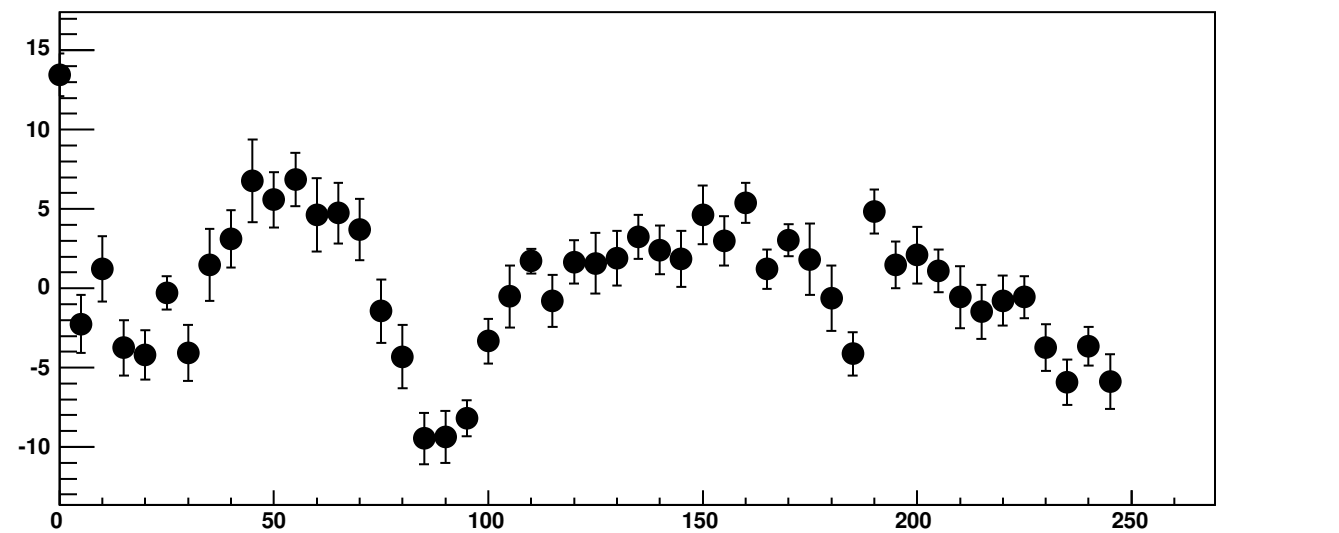
Chip 7, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold



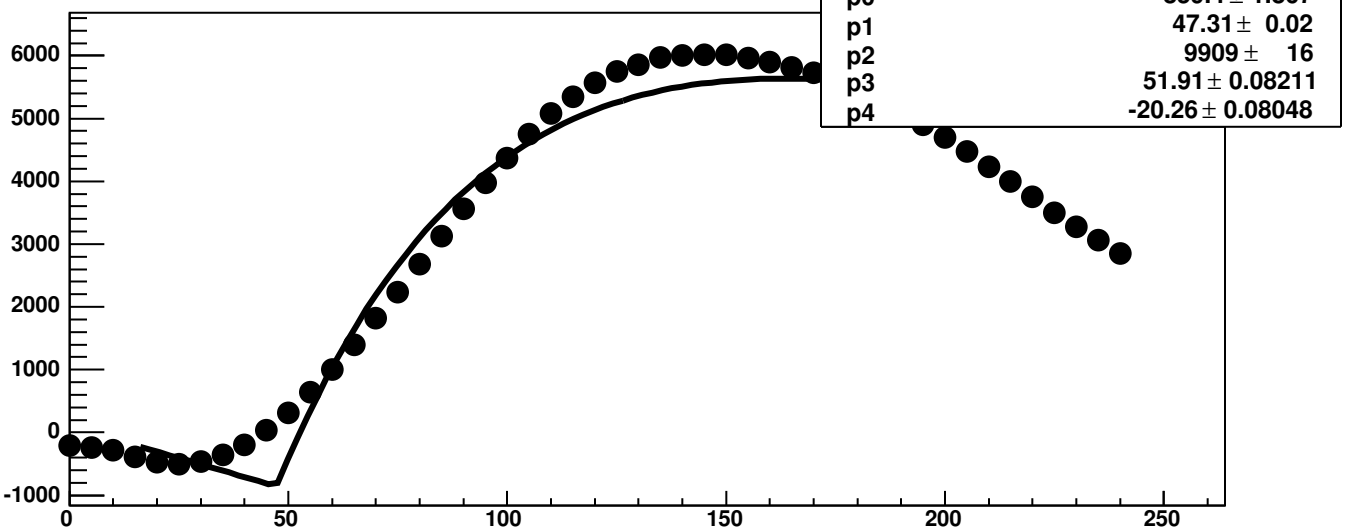
Chip 7, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold



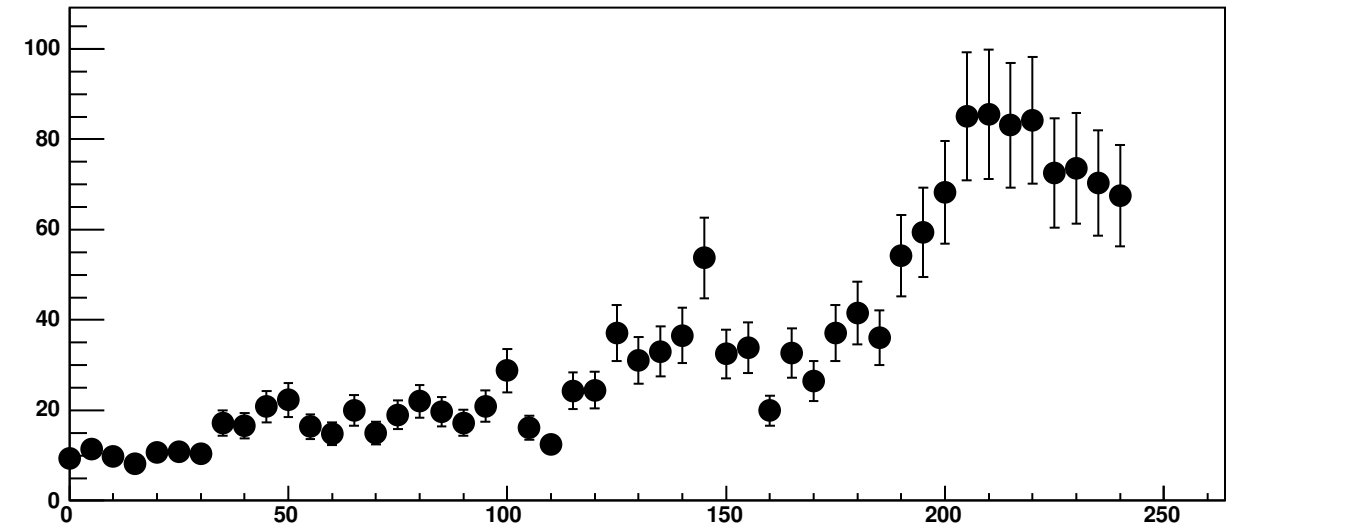
Chip 7, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold



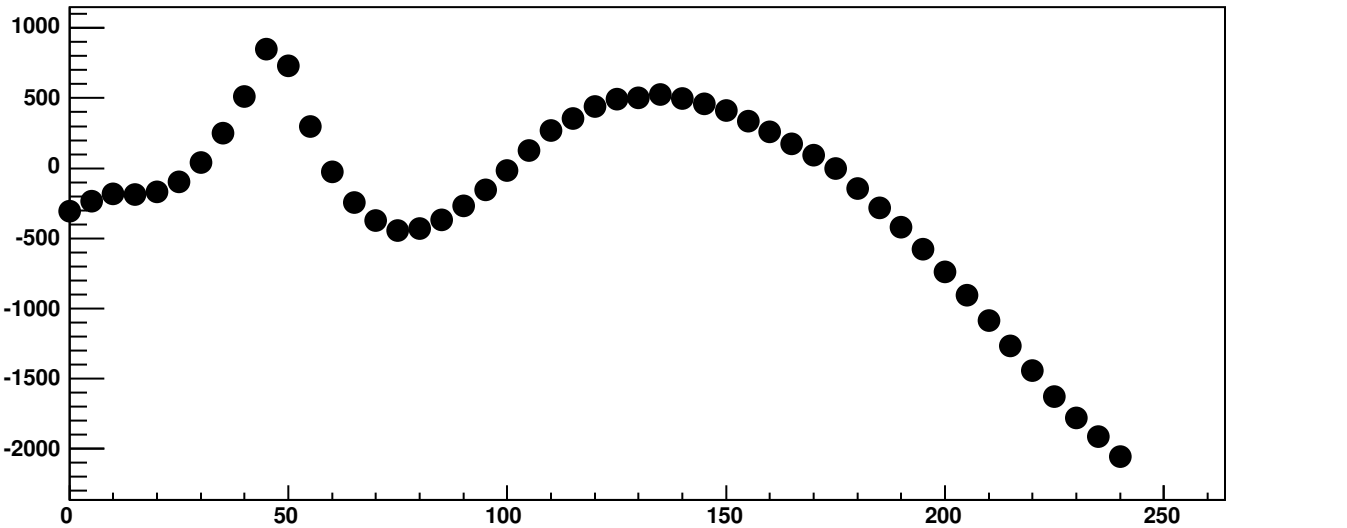
Chip 7, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold



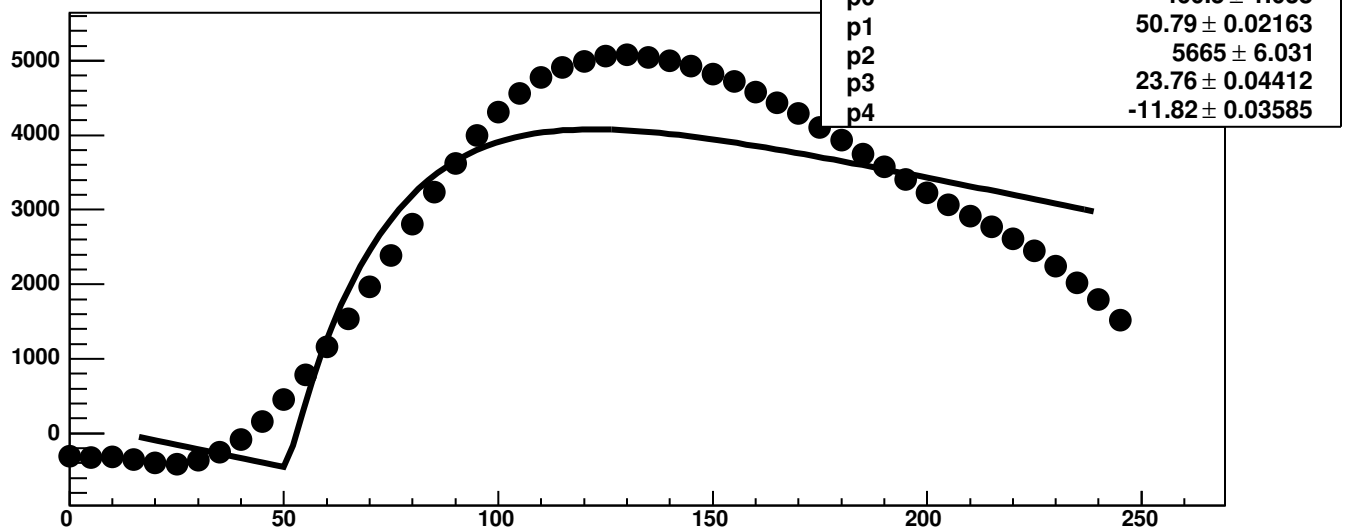
Chip 7, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



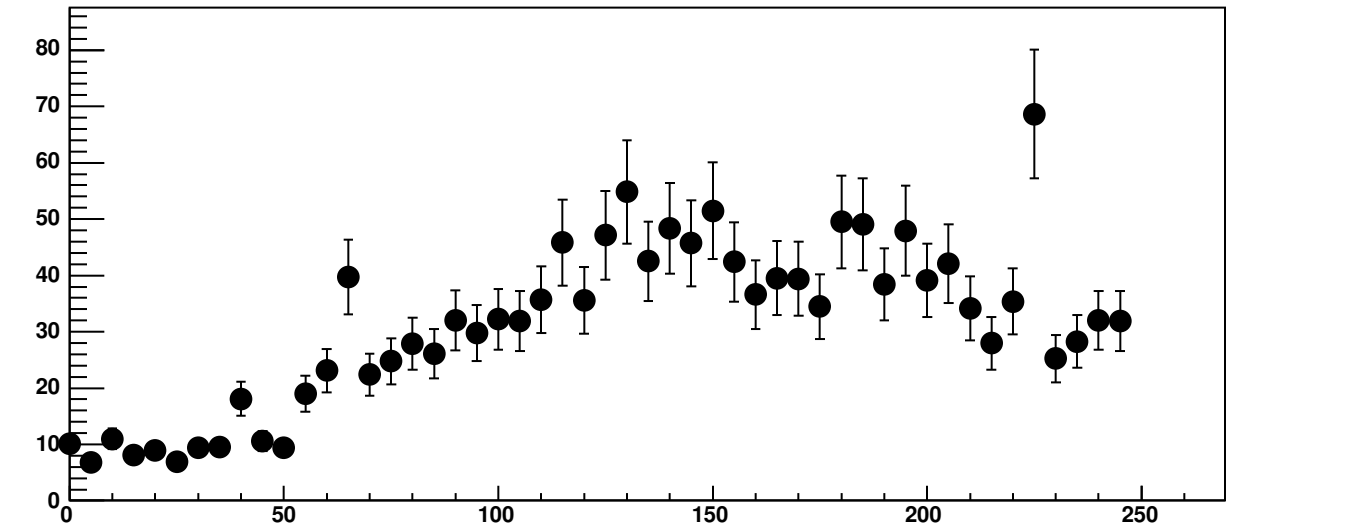
Chip 7, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold



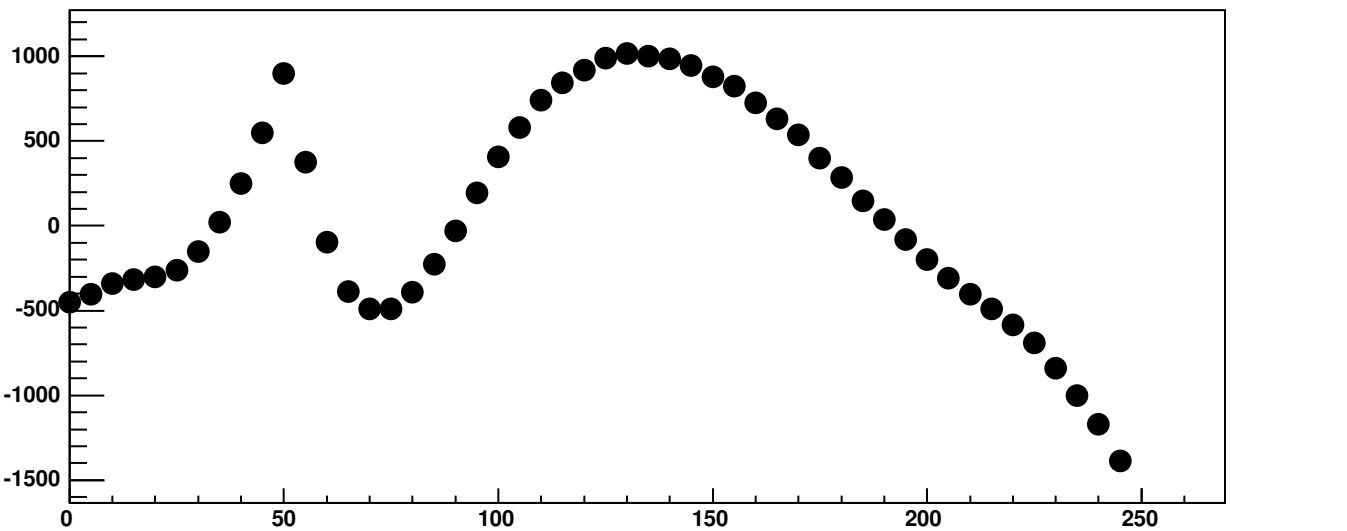
Chip 7, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold

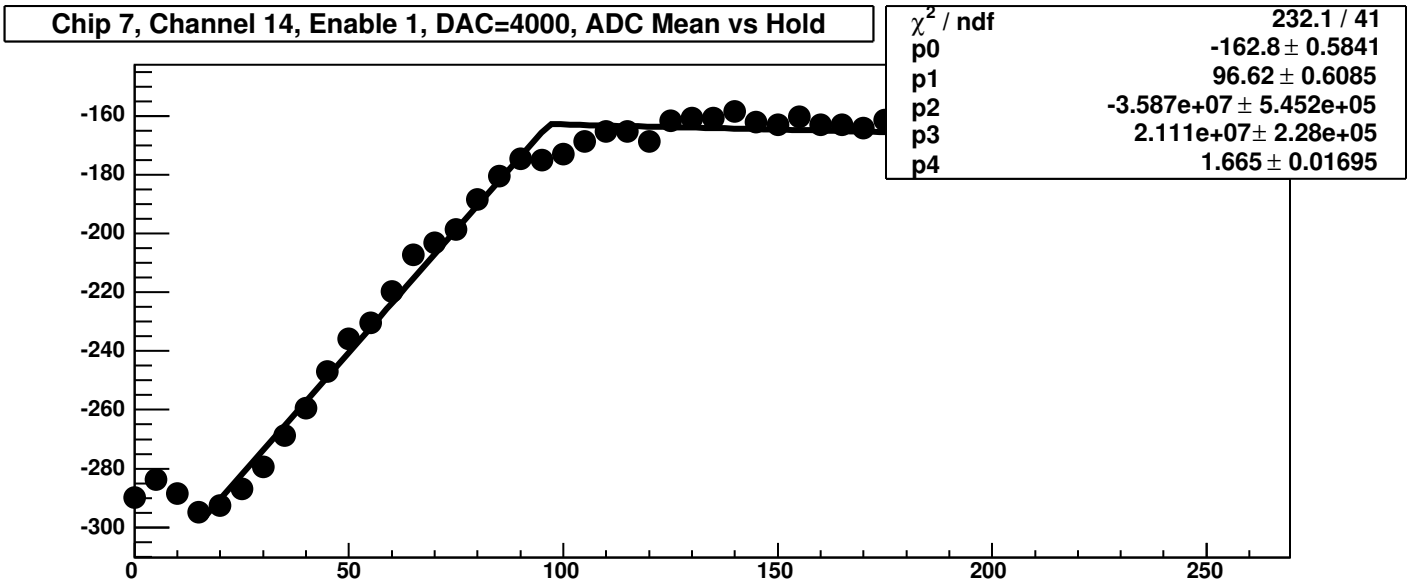


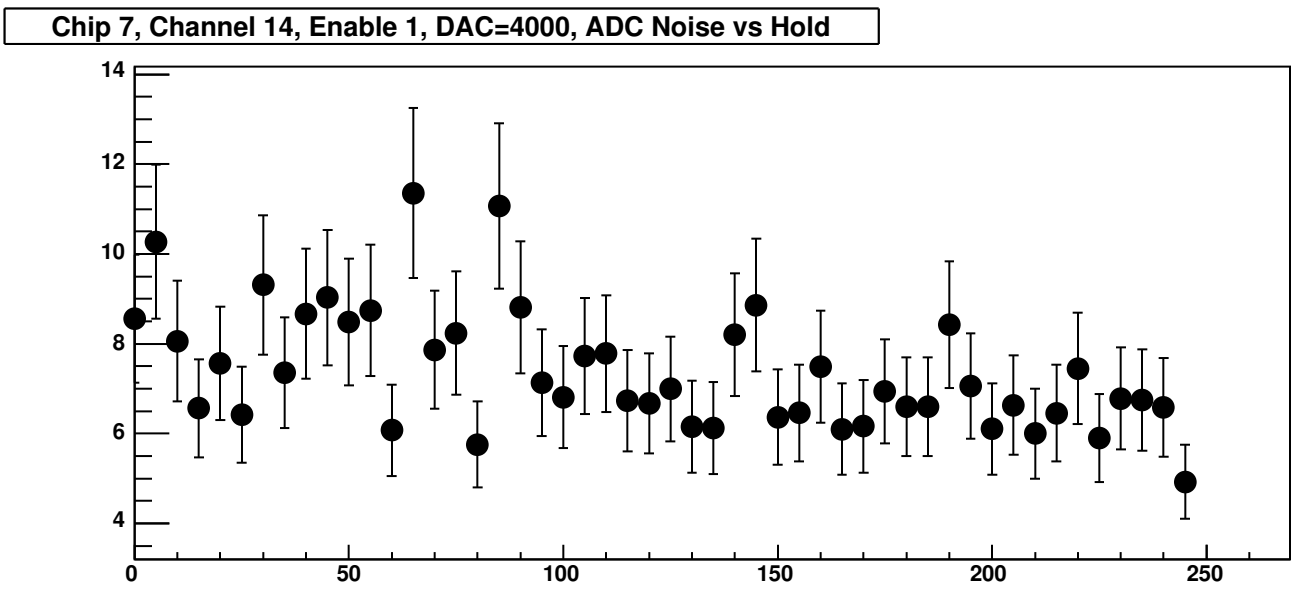
Chip 7, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold

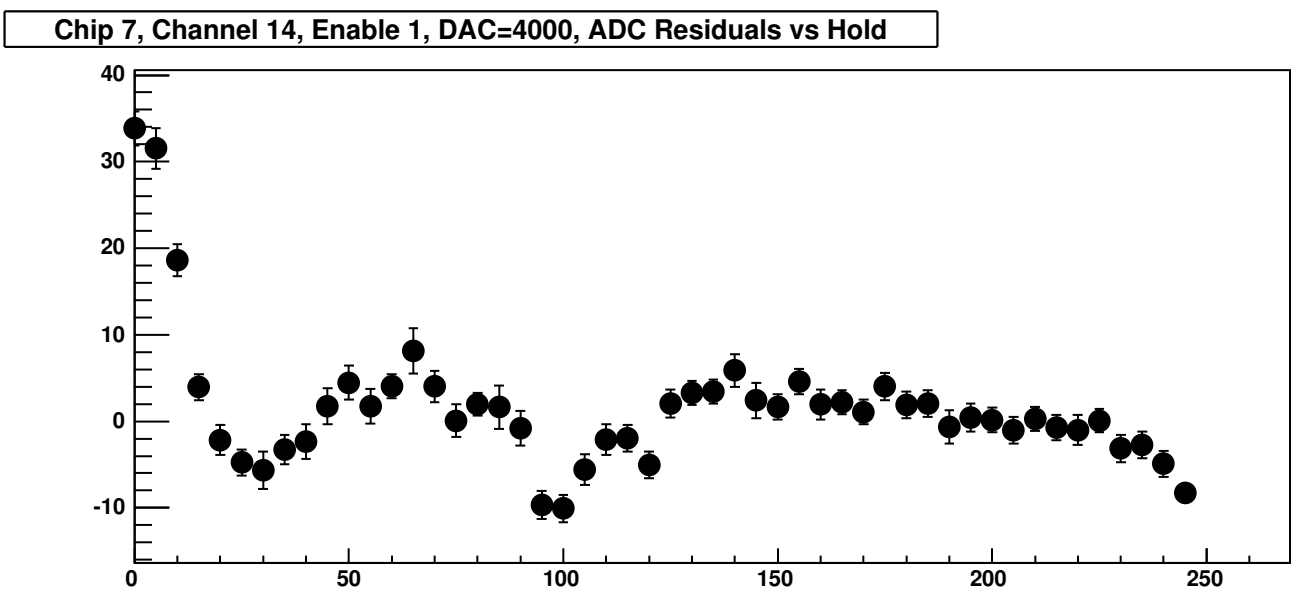


Chip 7, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

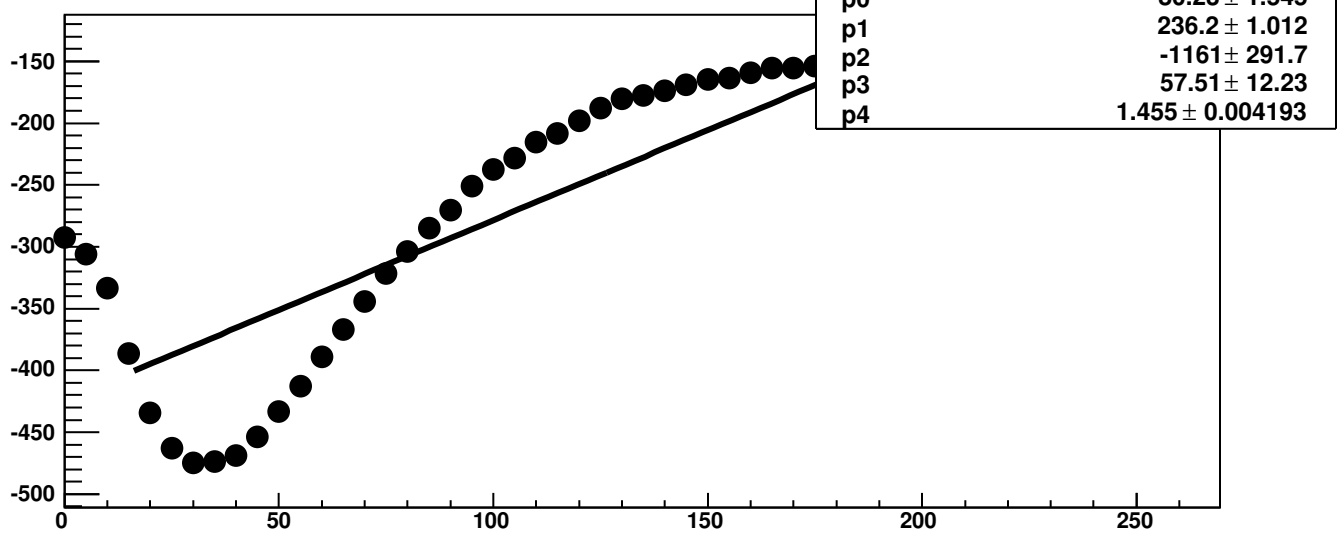



Chip 7, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 232.1 / 41
p0 -162.8 ± 0.5841
p1 96.62 ± 0.6085
p2 -3.587e+07 ± 5.452e+05
p3 2.111e+07 ± 2.28e+05
p4 1.665 ± 0.01695
-160
-180
-200
-220
-240
-260
-280
-300
0
50
100
150
200
250

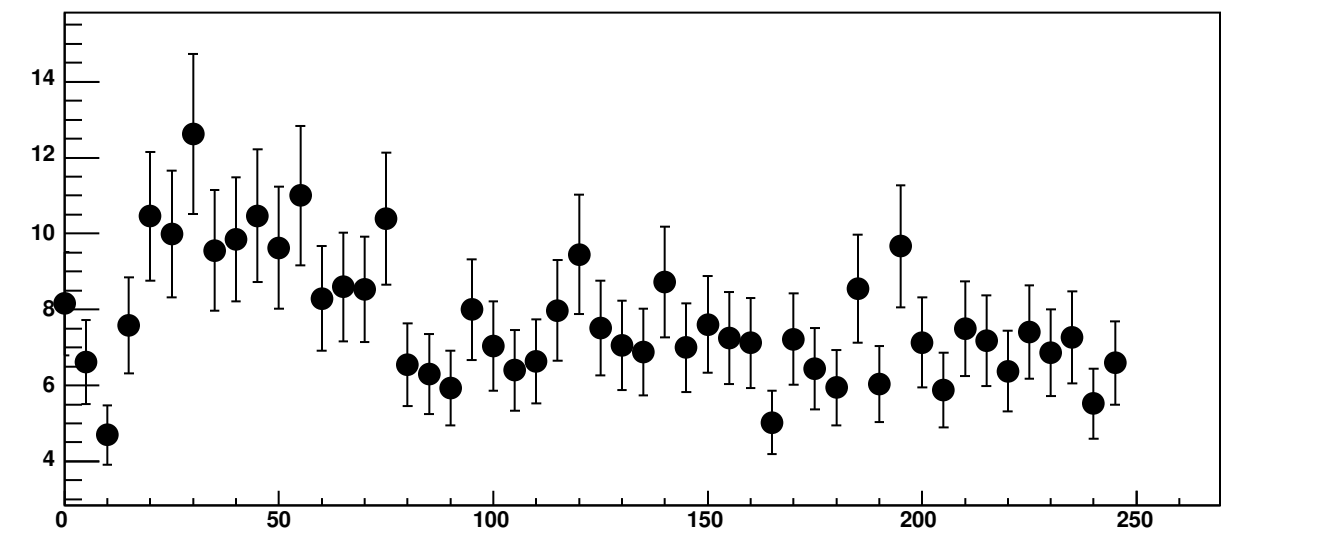

Chip 7, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 7, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

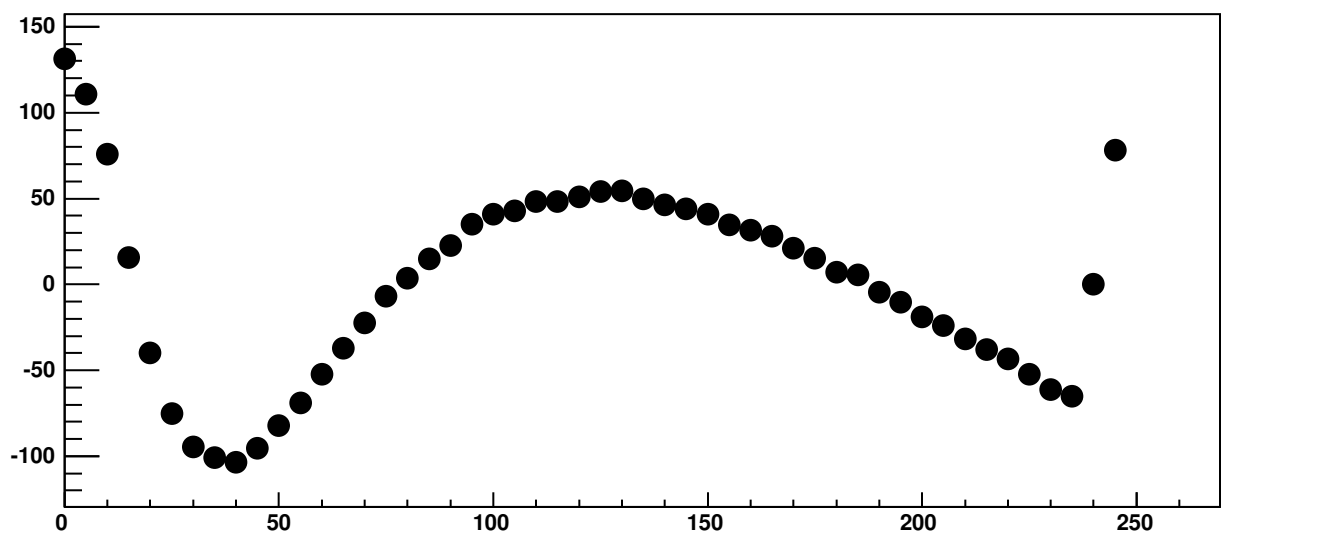
Chip 7, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

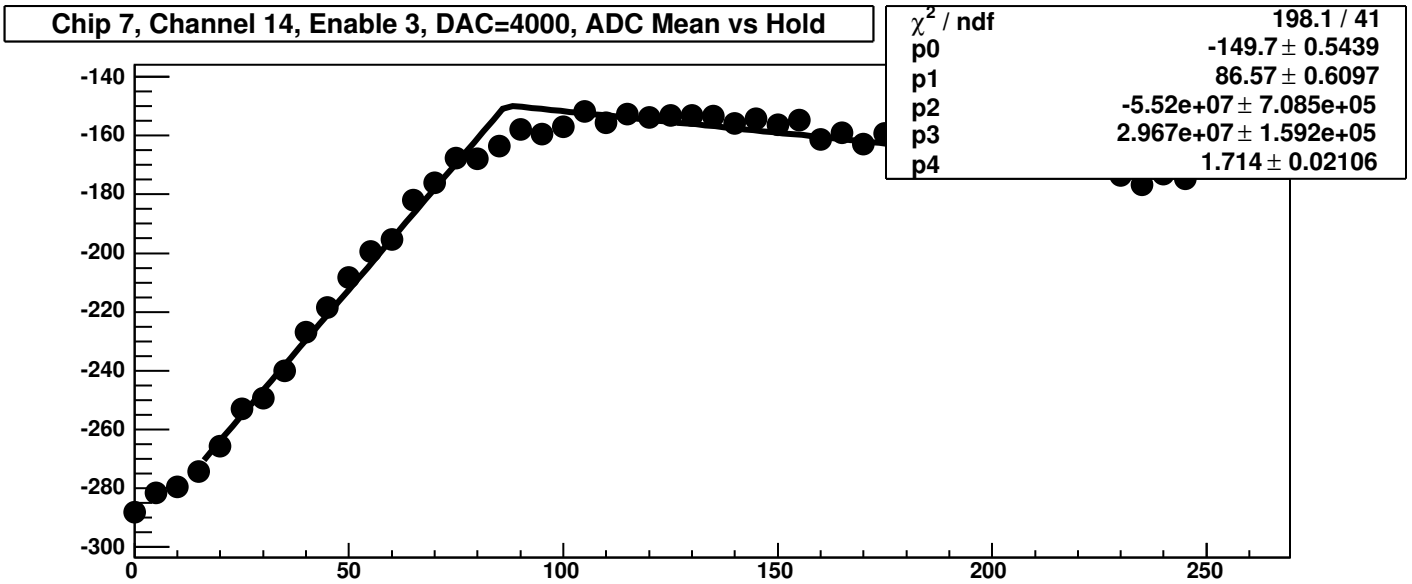


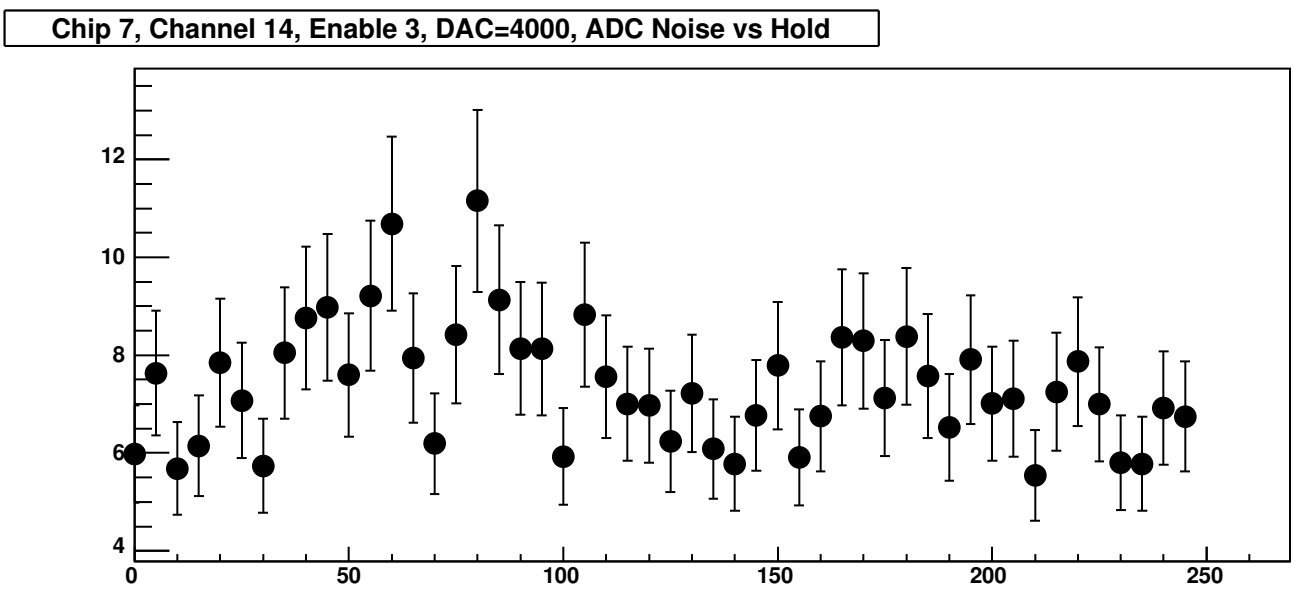
Chip 7, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold

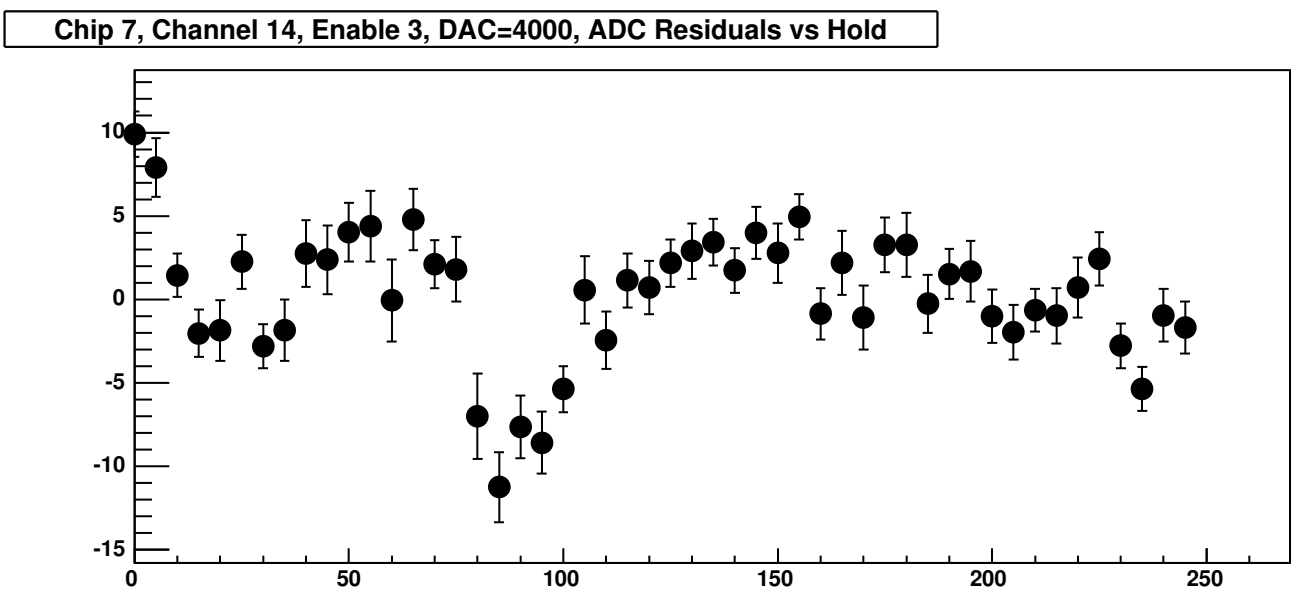


Chip 7, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold



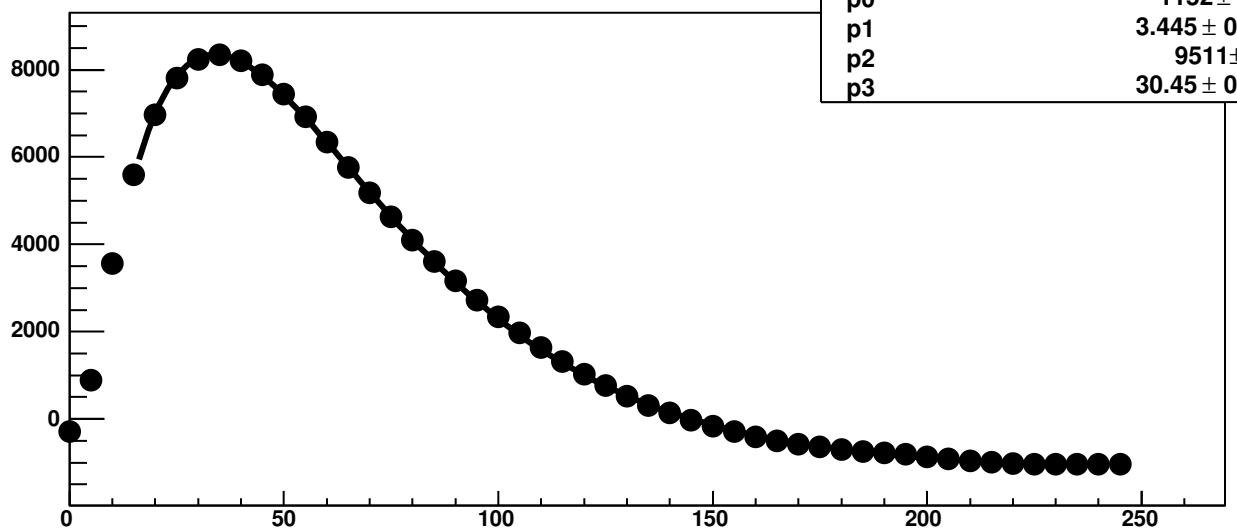

Chip 7, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 198.1 / 41
p0 -149.7 ± 0.5439
p1 86.57 ± 0.6097
p2 -5.52e+07 ± 7.085e+05
p3 2.967e+07 ± 1.592e+05
p4 1.714 ± 0.02106
-140
-160
-180
-200
-220
-240
-260
-280
-300
0
50
100
150
200
250


Chip 7, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

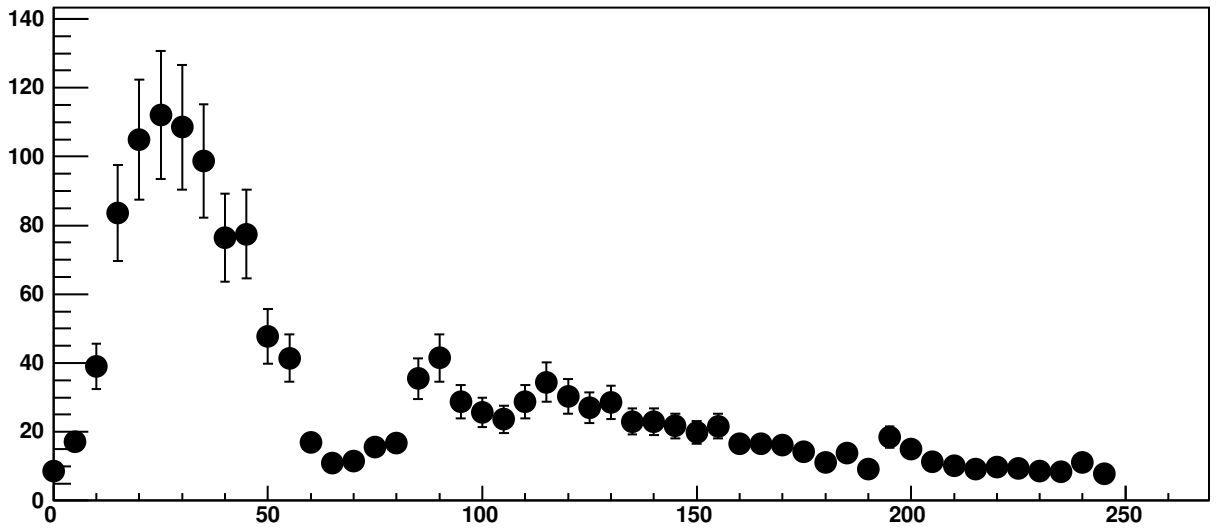

Chip 7, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
0
50
100
150
200
250

Chip 7, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold

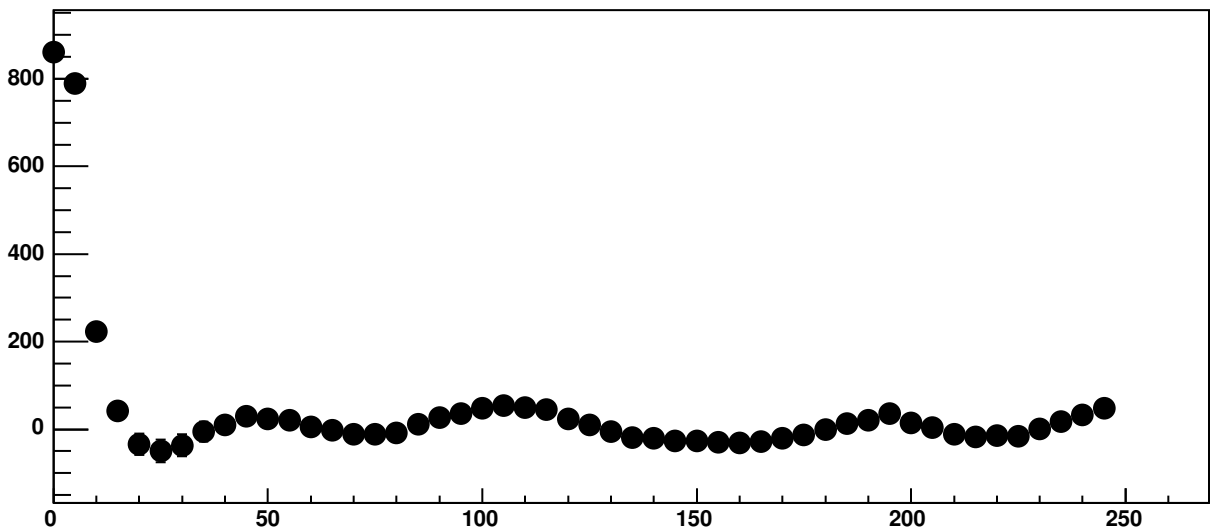
χ^2 / ndf	1305 / 42
p0	-1152 ± 0.9407
p1	3.445 ± 0.04108
p2	9511 ± 4.812
p3	30.45 ± 0.01828

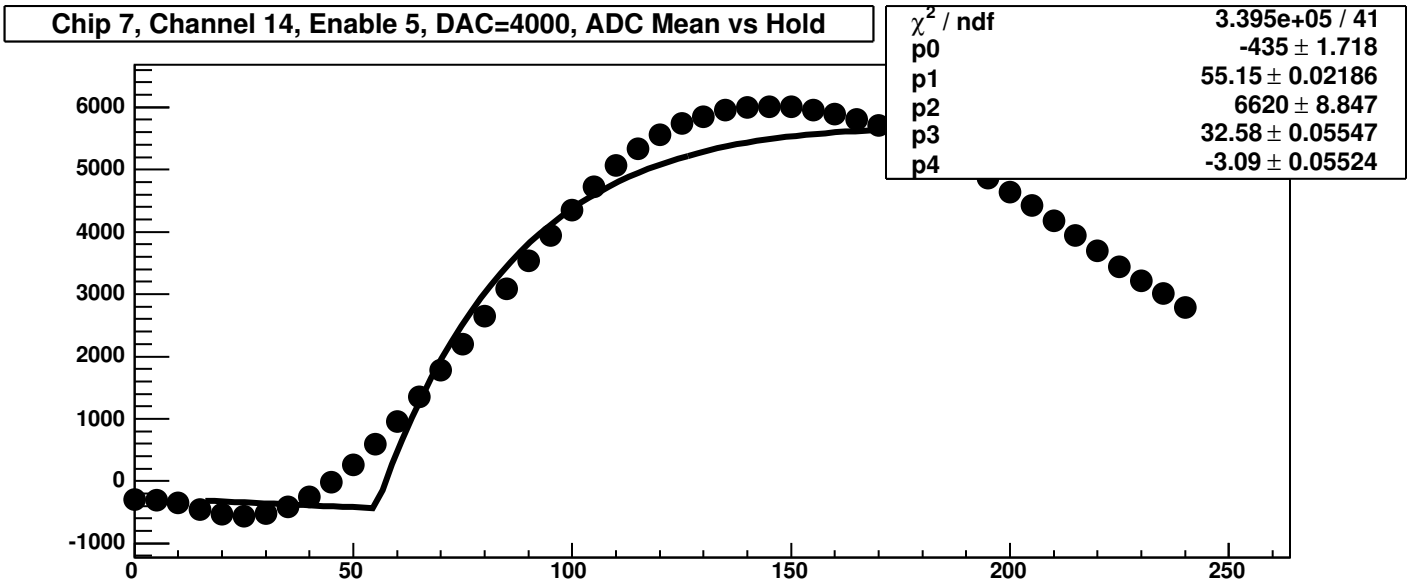


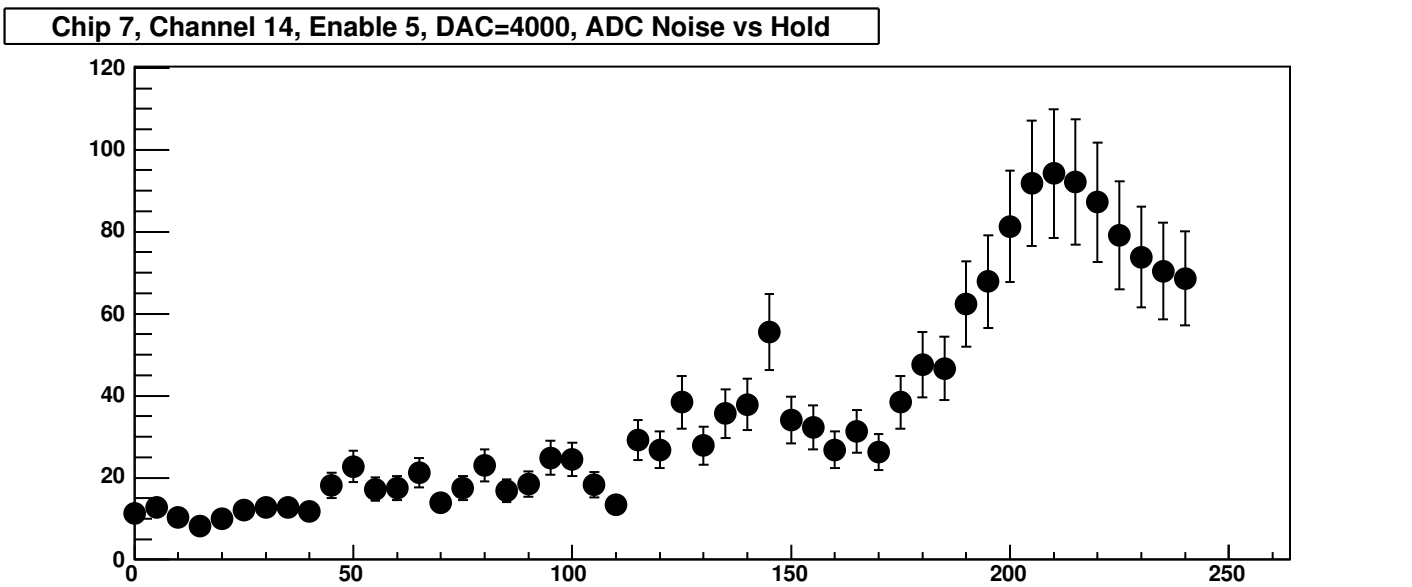
Chip 7, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold

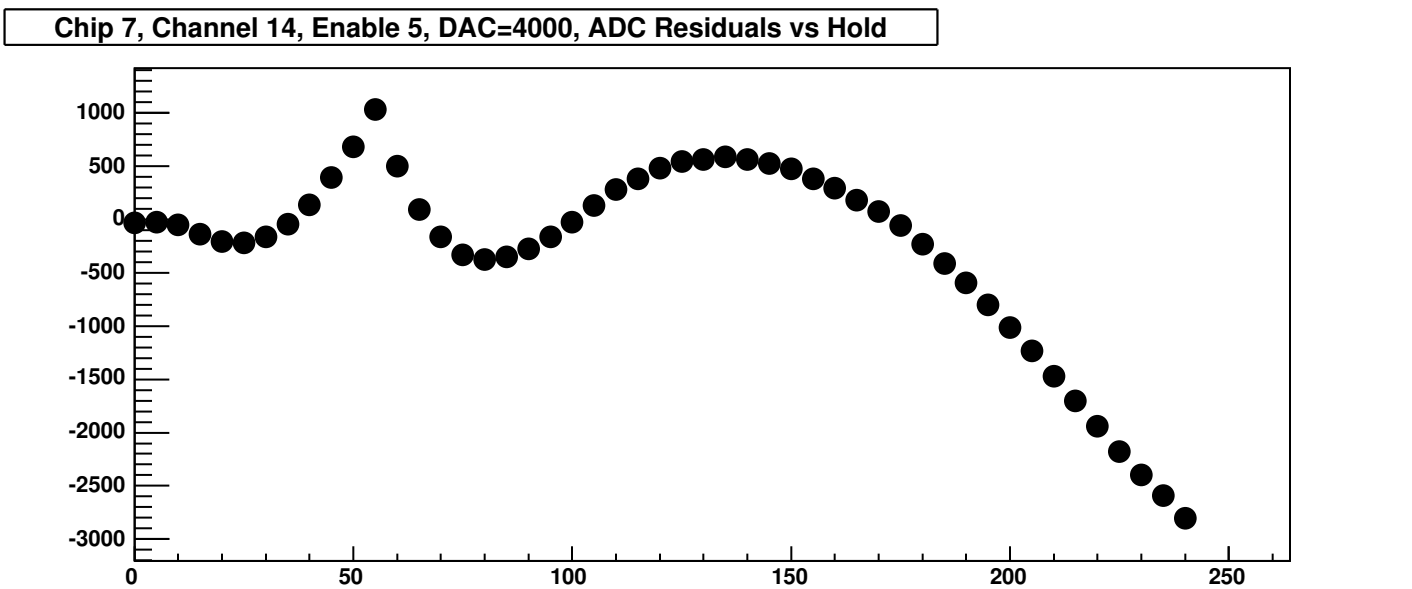


Chip 7, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold

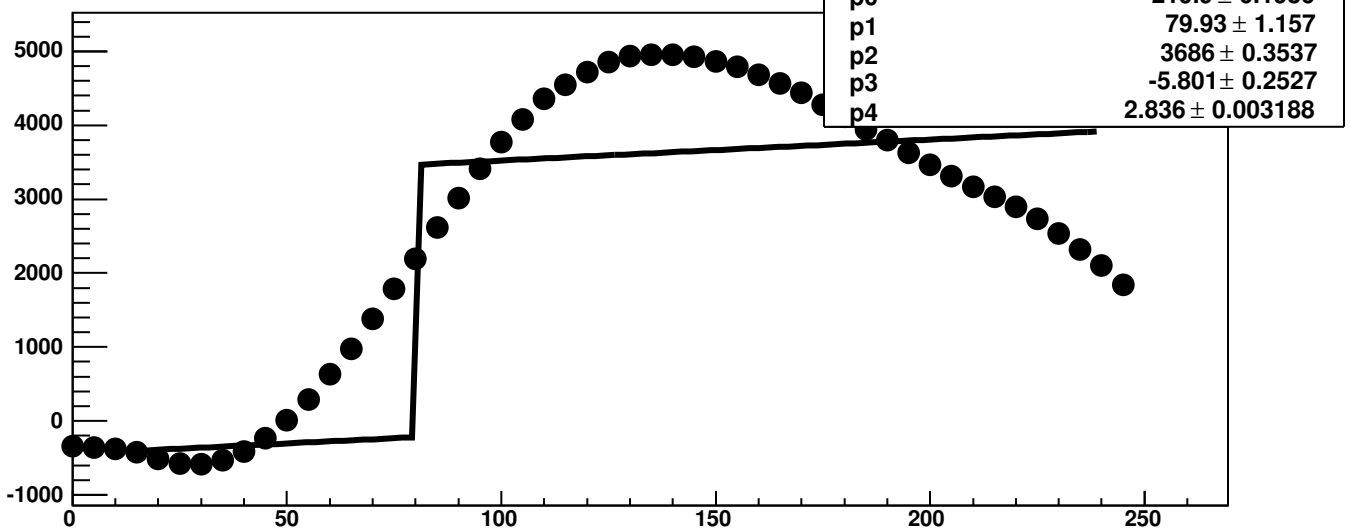



Chip 7, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.395e+05 / 41
p0 -435 ± 1.718
p1 55.15 ± 0.02186
p2 6620 ± 8.847
p3 32.58 ± 0.05547
p4 -3.09 ± 0.05524
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

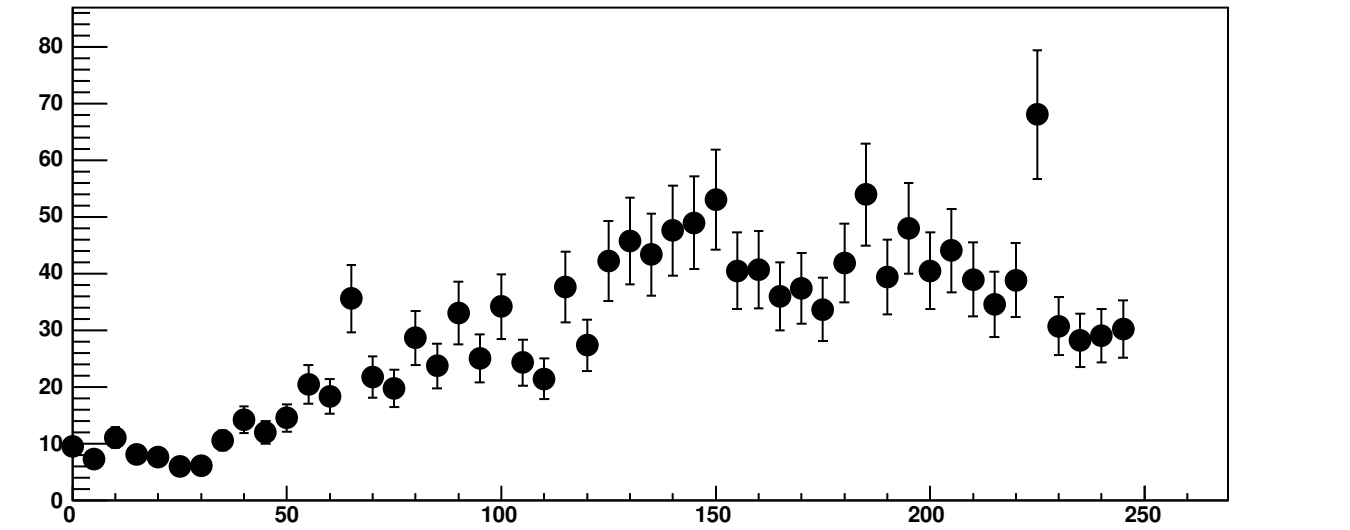

Chip 7, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 7, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250

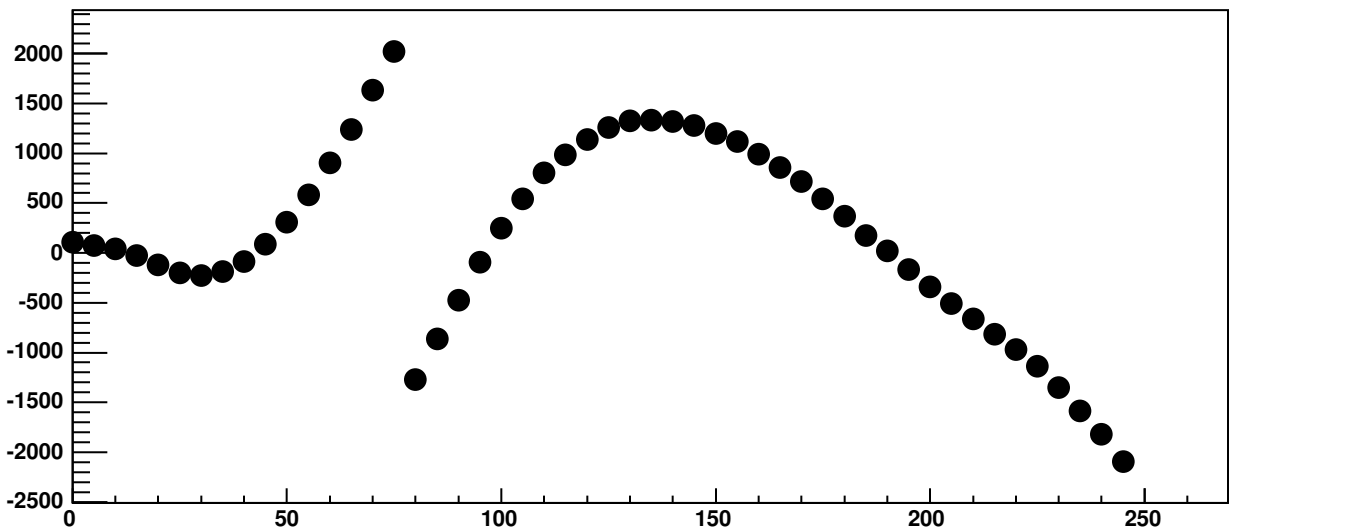
Chip 7, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold



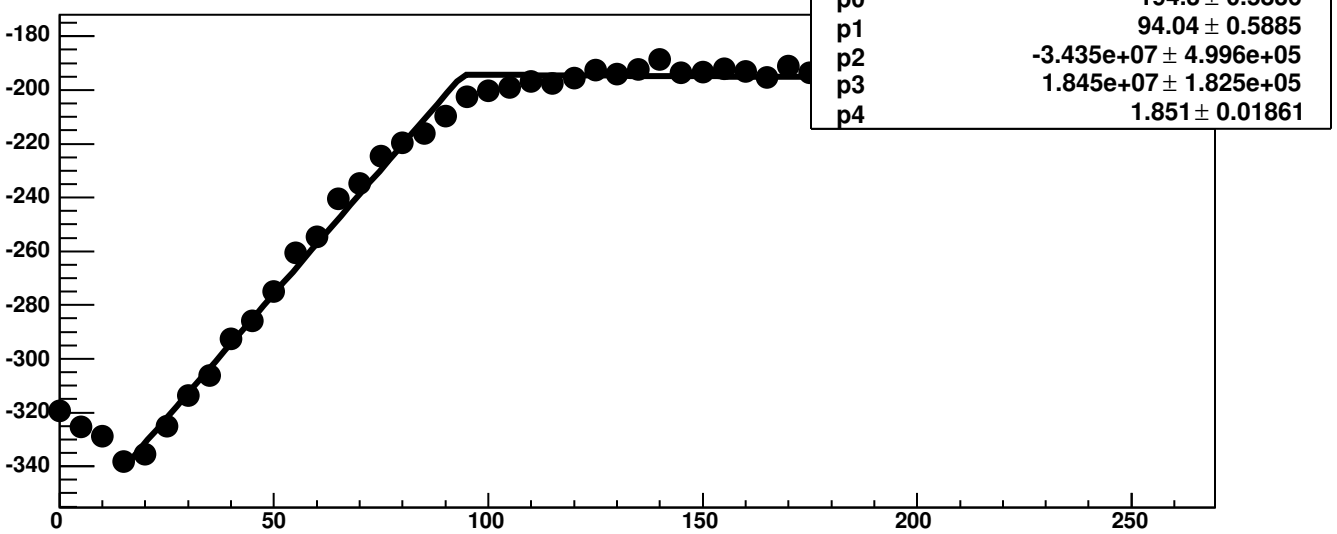
Chip 7, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



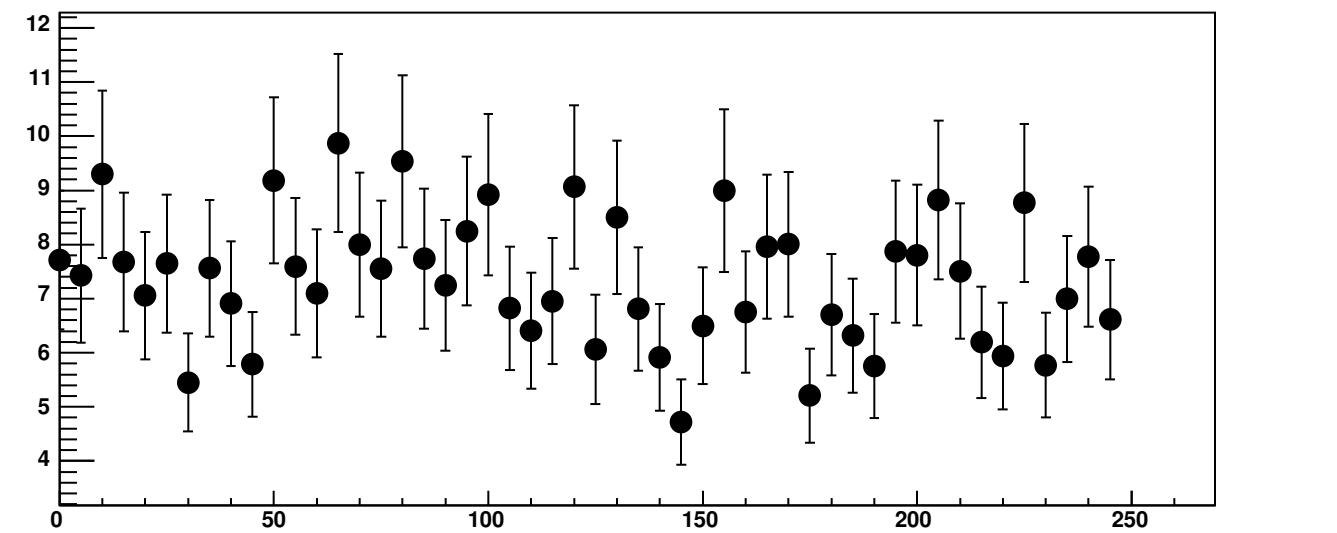
Chip 7, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold



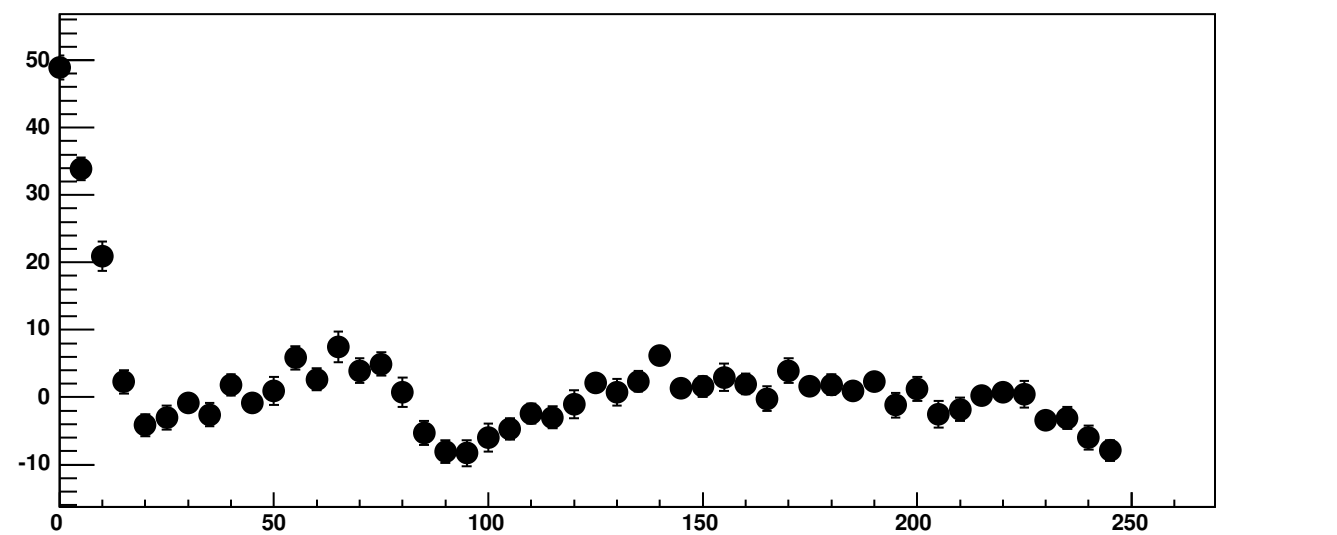
Chip 7, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold



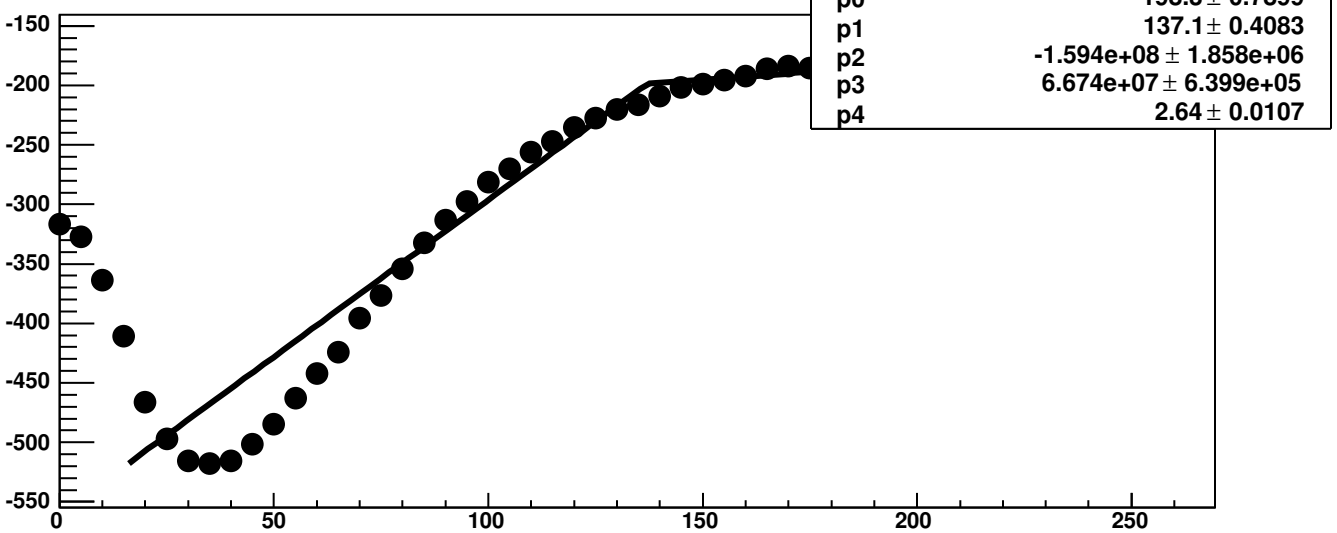
Chip 7, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



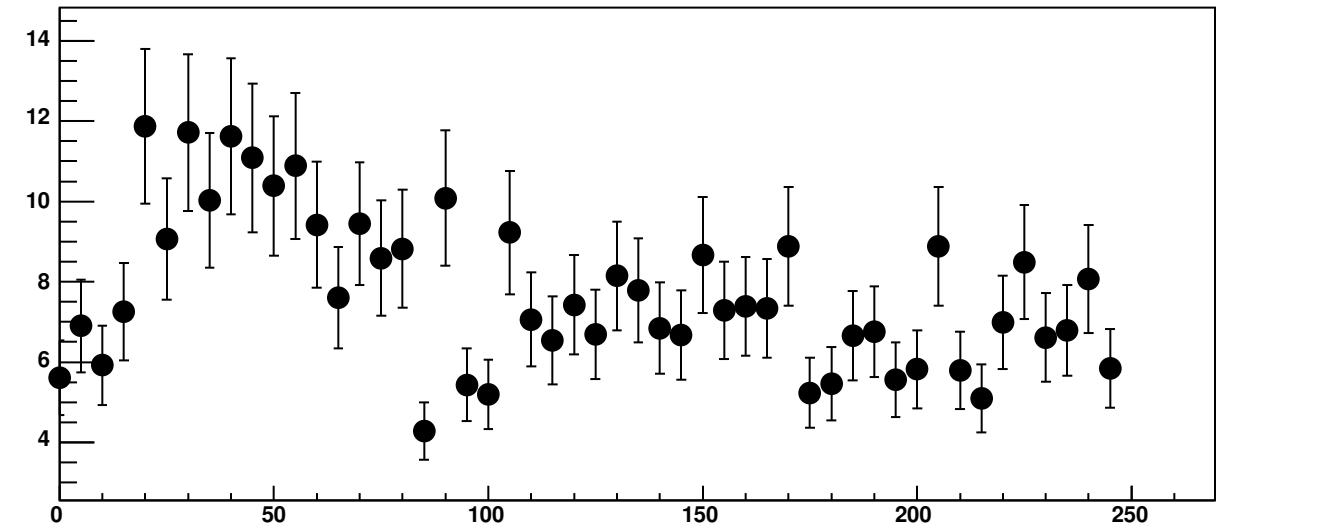
Chip 7, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold



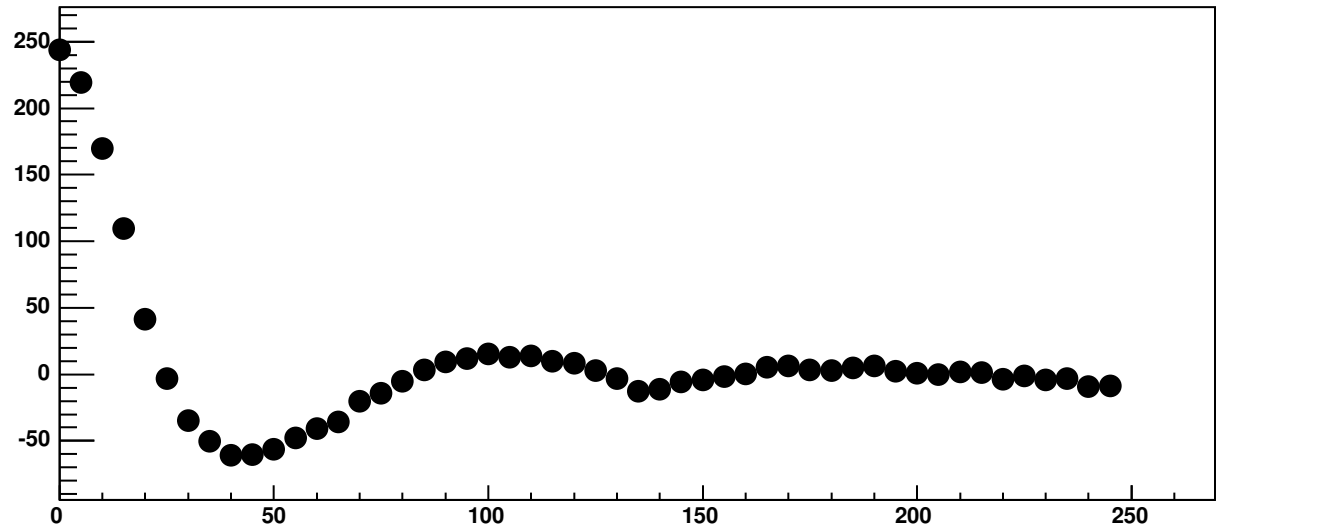
Chip 7, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold



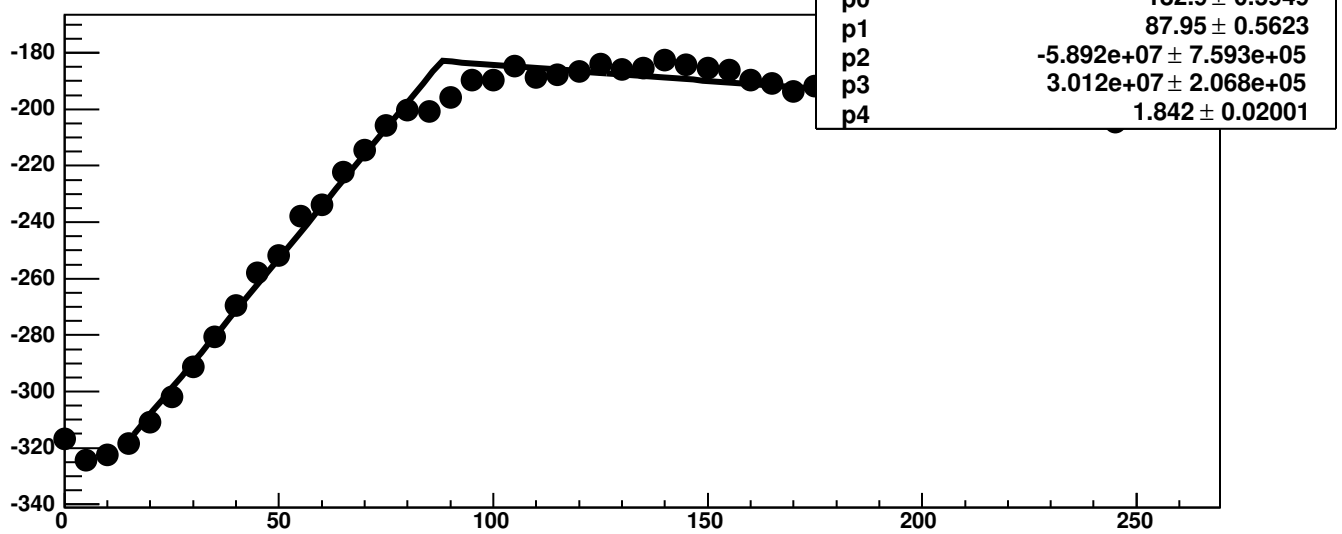
Chip 7, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold



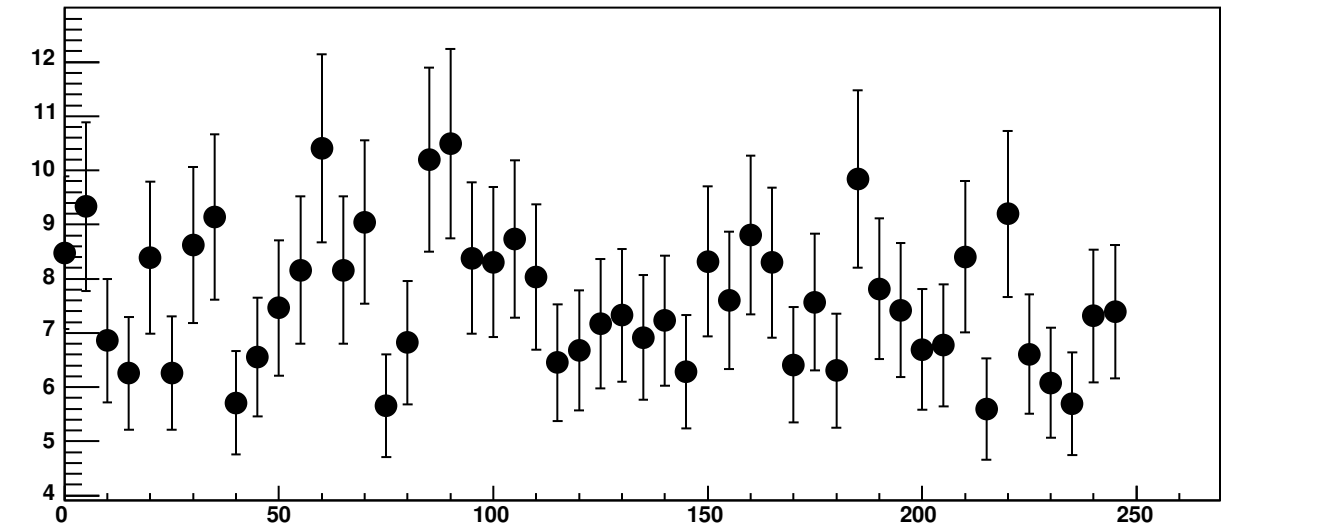
Chip 7, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold



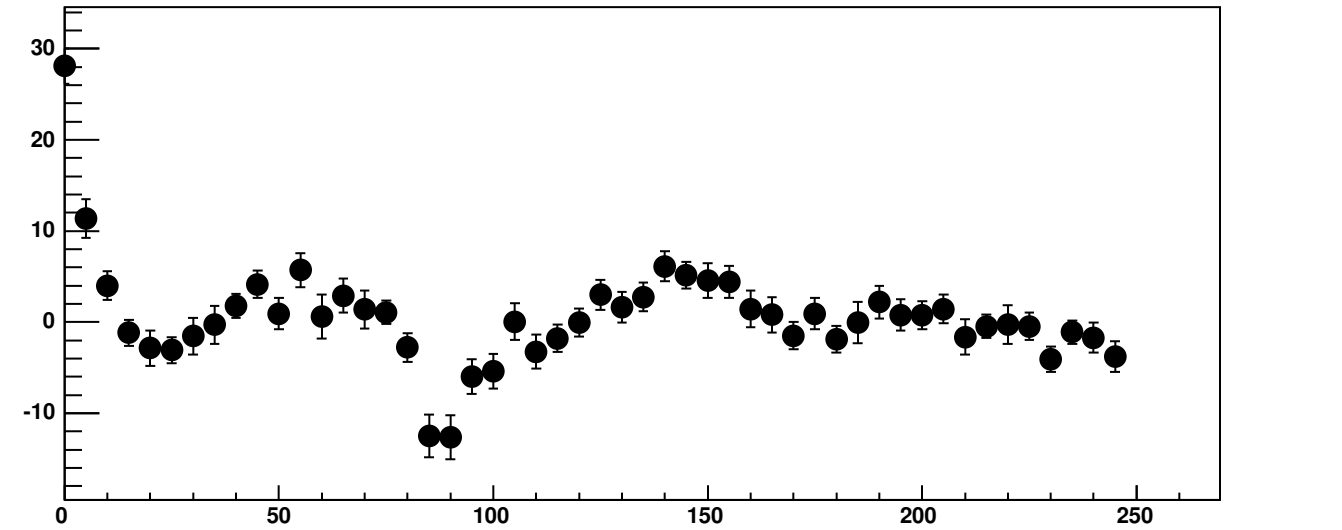
Chip 7, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold



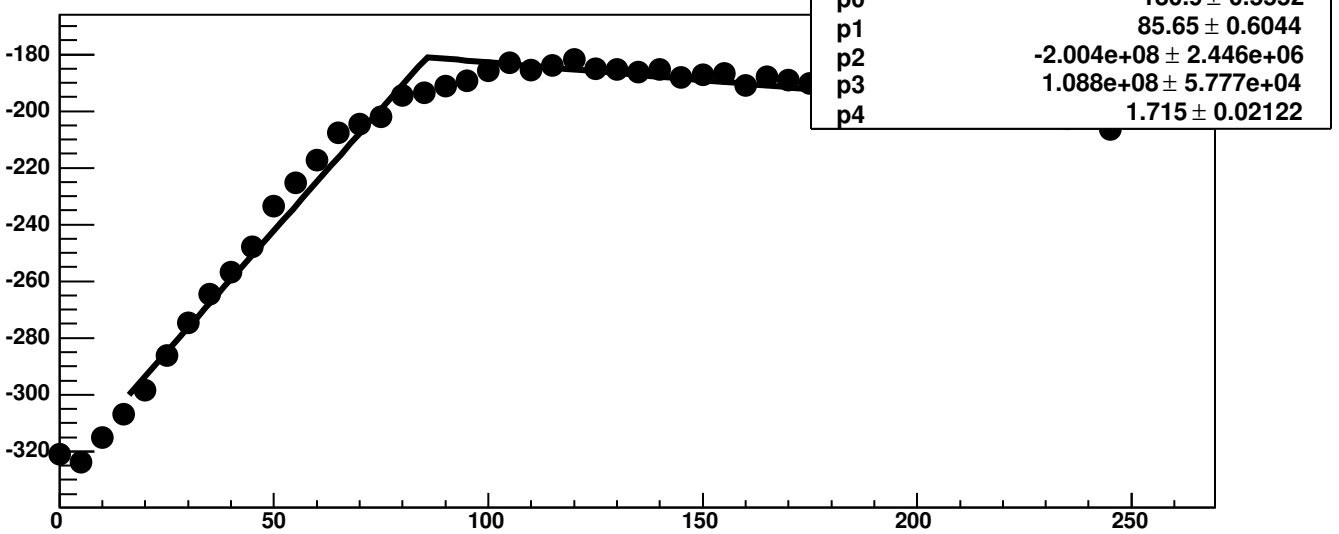
Chip 7, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



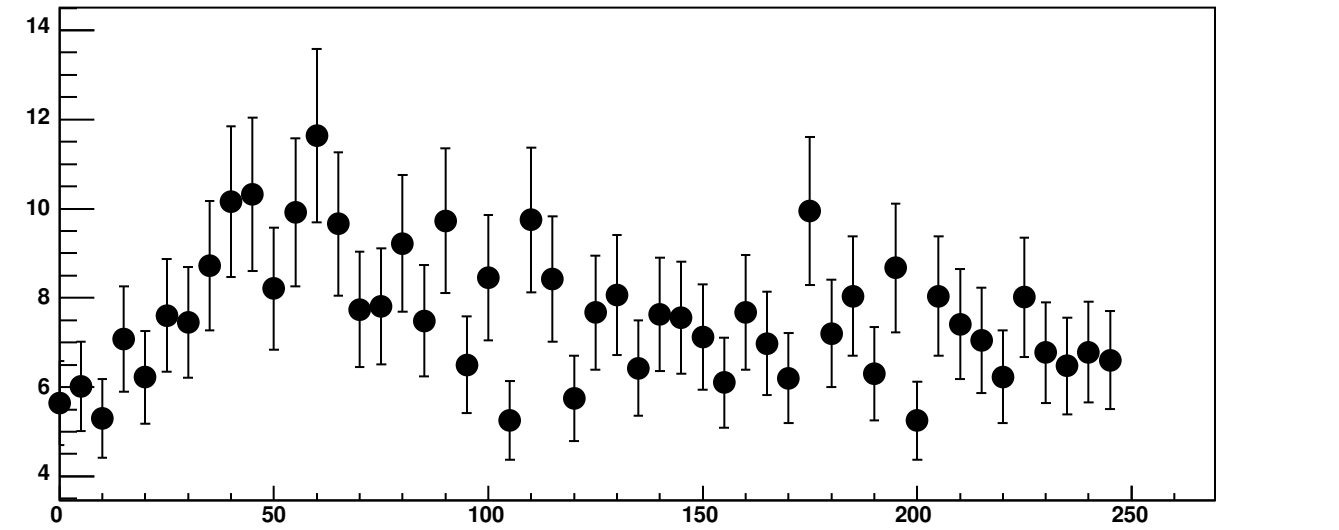
Chip 7, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold



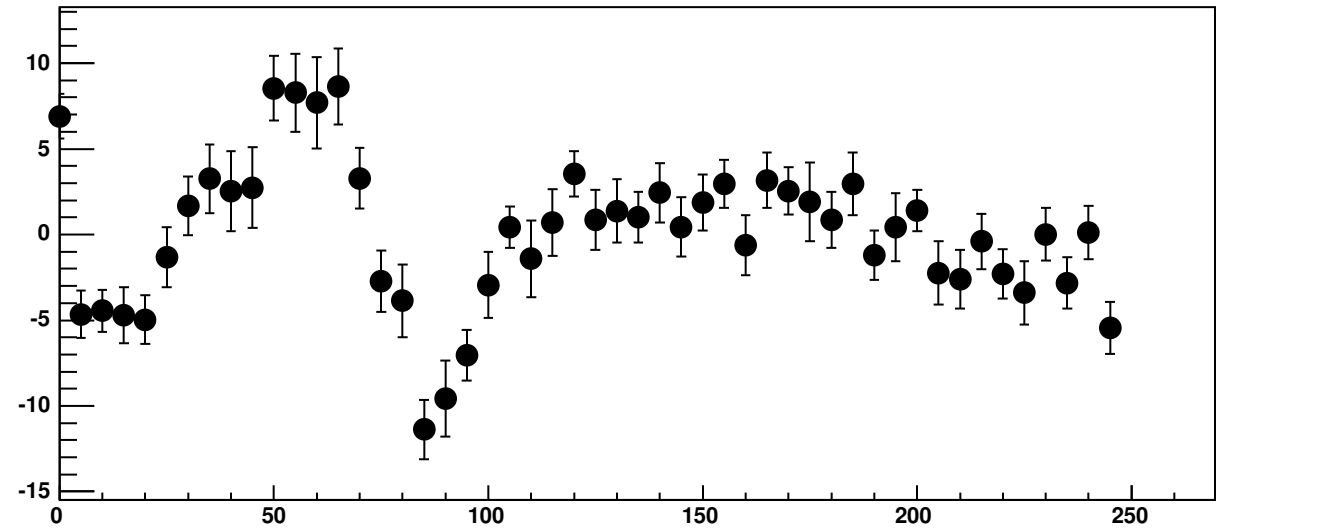
Chip 7, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

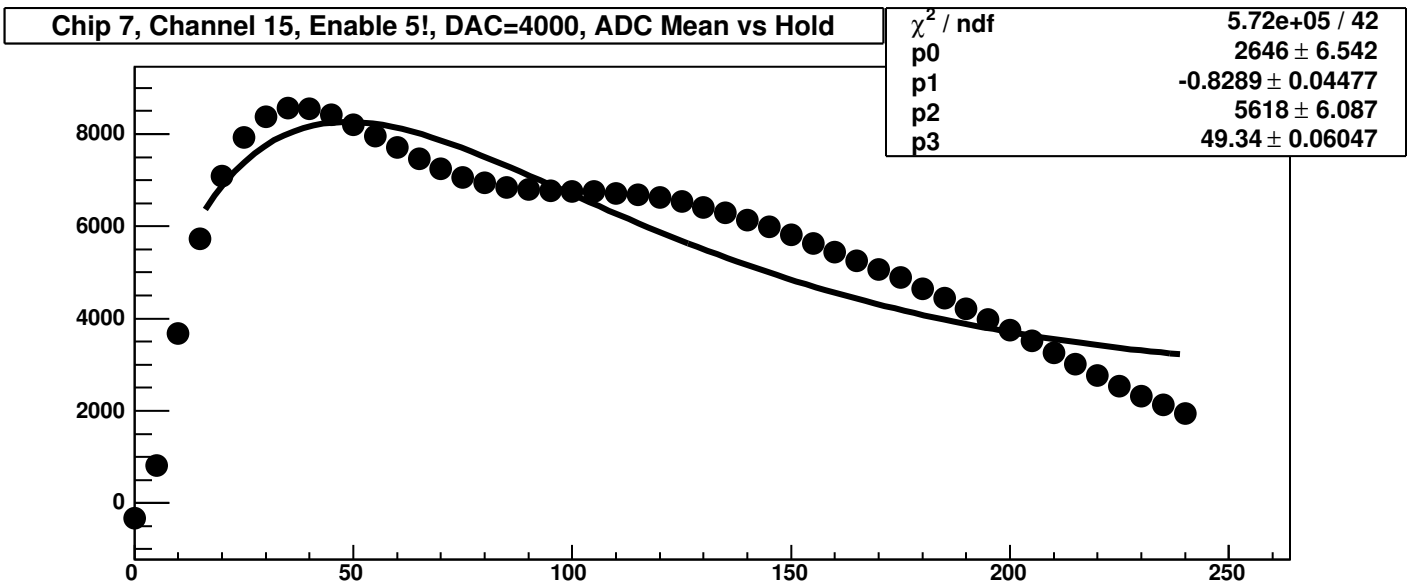


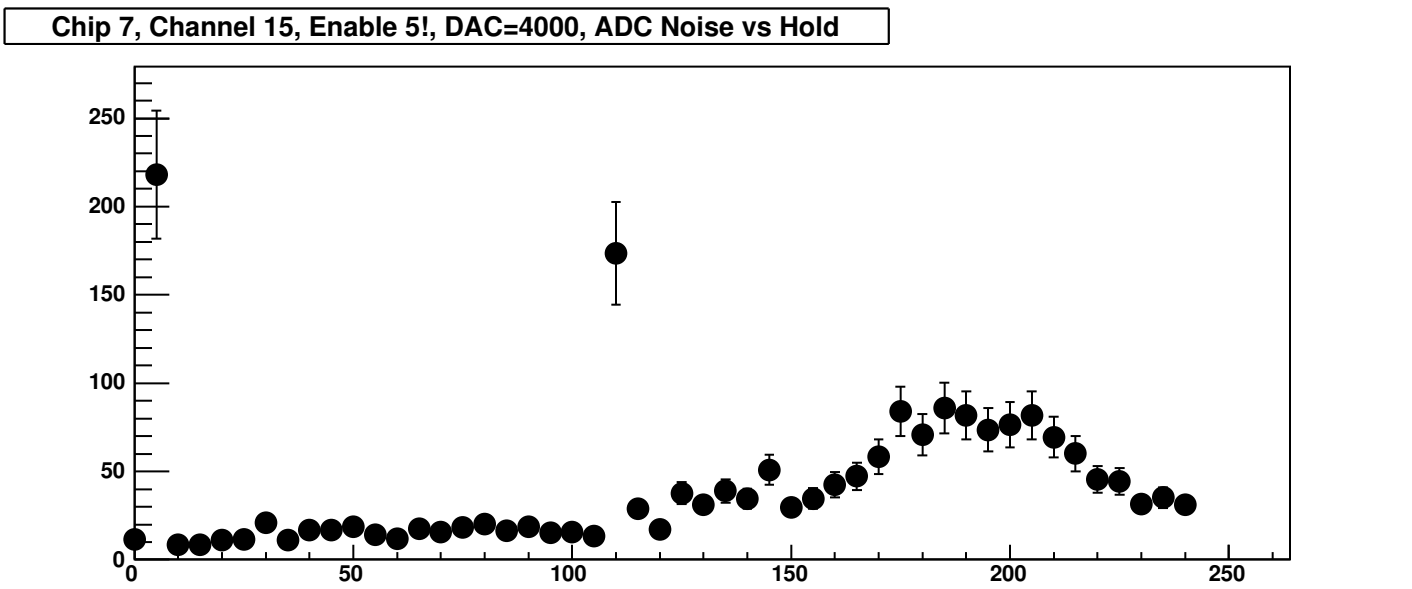
Chip 7, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold

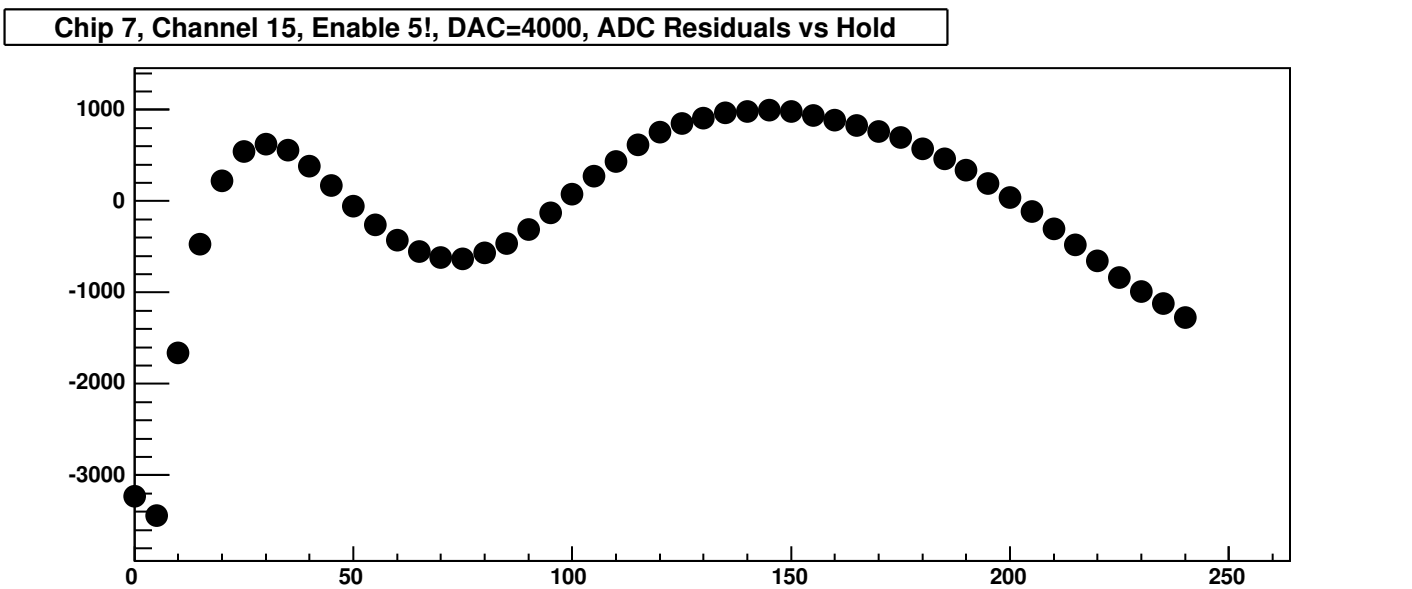


Chip 7, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

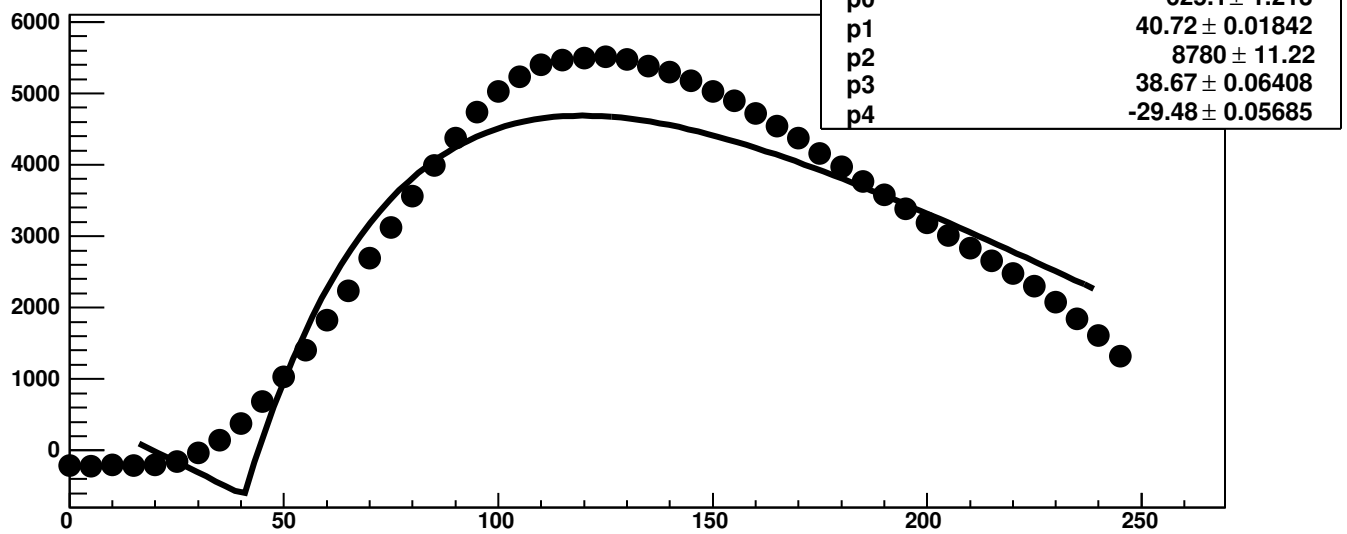



Chip 7, Channel 15, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.72e+05 / 42
p0 2646 ± 6.542
p1 -0.8289 ± 0.04477
p2 5618 ± 6.087
p3 49.34 ± 0.06047
8000
6000
4000
2000
0
0
50
100
150
200
250

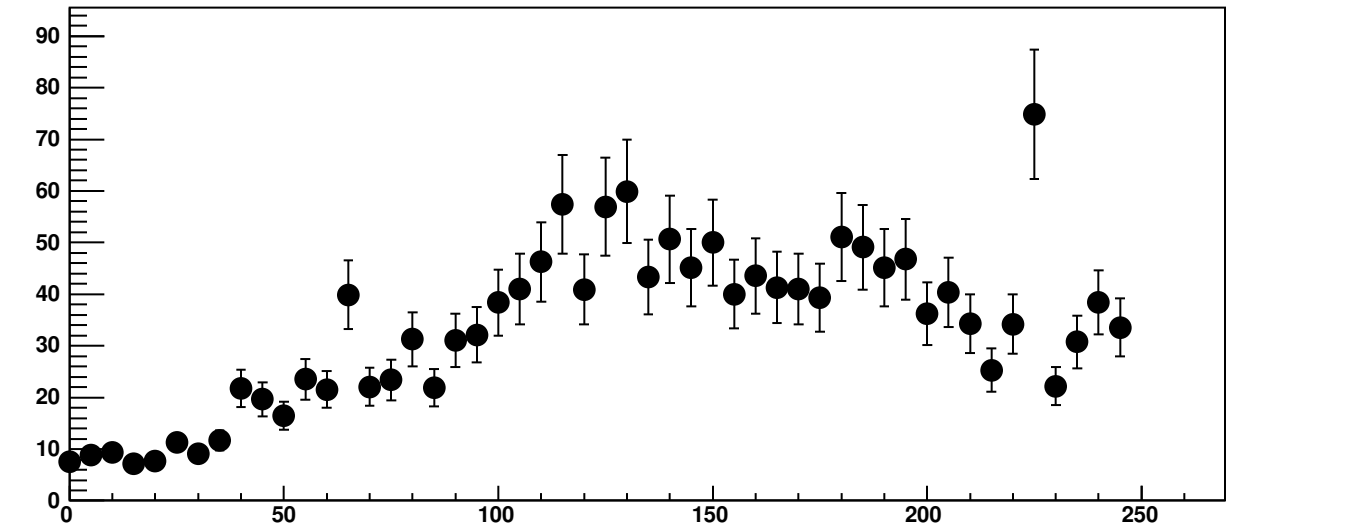

Chip 7, Channel 15, Enable 5!, DAC=4000, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250


Chip 7, Channel 15, Enable 5!, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

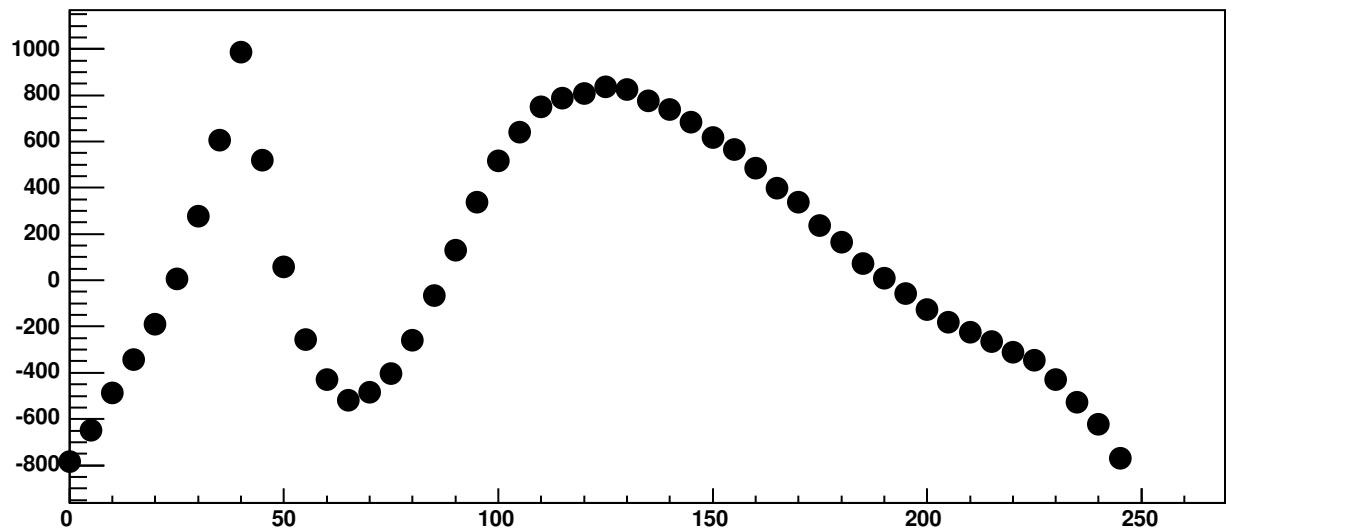
Chip 7, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

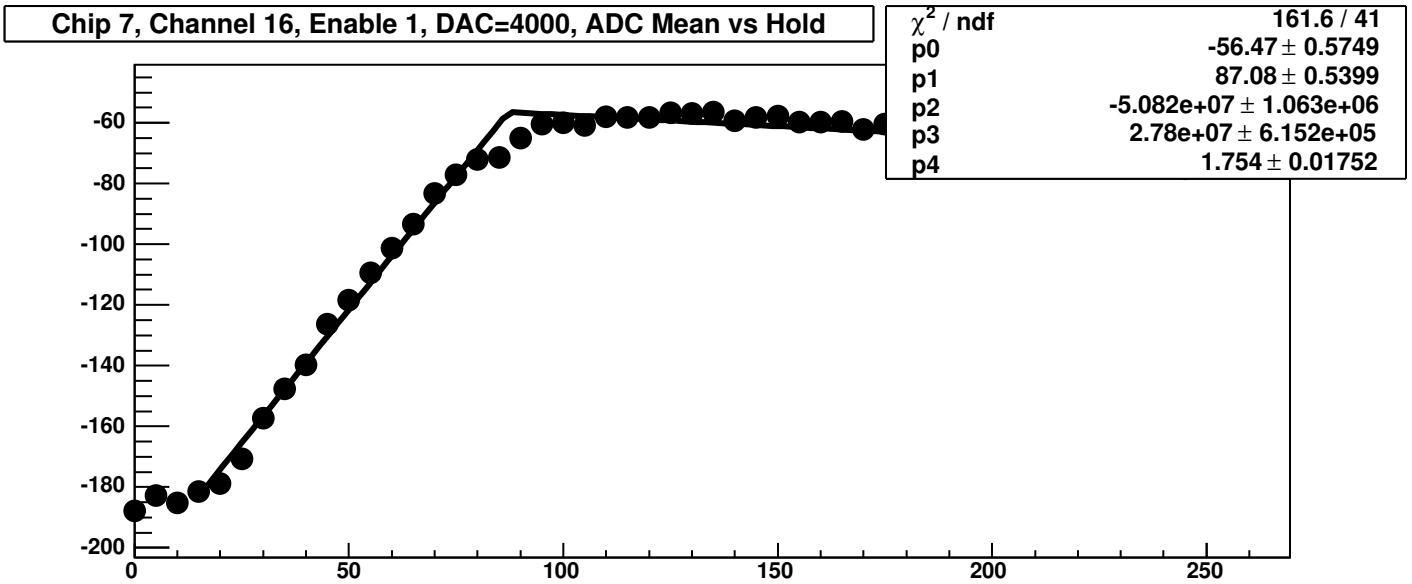


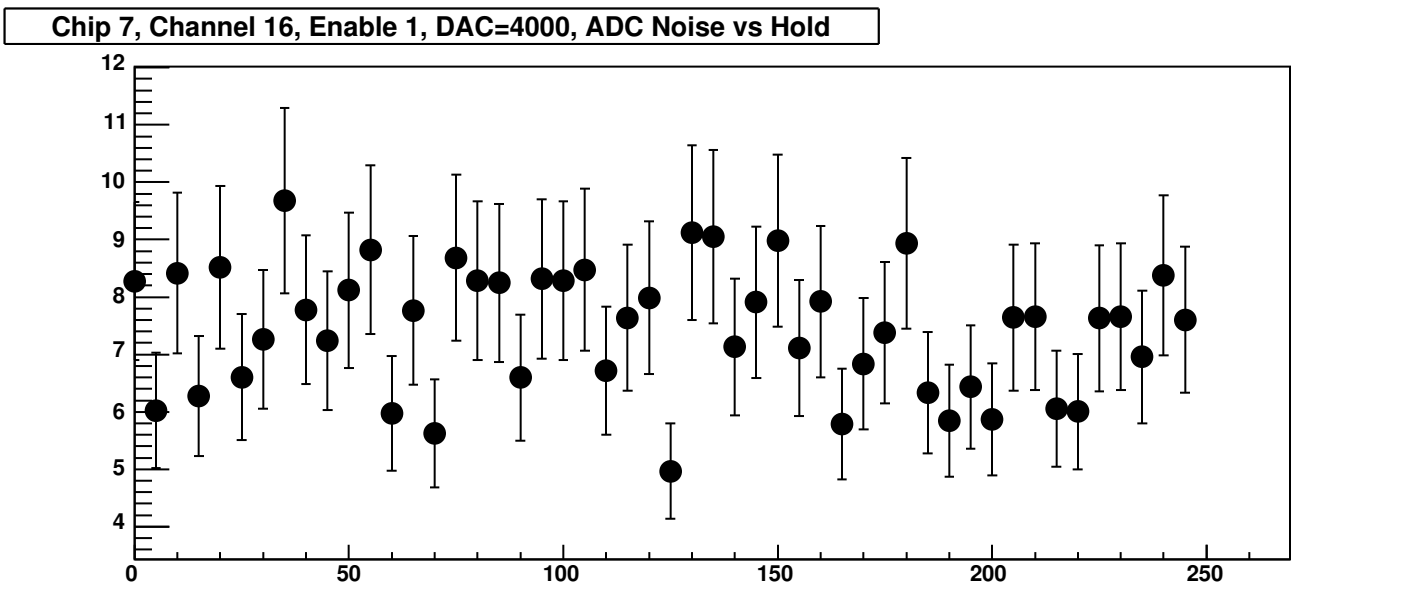
Chip 7, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold

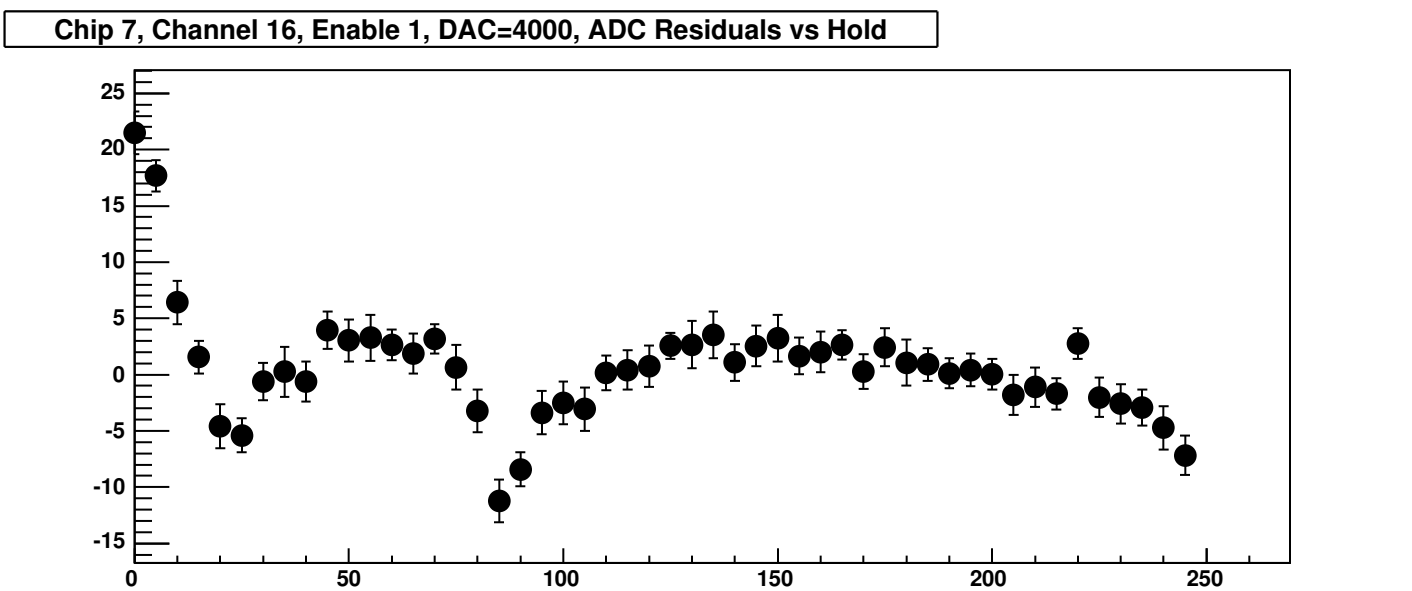


Chip 7, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold



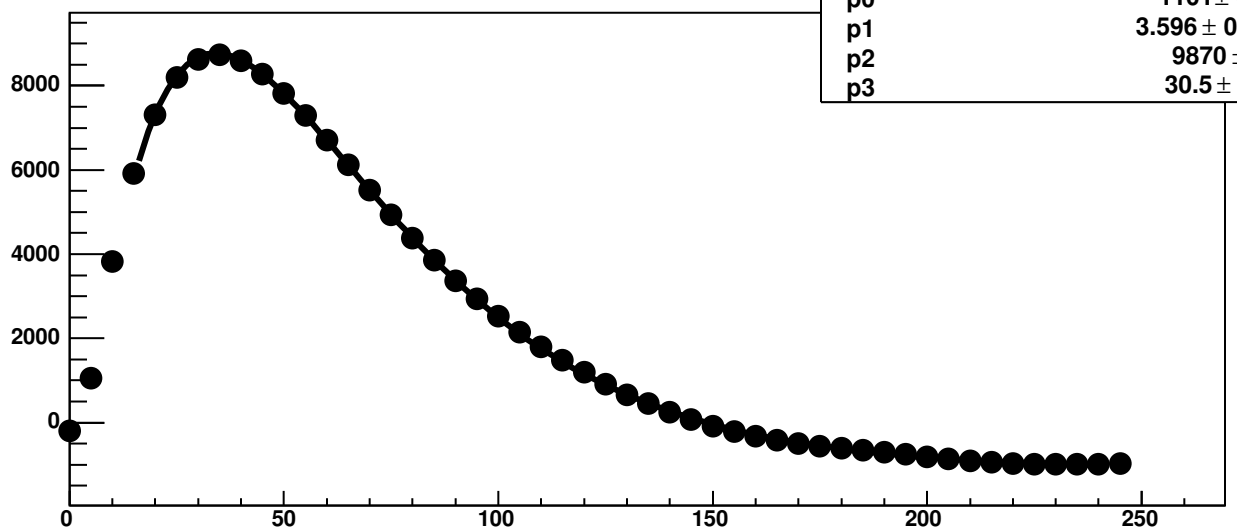

Chip 7, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 161.6 / 41
p0 -56.47 ± 0.5749
p1 87.08 ± 0.5399
p2 -5.082e+07 ± 1.063e+06
p3 2.78e+07 ± 6.152e+05
p4 1.754 ± 0.01752
-60
-80
-100
-120
-140
-160
-180
-200
0
50
100
150
200
250


Chip 7, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

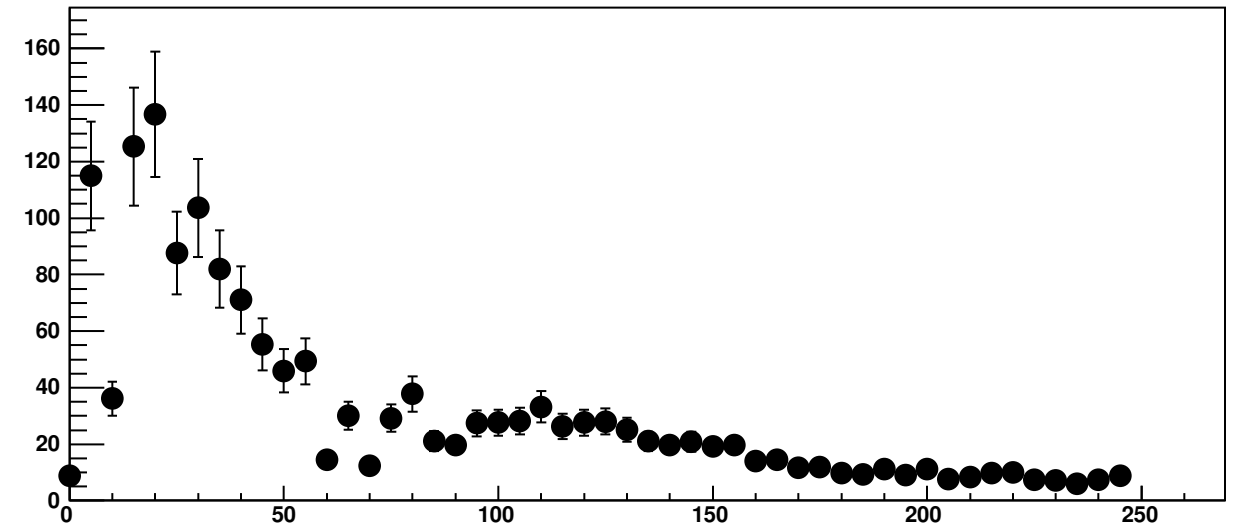

Chip 7, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

Chip 7, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold

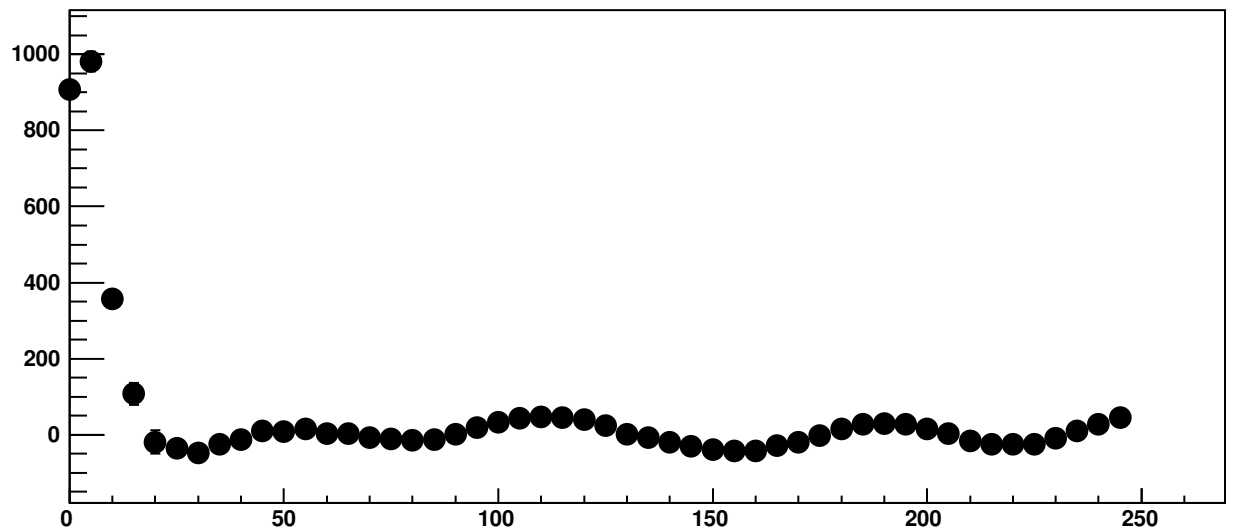
χ^2 / ndf	2232 / 42
p0	-1101 ± 0.7766
p1	3.596 ± 0.04665
p2	9870 ± 5.221
p3	30.5 ± 0.0169



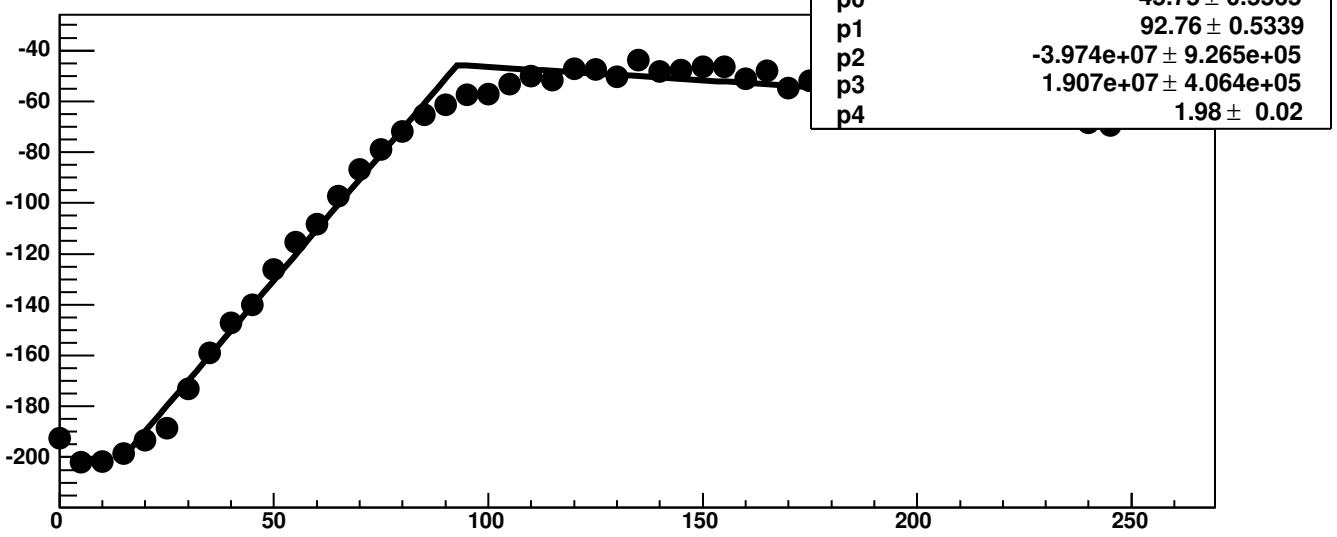
Chip 7, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold



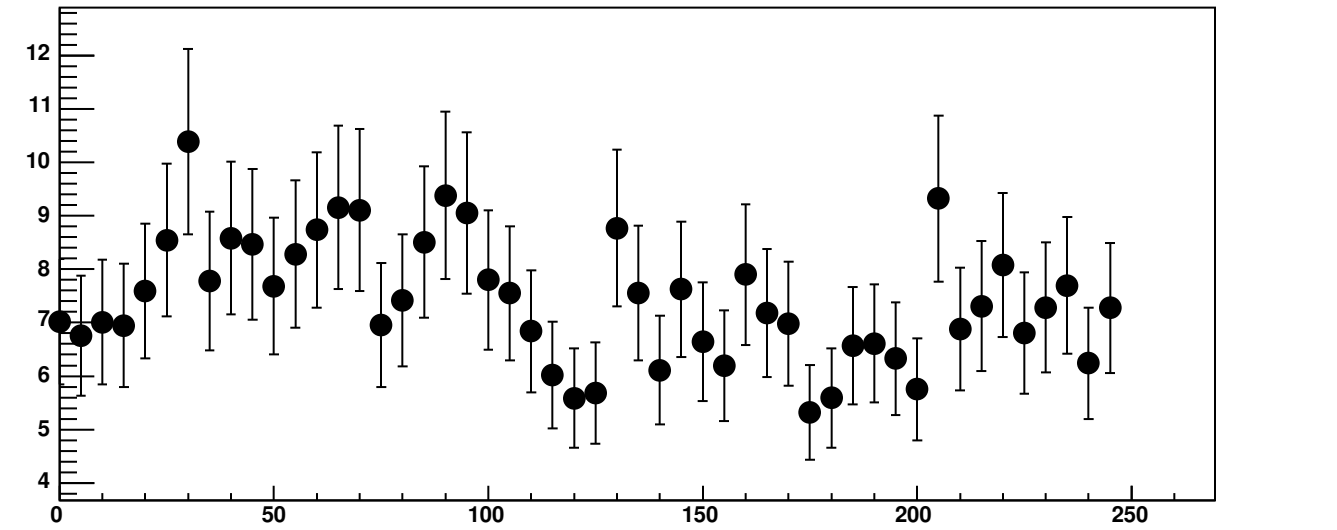
Chip 7, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold



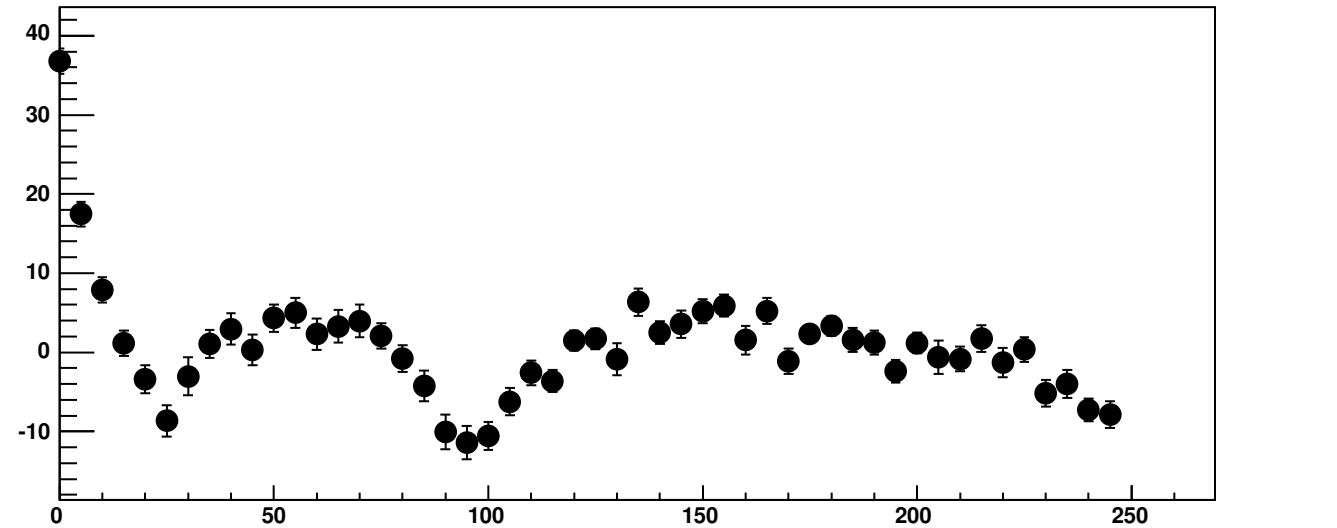
Chip 7, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold



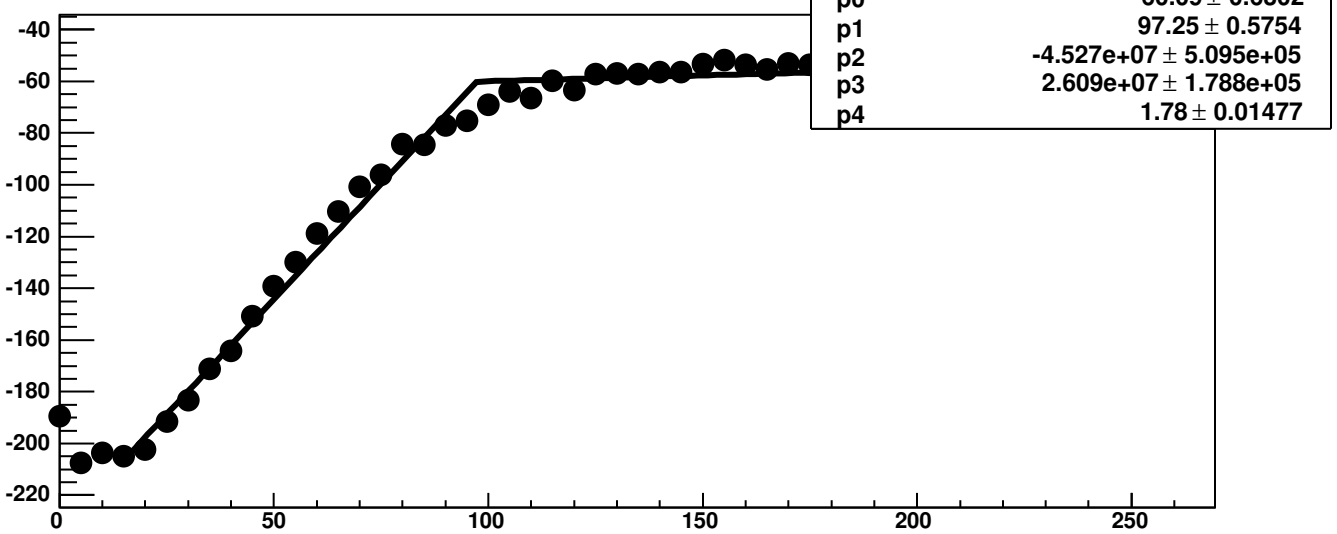
Chip 7, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



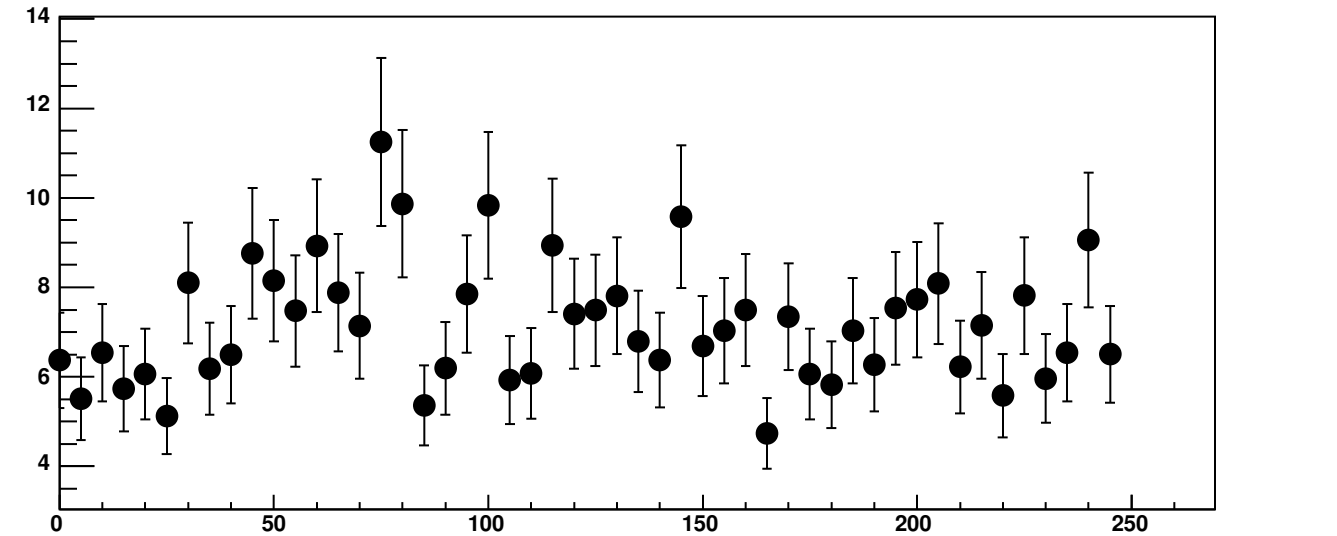
Chip 7, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold



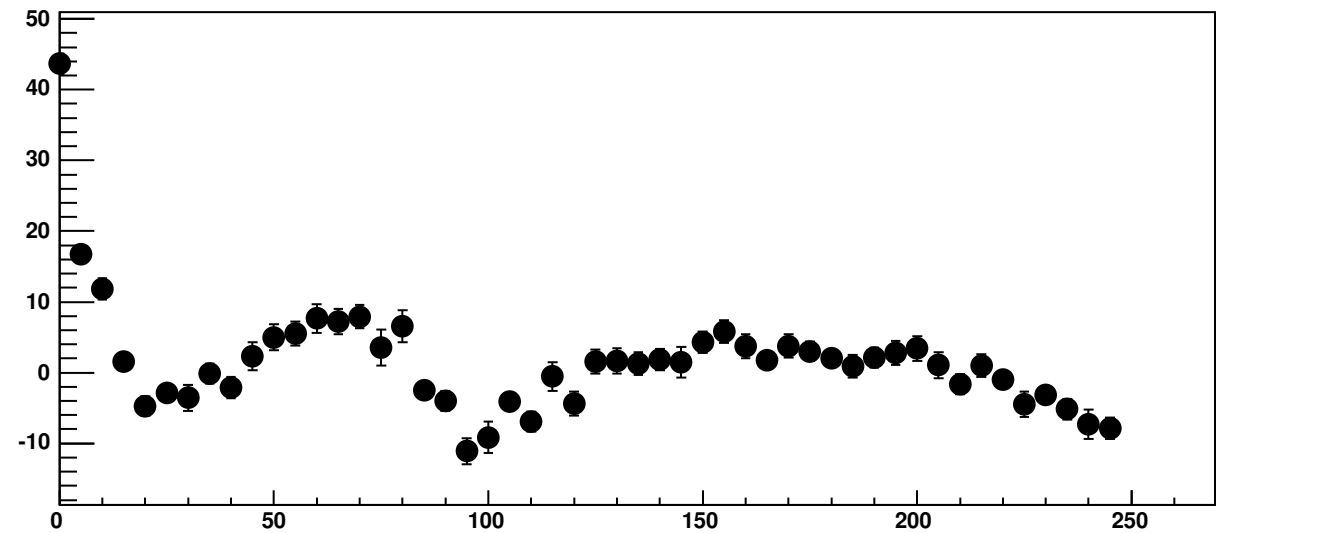
Chip 7, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

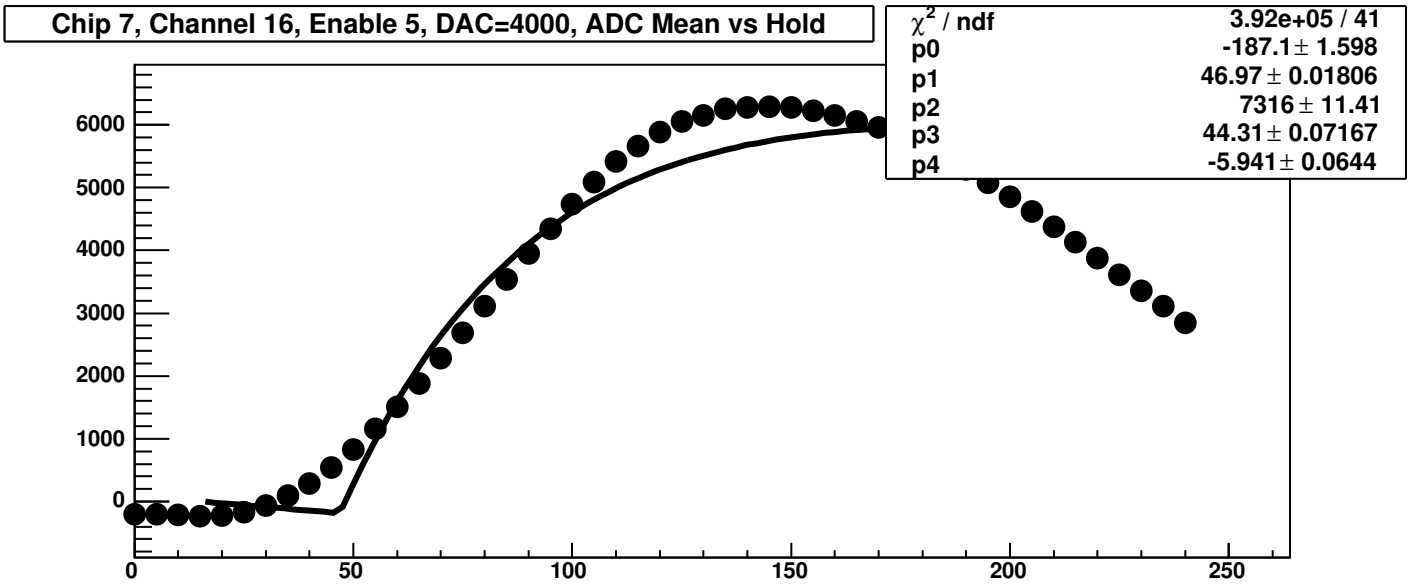


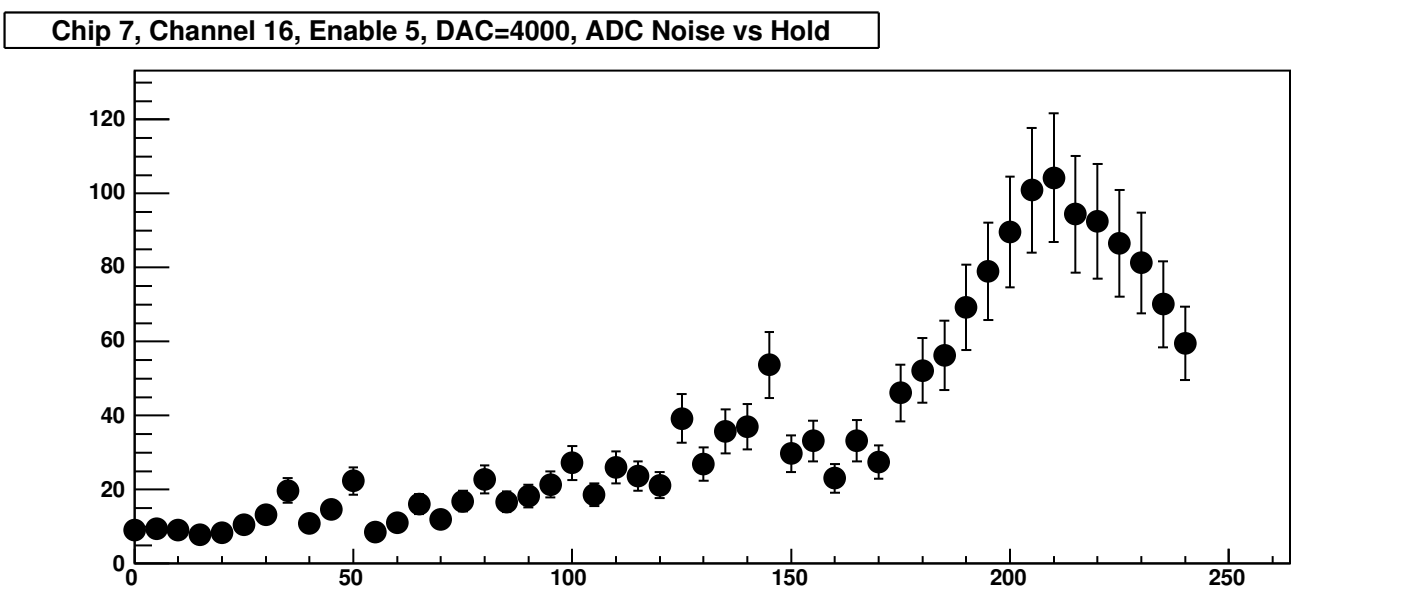
Chip 7, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold

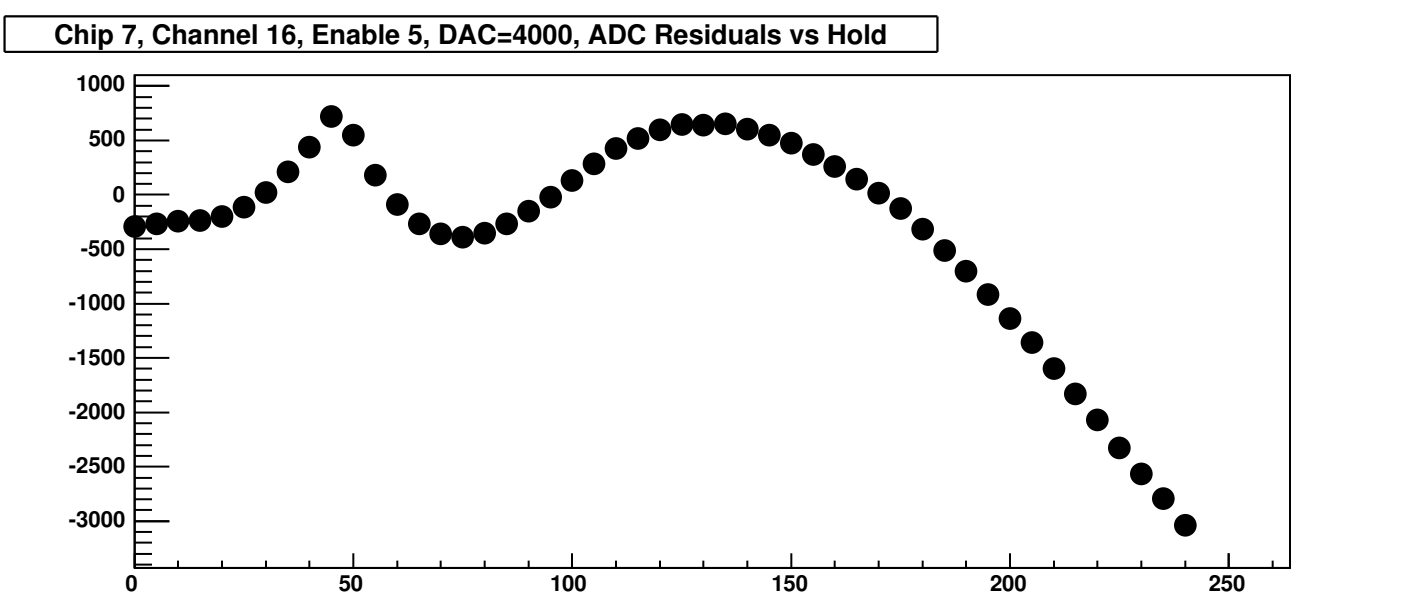


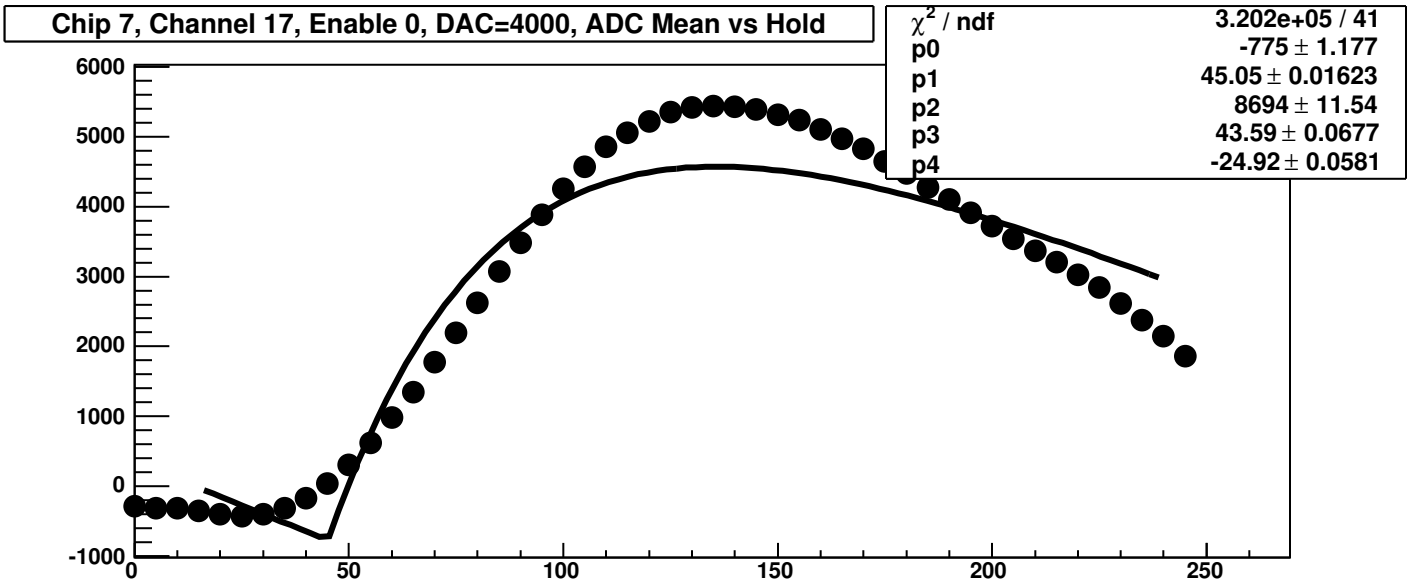
Chip 7, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

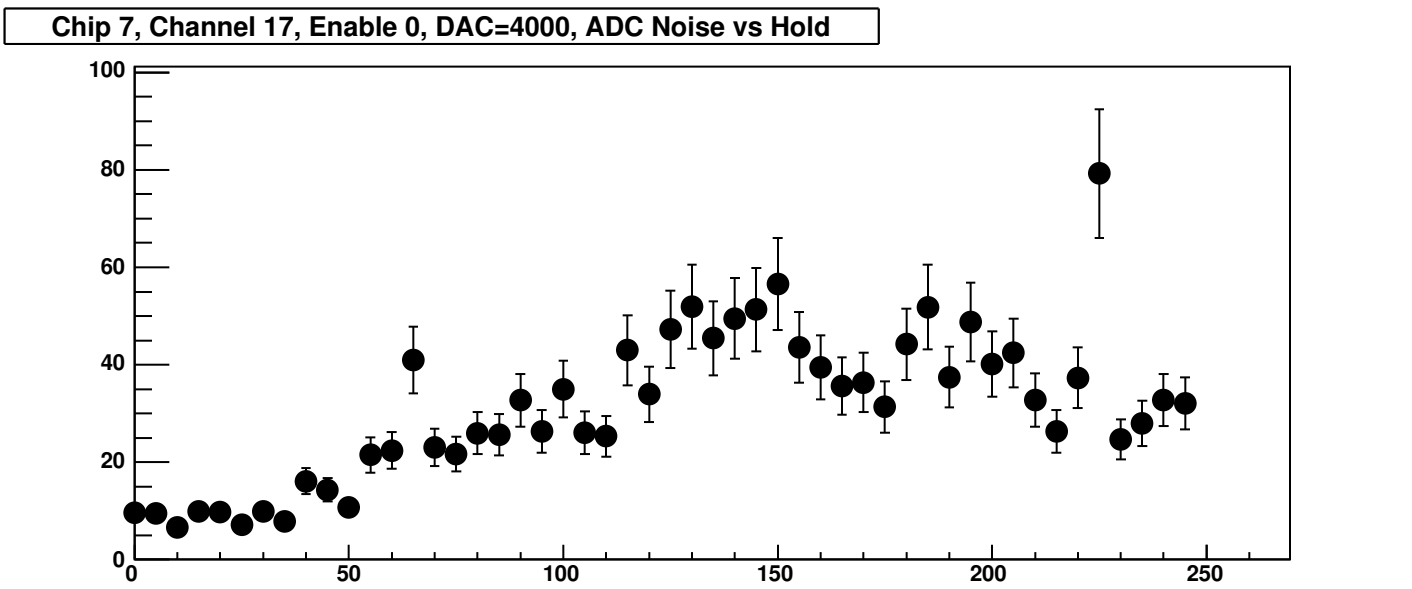


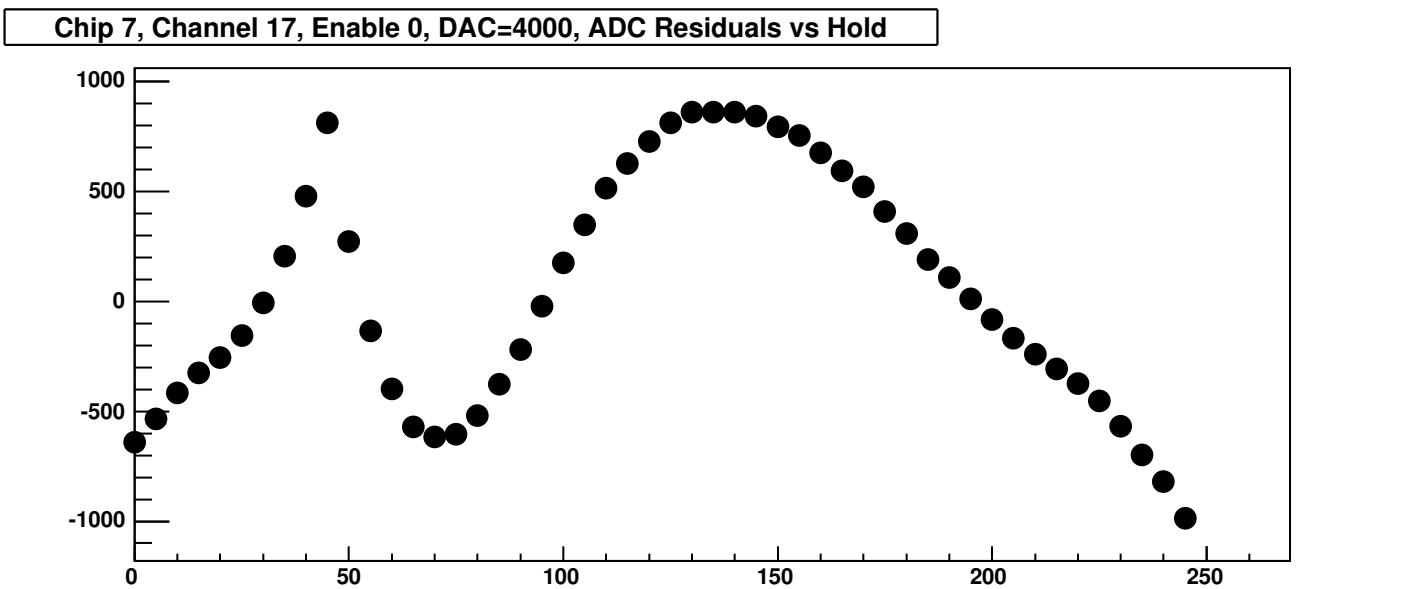

Chip 7, Channel 16, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.92e+05 / 41
p0 -187.1 ± 1.598
p1 46.97 ± 0.01806
p2 7316 ± 11.41
p3 44.31 ± 0.07167
p4 -5.941 ± 0.0644
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 7, Channel 16, Enable 5, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250

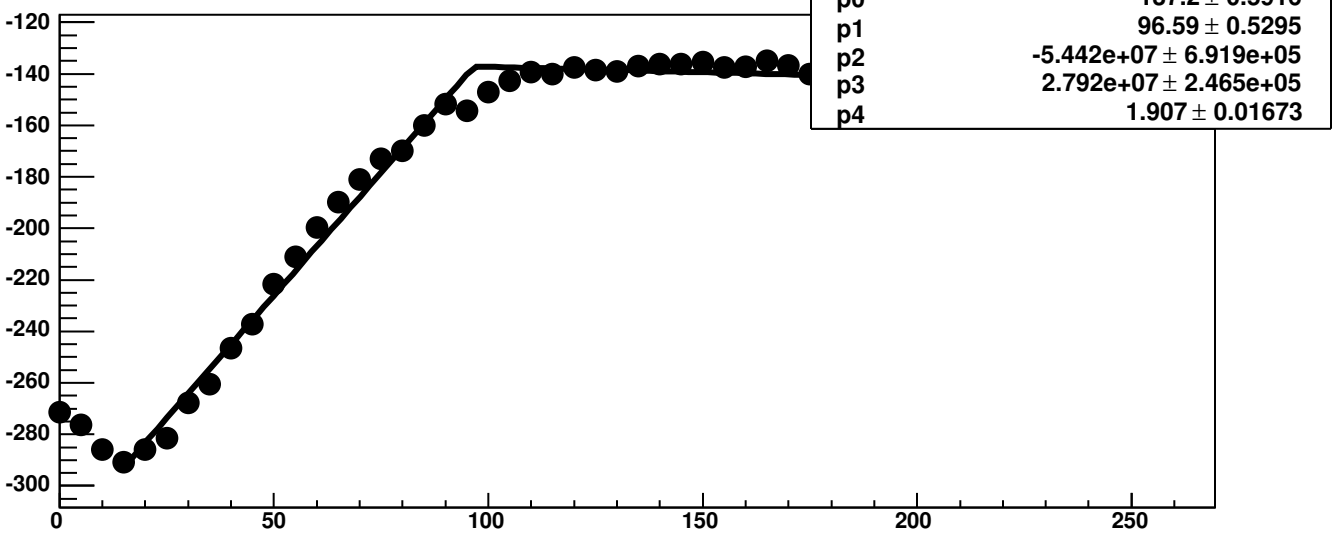

Chip 7, Channel 16, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250


Chip 7, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.202e+05 / 41
p0 -775 ± 1.177
p1 45.05 ± 0.01623
p2 8694 ± 11.54
p3 43.59 ± 0.0677
p4 -24.92 ± 0.0581
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

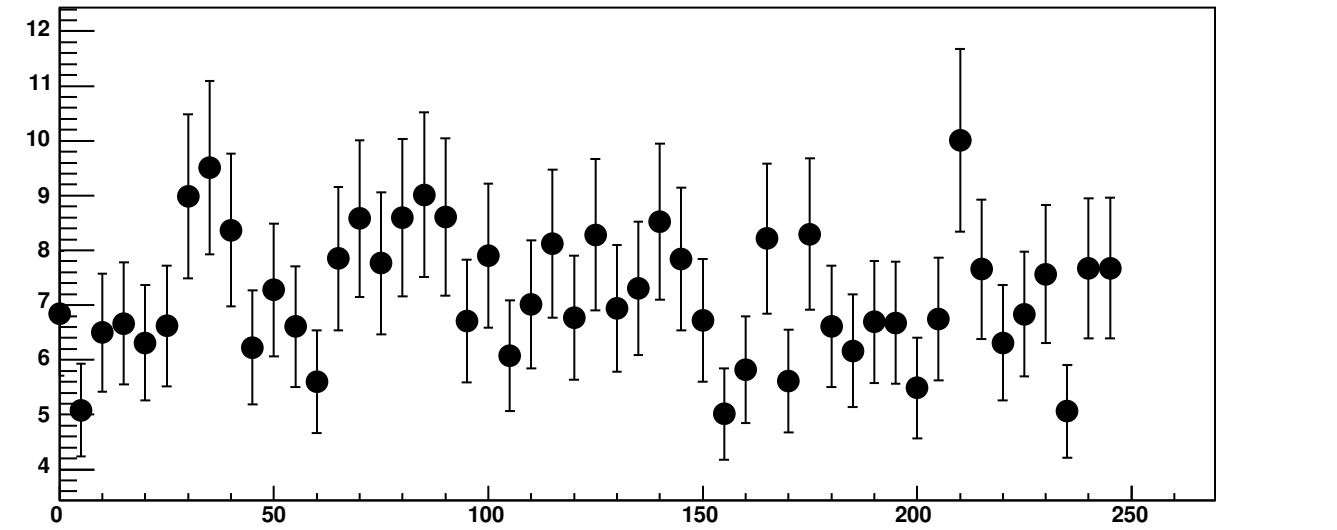

Chip 7, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250


Chip 7, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
0
50
100
150
200
250

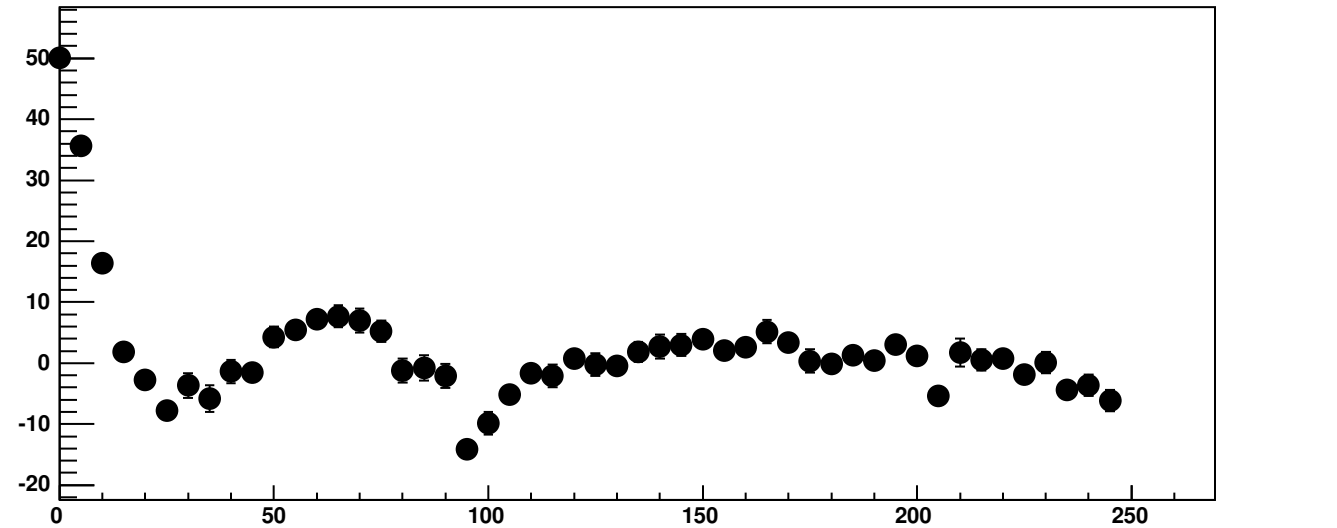
Chip 7, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold



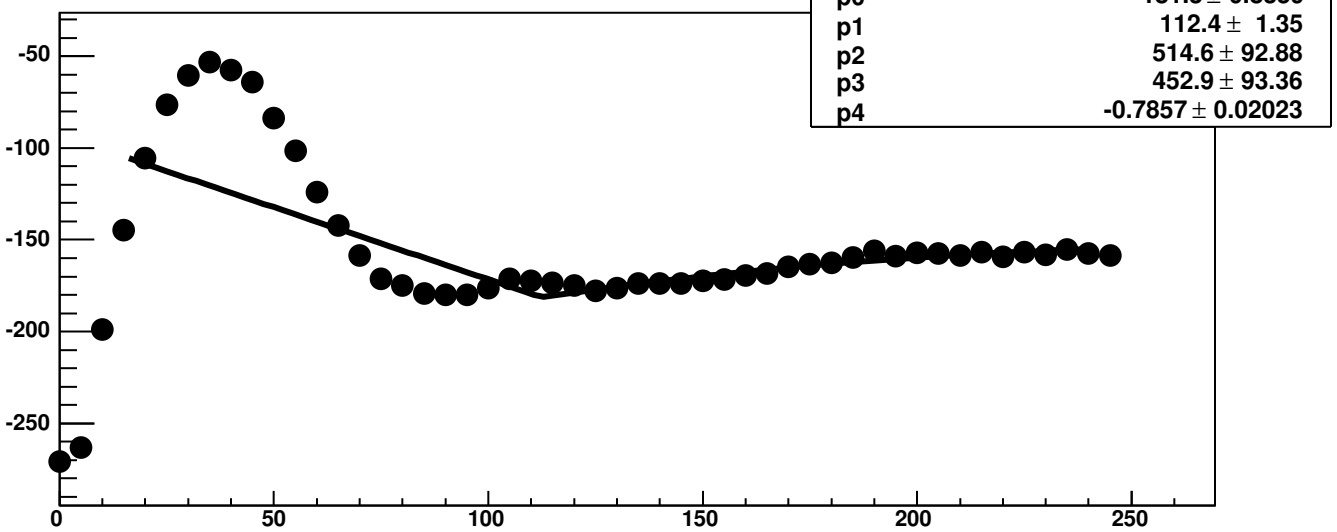
Chip 7, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



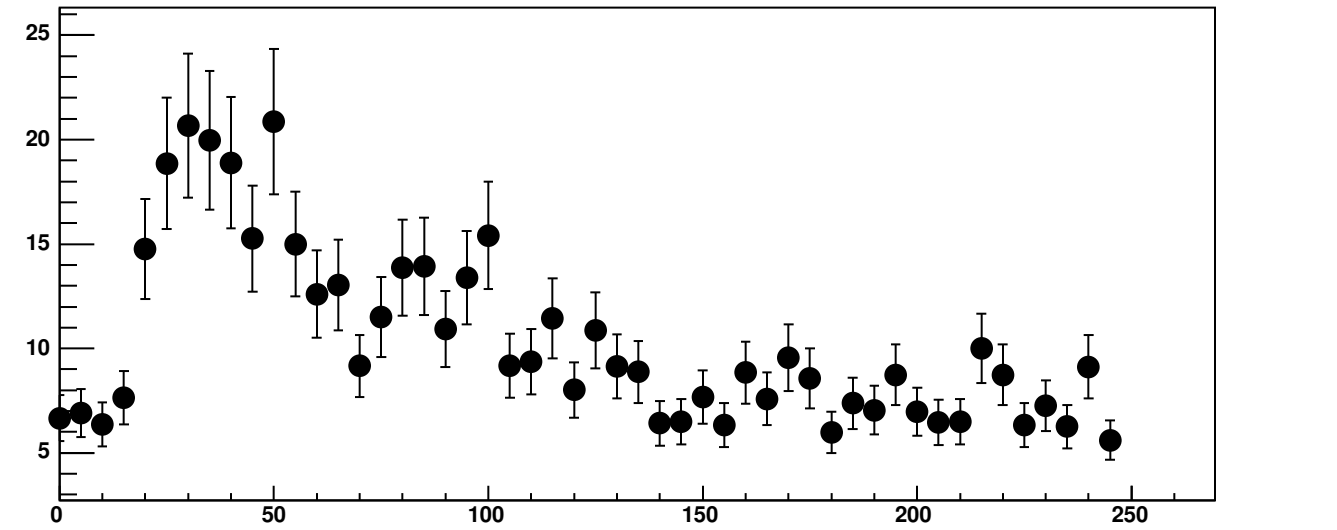
Chip 7, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold



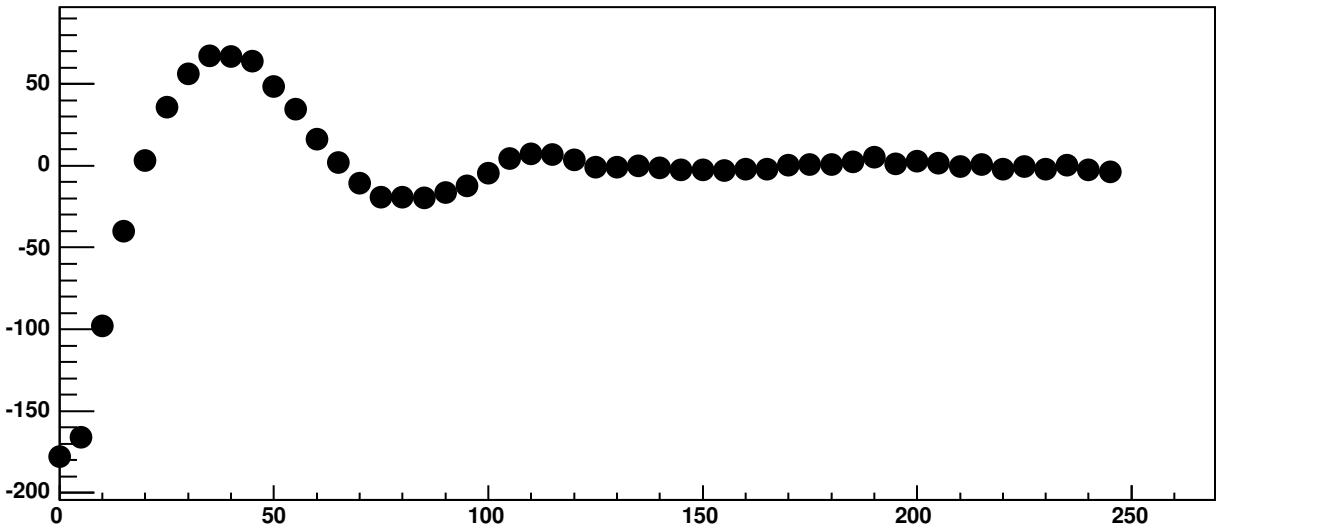
Chip 7, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold

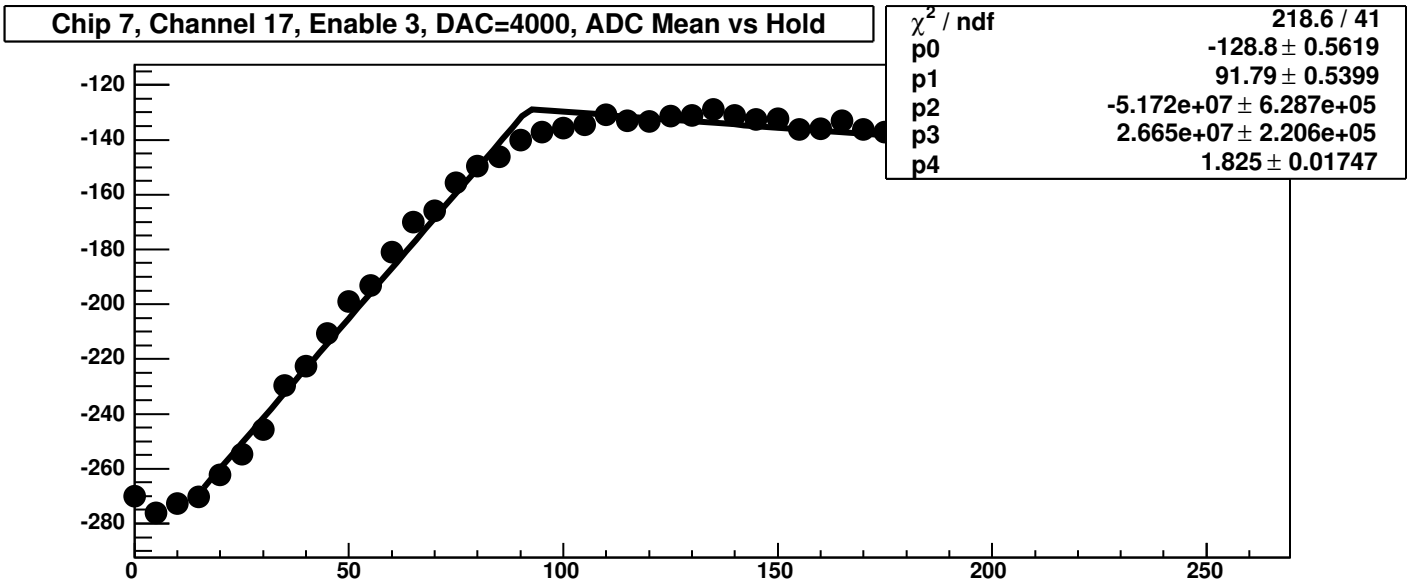


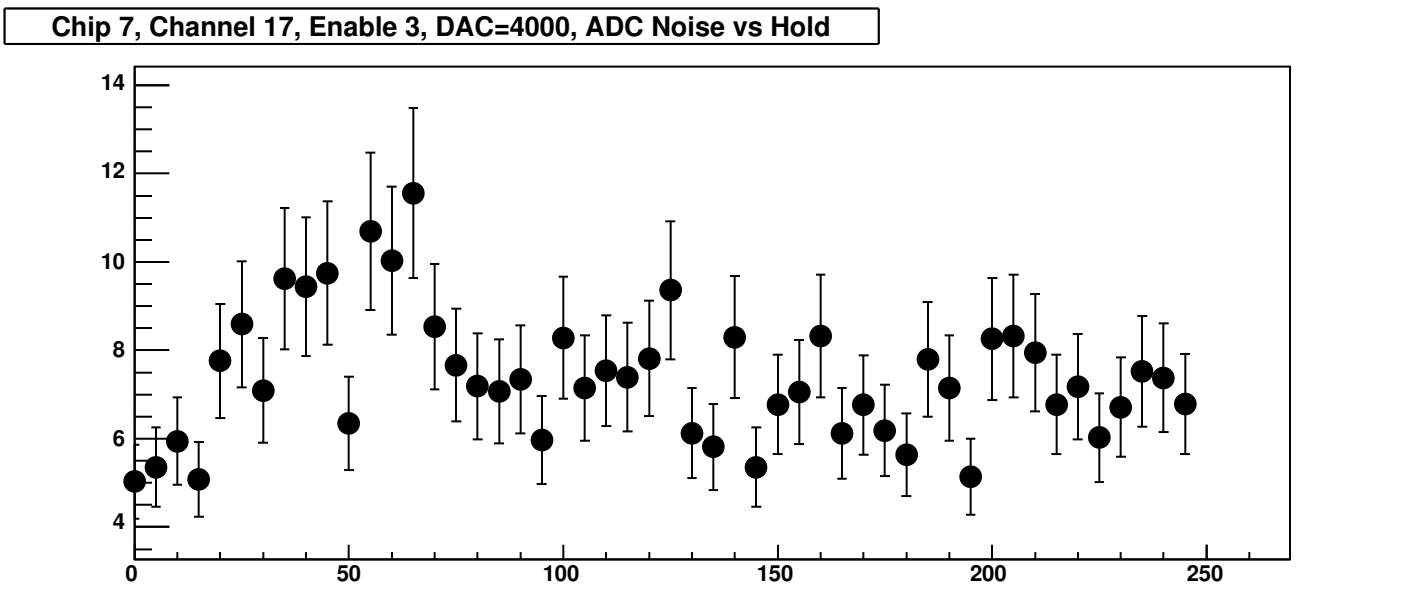
Chip 7, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold

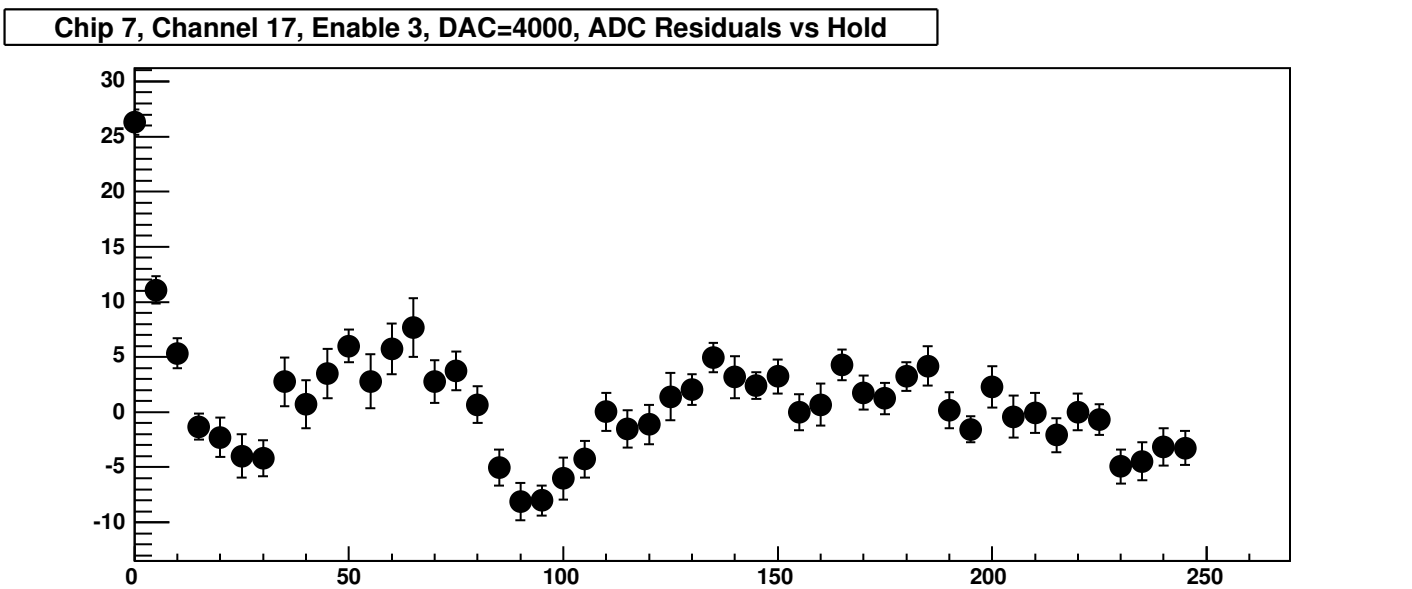


Chip 7, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold

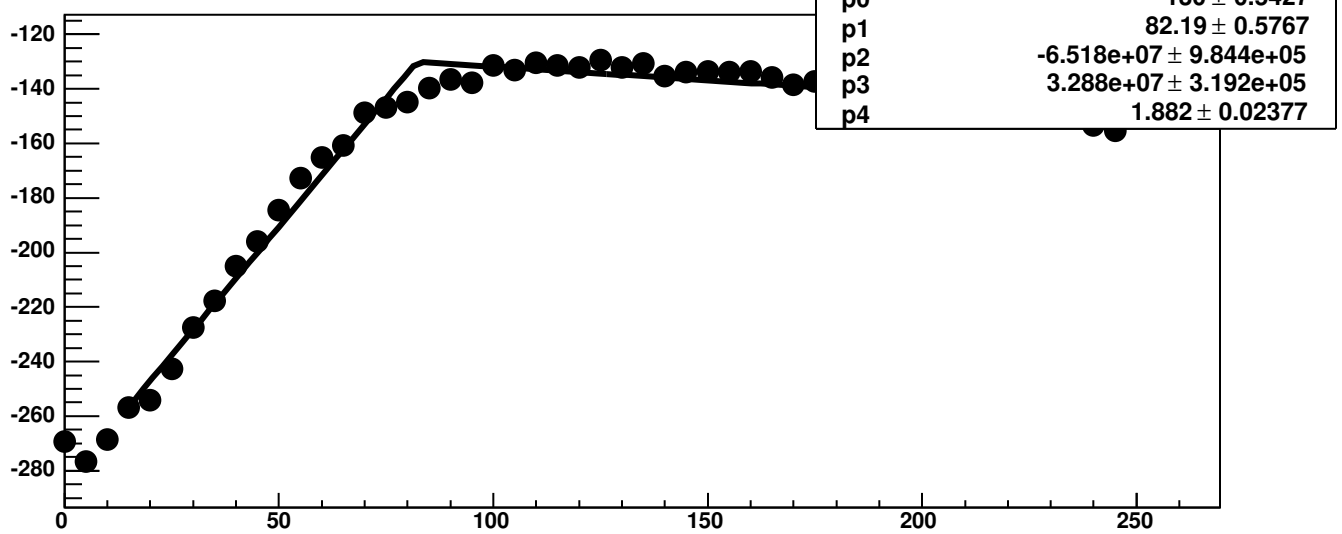



Chip 7, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 218.6 / 41
p0 -128.8 ± 0.5619
p1 91.79 ± 0.5399
p2 -5.172e+07 ± 6.287e+05
p3 2.665e+07 ± 2.206e+05
p4 1.825 ± 0.01747
-120
-140
-160
-180
-200
-220
-240
-260
-280
0
50
100
150
200
250

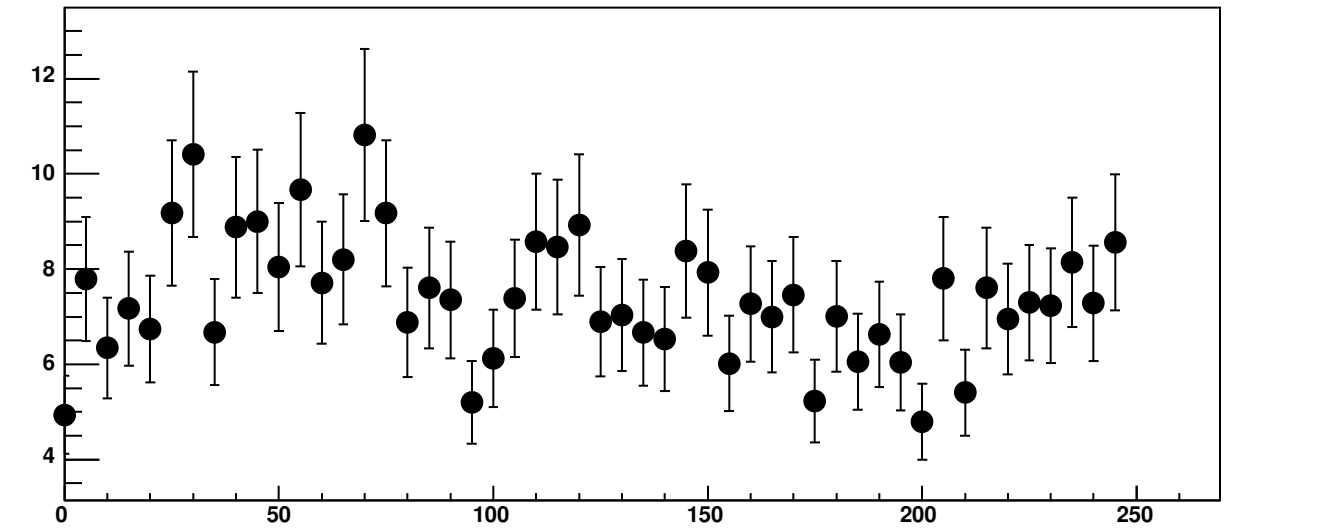

Chip 7, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 7, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
0
50
100
150
200
250

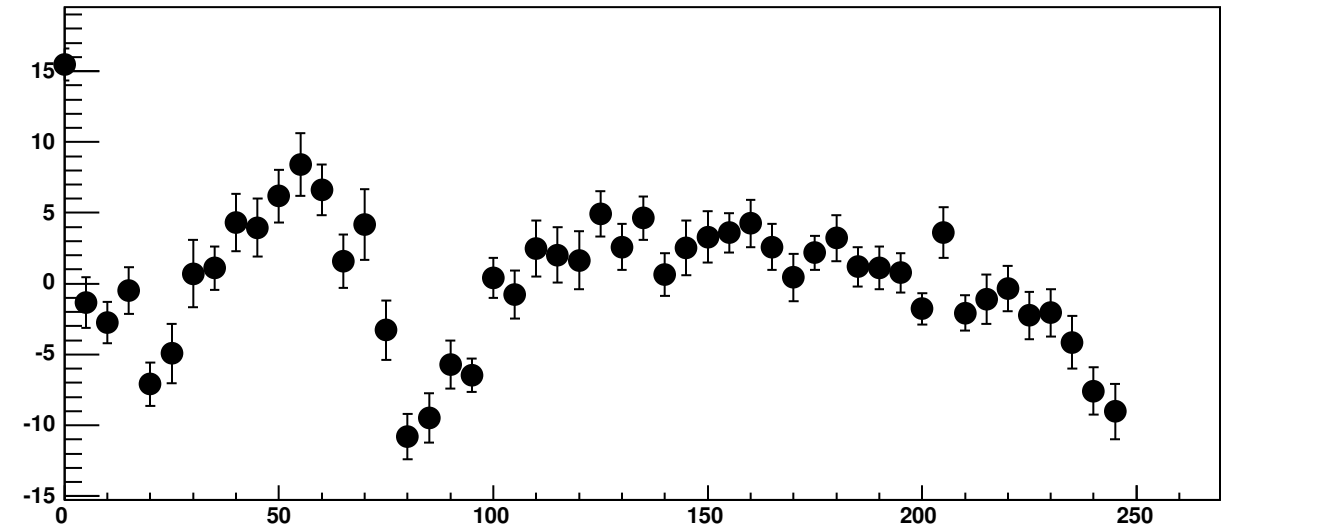
Chip 7, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold

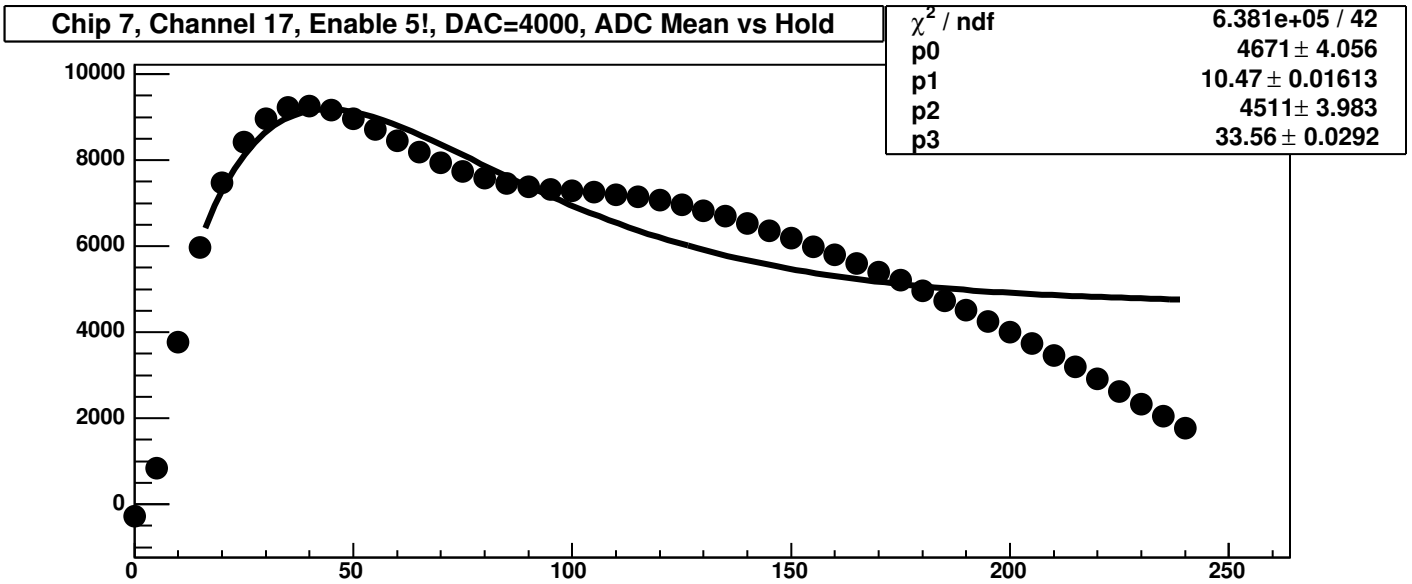


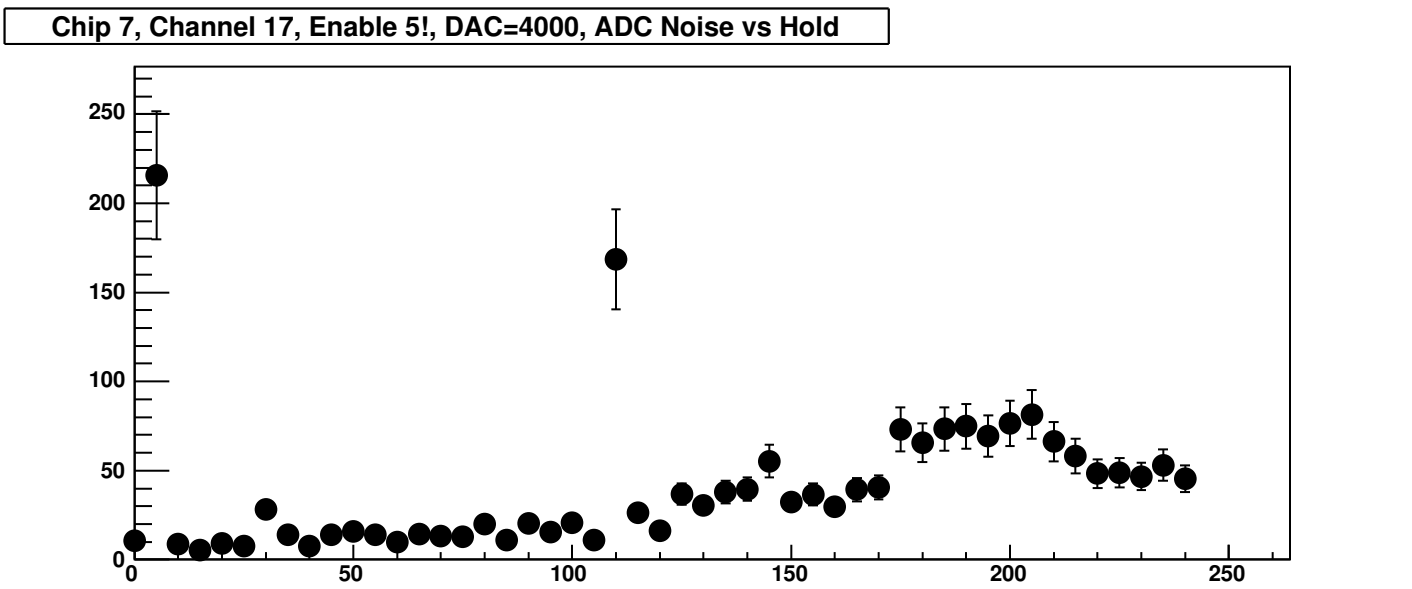
Chip 7, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold

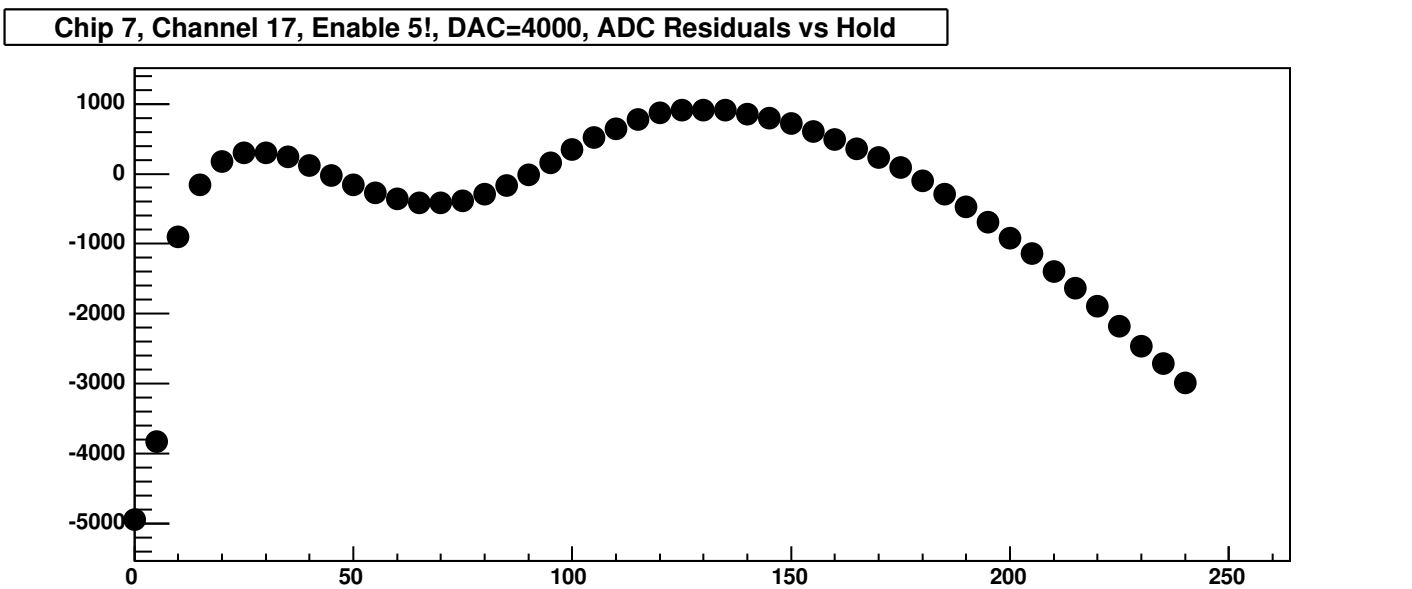


Chip 7, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

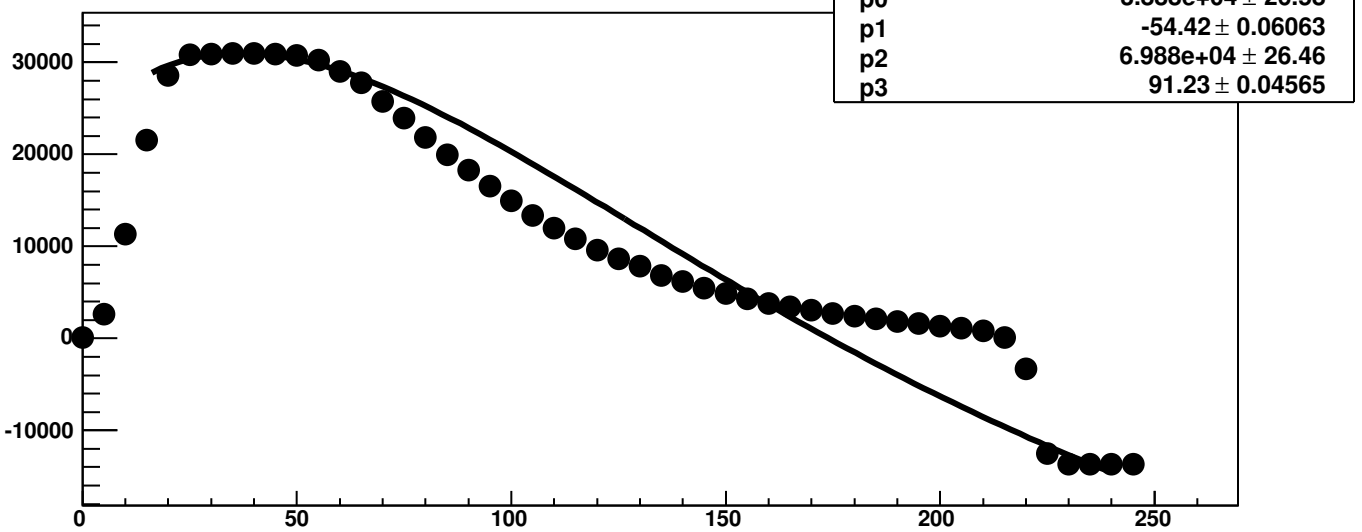



Chip 7, Channel 17, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 6.381e+05 / 42
p0 4671 ± 4.056
p1 10.47 ± 0.01613
p2 4511 ± 3.983
p3 33.56 ± 0.0292
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

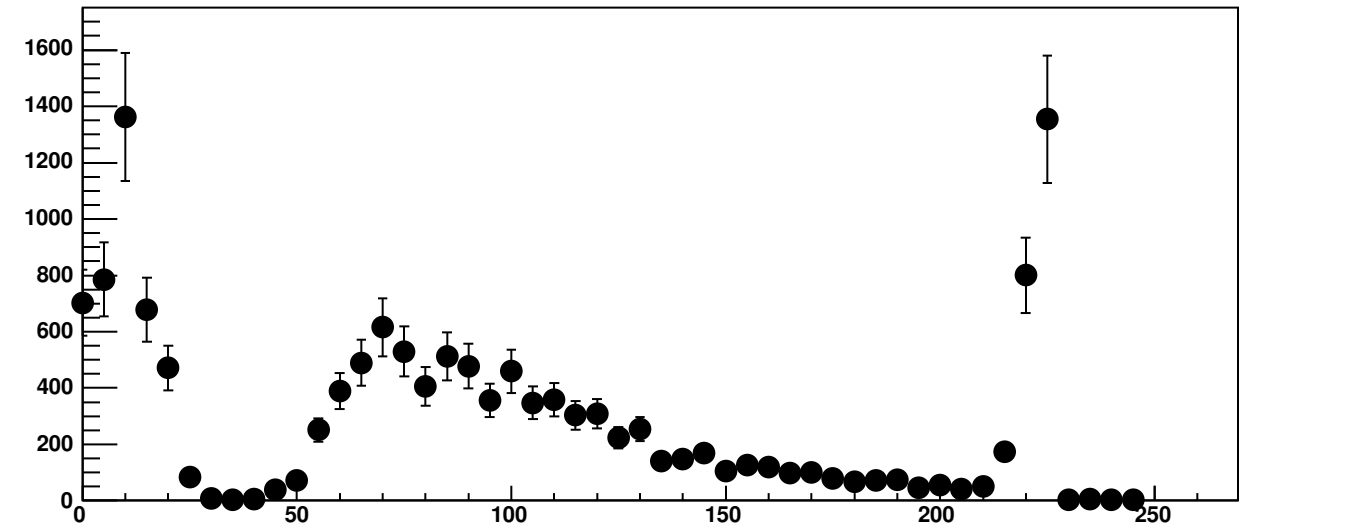

Chip 7, Channel 17, Enable 5!, DAC=4000, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250


Chip 7, Channel 17, Enable 5!, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

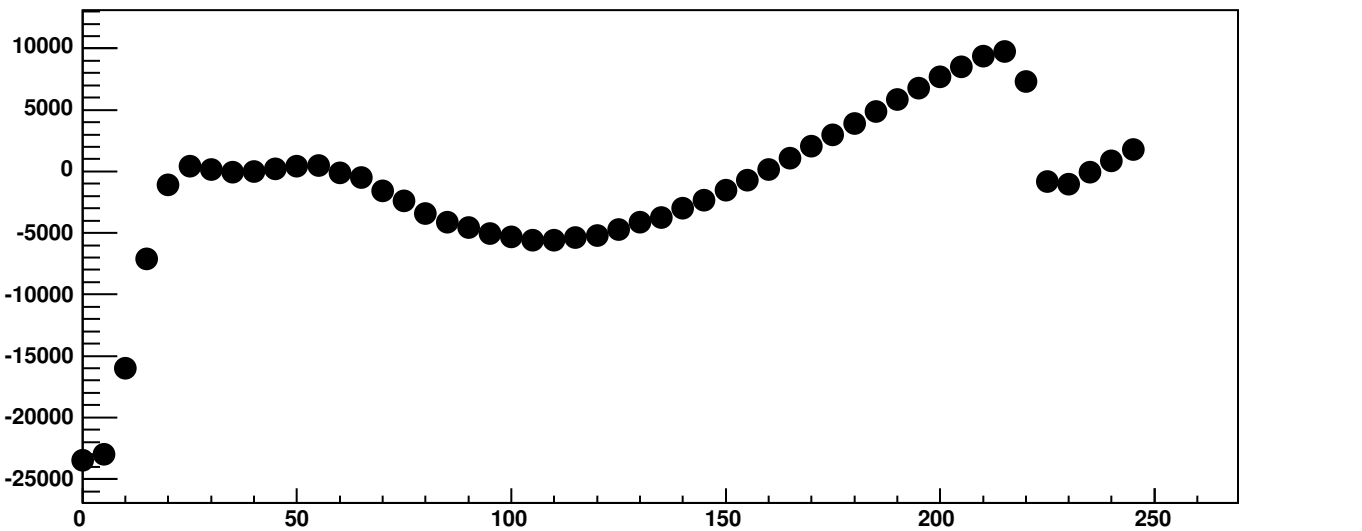
Chip 8, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold



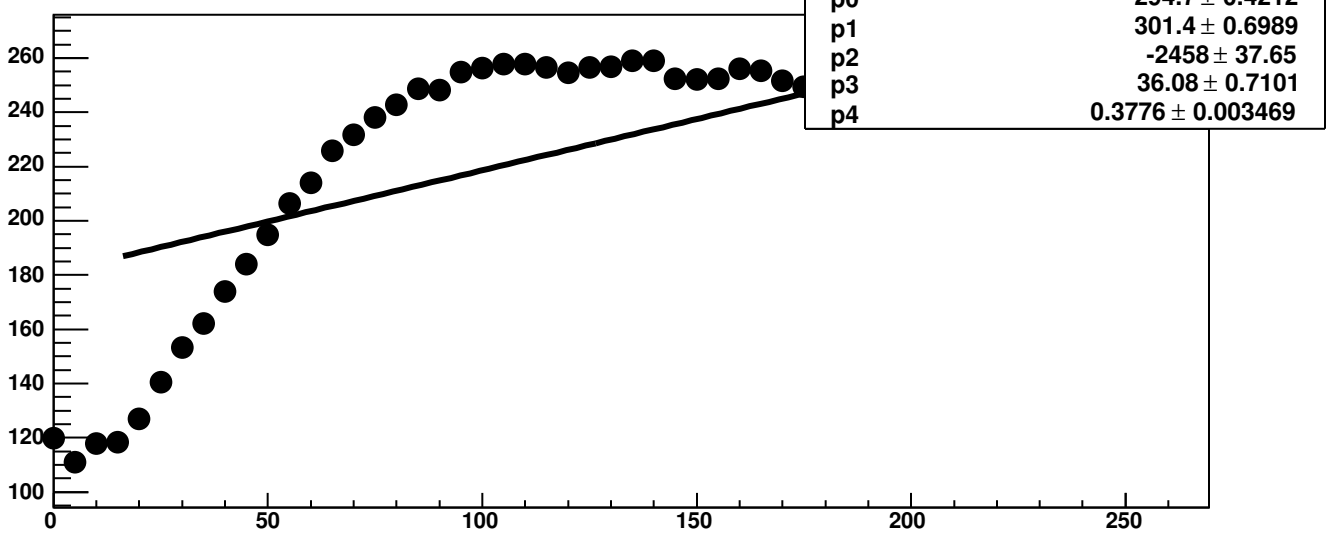
Chip 8, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold



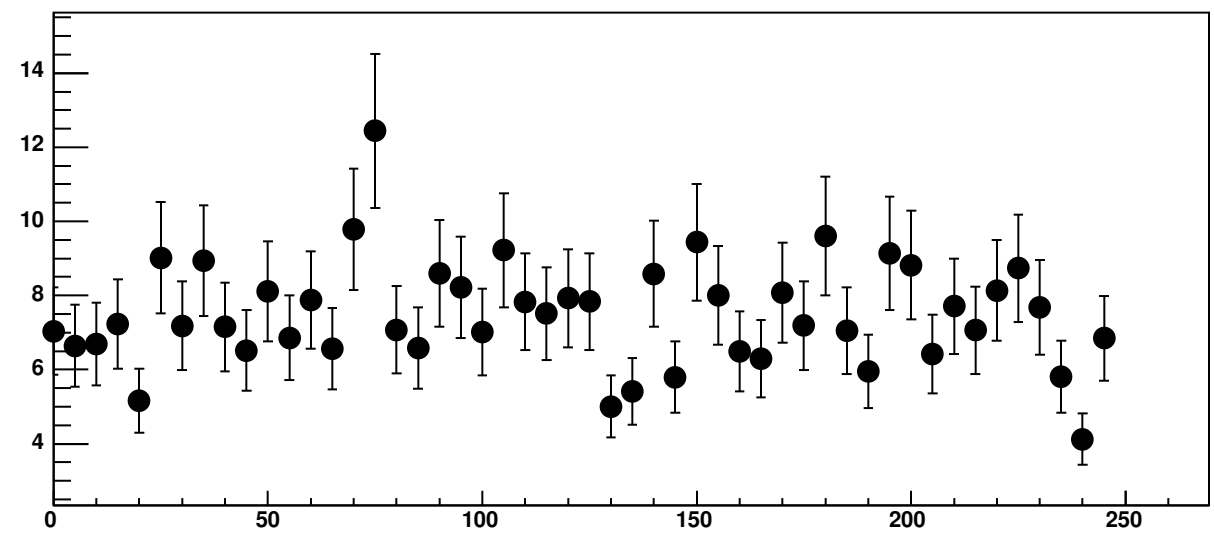
Chip 8, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold



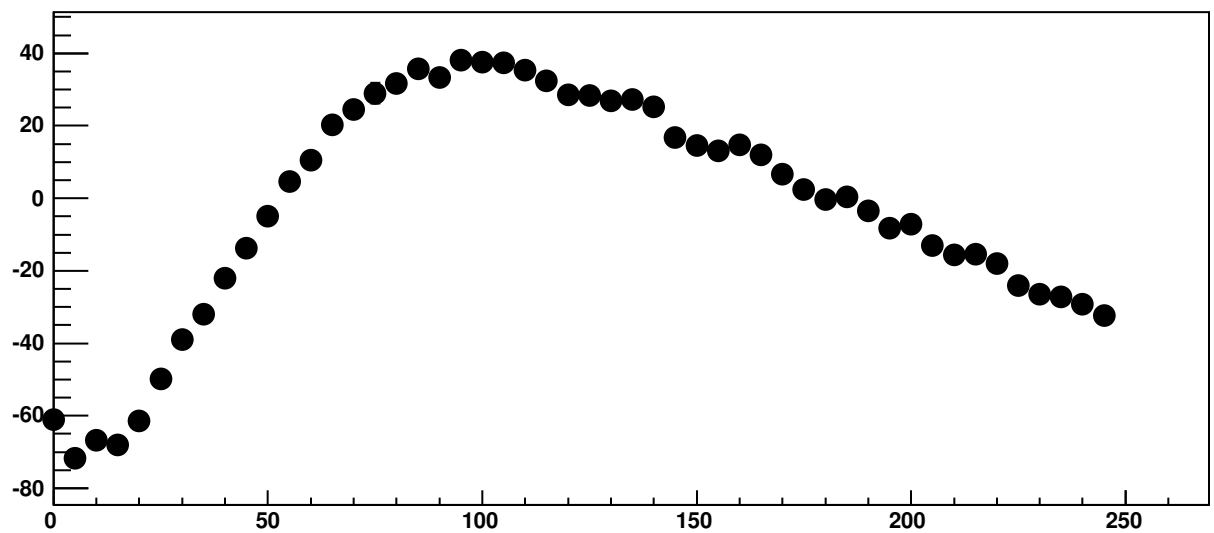
Chip 8, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold

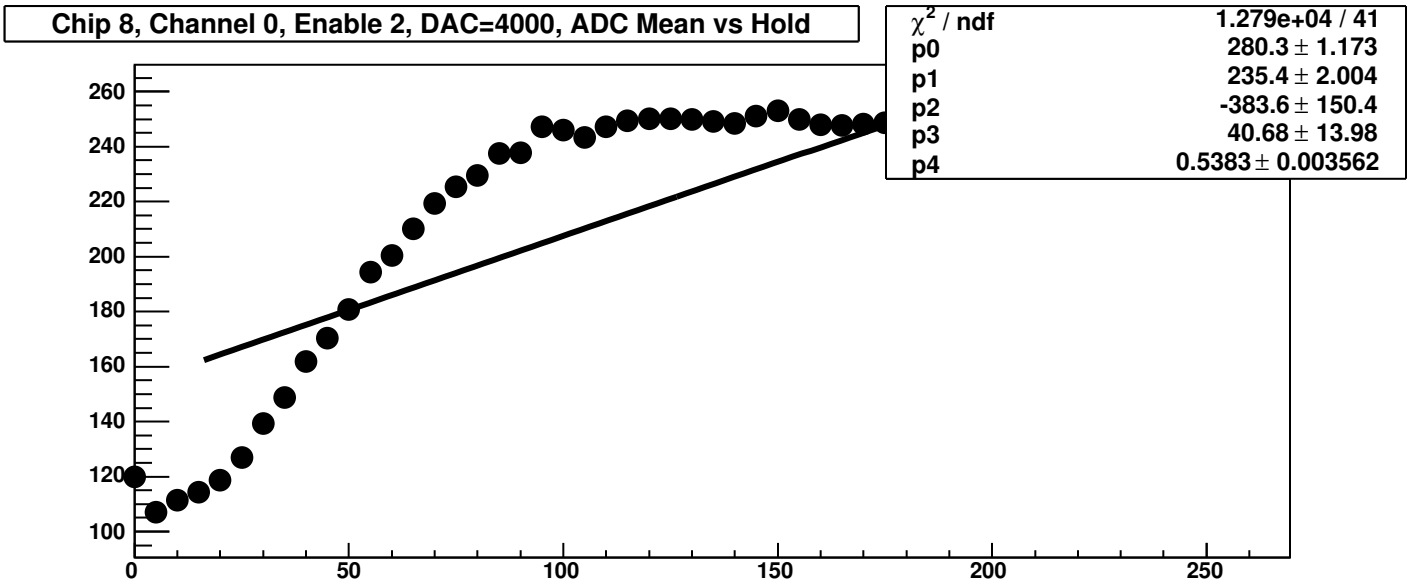


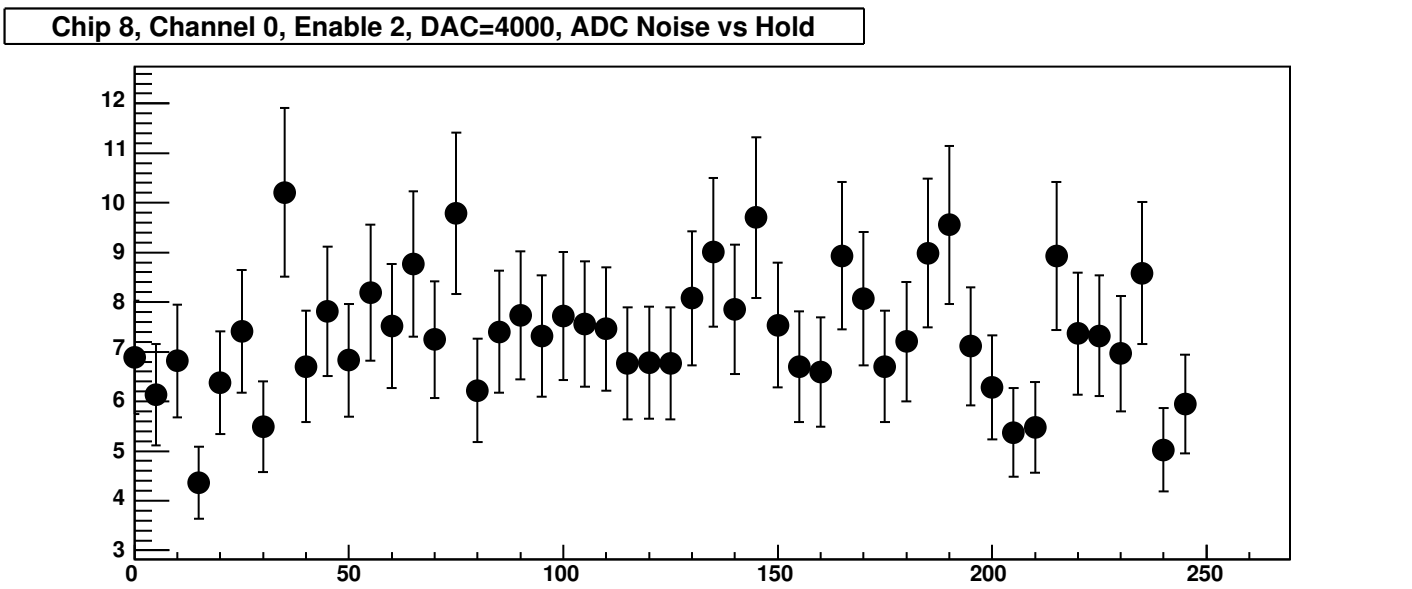
Chip 8, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold

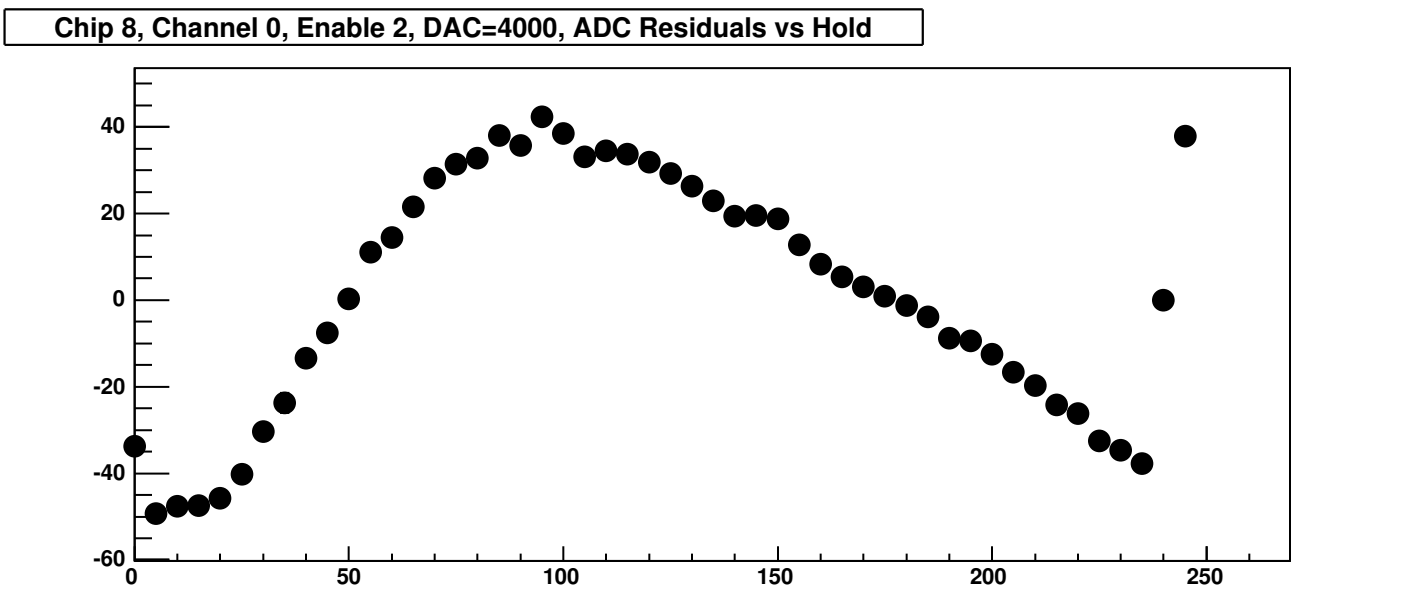


Chip 8, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold

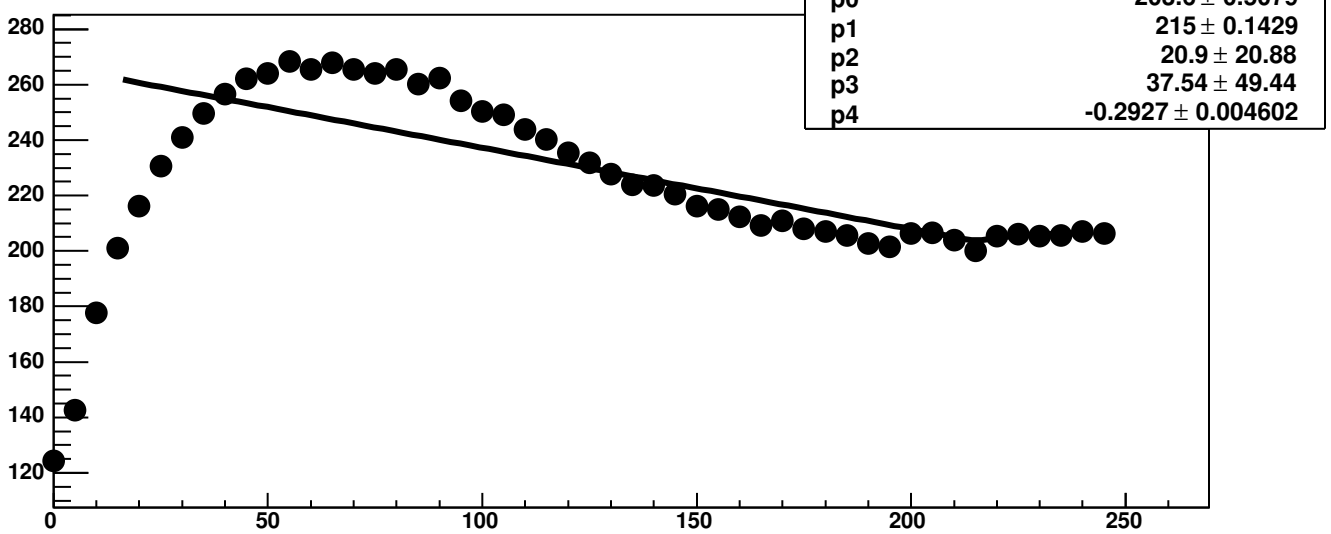



Chip 8, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.279e+04 / 41
p0 280.3 ± 1.173
p1 235.4 ± 2.004
p2 -383.6 ± 150.4
p3 40.68 ± 13.98
p4 0.5383 ± 0.003562
260
240
220
200
180
160
140
120
100
0
50
100
150
200
250

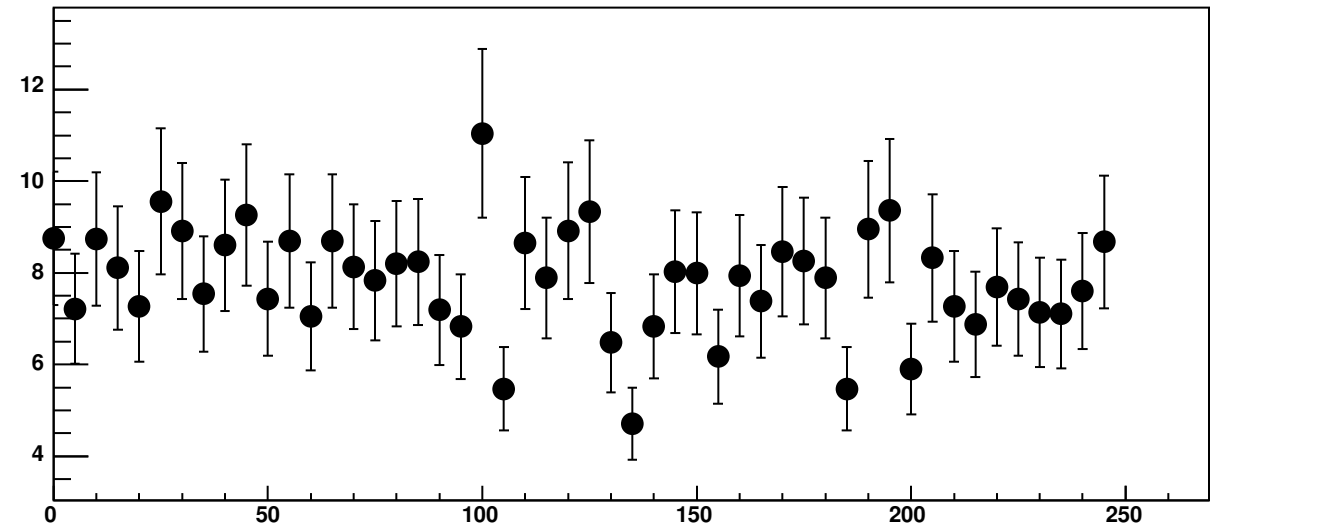

Chip 8, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 8, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
0
50
100
150
200
250

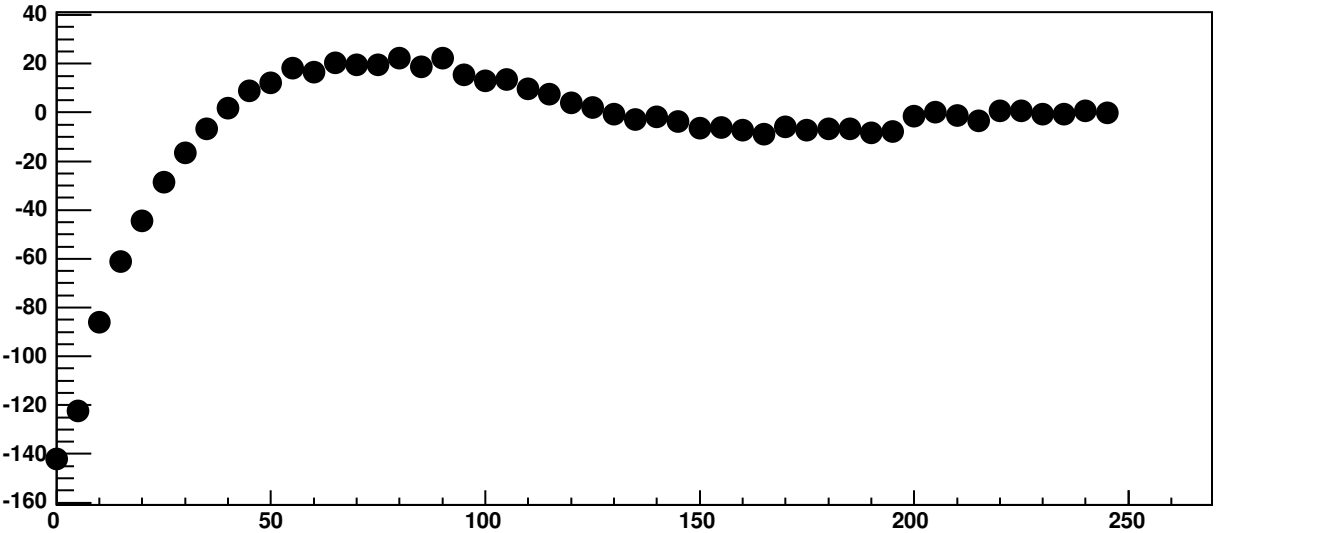
Chip 8, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold



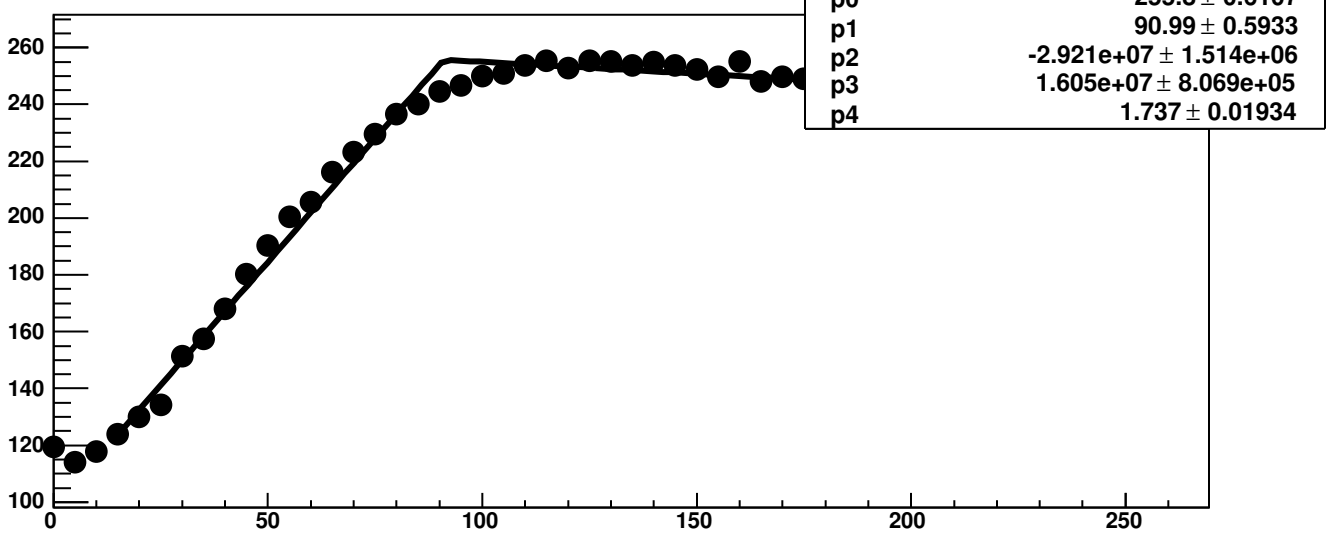
Chip 8, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold



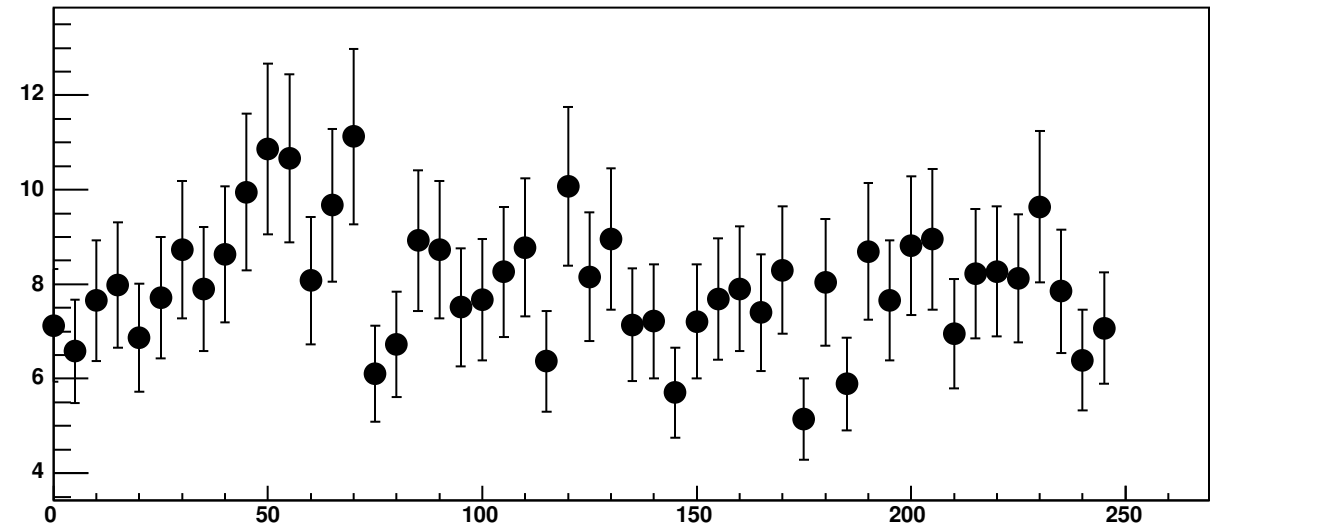
Chip 8, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold



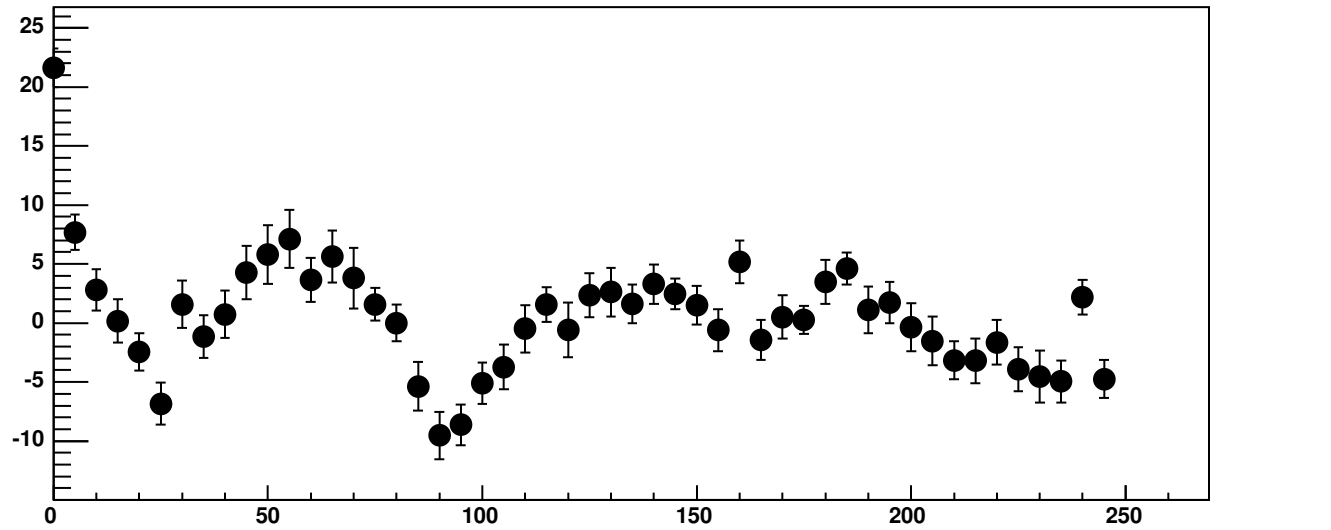
Chip 8, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold



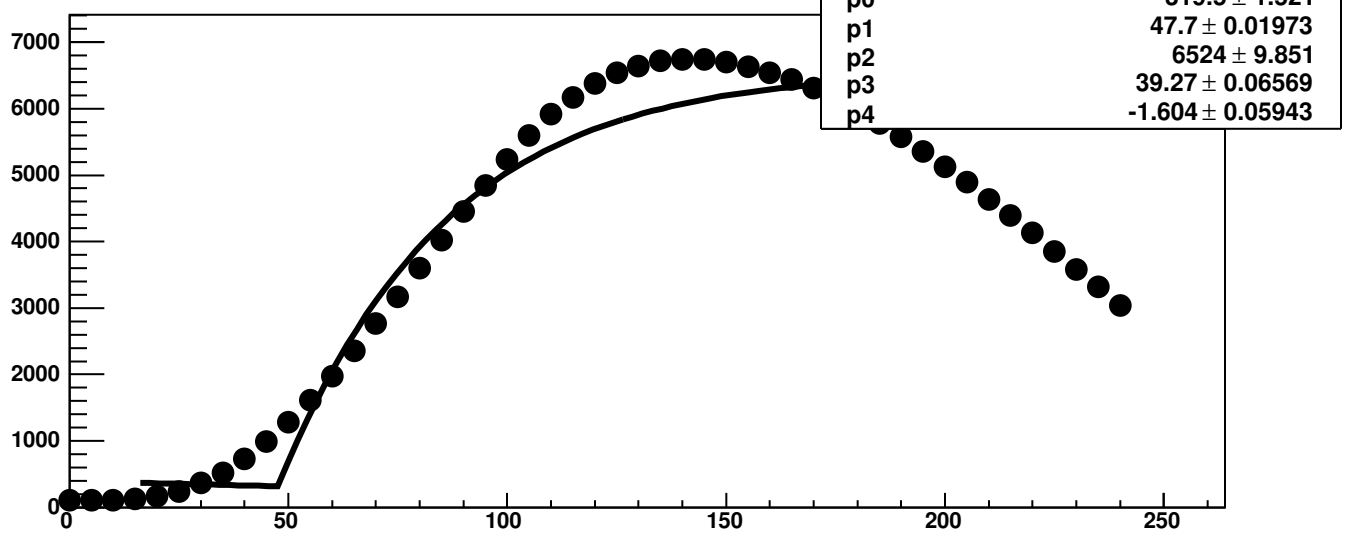
Chip 8, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



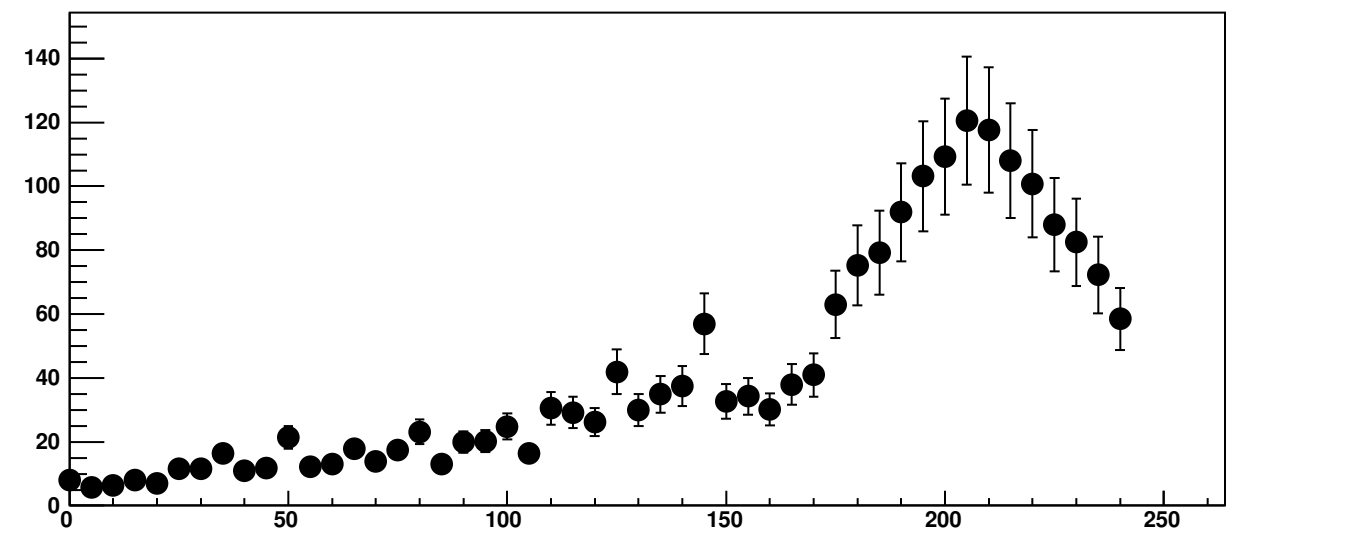
Chip 8, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold



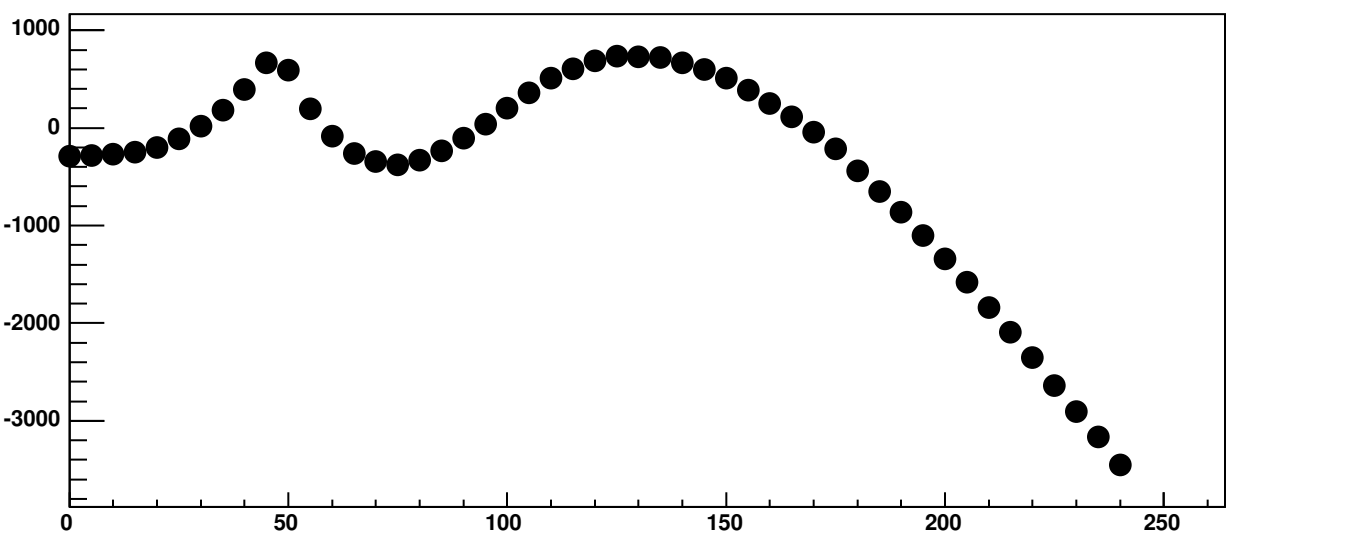
Chip 8, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold



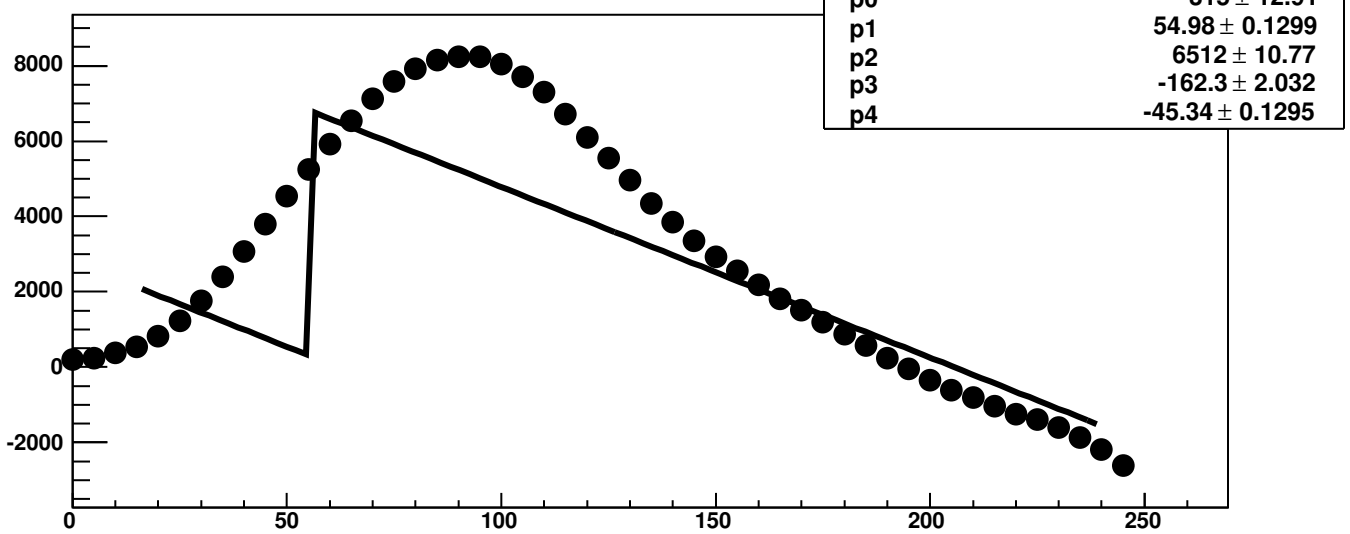
Chip 8, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



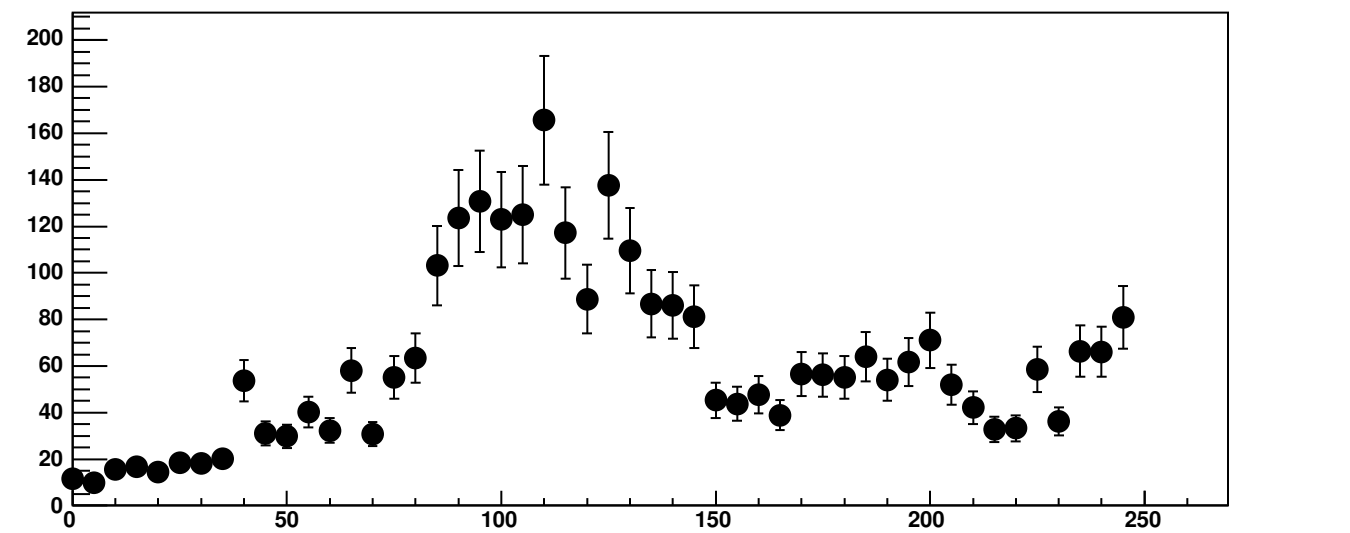
Chip 8, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold



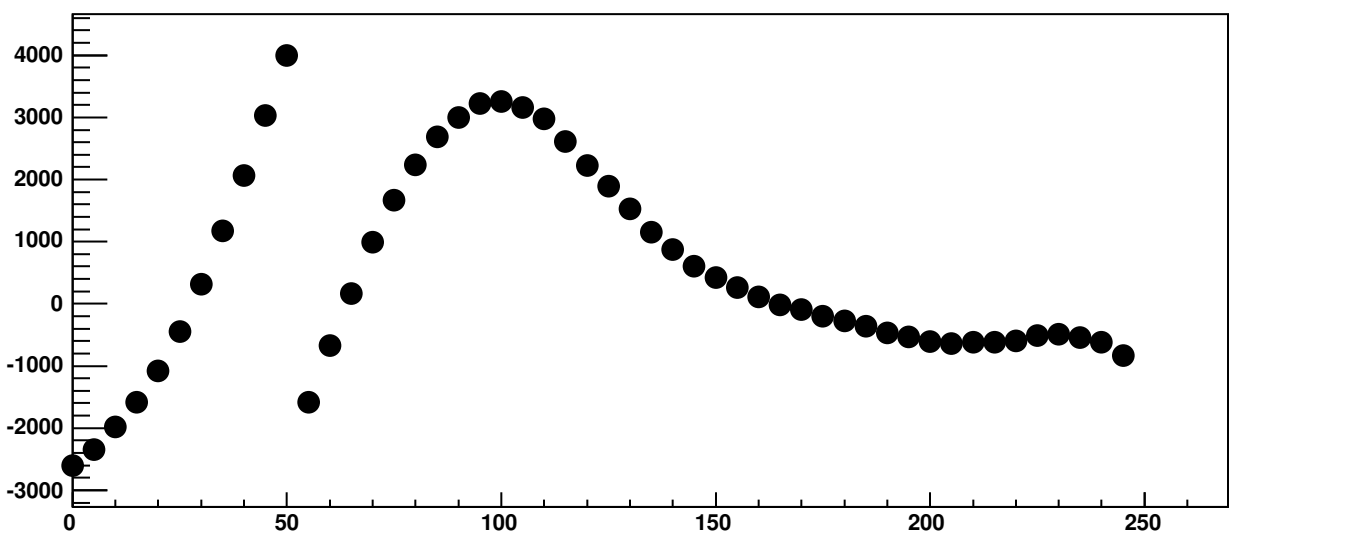
Chip 8, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold



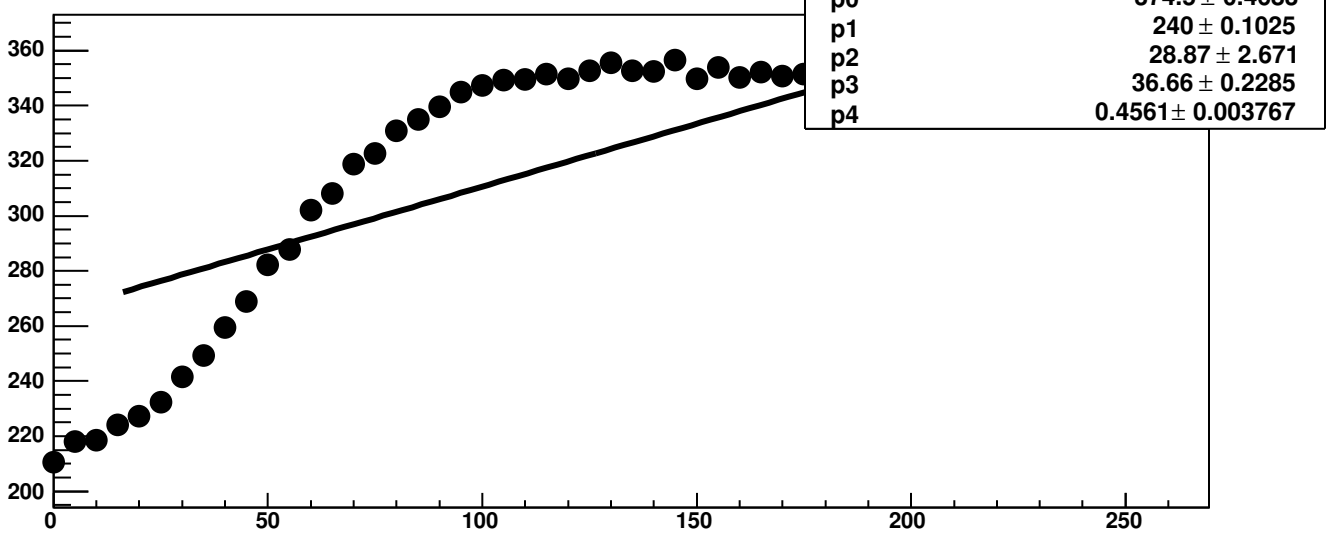
Chip 8, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold



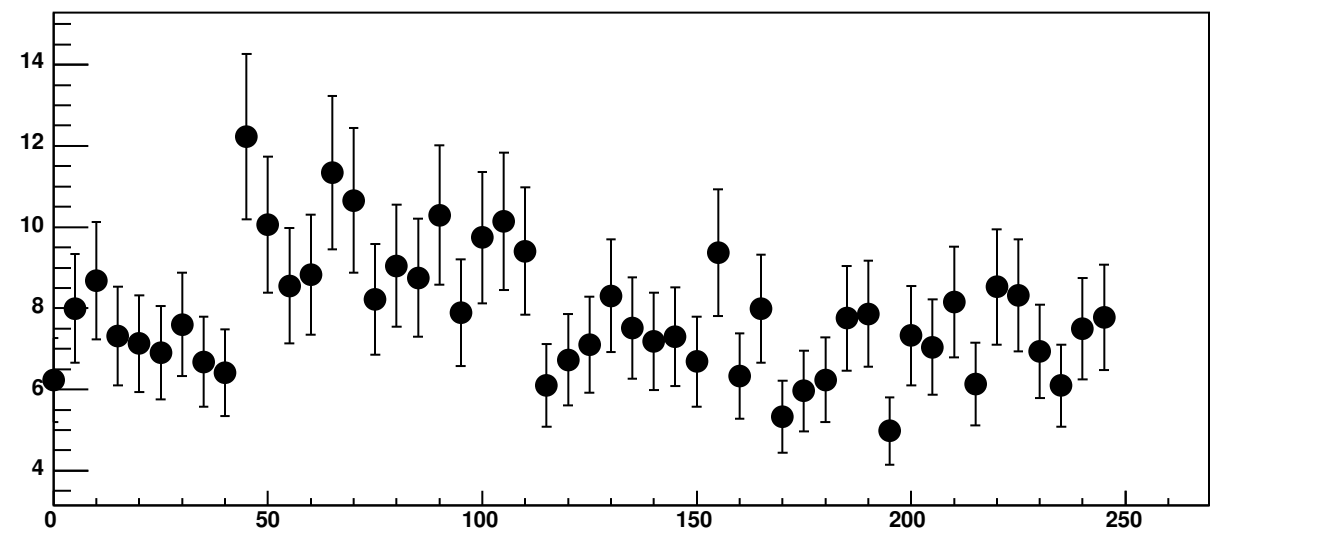
Chip 8, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold



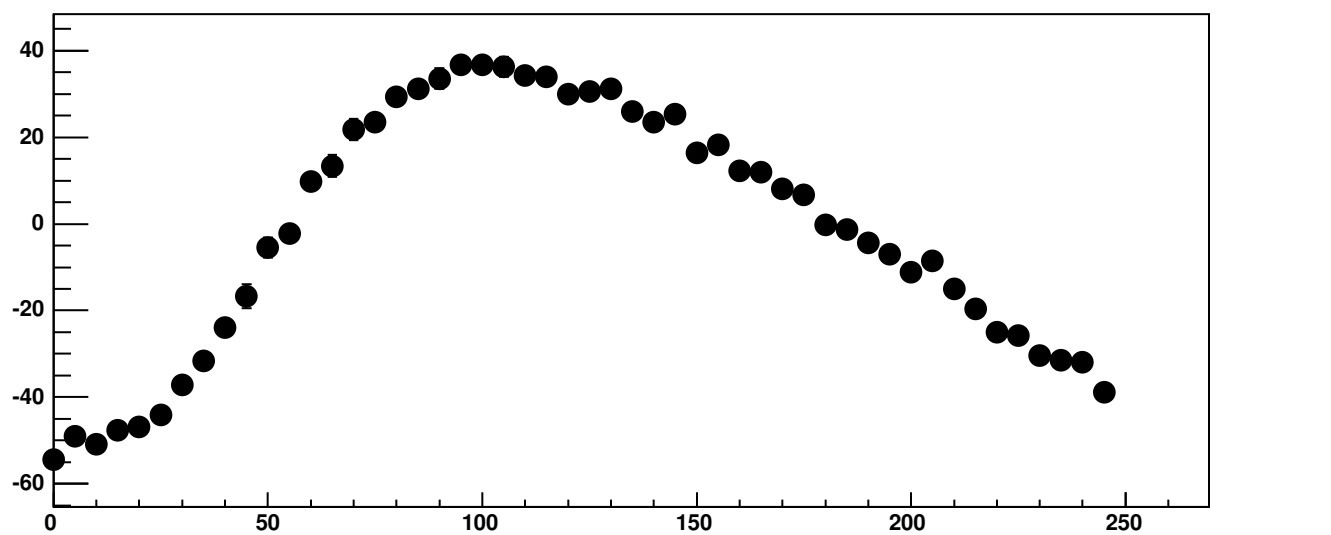
Chip 8, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold



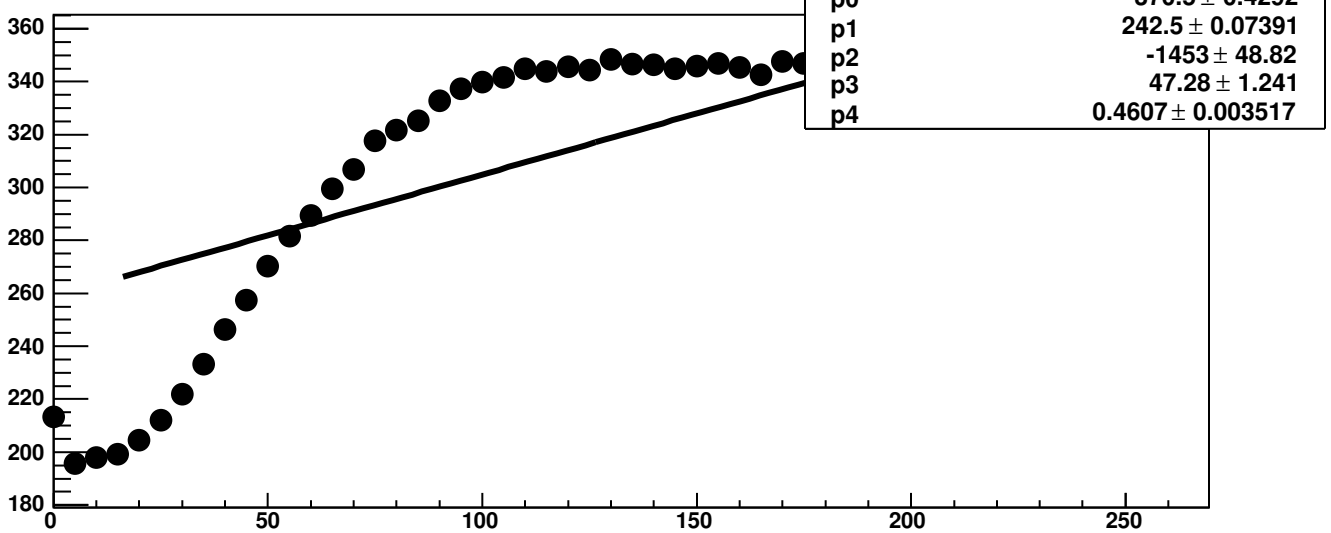
Chip 8, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



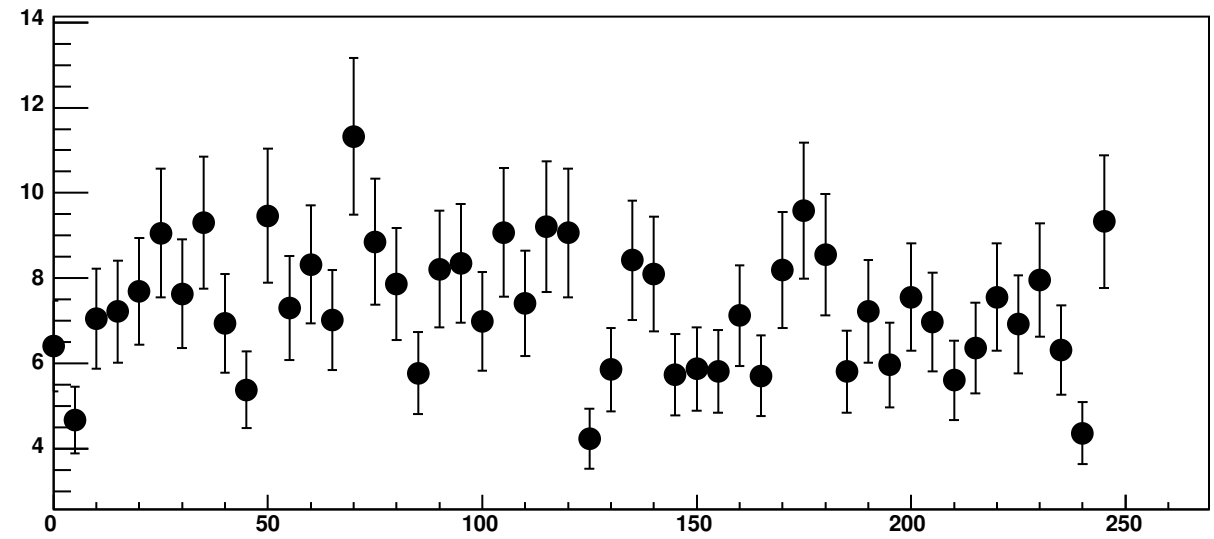
Chip 8, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold



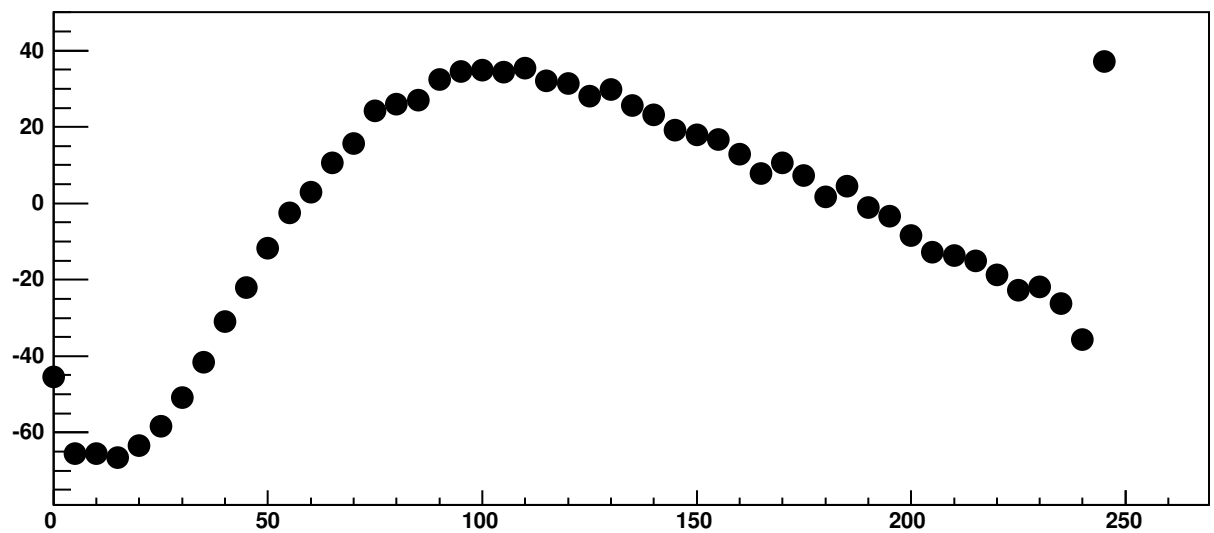
Chip 8, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold



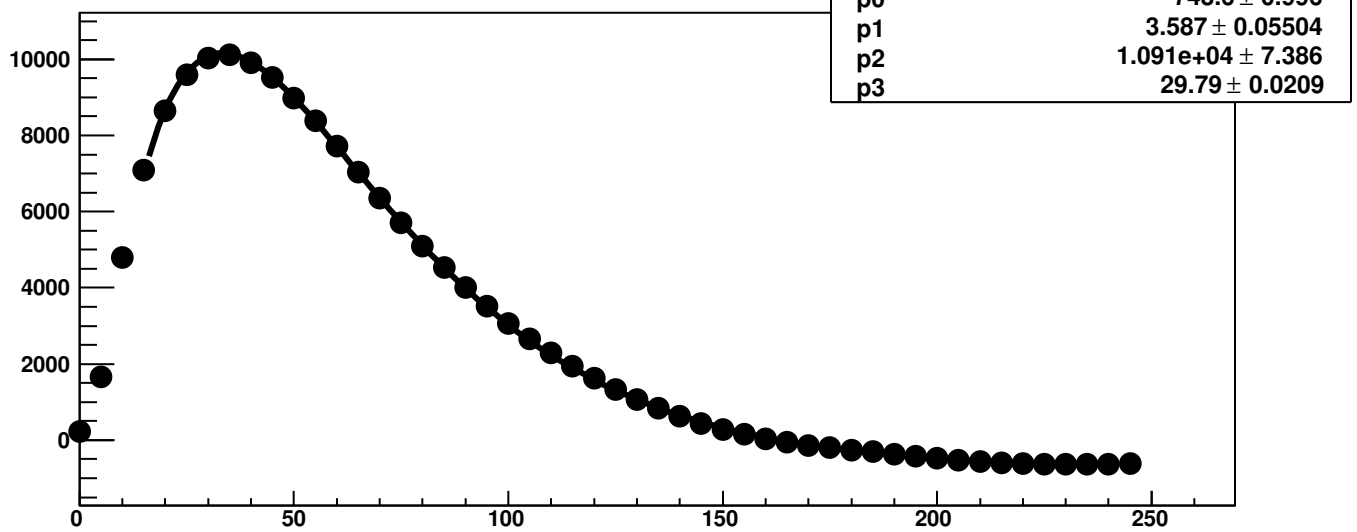
Chip 8, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



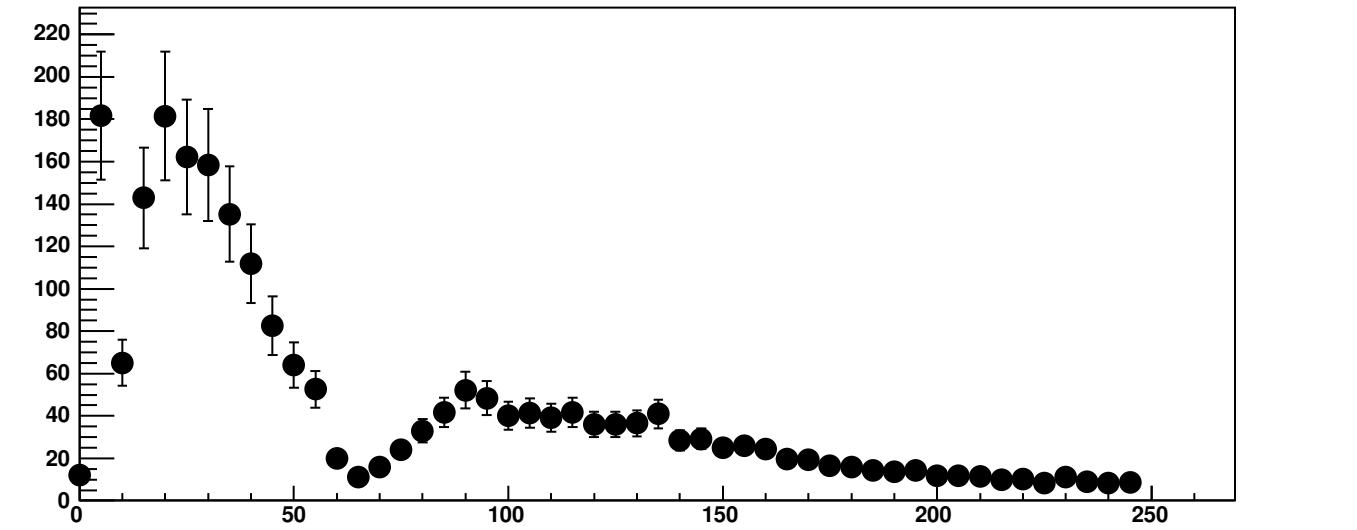
Chip 8, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold



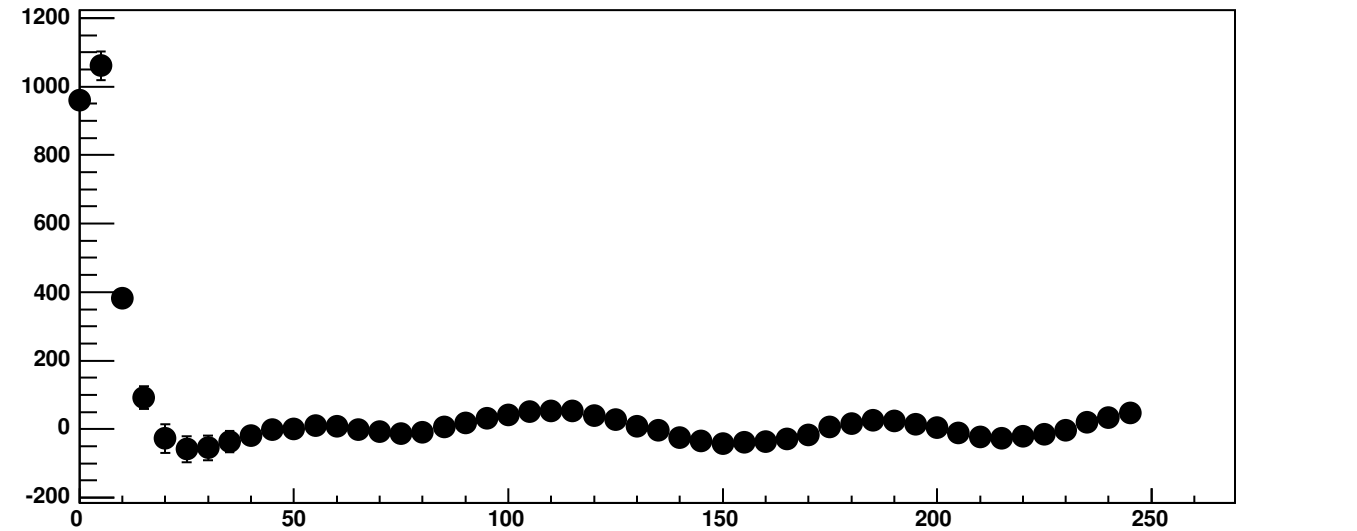
Chip 8, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold



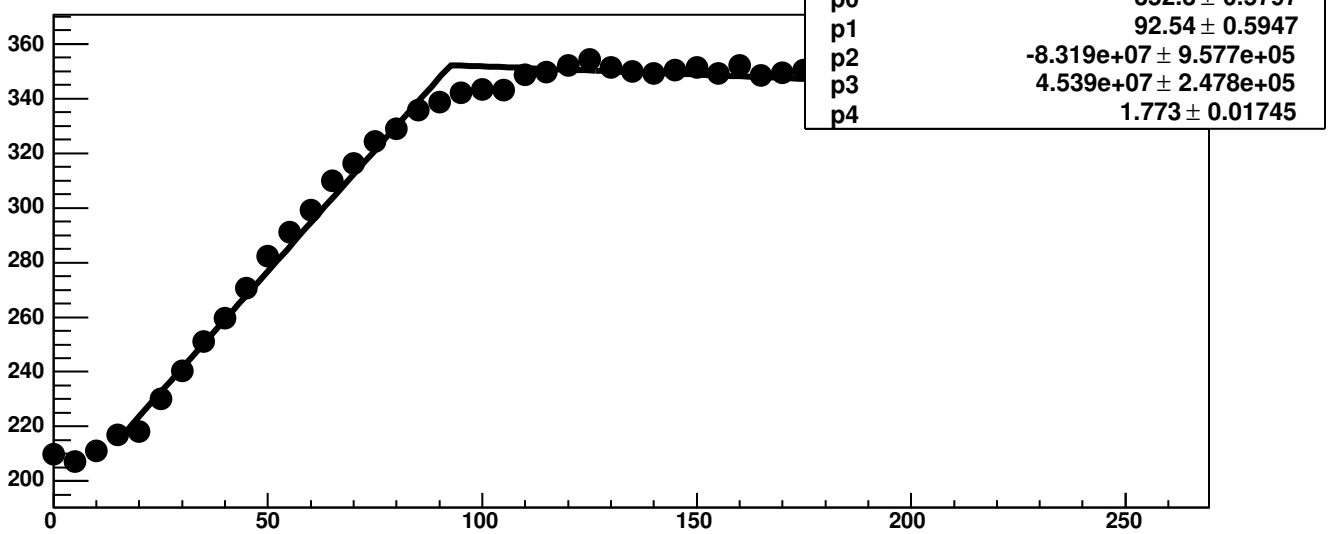
Chip 8, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold



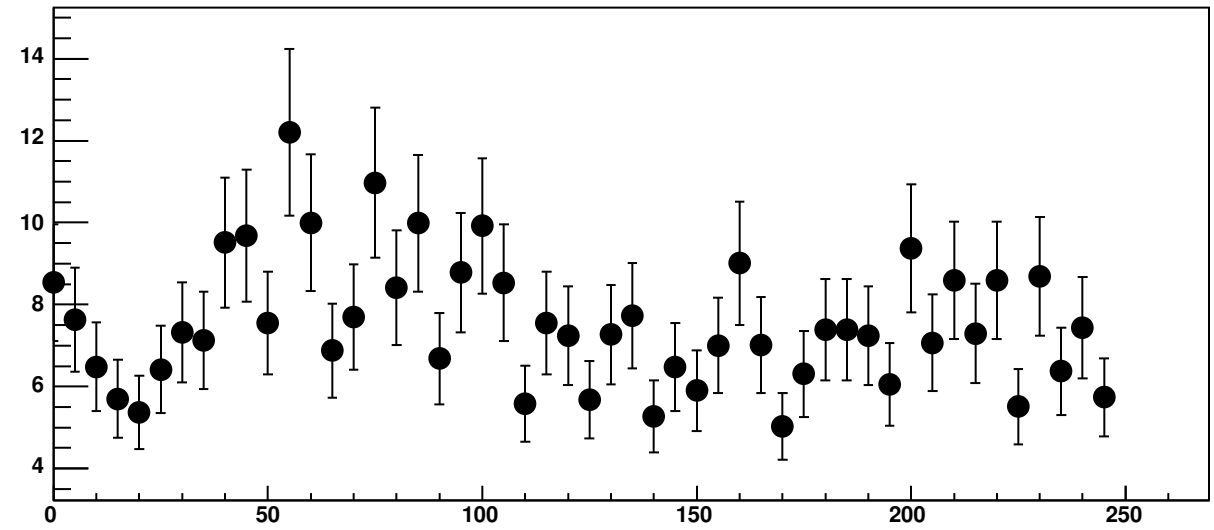
Chip 8, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold



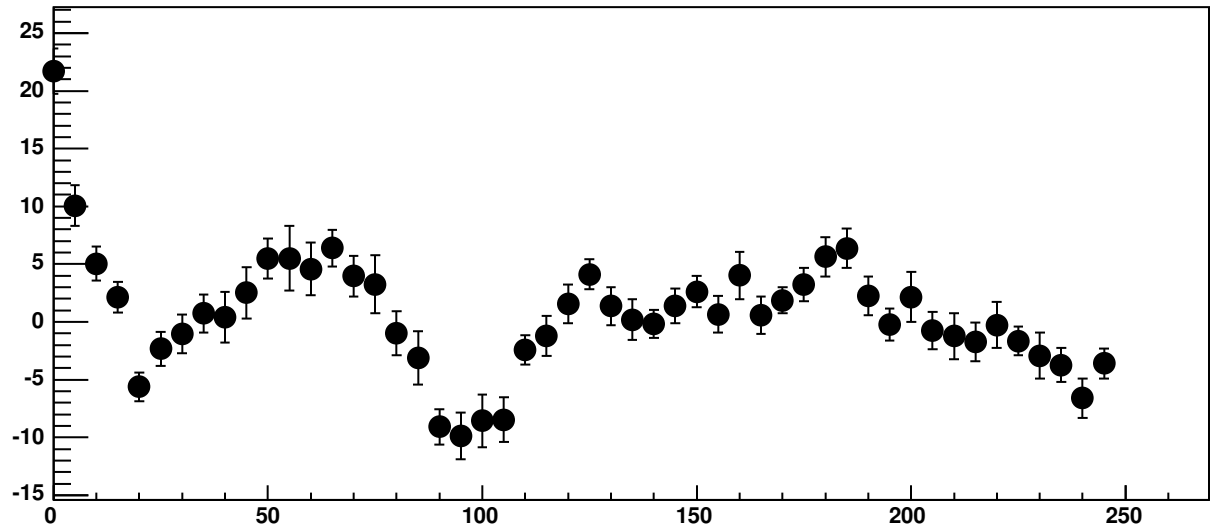
Chip 8, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

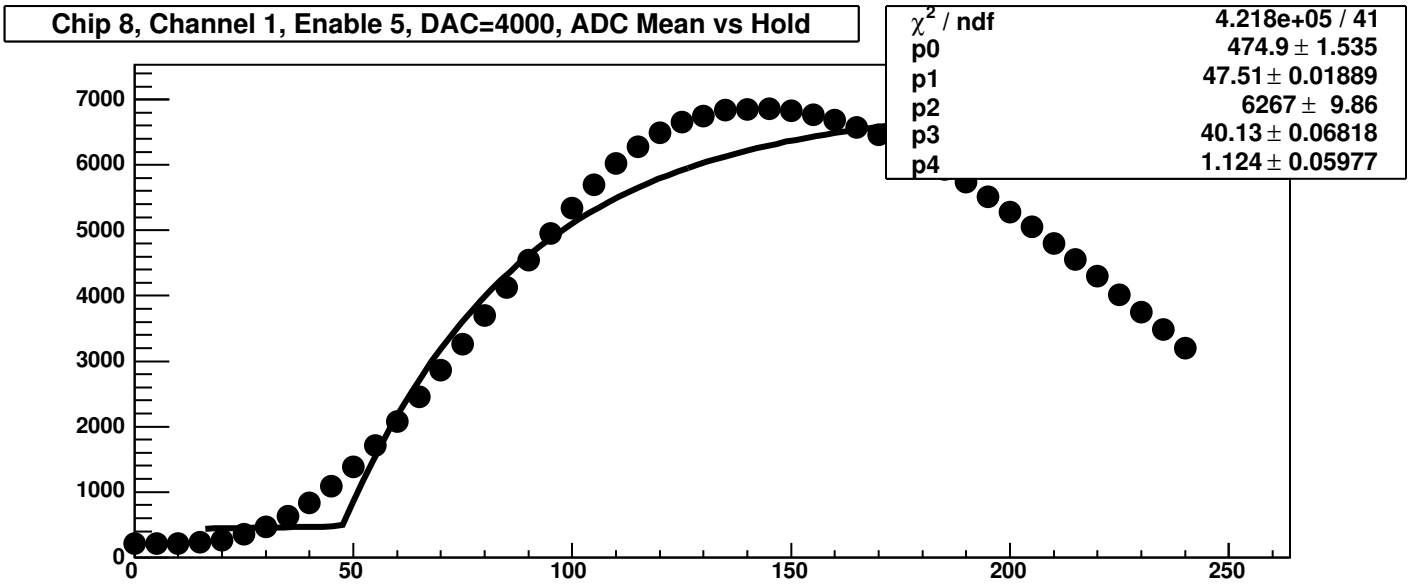


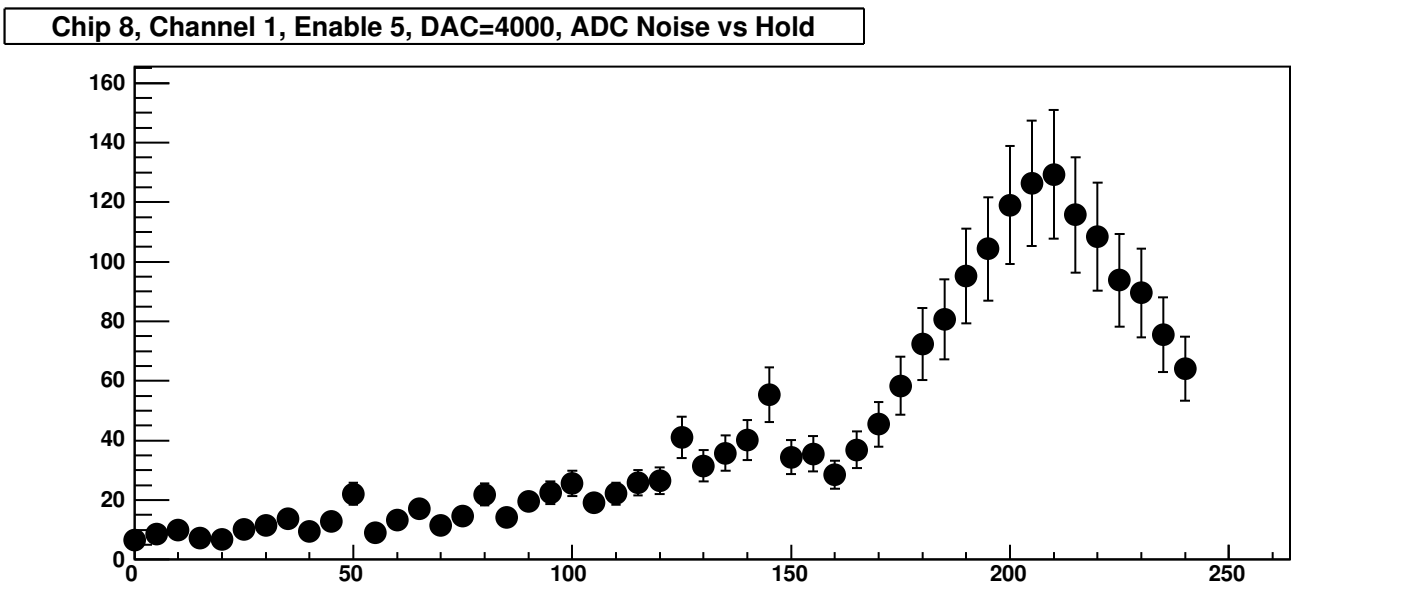
Chip 8, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold

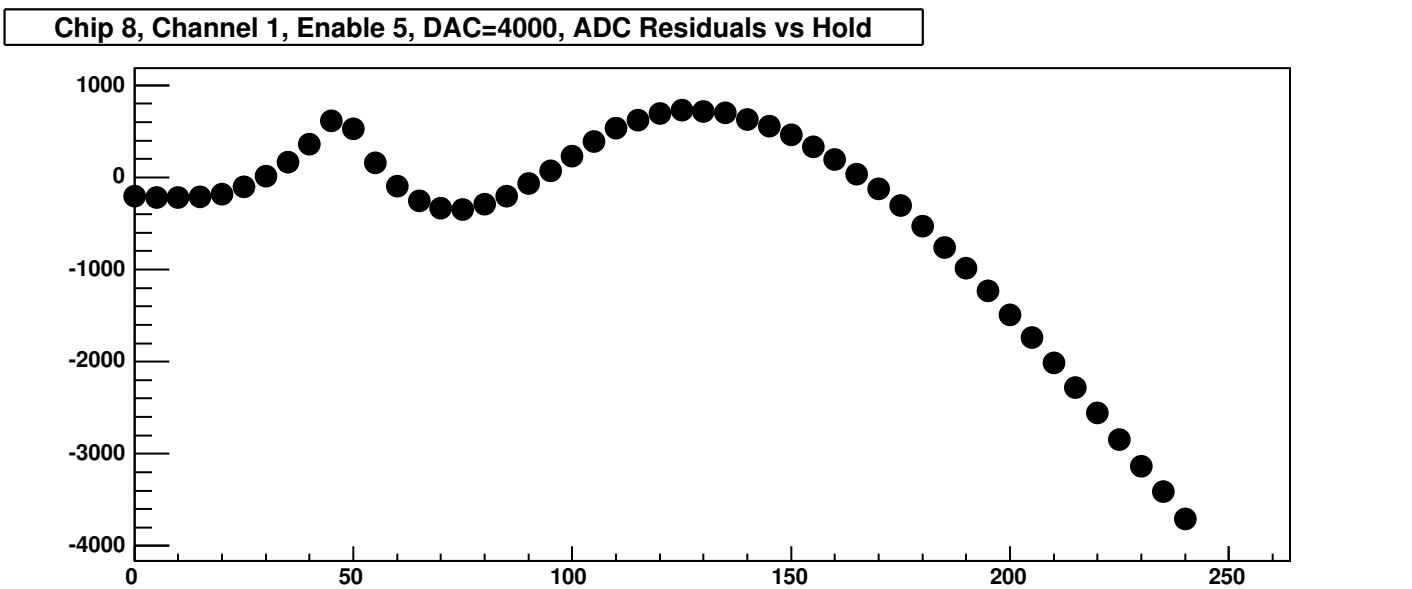


Chip 8, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

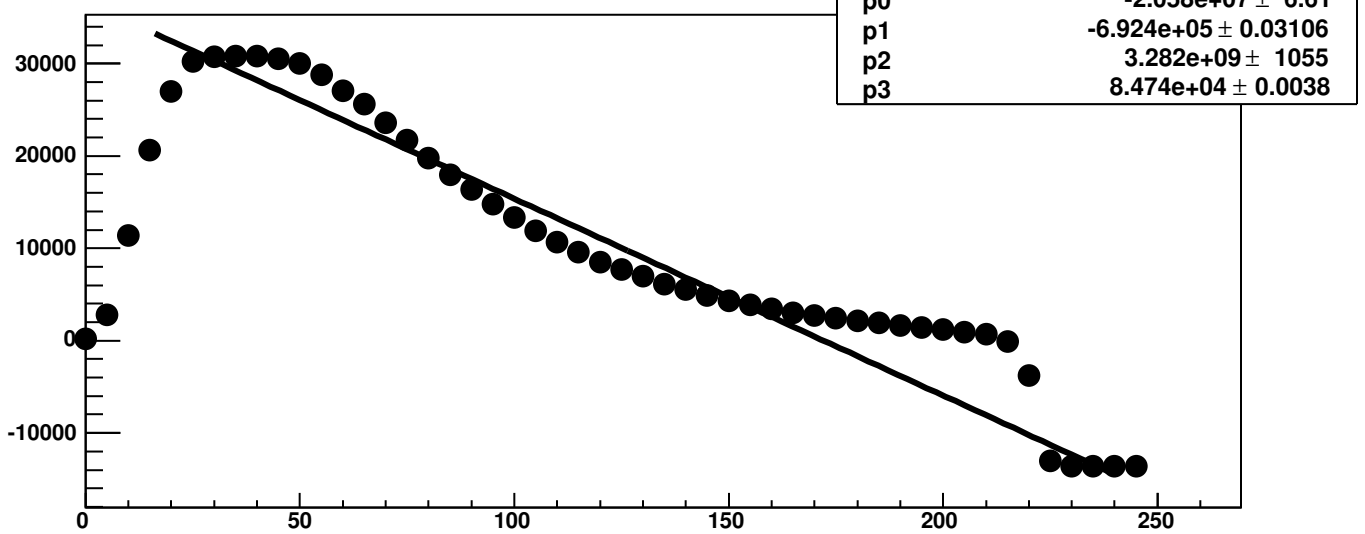



Chip 8, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.218e+05 / 41
p0 474.9 ± 1.535
p1 47.51 ± 0.01889
p2 6267 ± 9.86
p3 40.13 ± 0.06818
p4 1.124 ± 0.05977
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

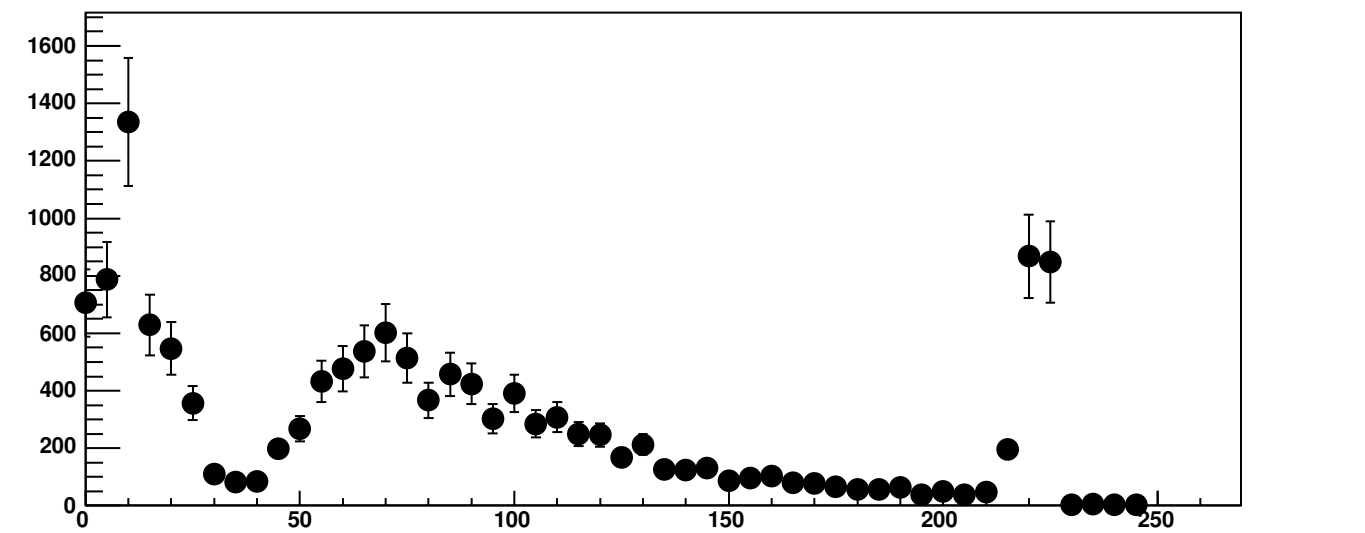

Chip 8, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 8, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

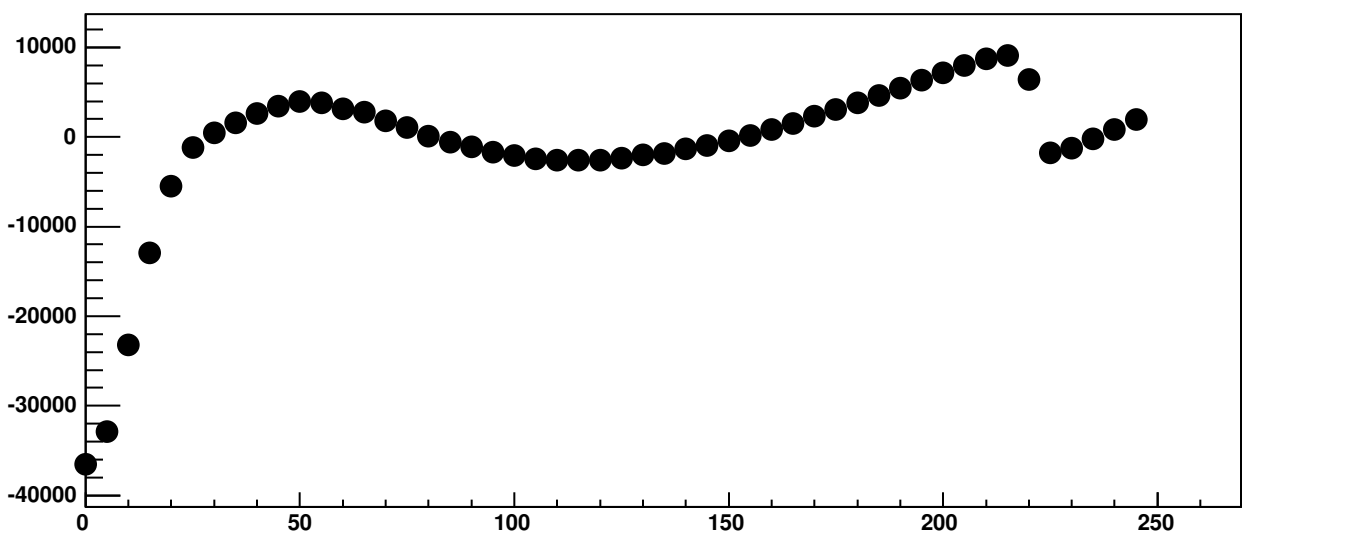
Chip 8, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold



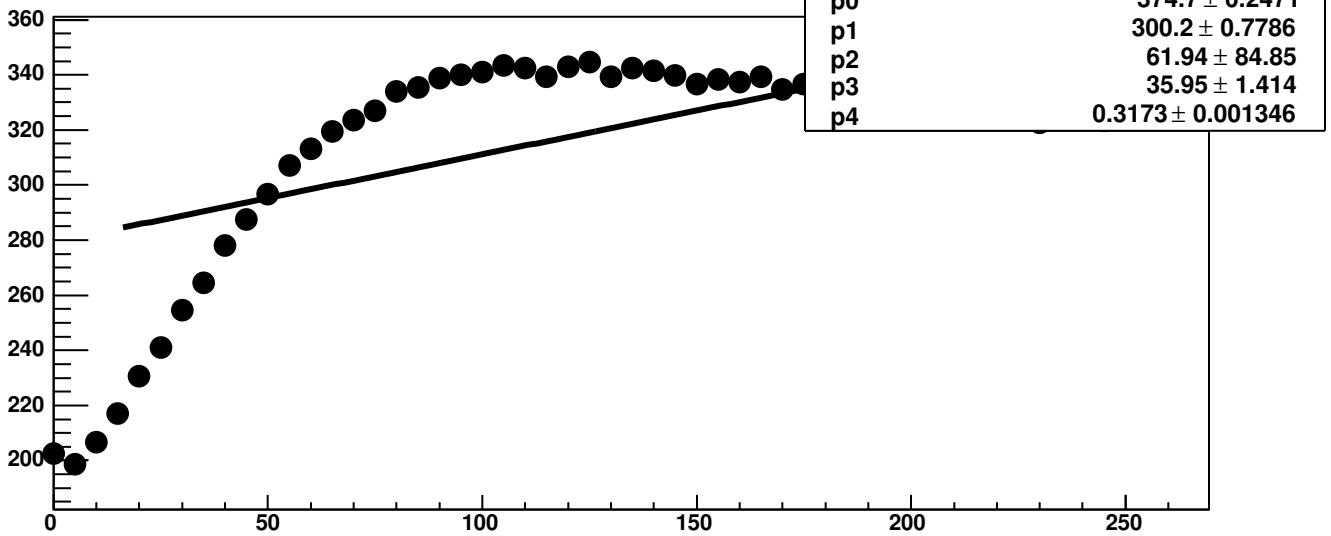
Chip 8, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold



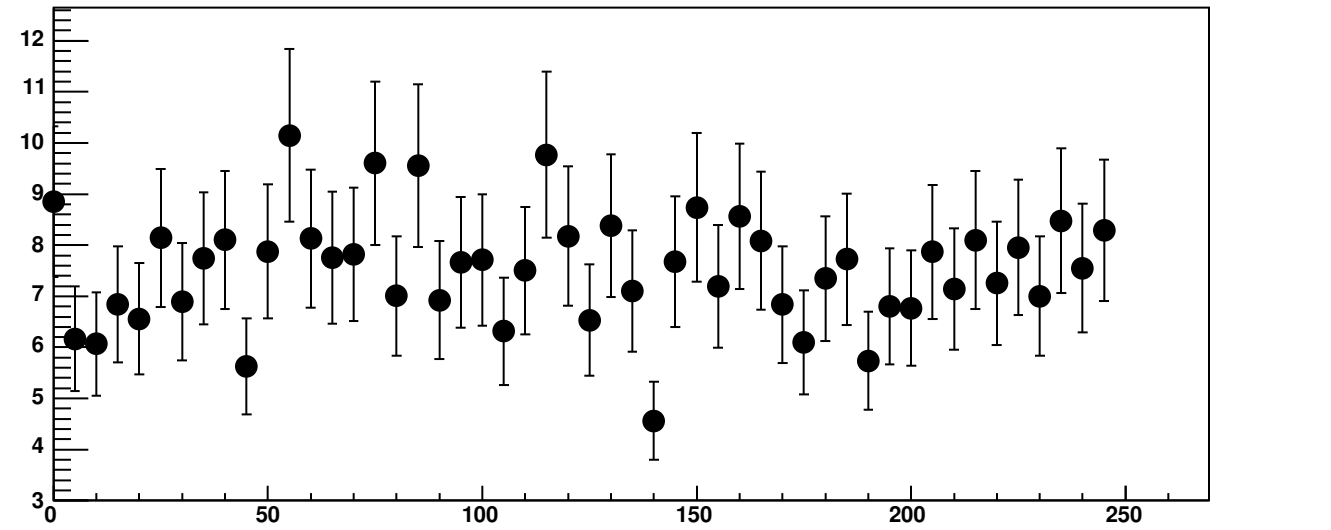
Chip 8, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold



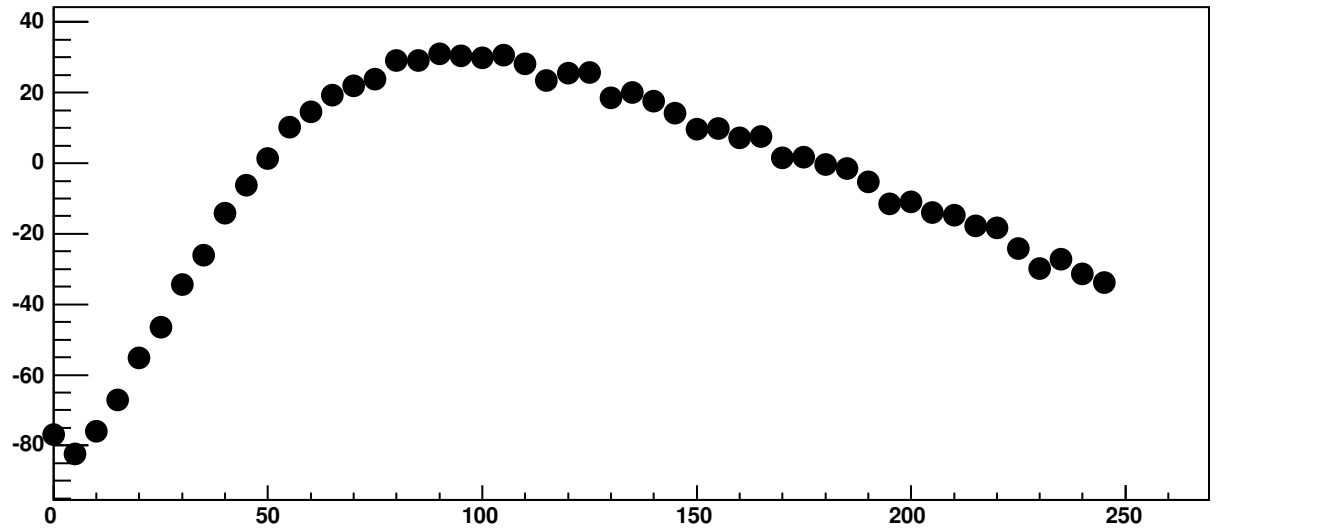
Chip 8, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold



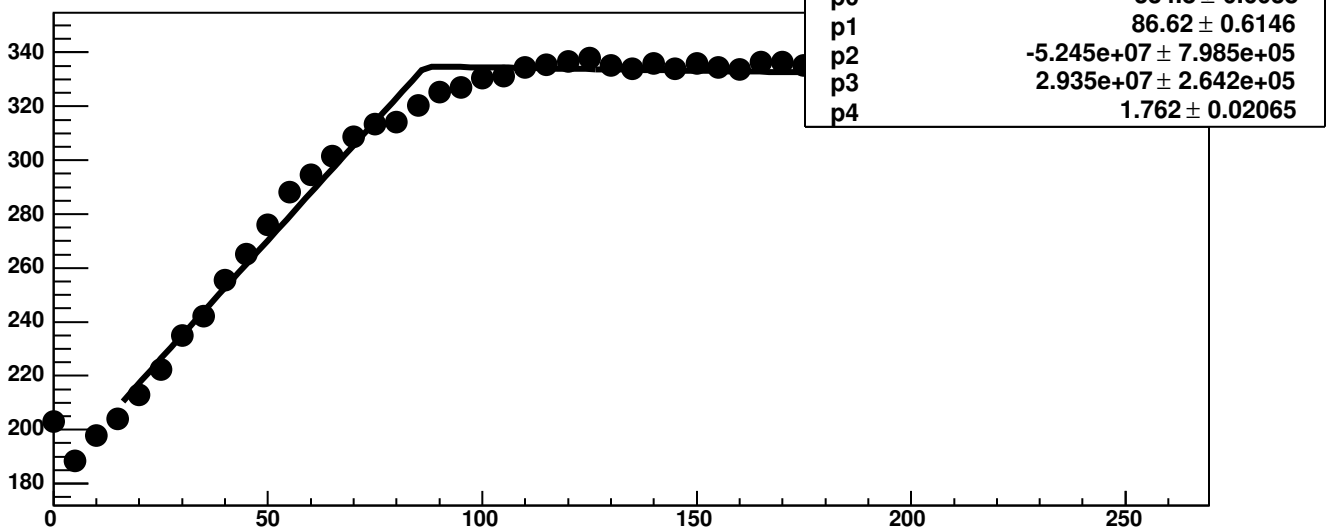
Chip 8, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



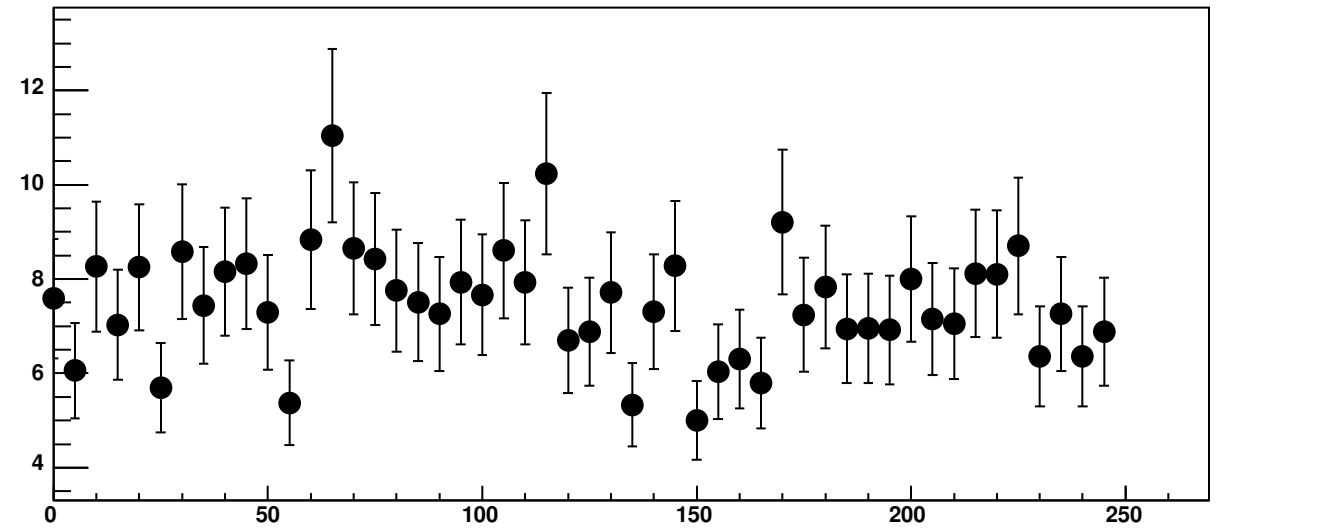
Chip 8, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold



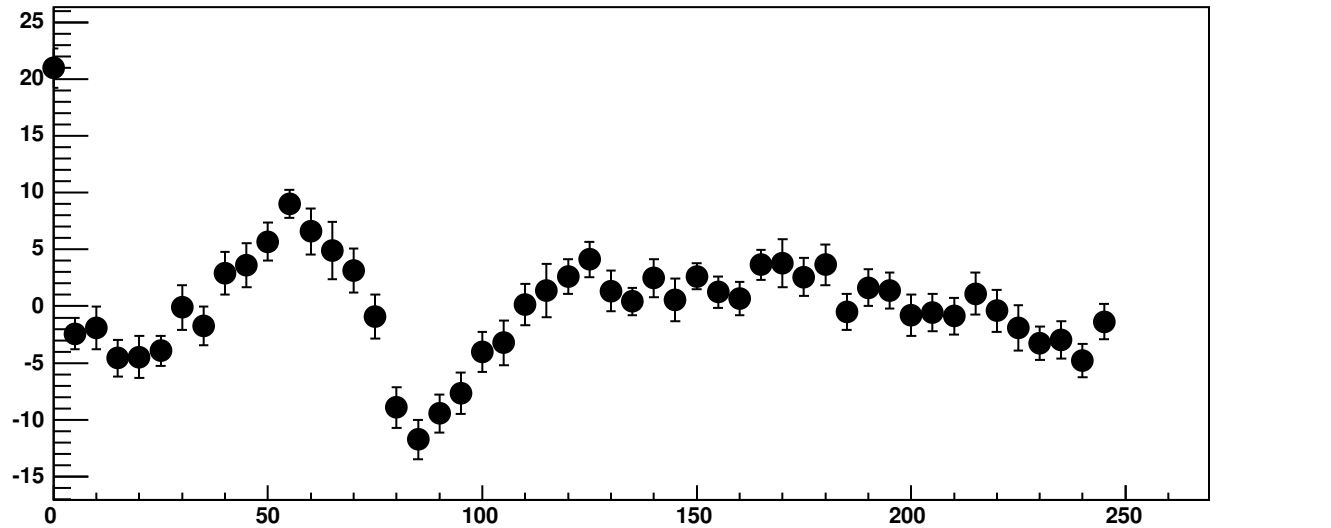
Chip 8, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold



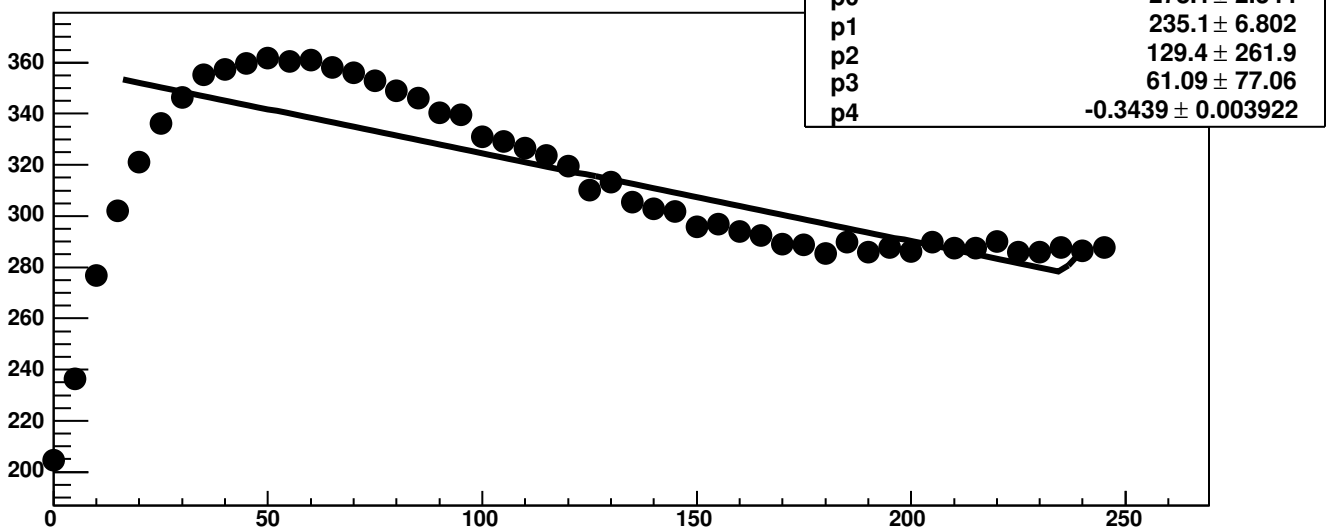
Chip 8, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



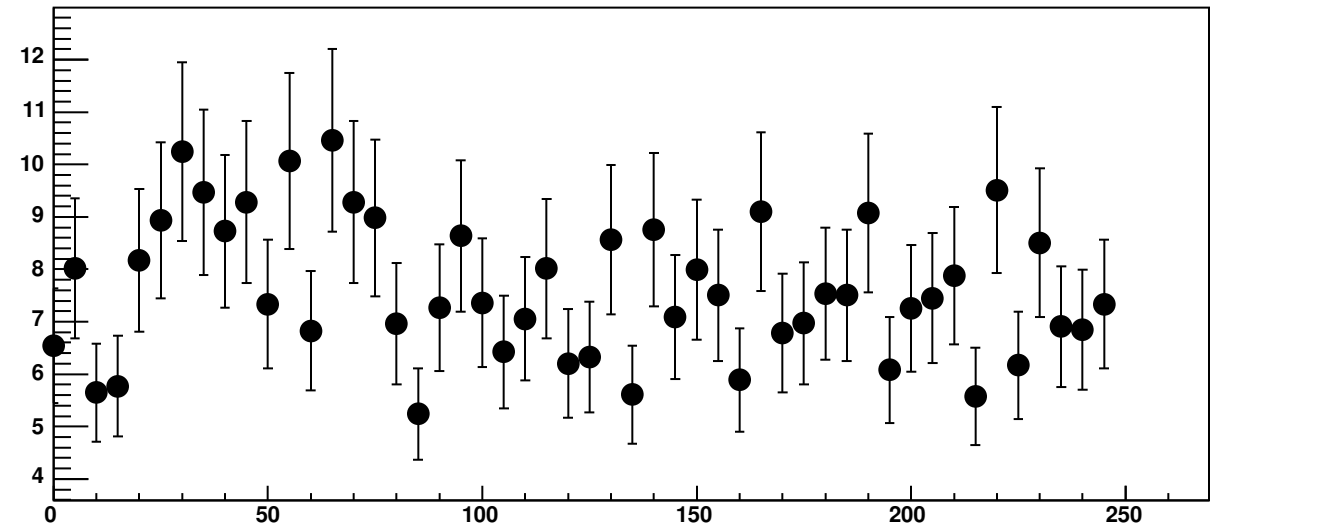
Chip 8, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold



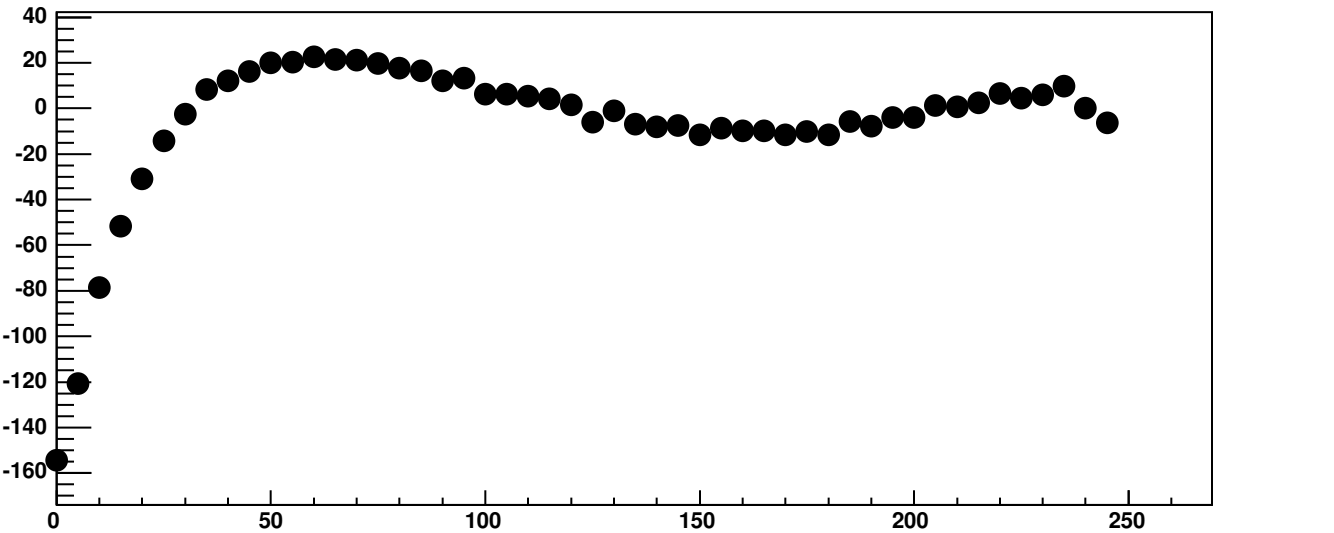
Chip 8, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold

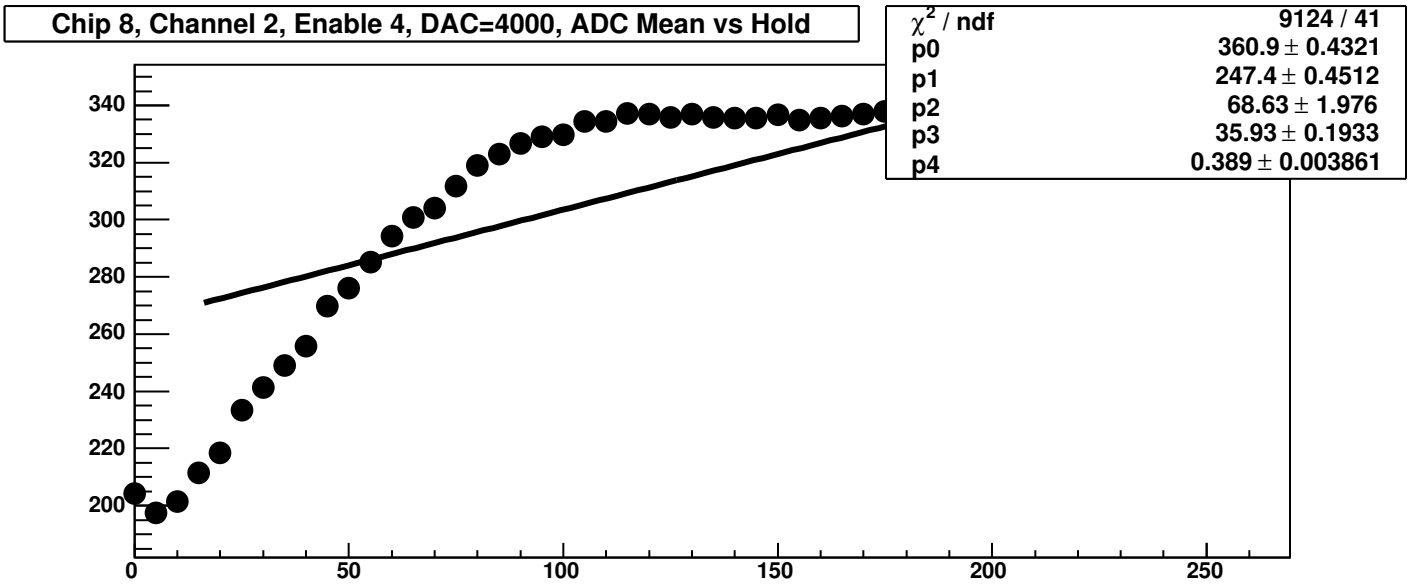


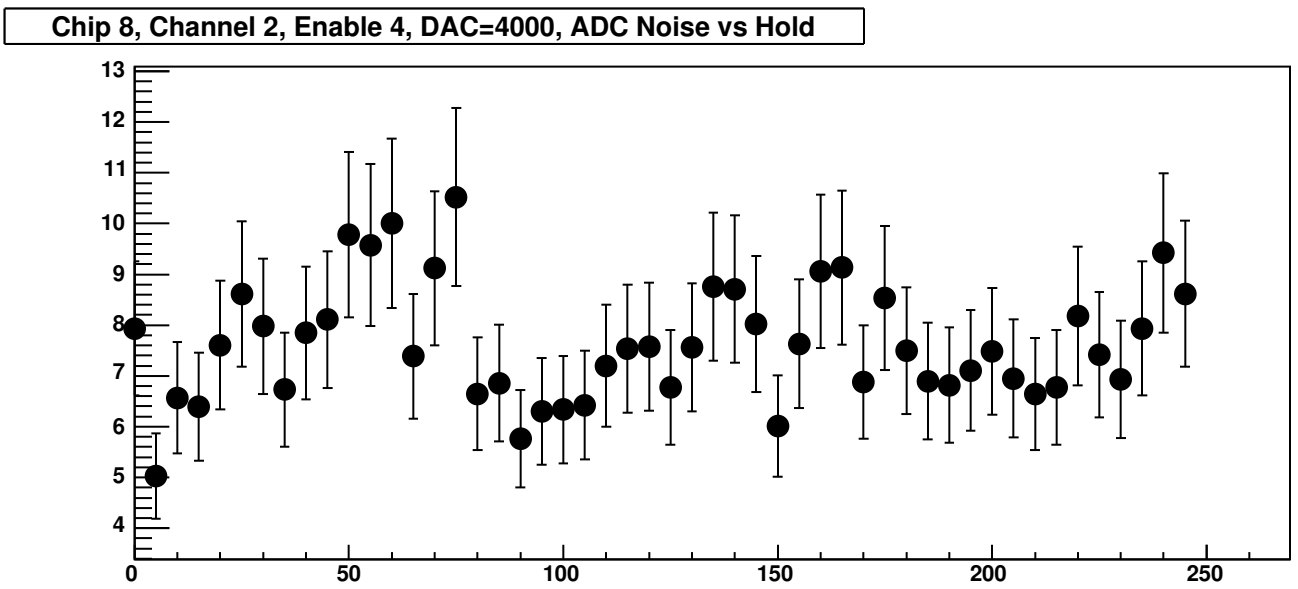
Chip 8, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold

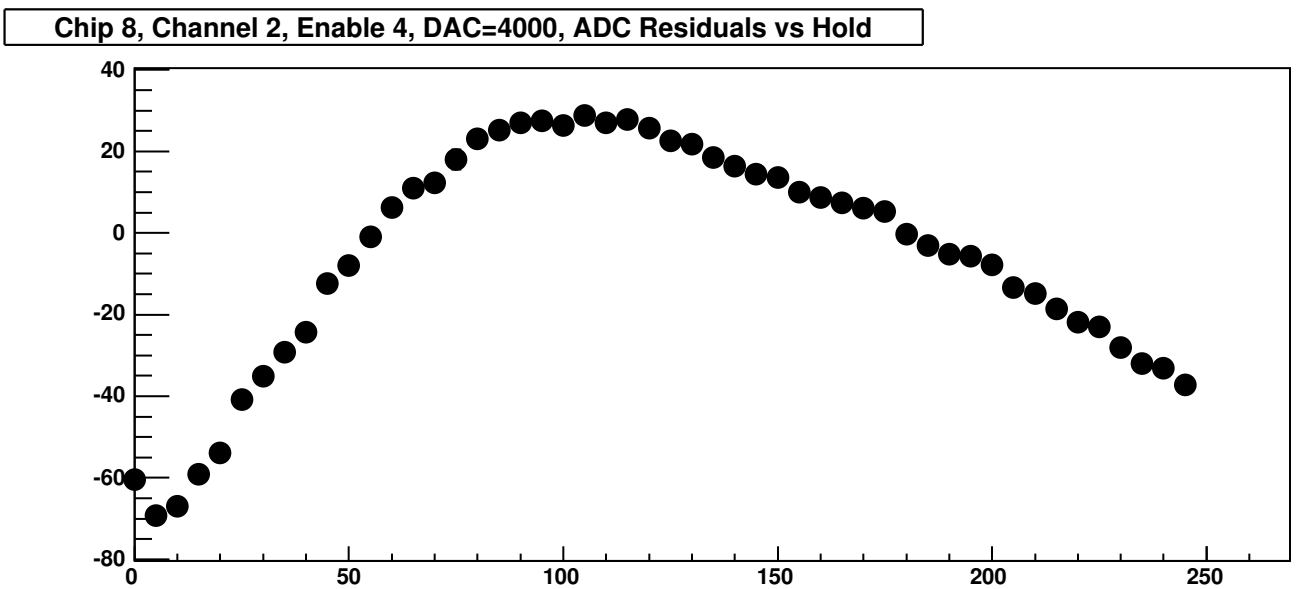


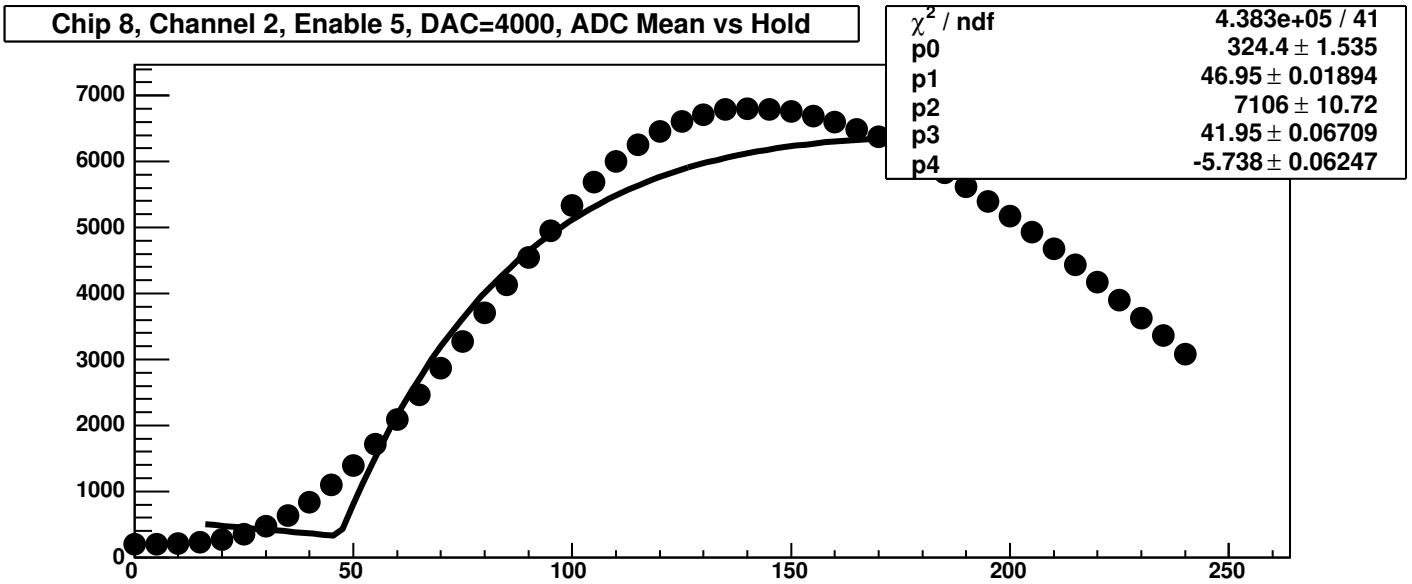
Chip 8, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold

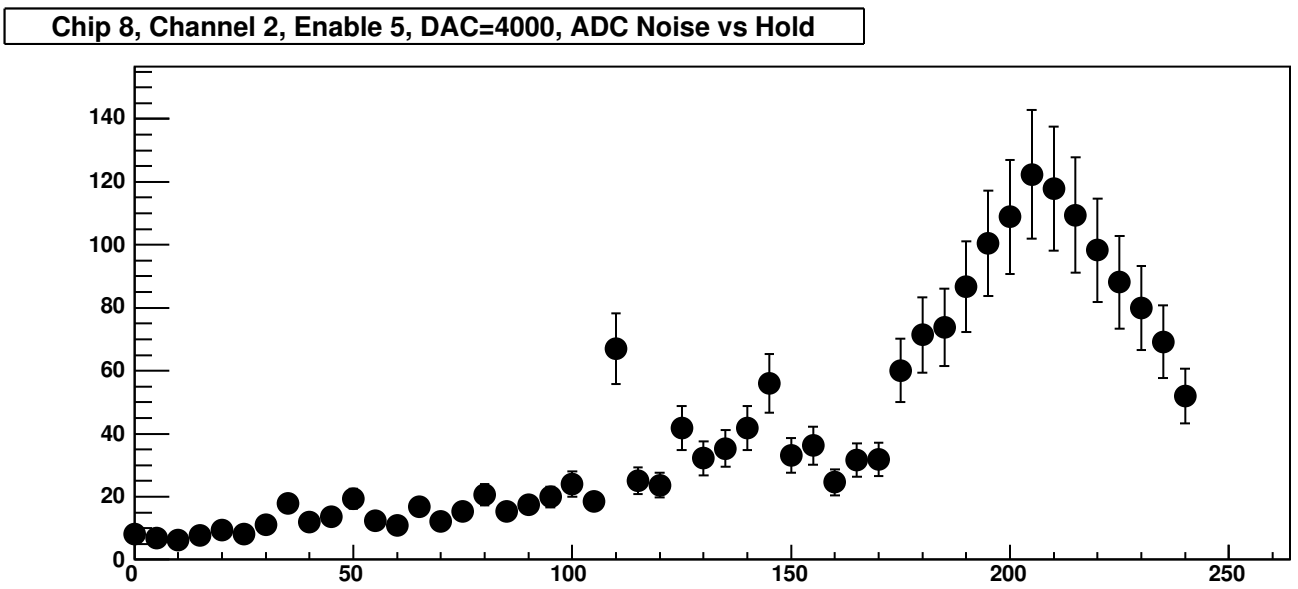


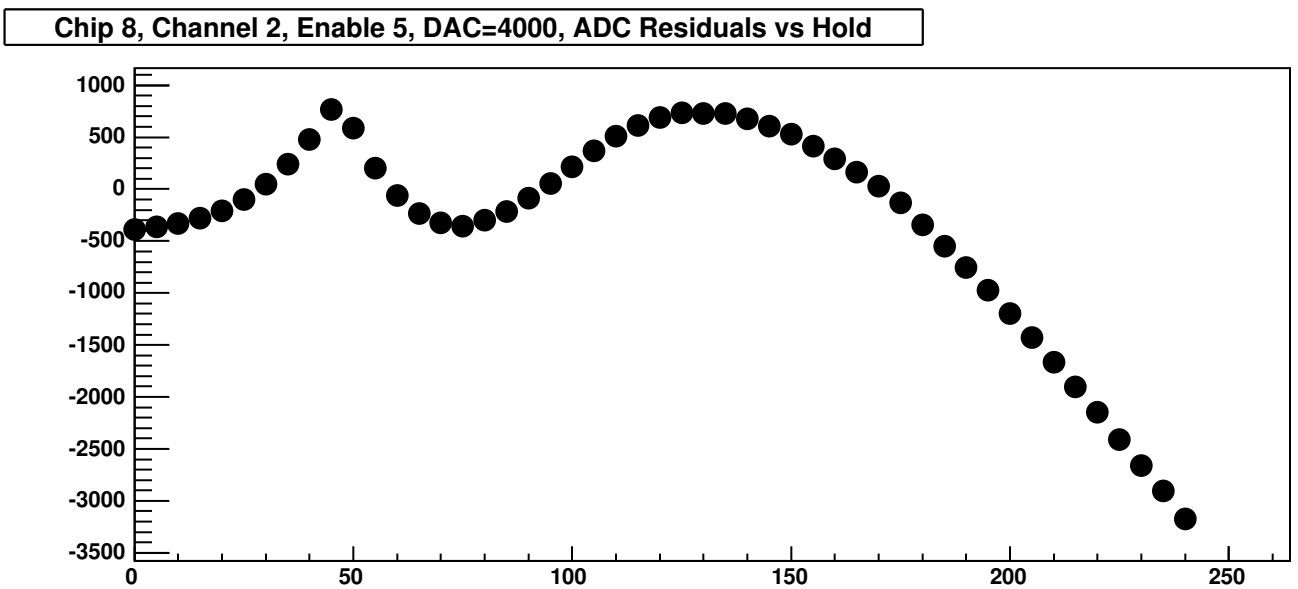

Chip 8, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 9124 / 41
p0 360.9 ± 0.4321
p1 247.4 ± 0.4512
p2 68.63 ± 1.976
p3 35.93 ± 0.1933
p4 0.389 ± 0.003861
340
320
300
280
260
240
220
200
0
50
100
150
200
250

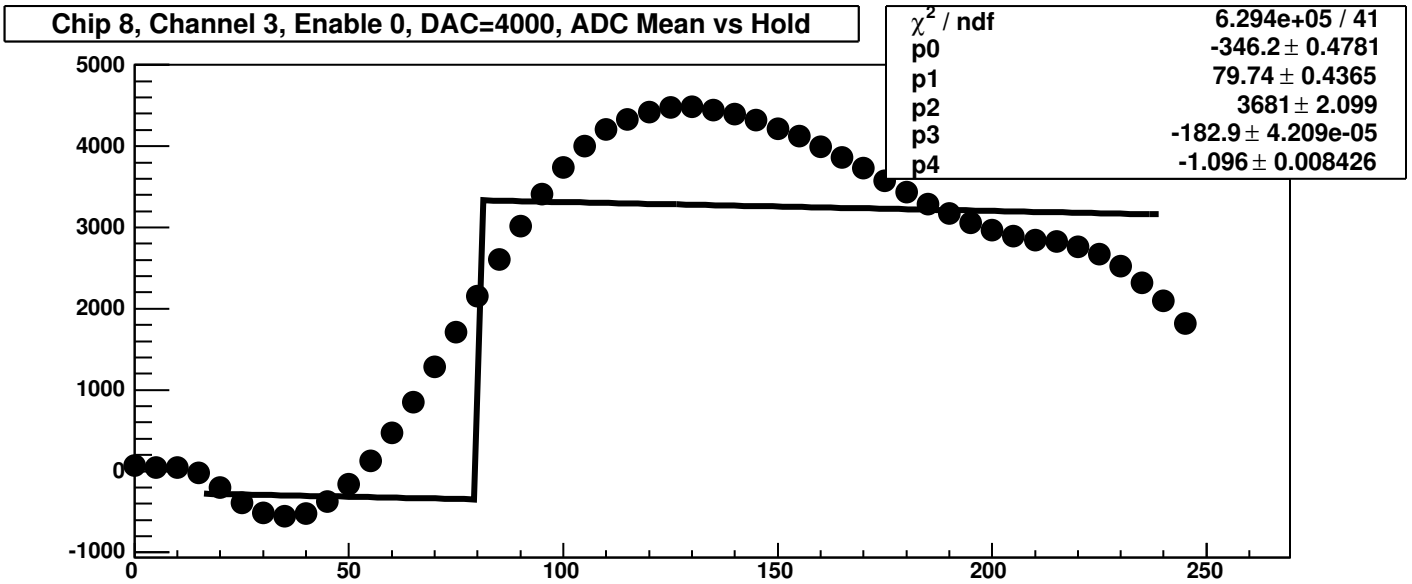

Chip 8, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

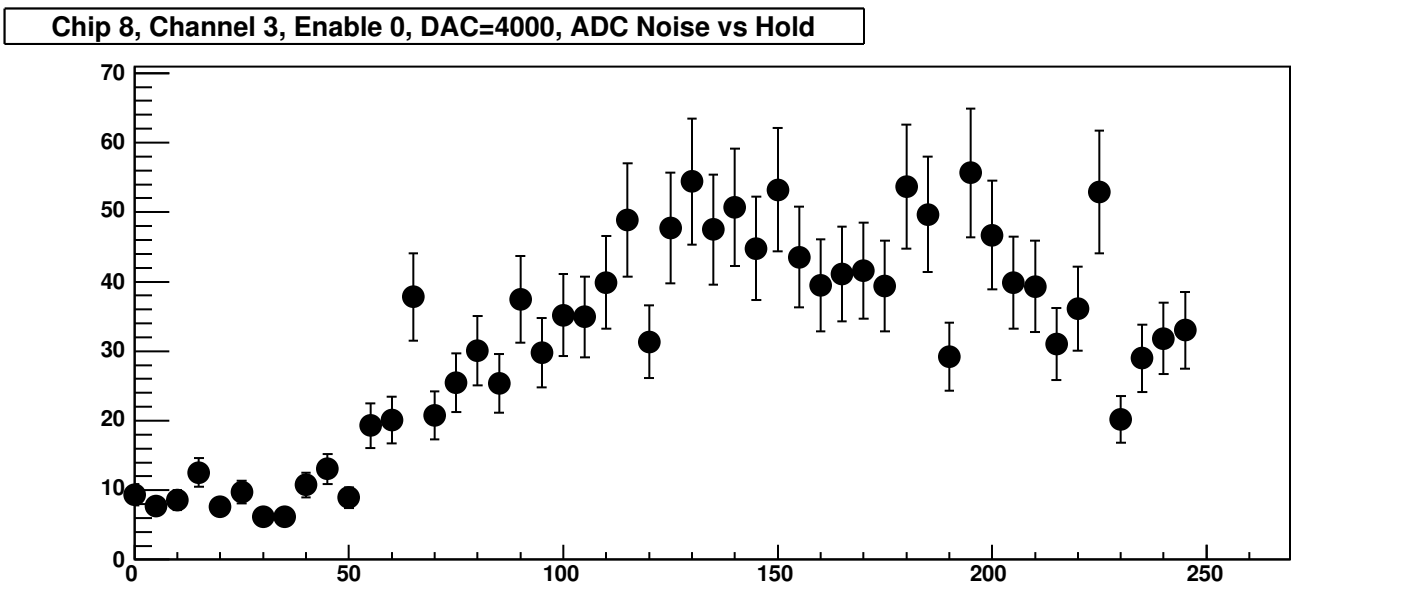

Chip 8, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
0
50
100
150
200
250

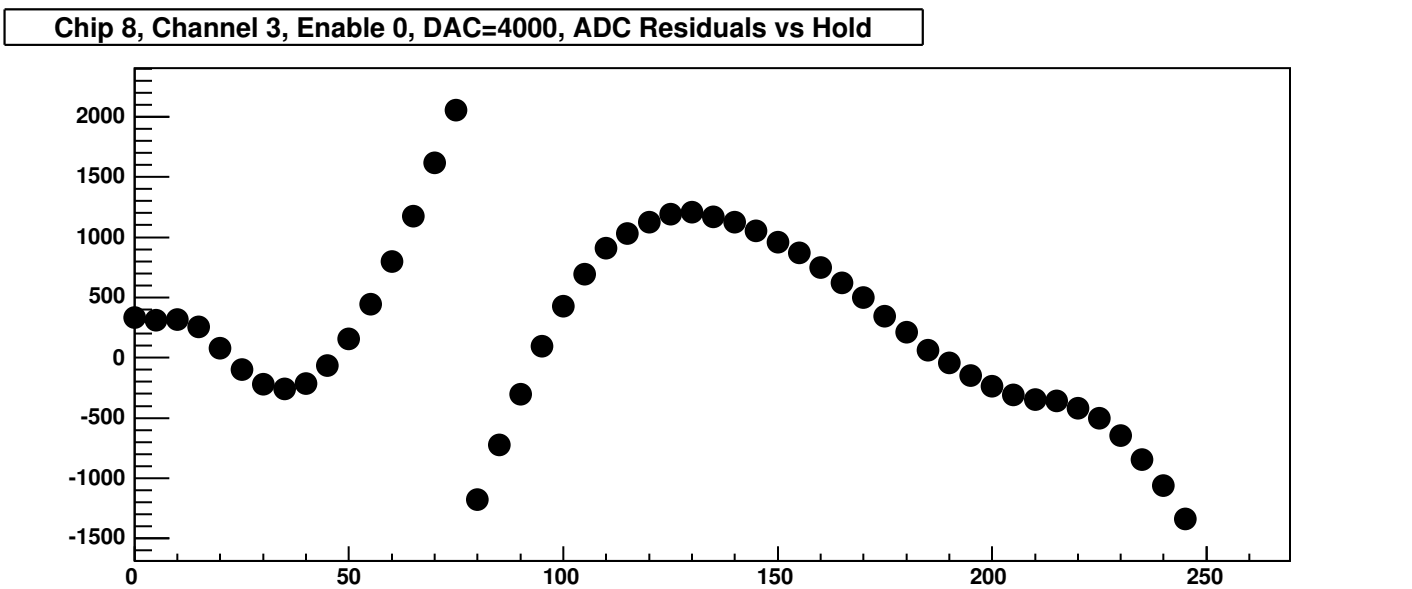

Chip 8, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.383e+05 / 41
p0 324.4 ± 1.535
p1 46.95 ± 0.01894
p2 7106 ± 10.72
p3 41.95 ± 0.06709
p4 -5.738 ± 0.06247
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 8, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 8, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
0
50
100
150
200
250

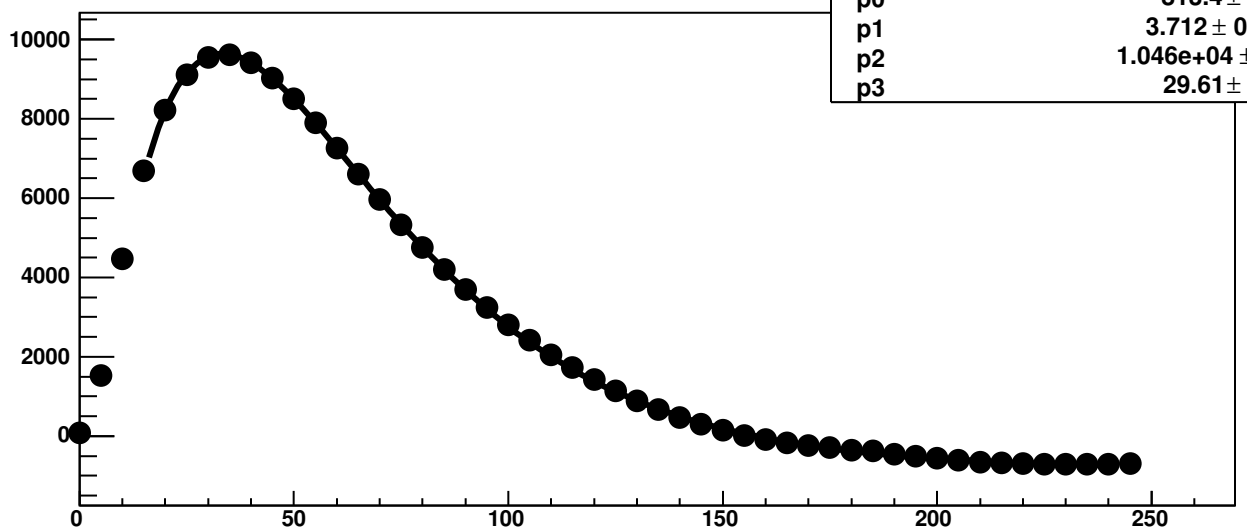

Chip 8, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 6.294e+05 / 41
p0 -346.2 ± 0.4781
p1 79.74 ± 0.4365
p2 3681 ± 2.099
p3 -182.9 ± 4.209e-05
p4 -1.096 ± 0.008426
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 8, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold
70
60
50
40
30
20
10
0
0
50
100
150
200
250

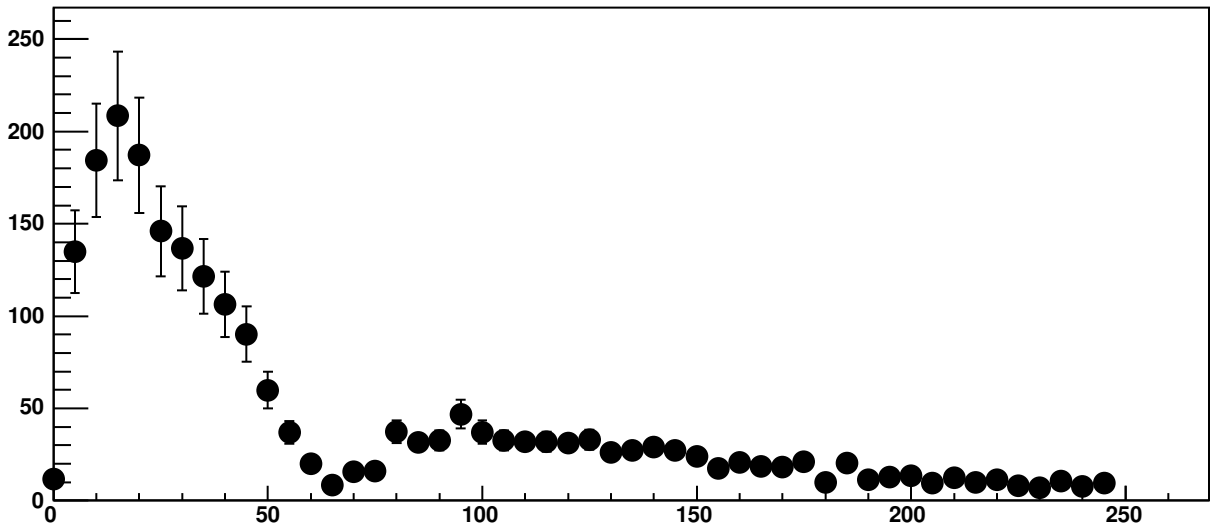

Chip 8, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

Chip 8, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold

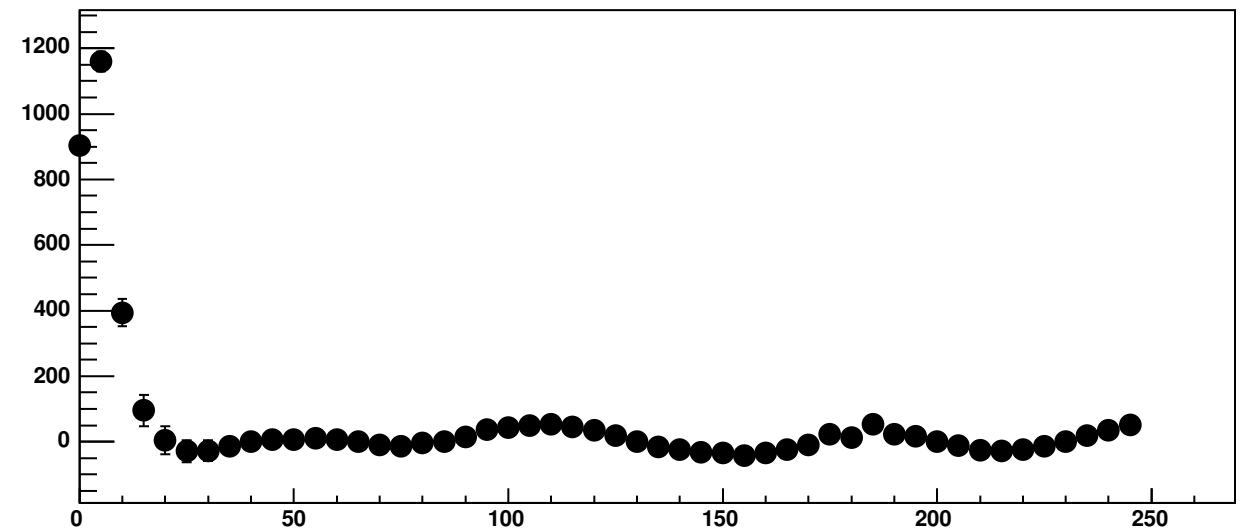
χ^2 / ndf	1671 / 42
p0	-813.4 ± 0.9157
p1	3.712 ± 0.06494
p2	1.046e+04 ± 8.082
p3	29.61 ± 0.0203



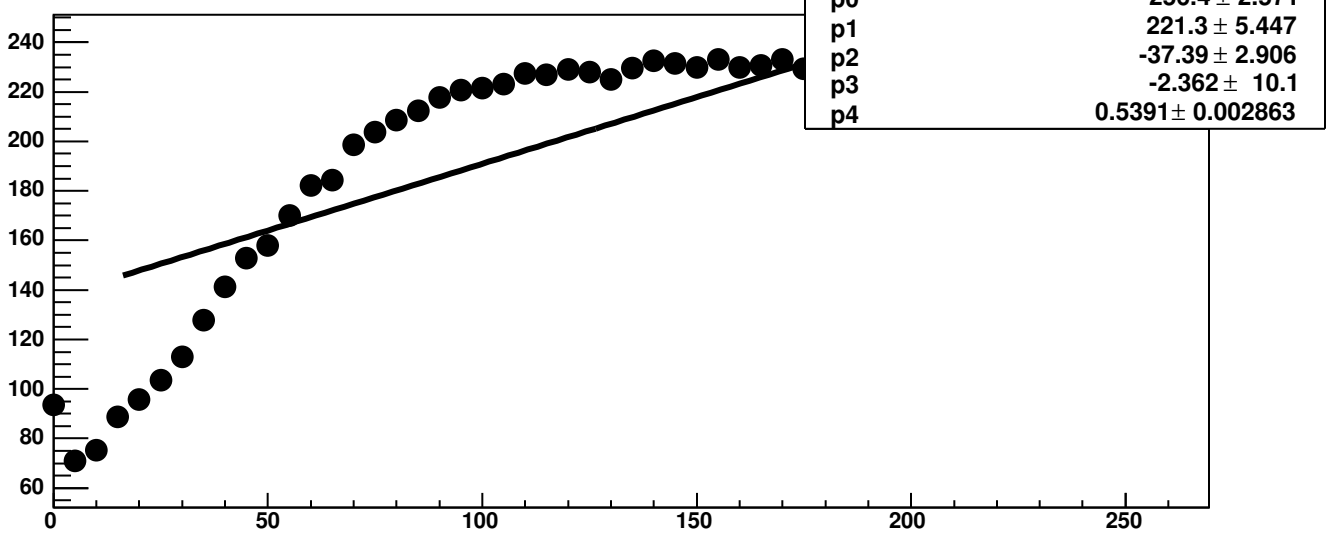
Chip 8, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold



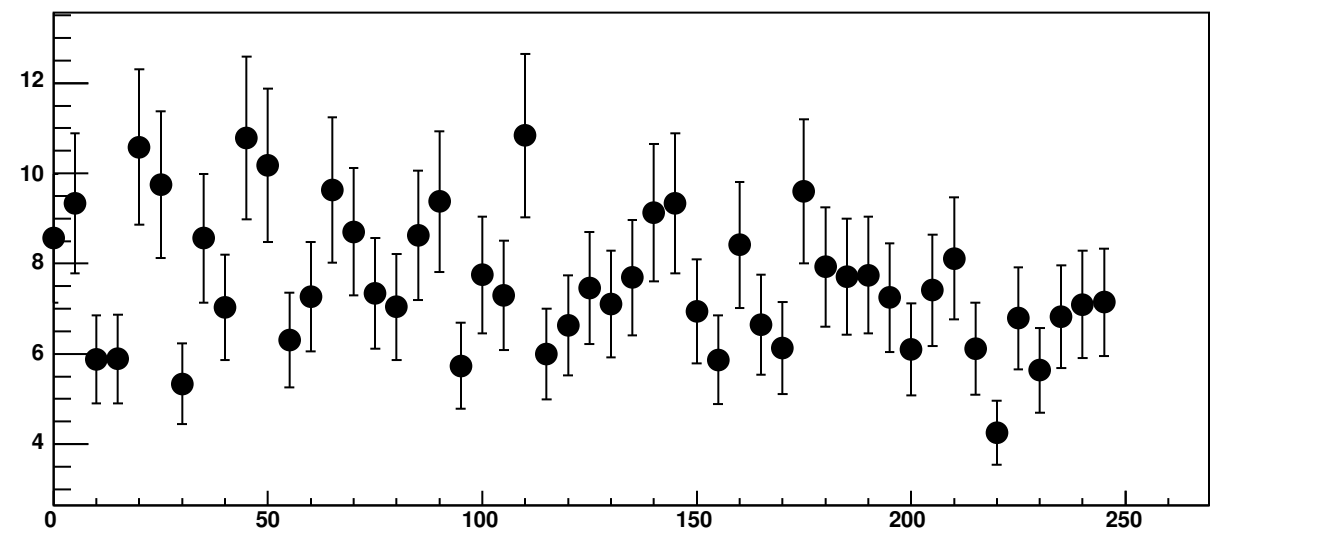
Chip 8, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold



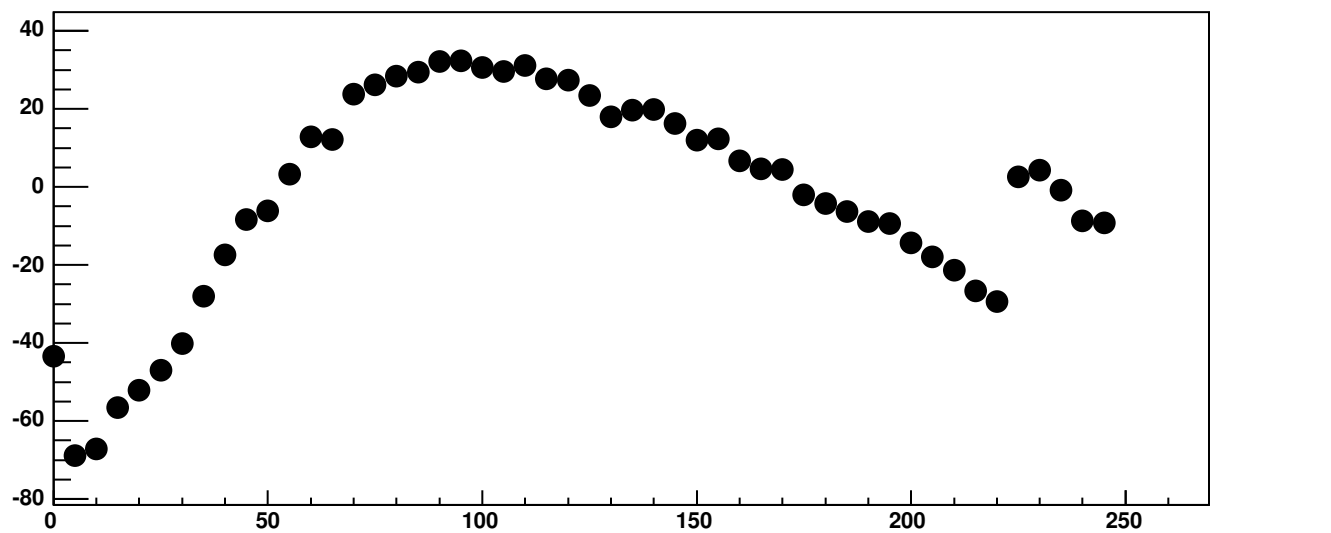
Chip 8, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

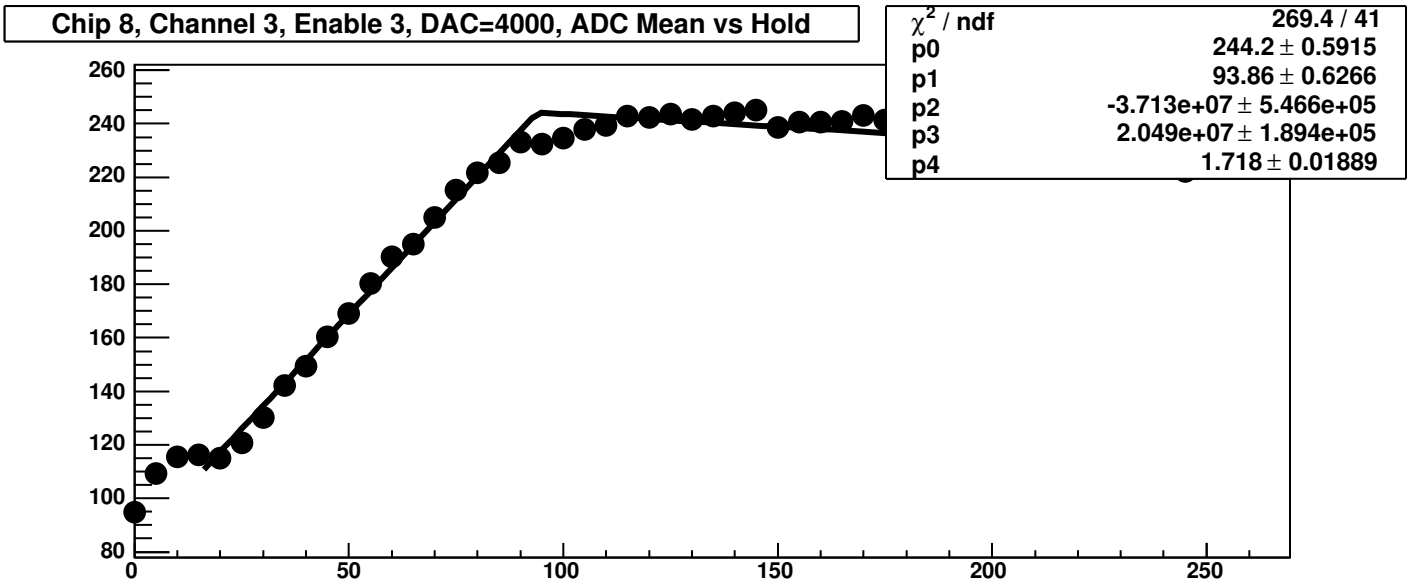


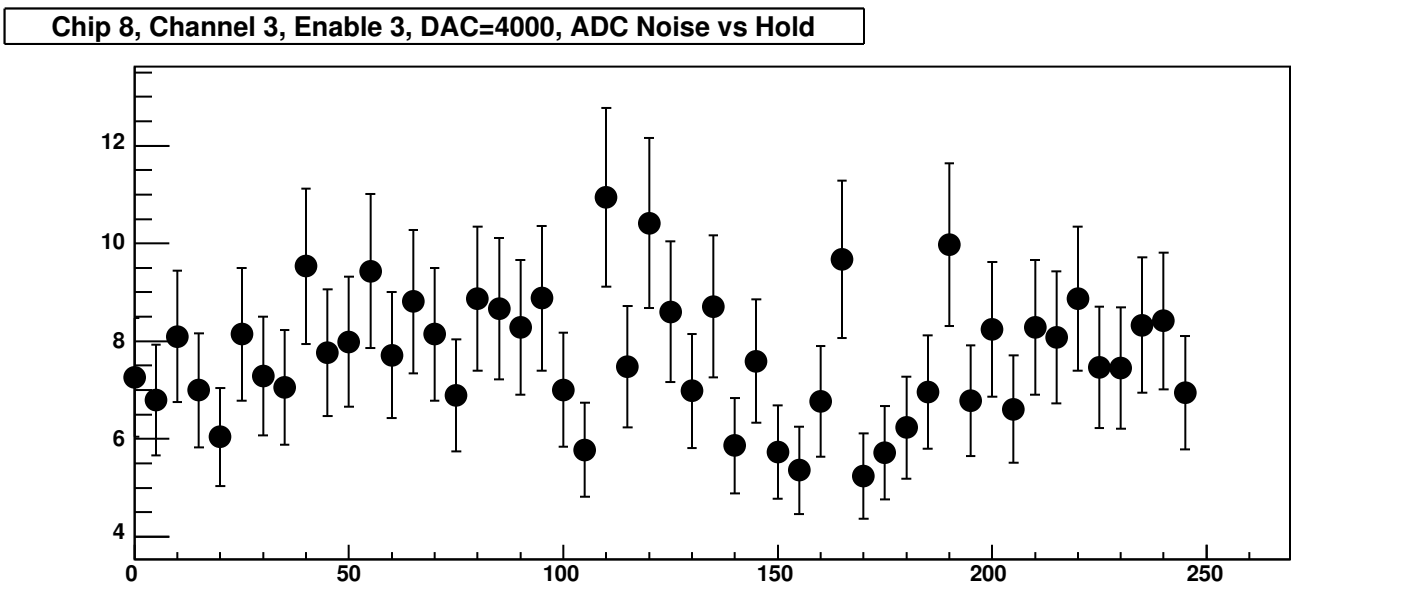
Chip 8, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold

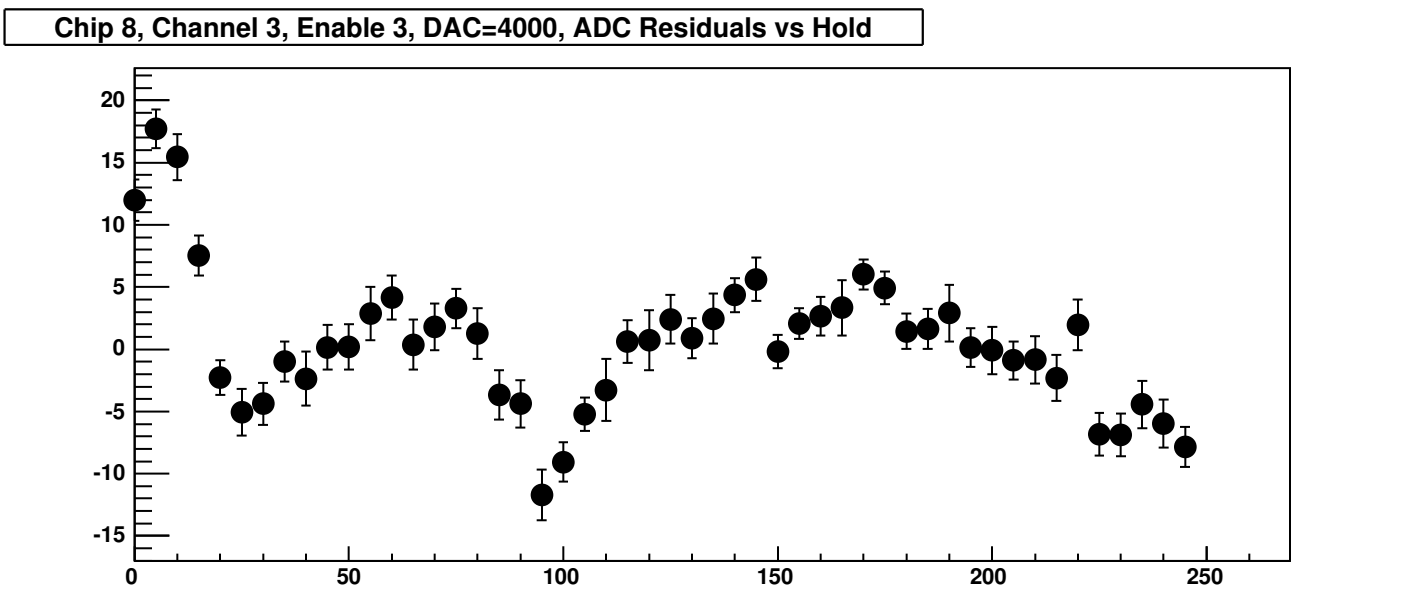


Chip 8, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

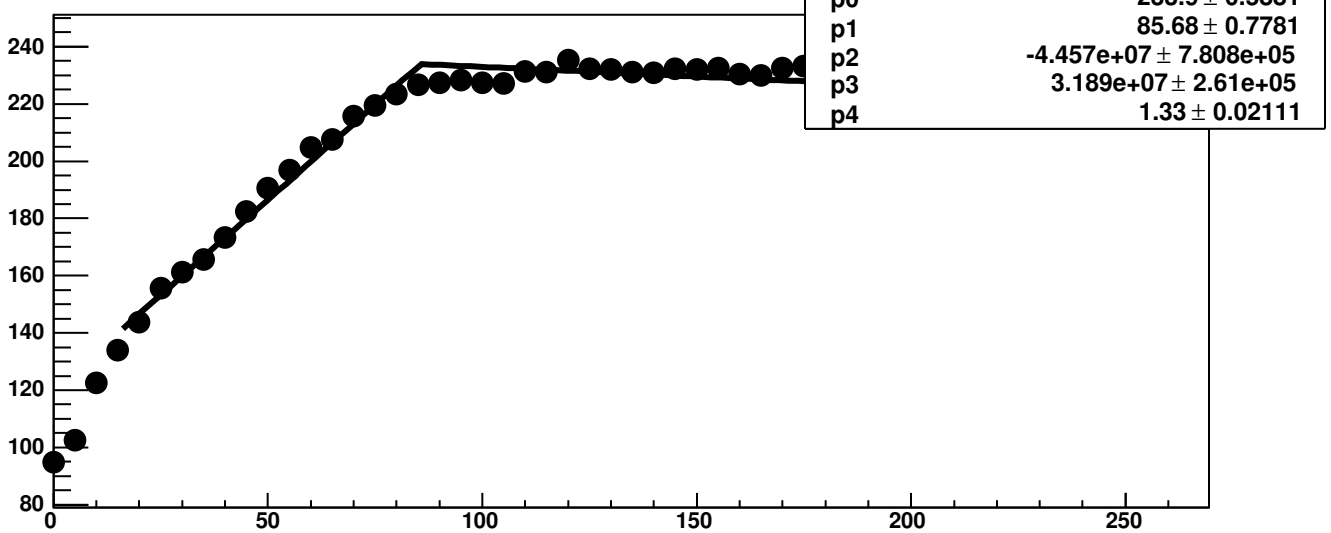



Chip 8, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 269.4 / 41
p0 244.2 ± 0.5915
p1 93.86 ± 0.6266
p2 -3.713e+07 ± 5.466e+05
p3 2.049e+07 ± 1.894e+05
p4 1.718 ± 0.01889

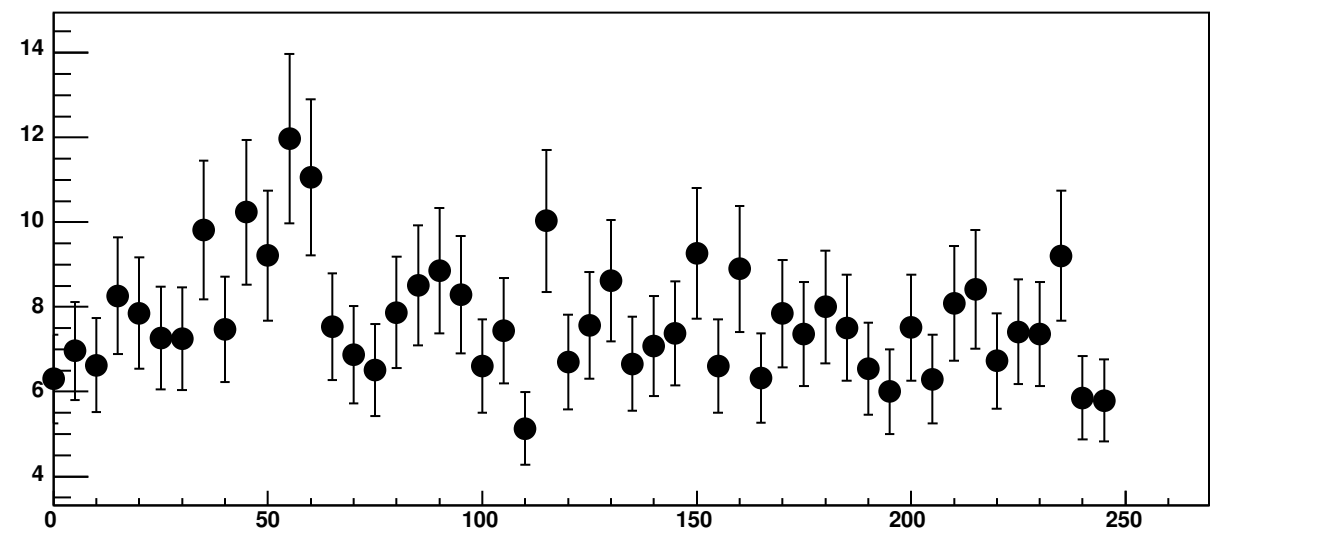

Chip 8, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold


Chip 8, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

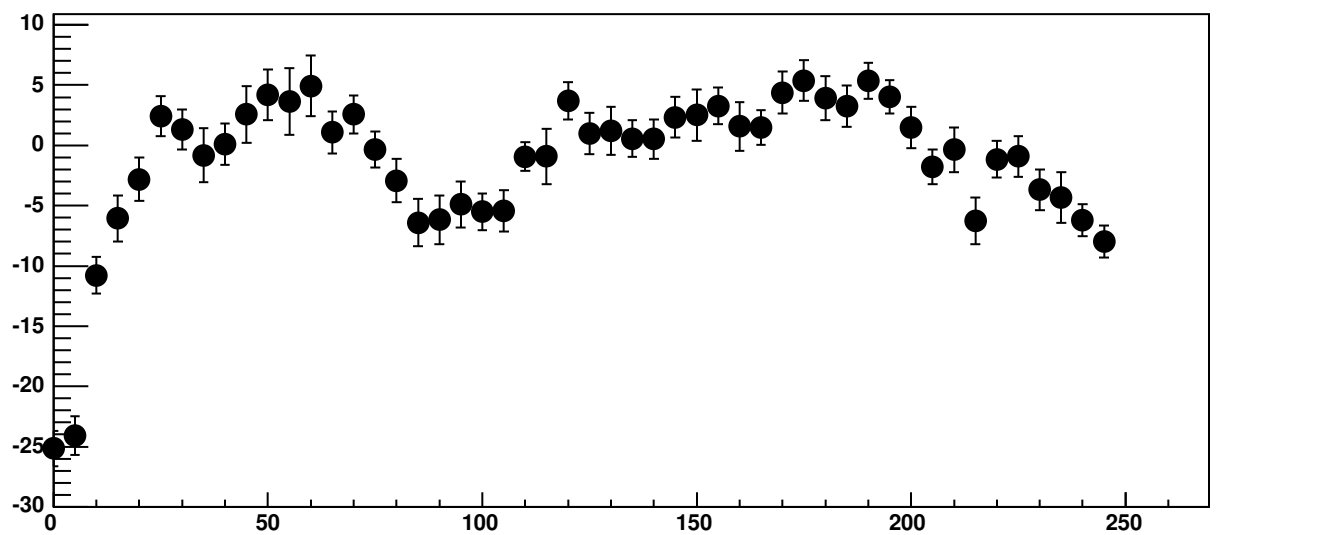
Chip 8, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold

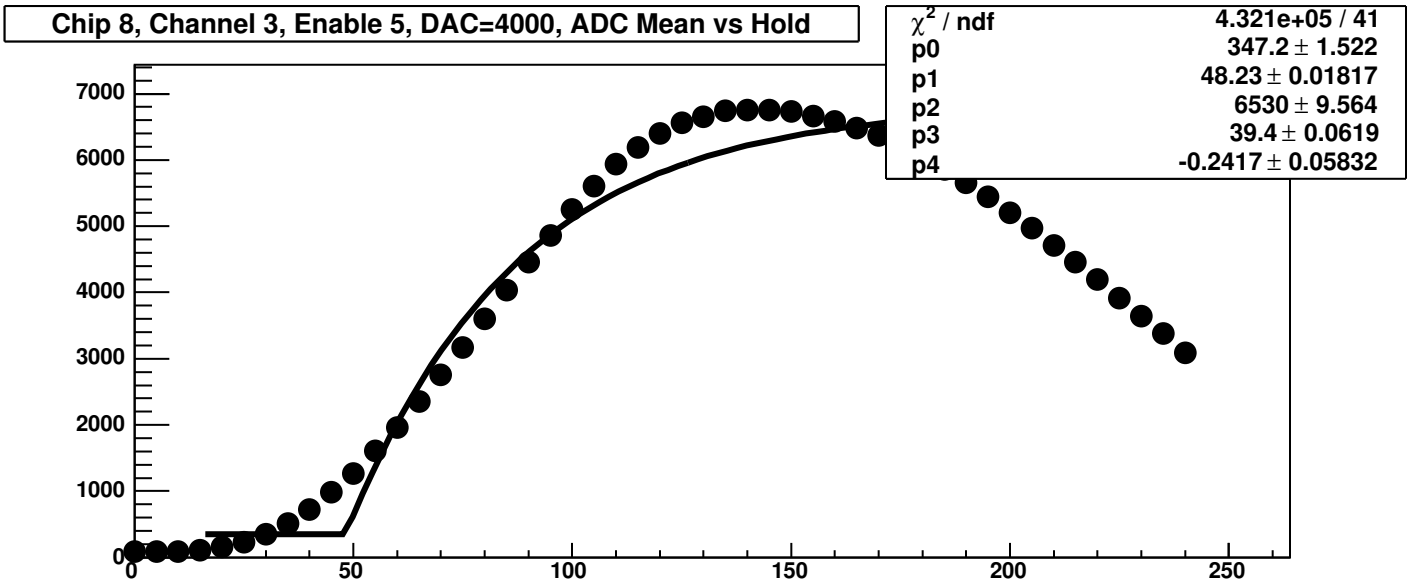


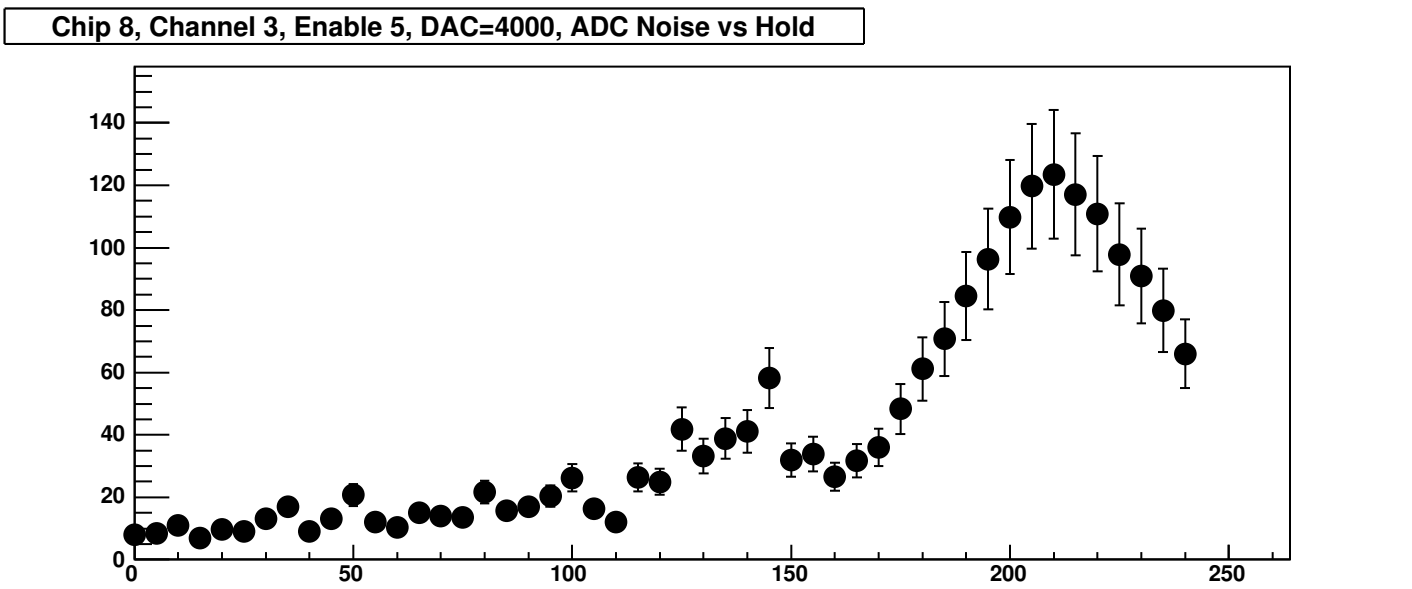
Chip 8, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold

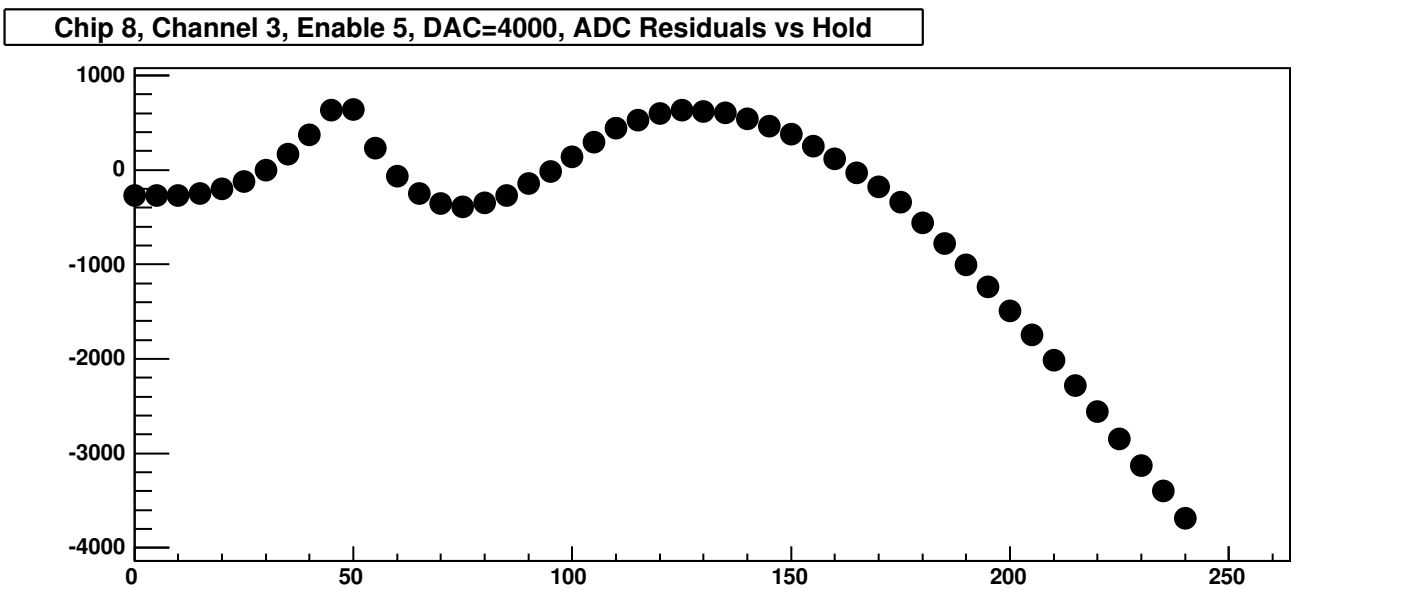


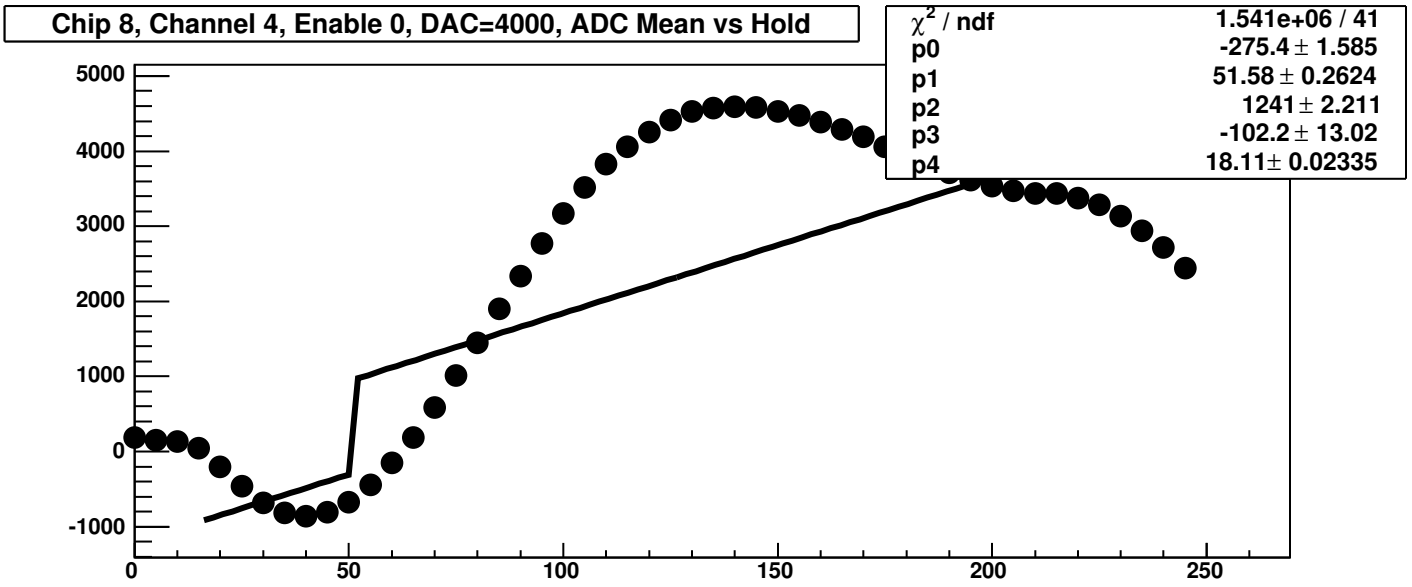
Chip 8, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold

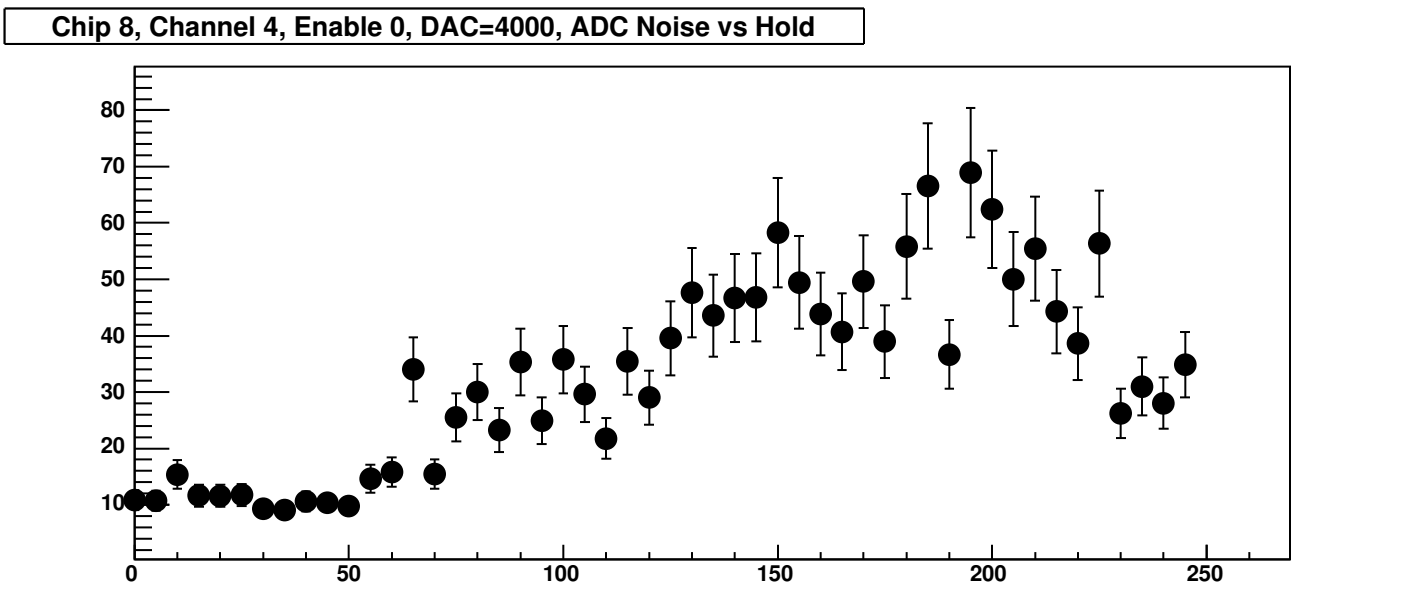


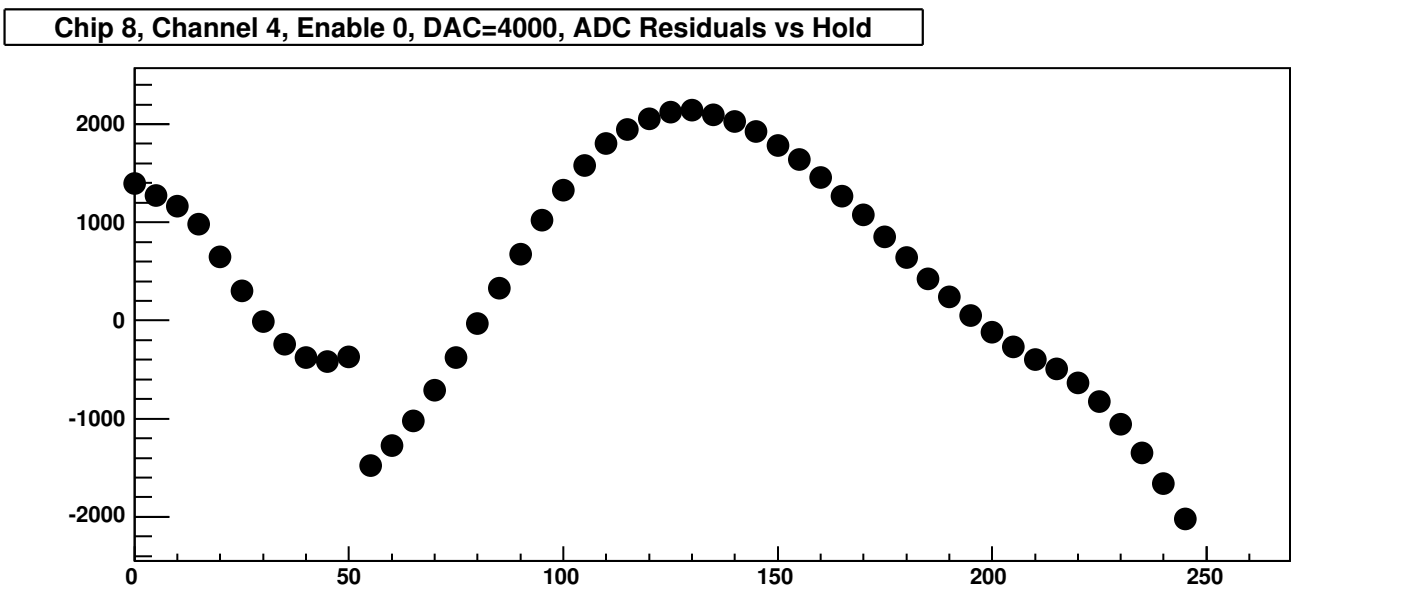

Chip 8, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.321e+05 / 41
p0 347.2 ± 1.522
p1 48.23 ± 0.01817
p2 6530 ± 9.564
p3 39.4 ± 0.0619
p4 -0.2417 ± 0.05832
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 8, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250

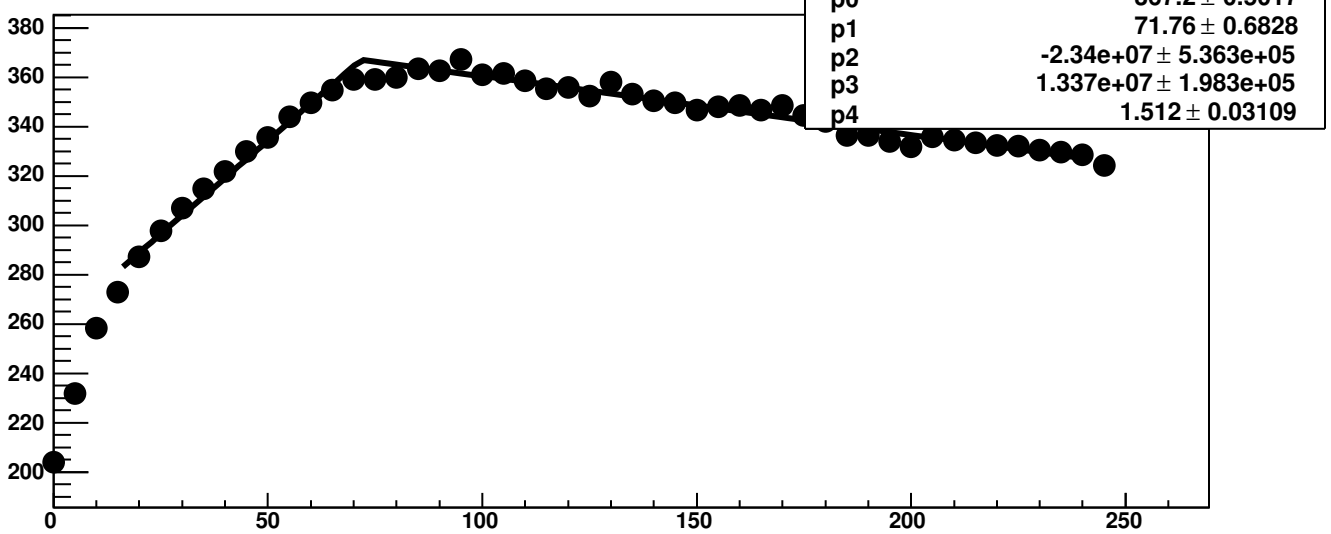

Chip 8, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250


Chip 8, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.541e+06 / 41
p0 -275.4 ± 1.585
p1 51.58 ± 0.2624
p2 1241 ± 2.211
p3 -102.2 ± 13.02
p4 18.11 ± 0.02335
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

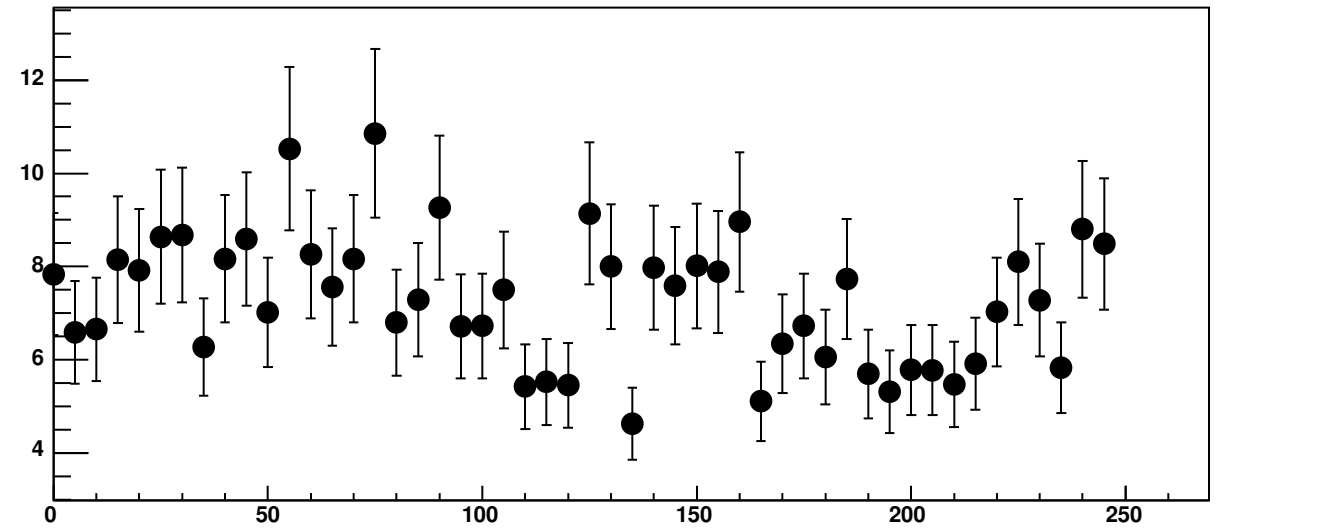

Chip 8, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold
80
70
60
50
40
30
20
10
0
50
100
150
200
250


Chip 8, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
0
50
100
150
200
250

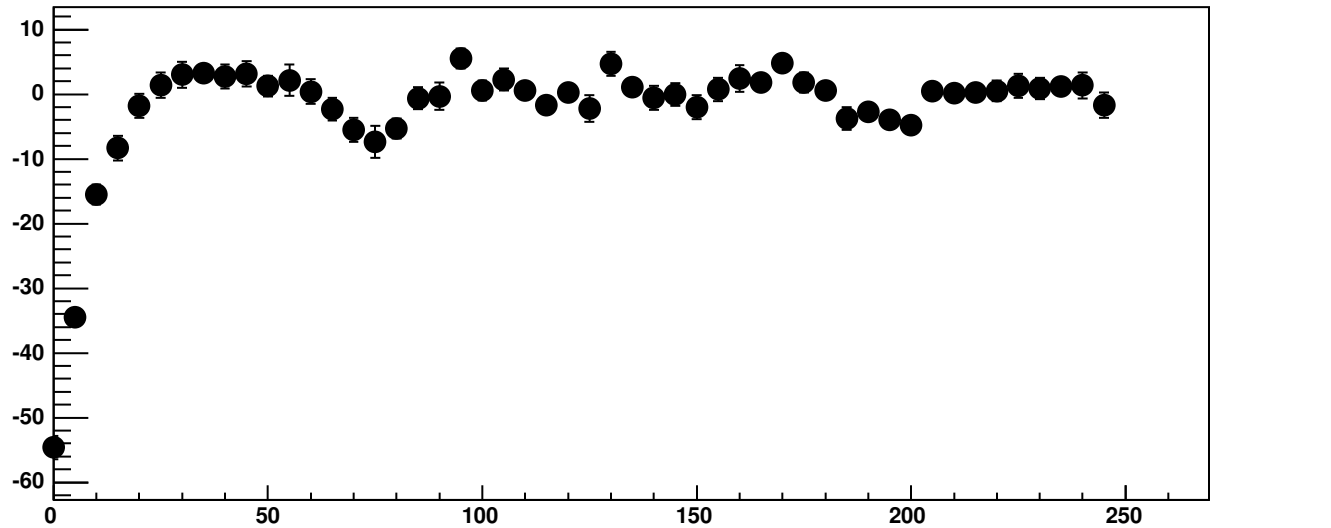
Chip 8, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold



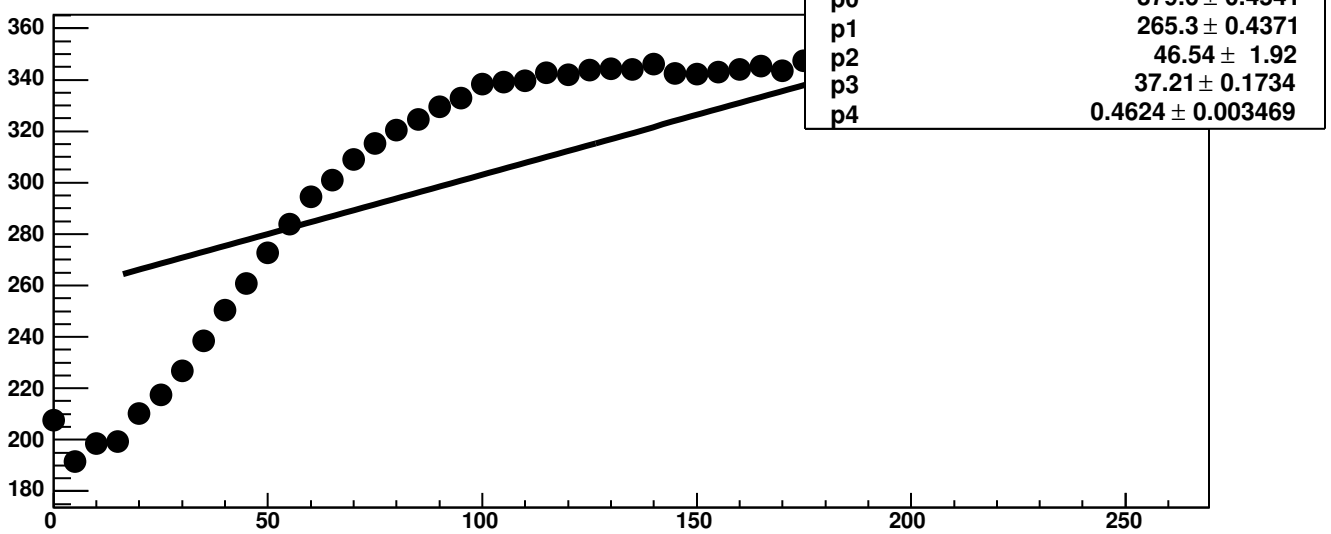
Chip 8, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold



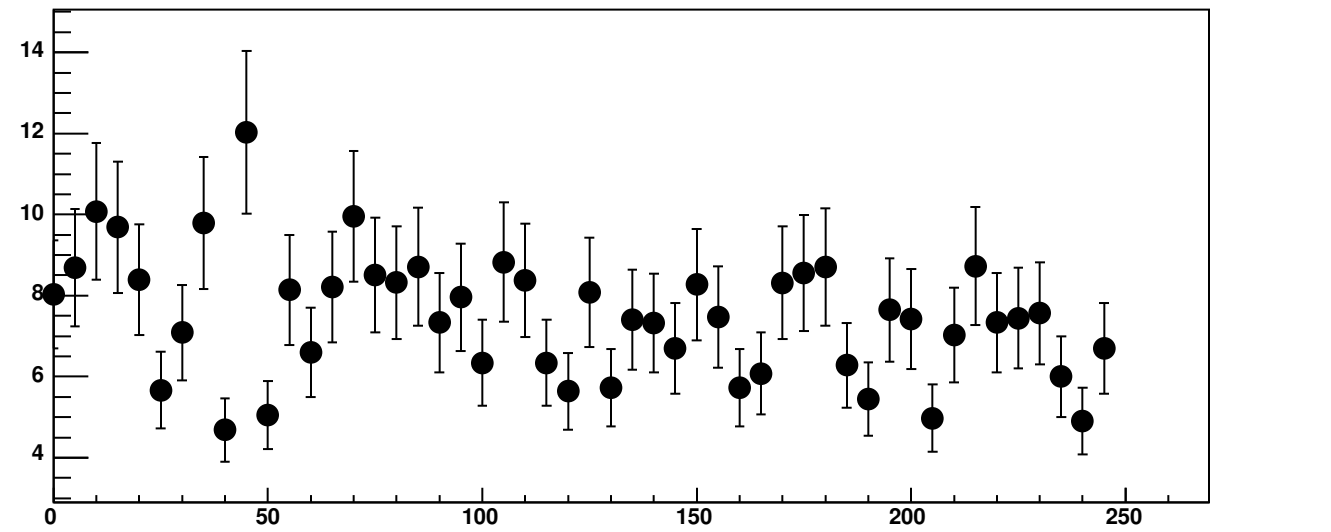
Chip 8, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold



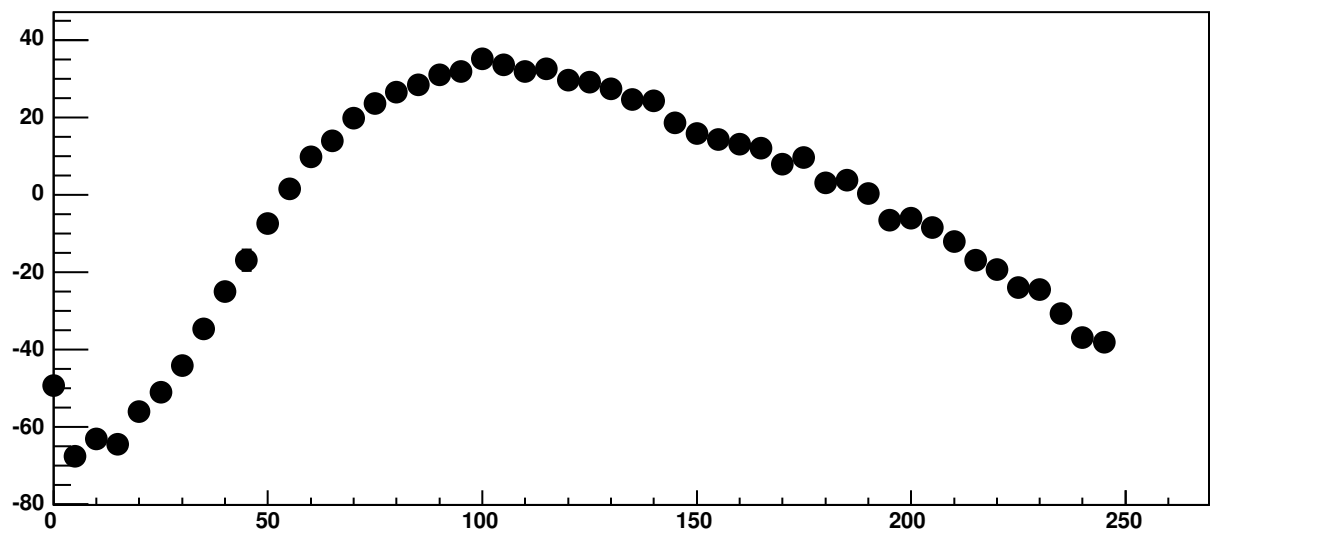
Chip 8, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold



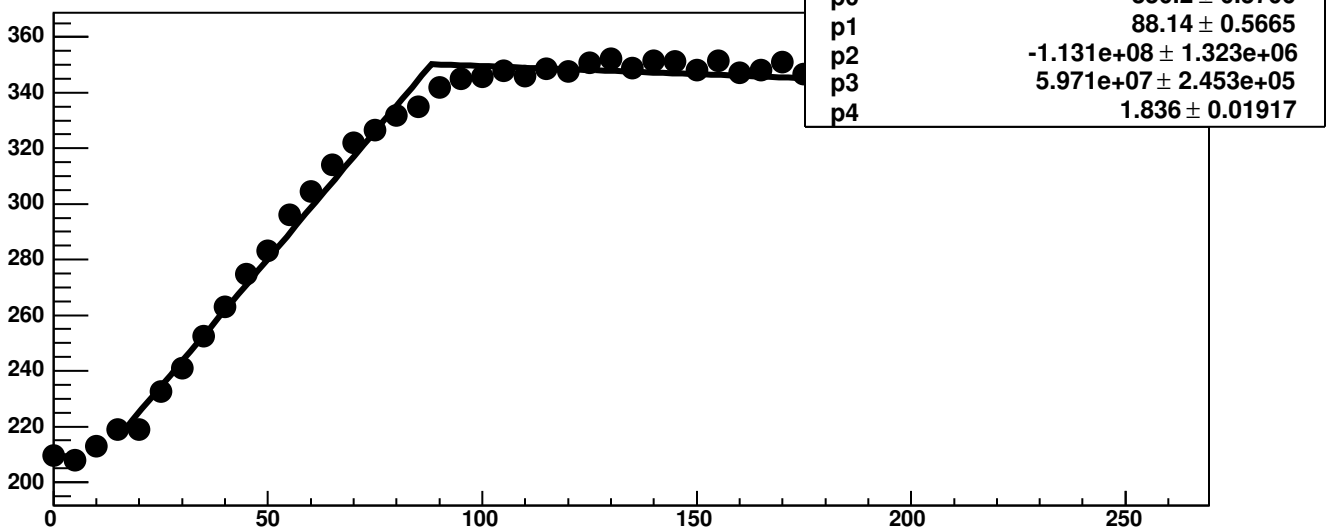
Chip 8, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



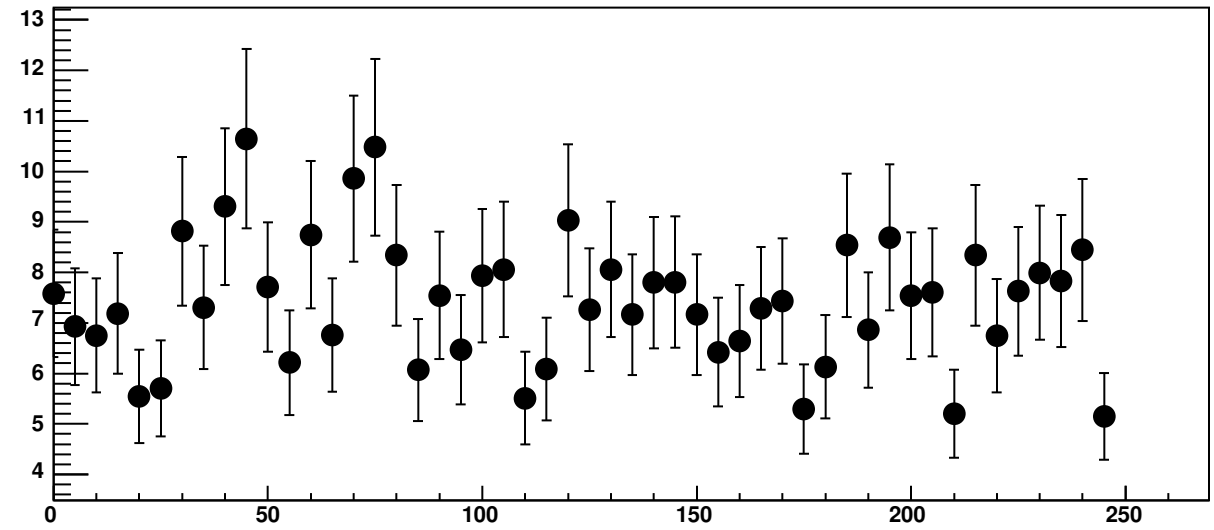
Chip 8, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold



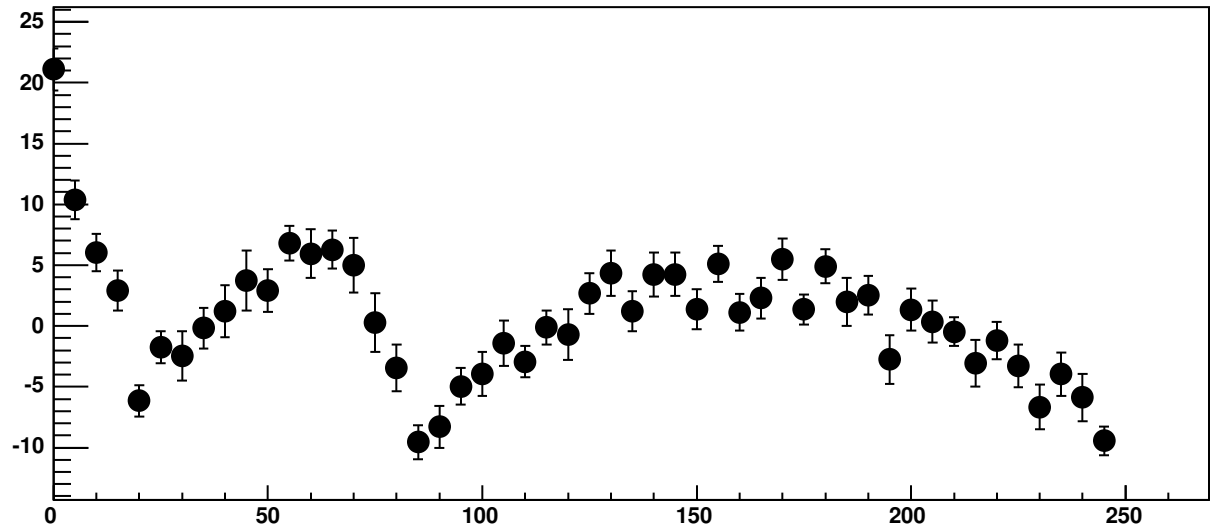
Chip 8, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold



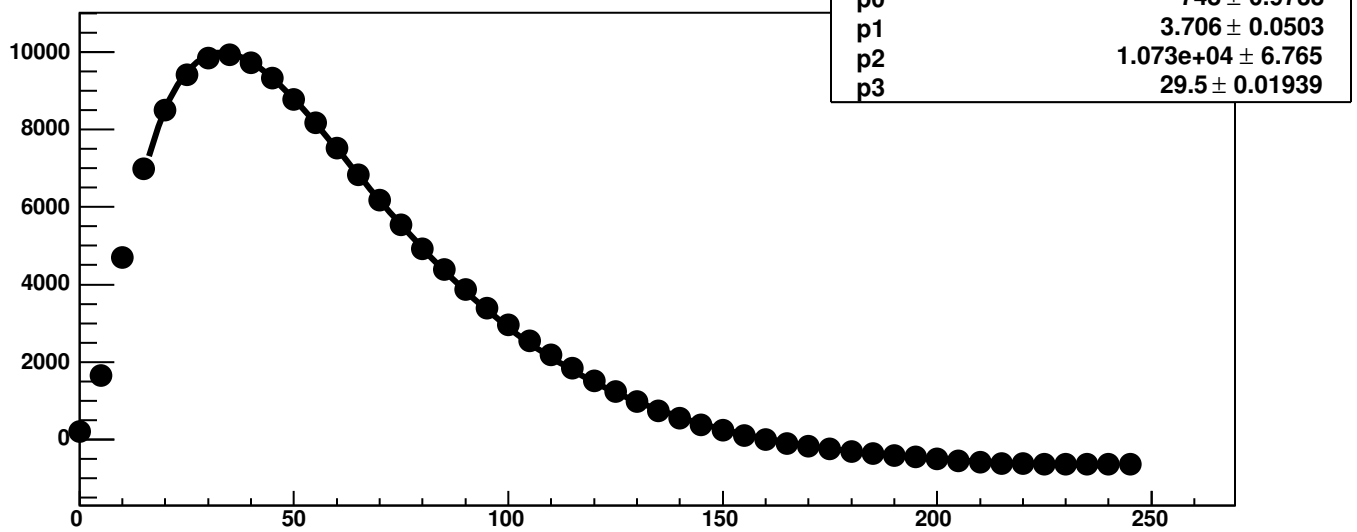
Chip 8, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



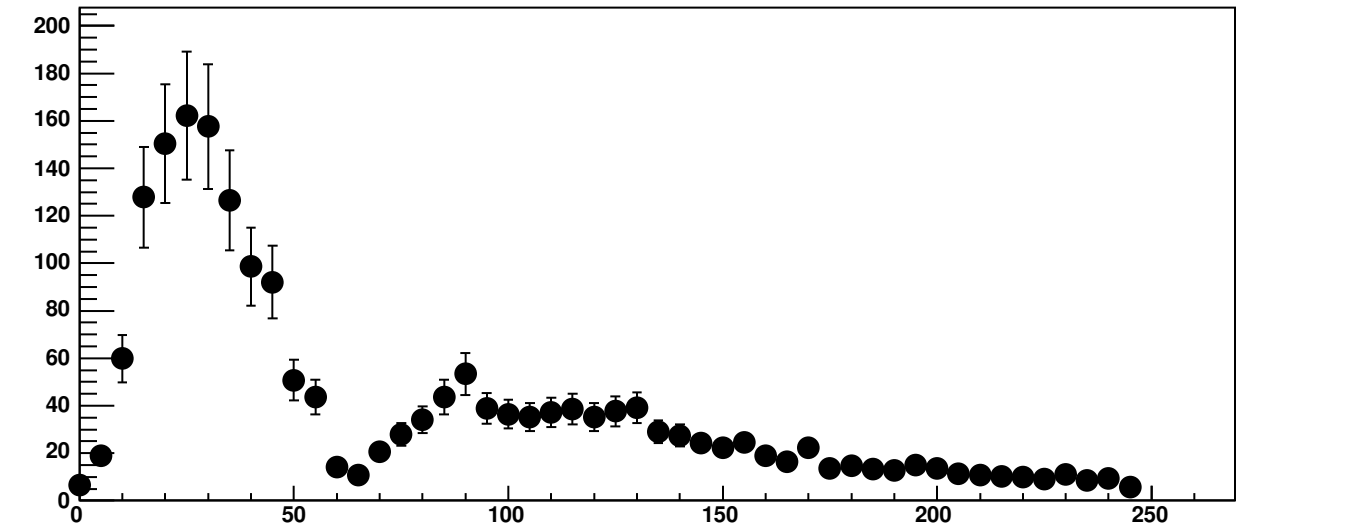
Chip 8, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold



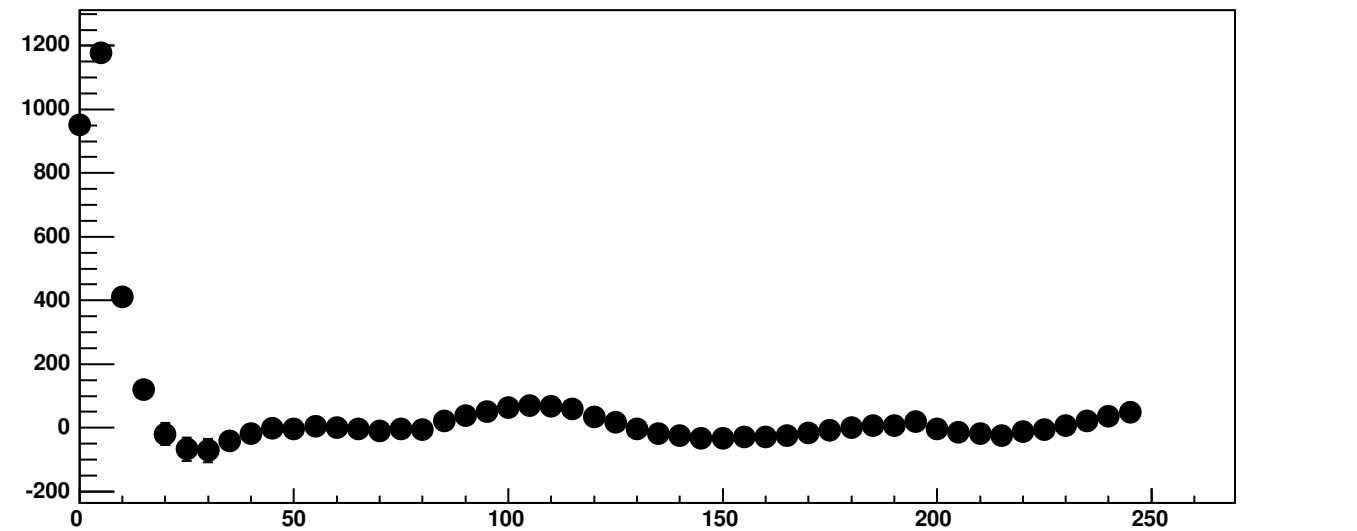
Chip 8, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold

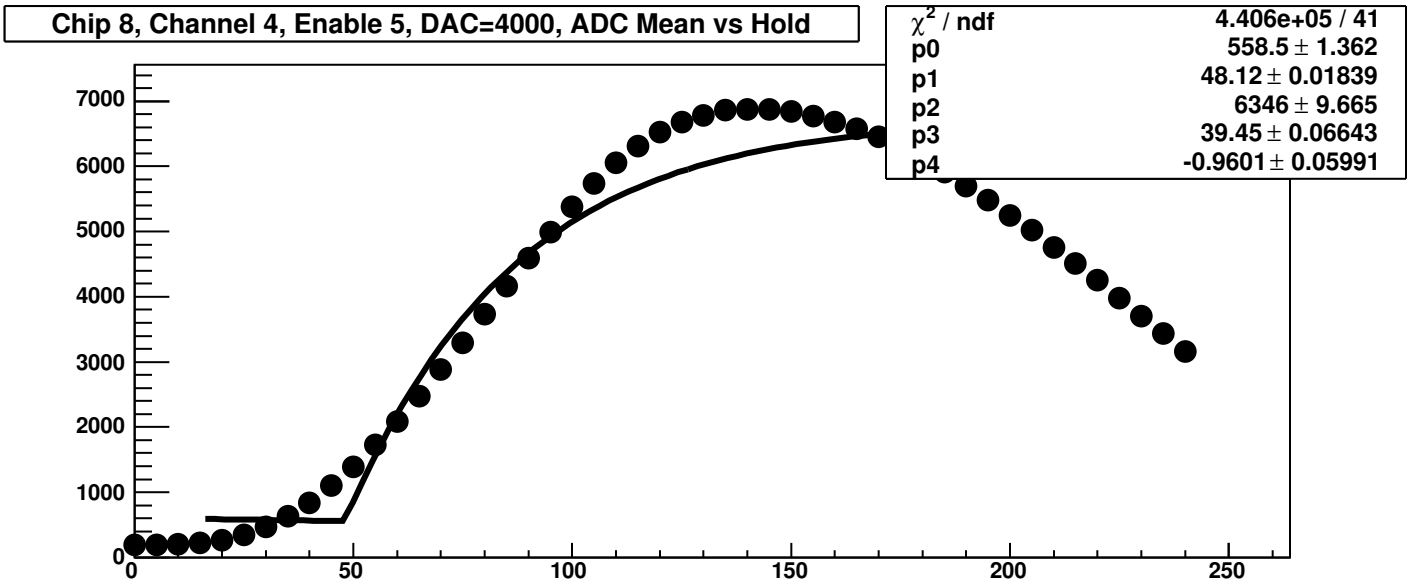


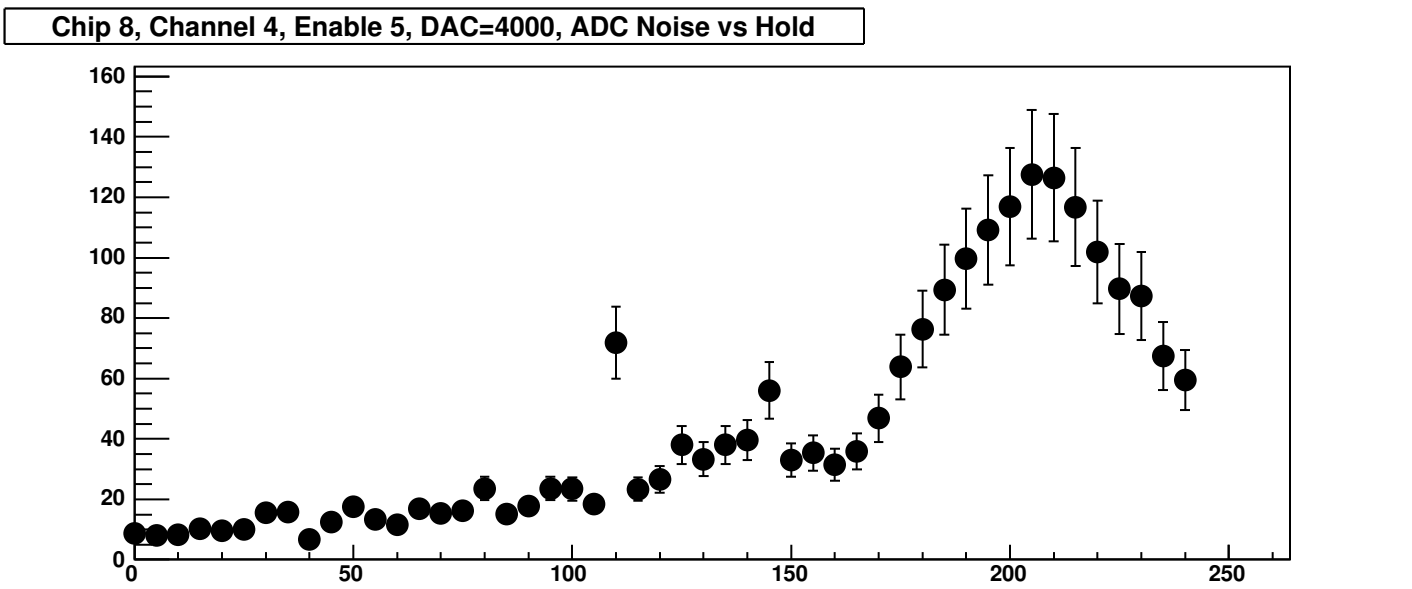
Chip 8, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold

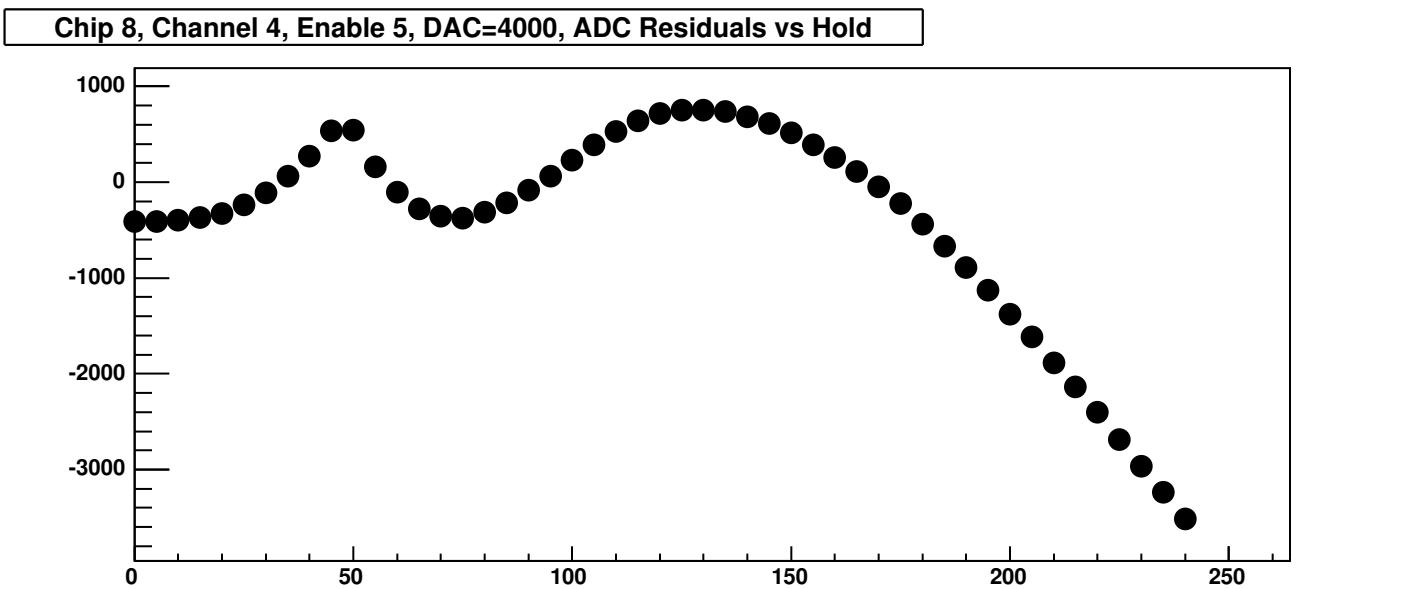


Chip 8, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold

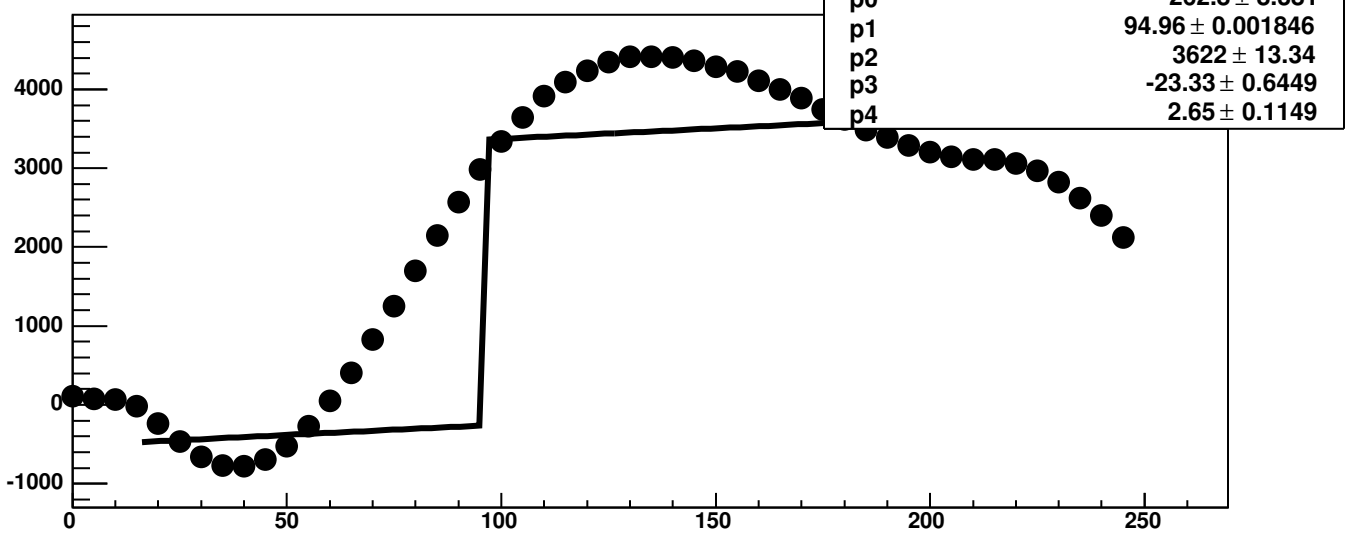



Chip 8, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.406e+05 / 41
p0 558.5 ± 1.362
p1 48.12 ± 0.01839
p2 6346 ± 9.665
p3 39.45 ± 0.06643
p4 -0.9601 ± 0.05991
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

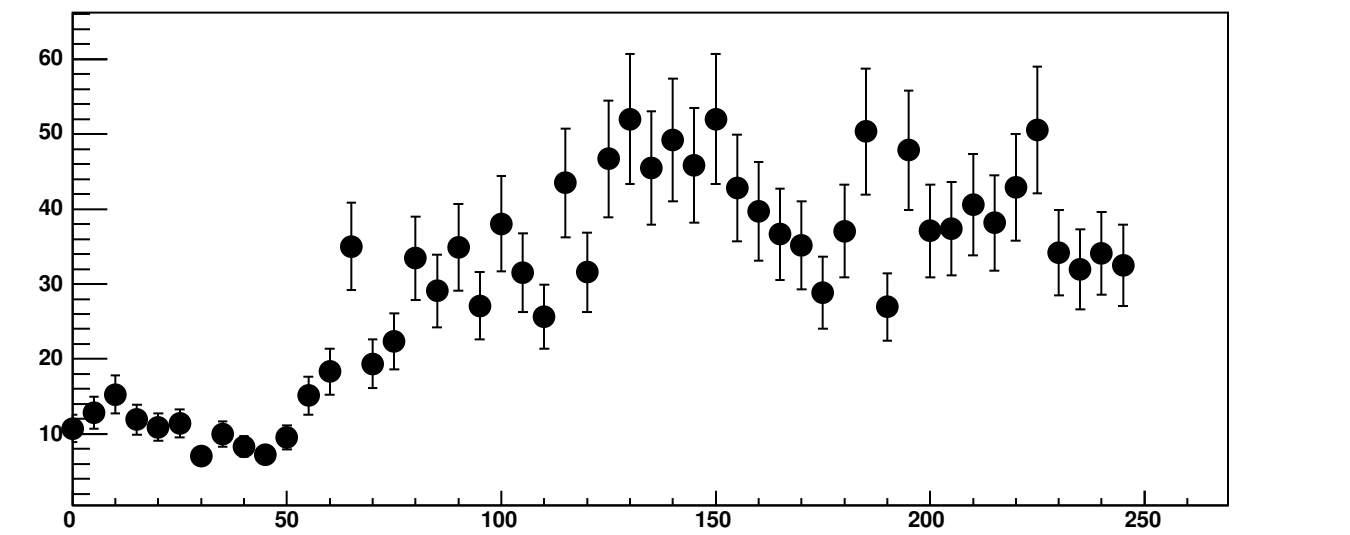

Chip 8, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 8, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

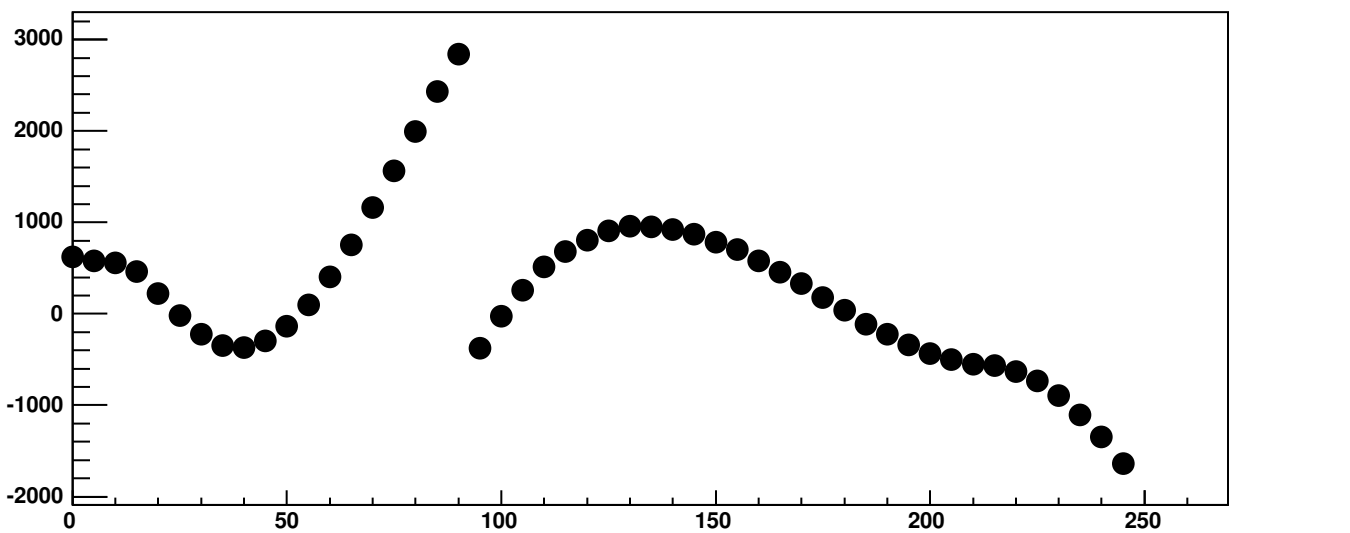
Chip 8, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold



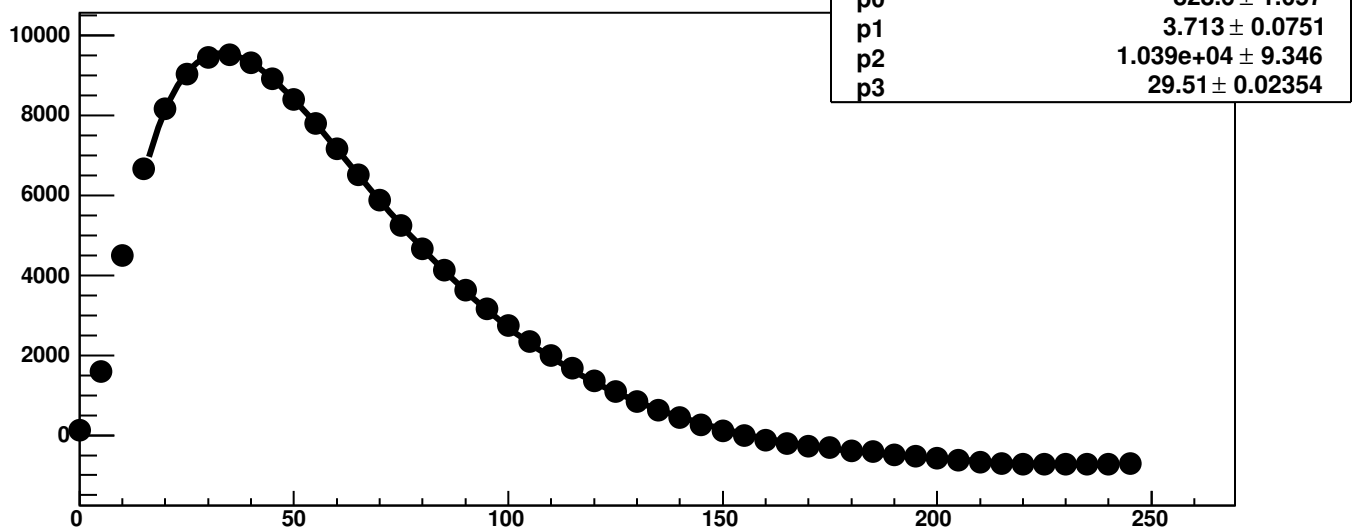
Chip 8, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



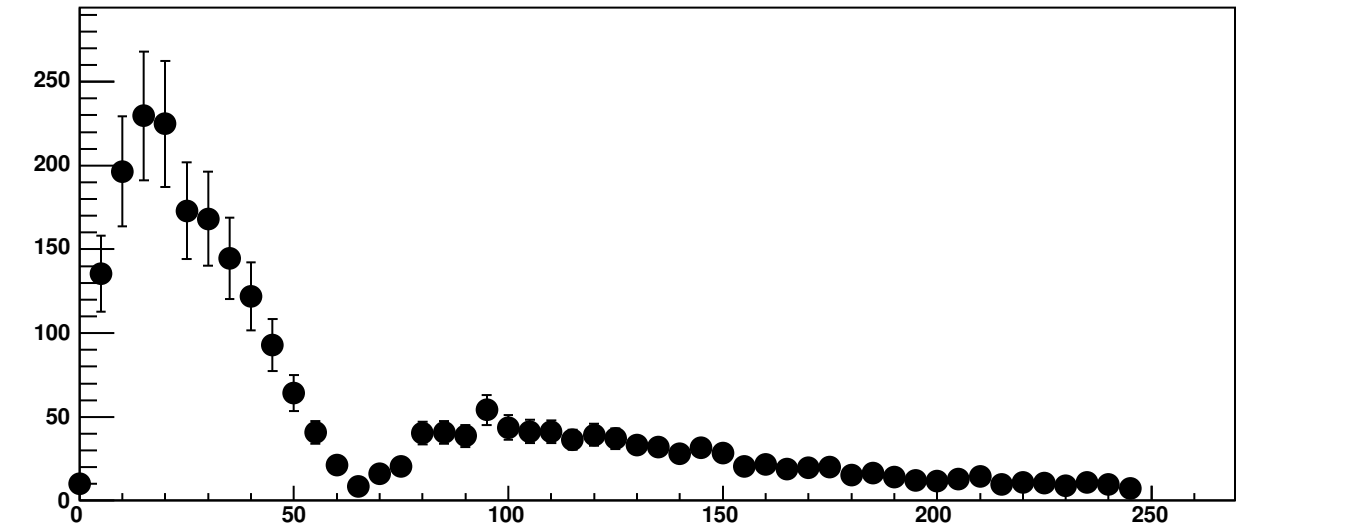
Chip 8, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold



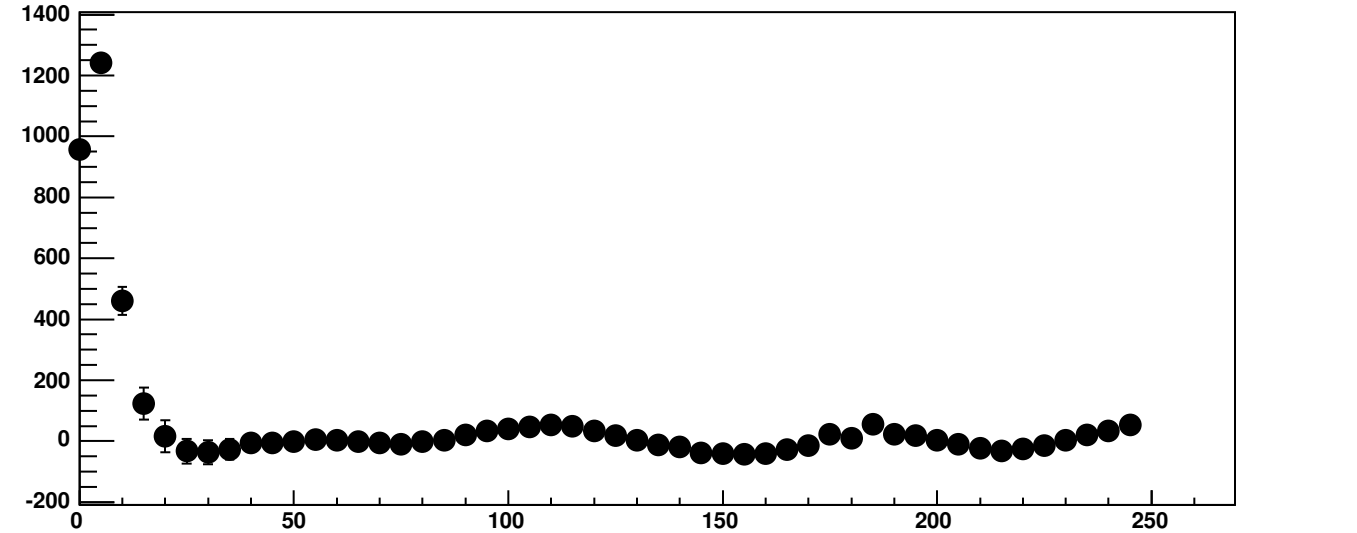
Chip 8, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold

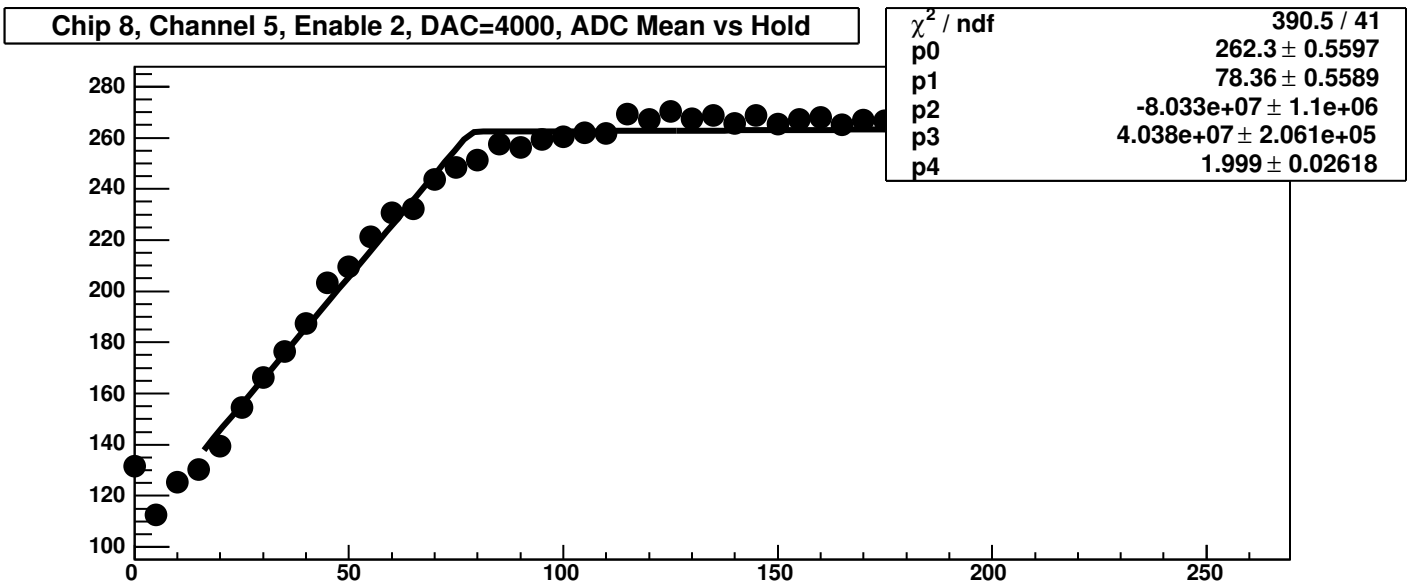


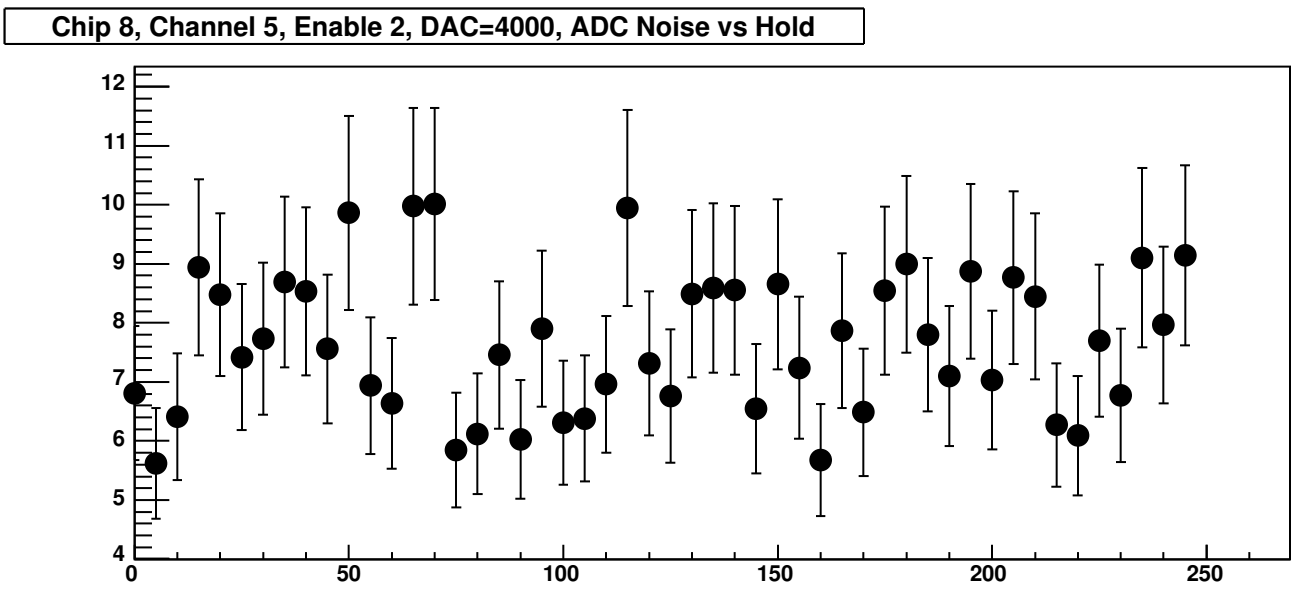
Chip 8, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold

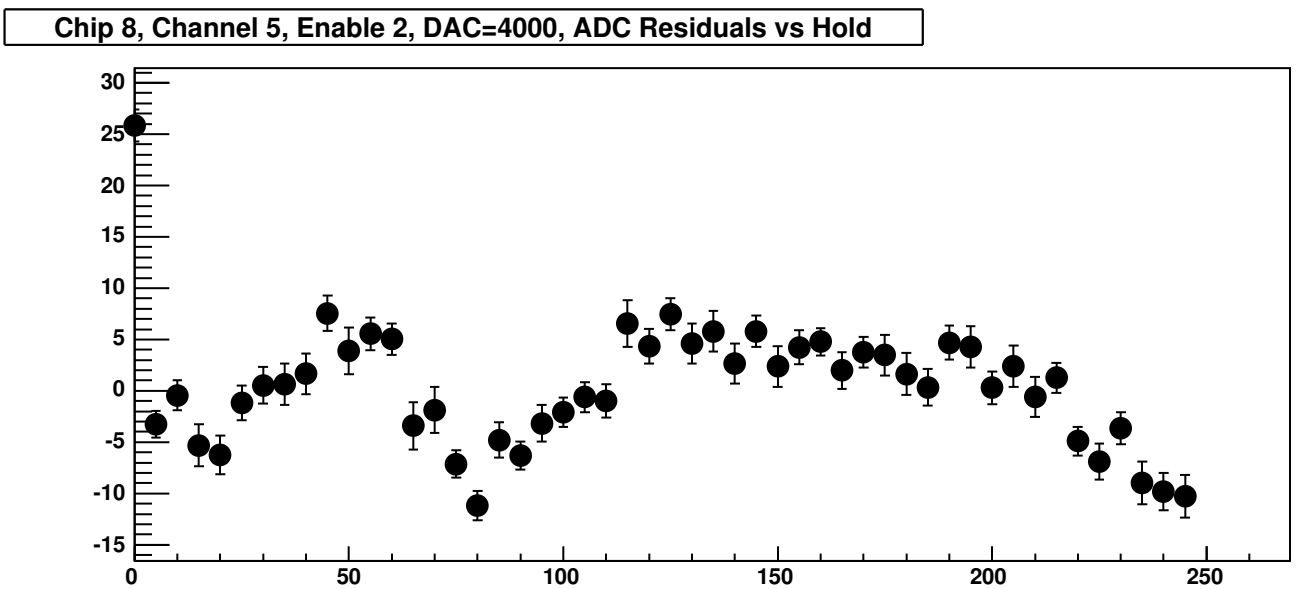


Chip 8, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold

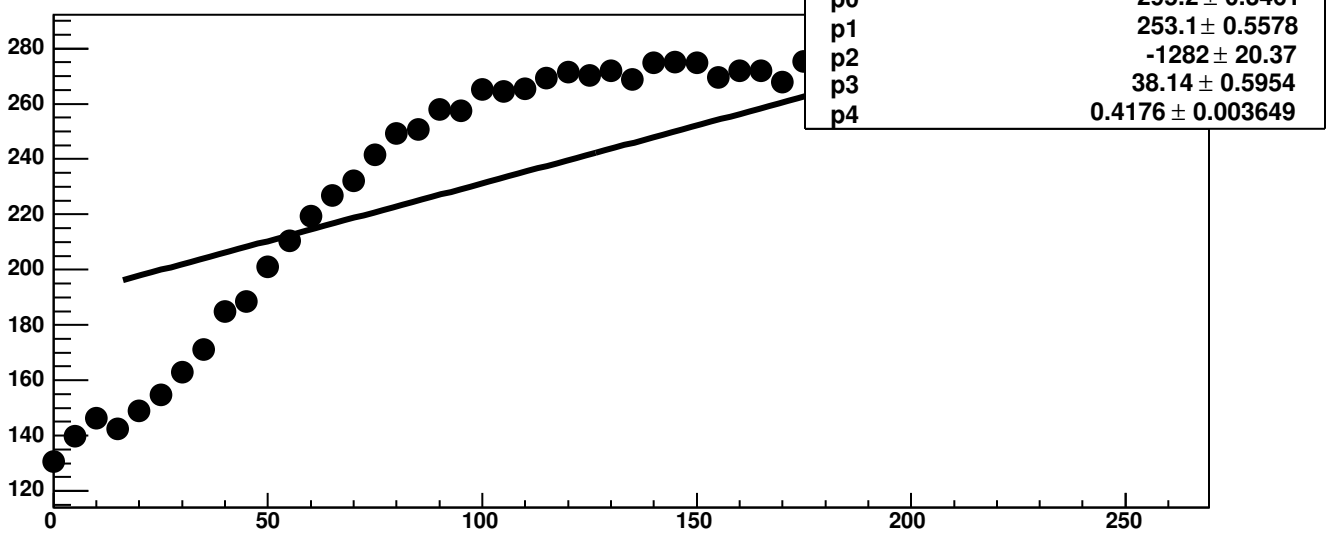



Chip 8, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 390.5 / 41
p0 262.3 ± 0.5597
p1 78.36 ± 0.5589
p2 -8.033e+07 ± 1.1e+06
p3 4.038e+07 ± 2.061e+05
p4 1.999 ± 0.02618
280
260
240
220
200
180
160
140
120
100
0
50
100
150
200
250

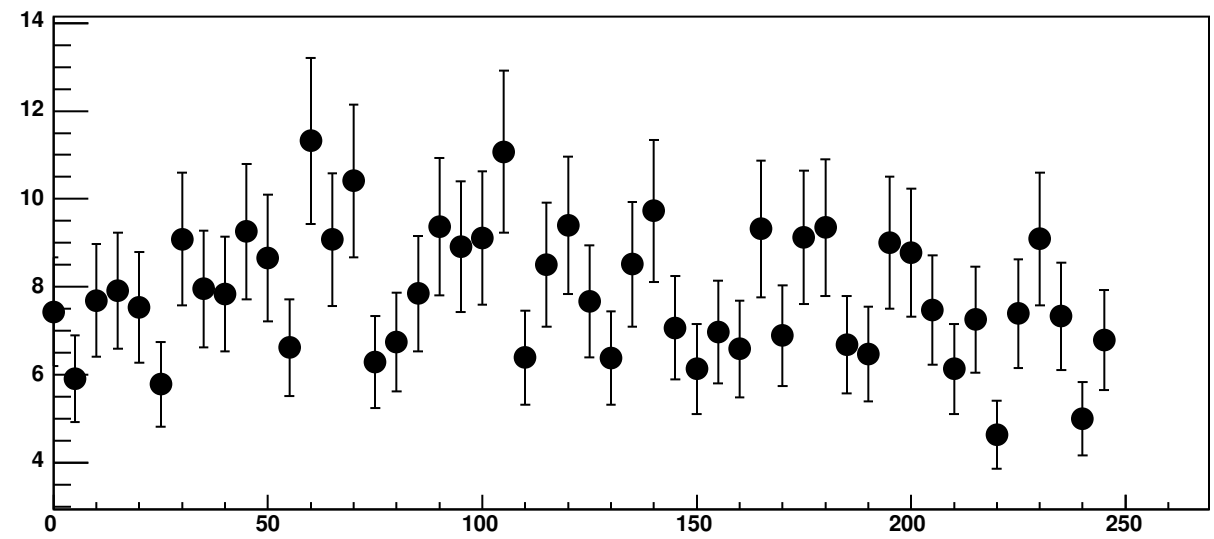

Chip 8, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 8, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

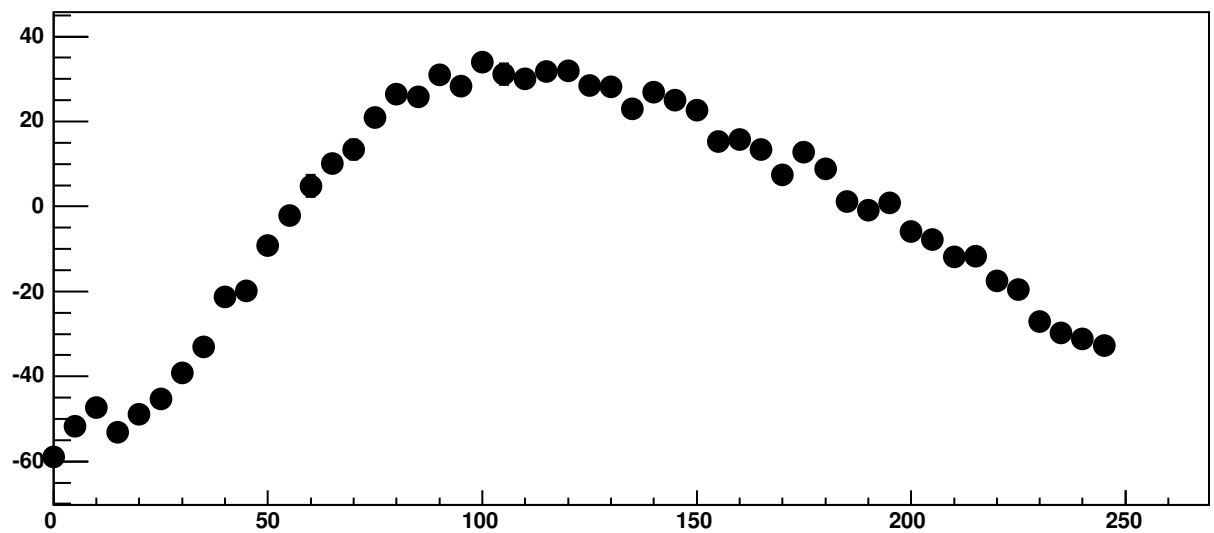
Chip 8, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold



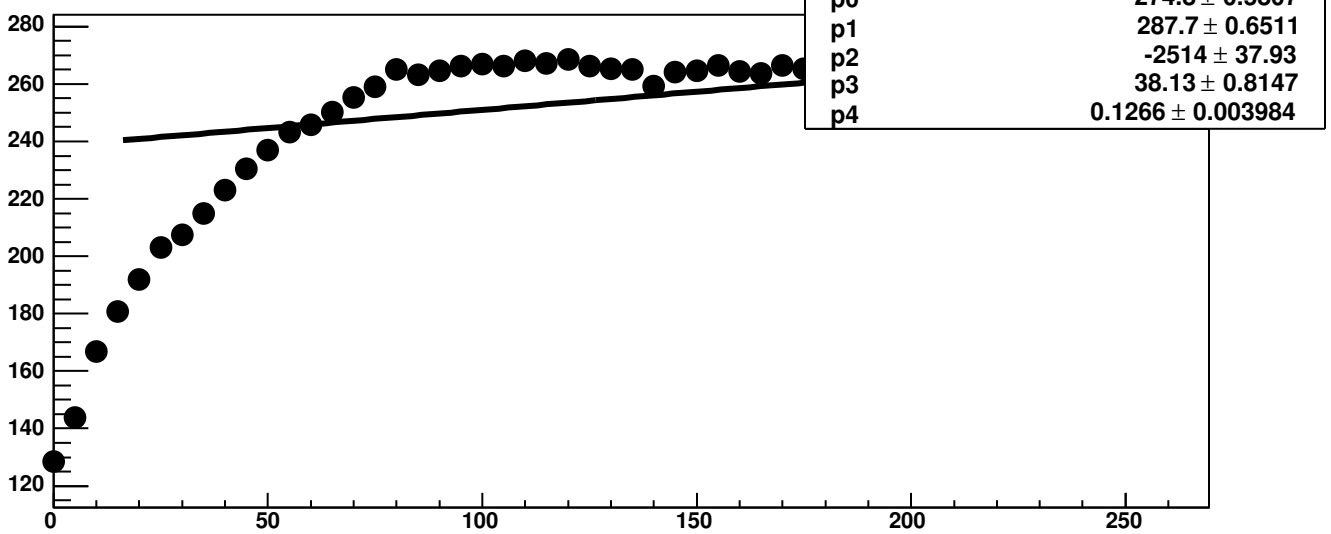
Chip 8, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



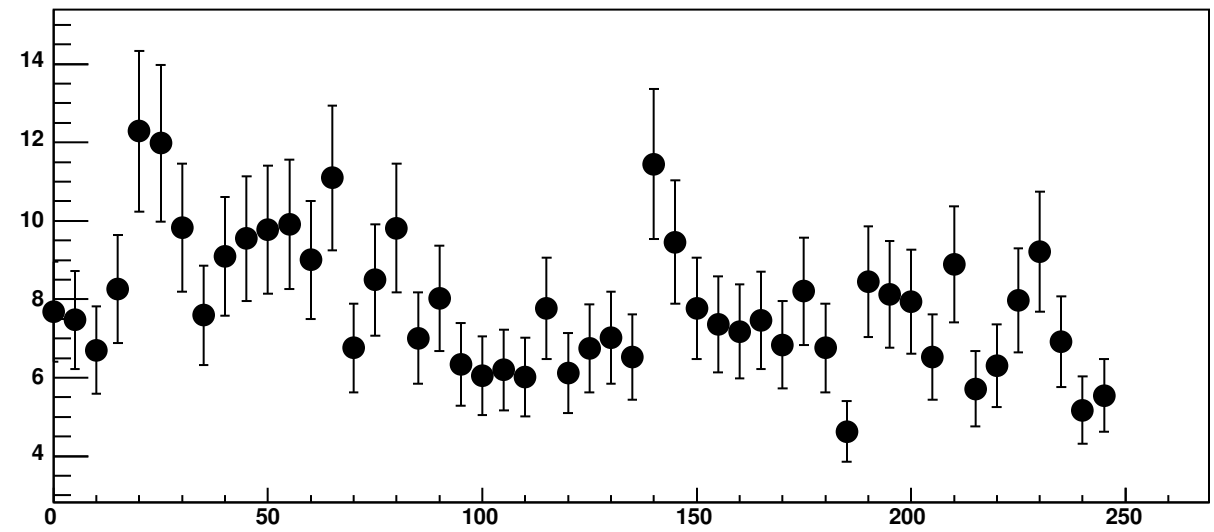
Chip 8, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold



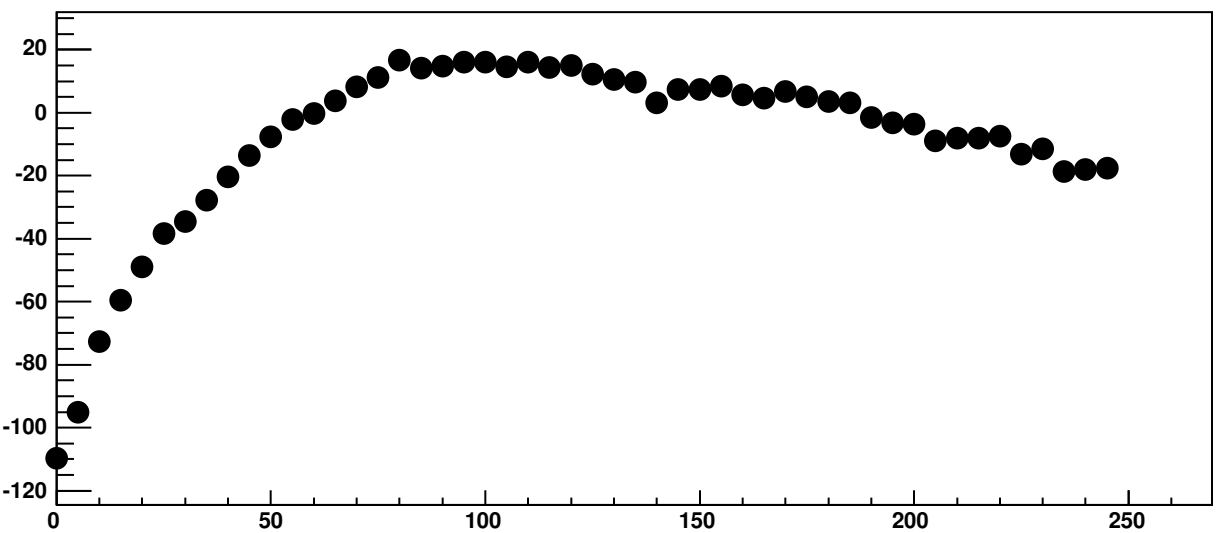
Chip 8, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold

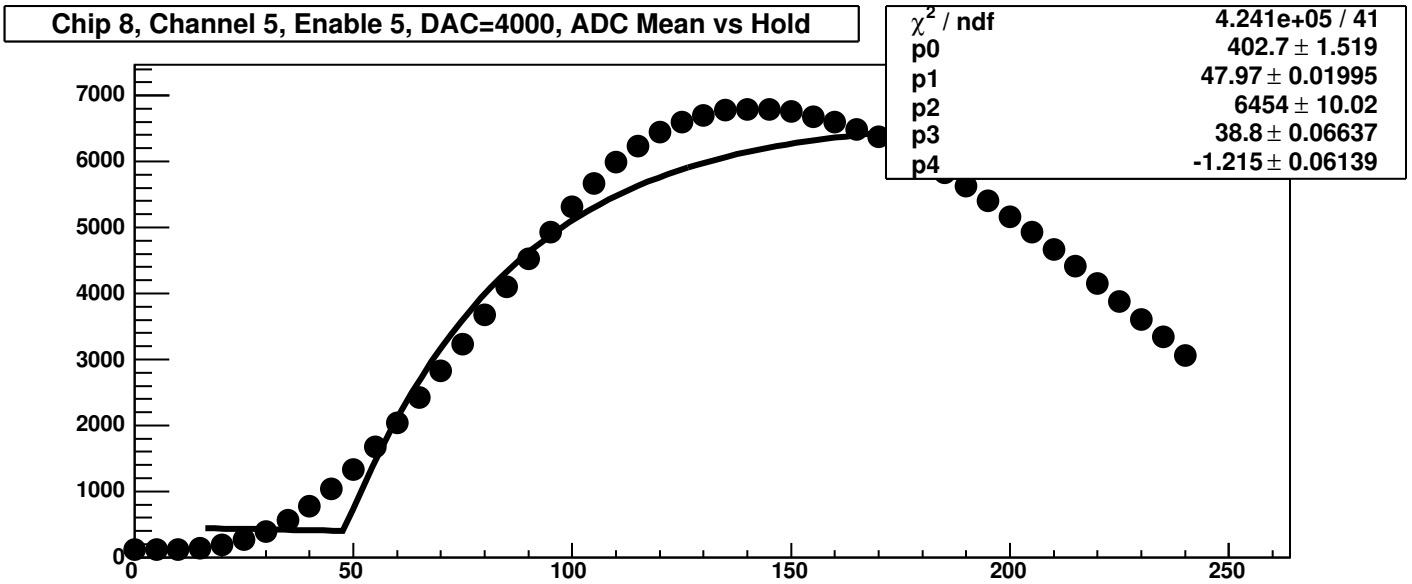


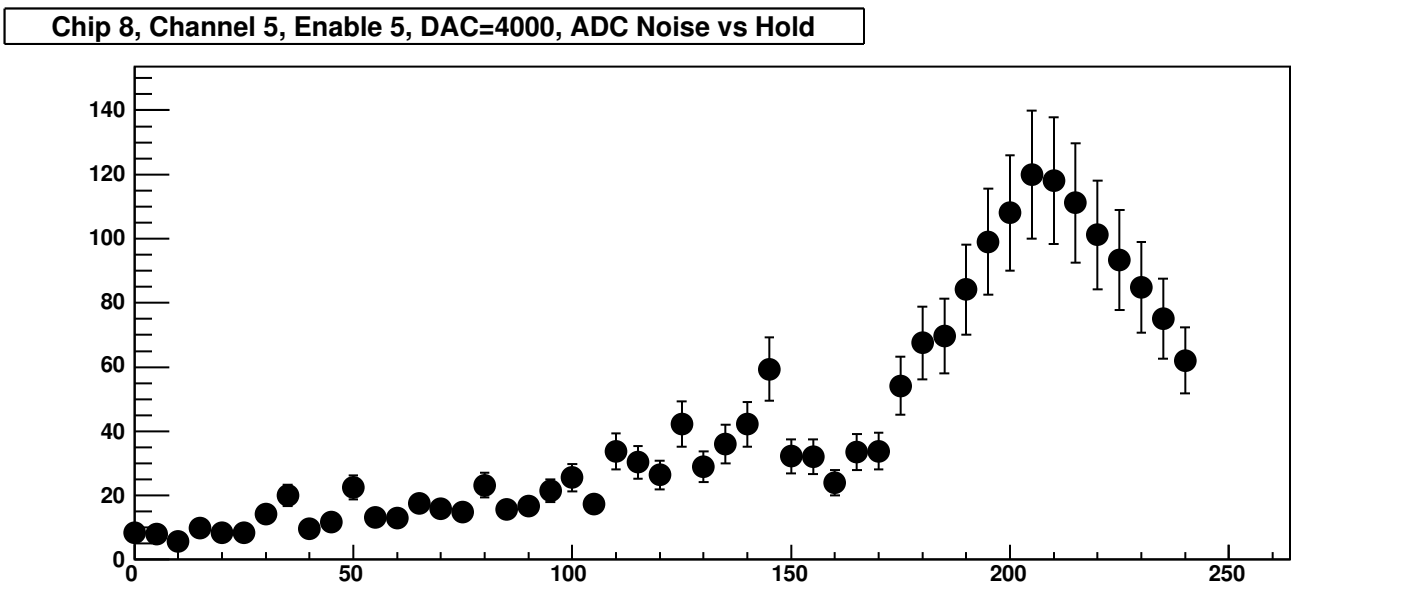
Chip 8, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold

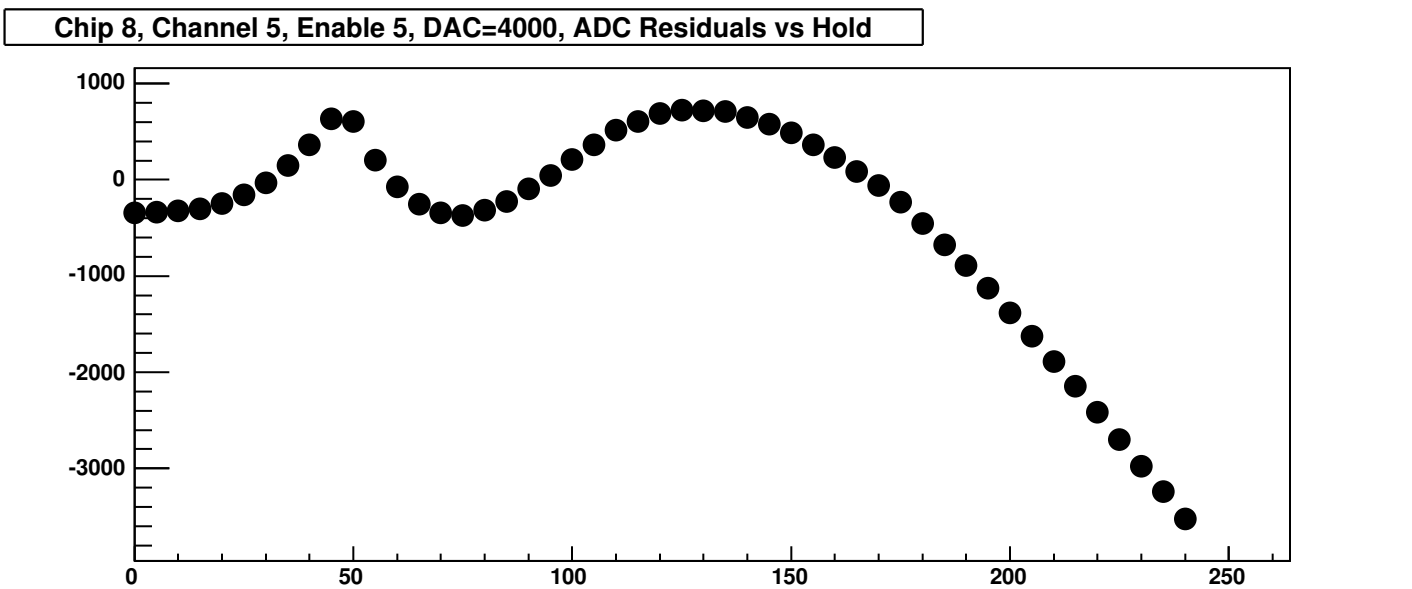


Chip 8, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold

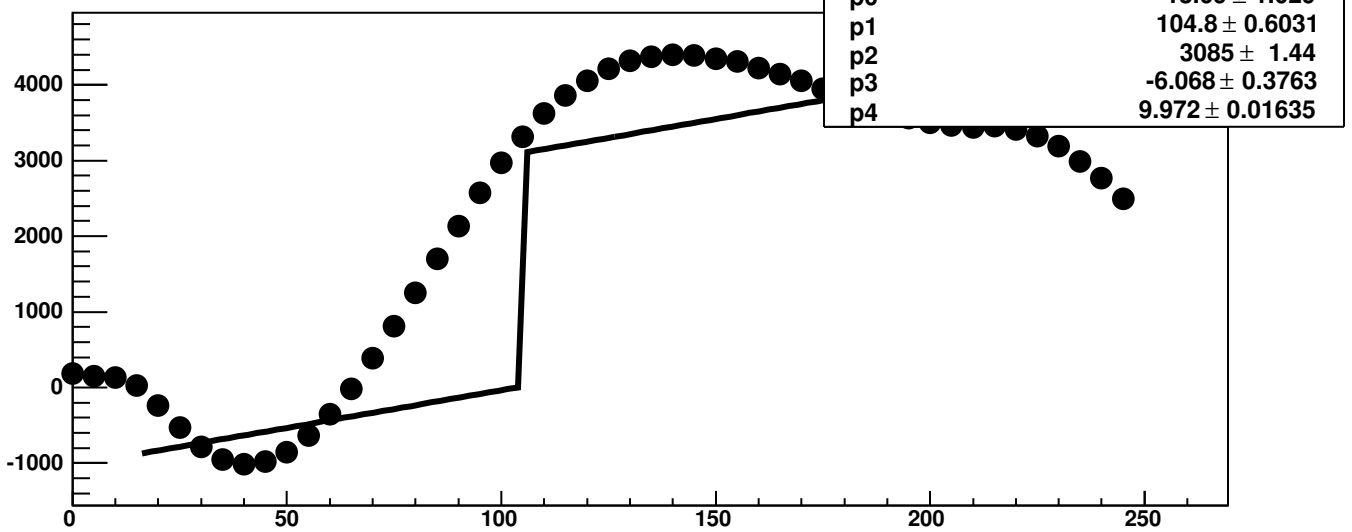



Chip 8, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.241e+05 / 41
p0 402.7 ± 1.519
p1 47.97 ± 0.01995
p2 6454 ± 10.02
p3 38.8 ± 0.06637
p4 -1.215 ± 0.06139
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

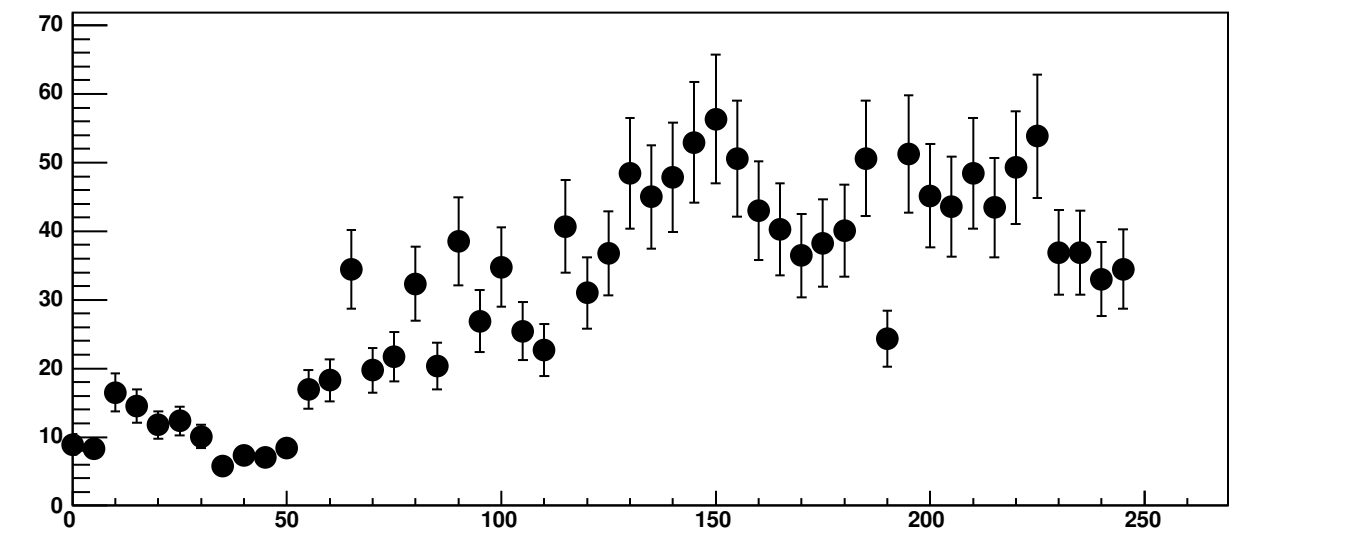

Chip 8, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 8, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

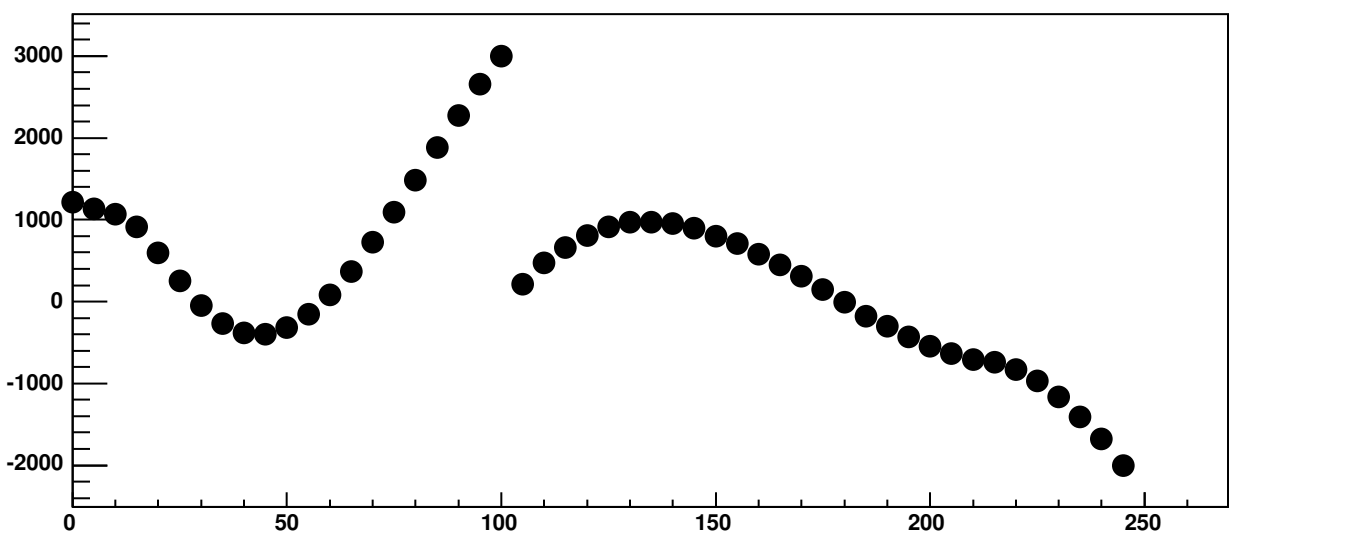
Chip 8, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold

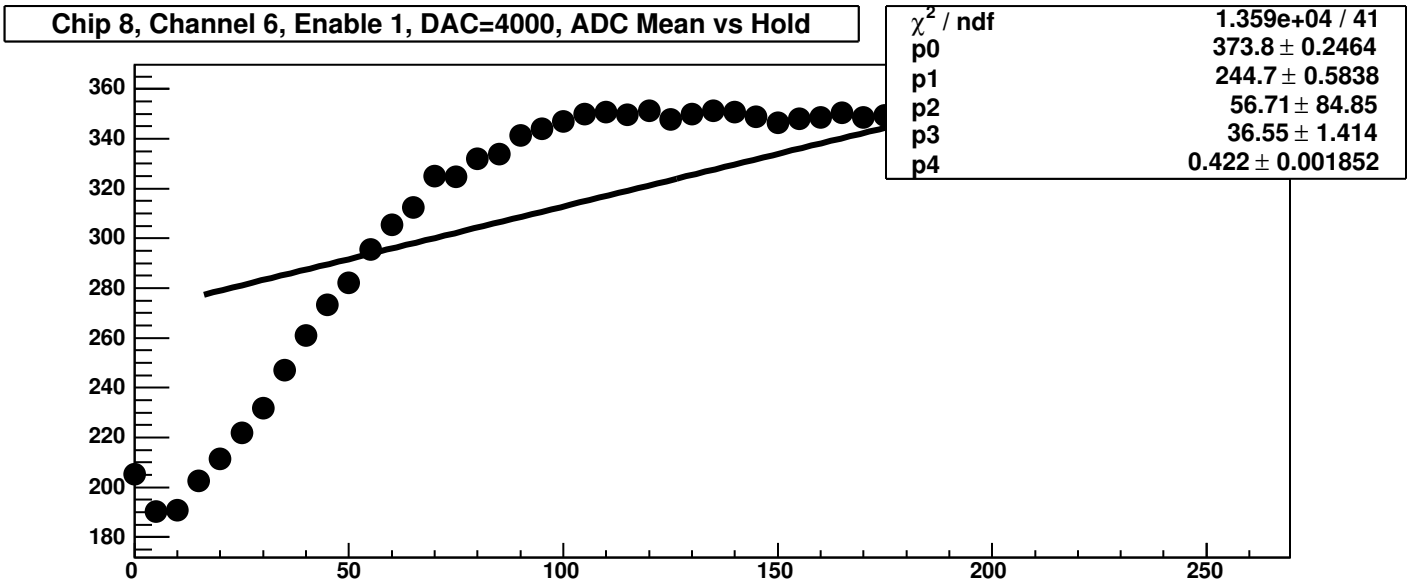


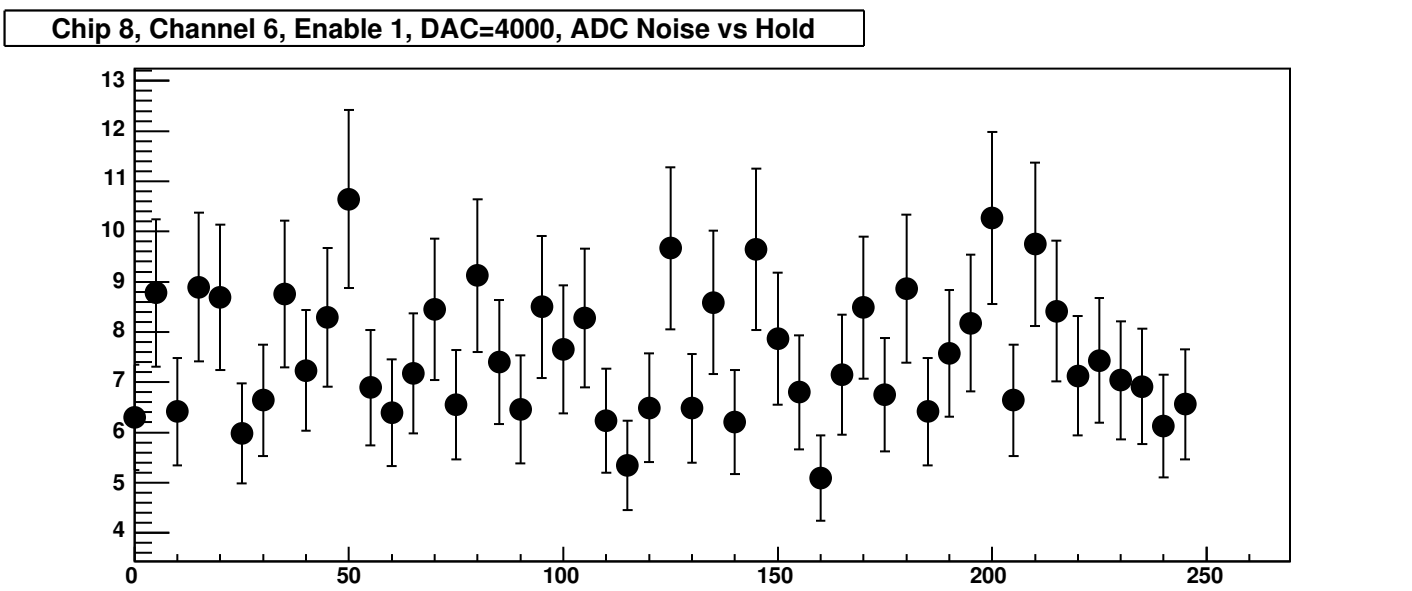
Chip 8, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold

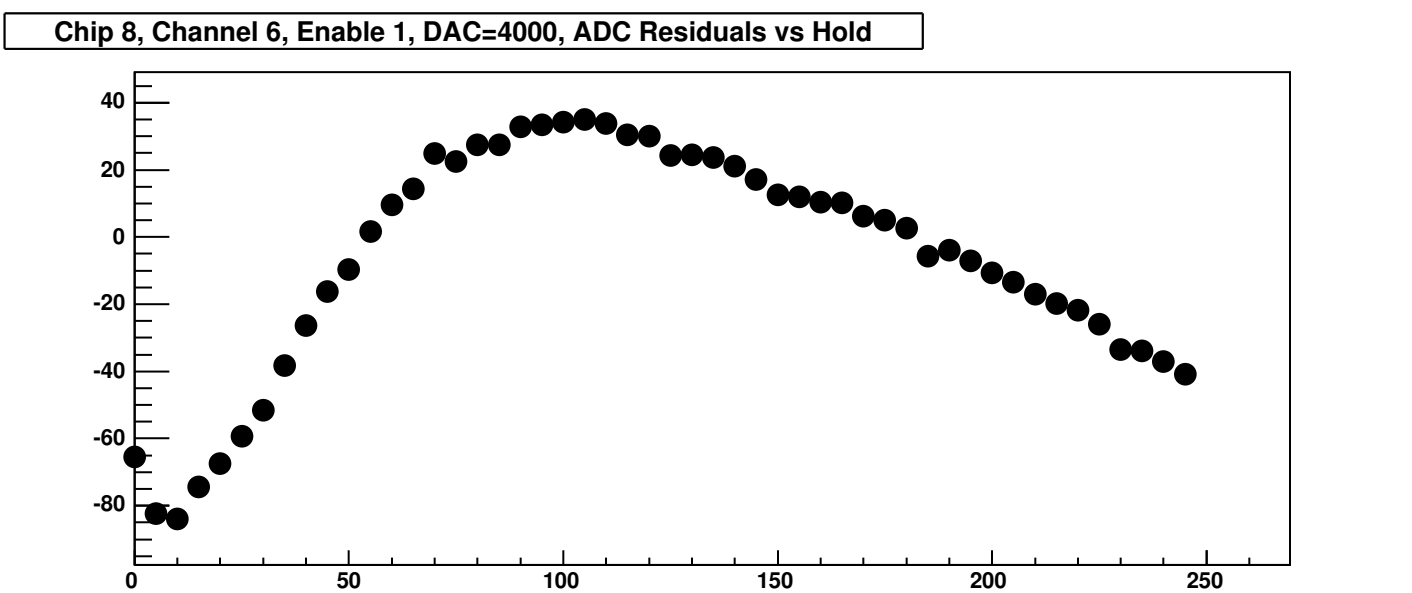


Chip 8, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold



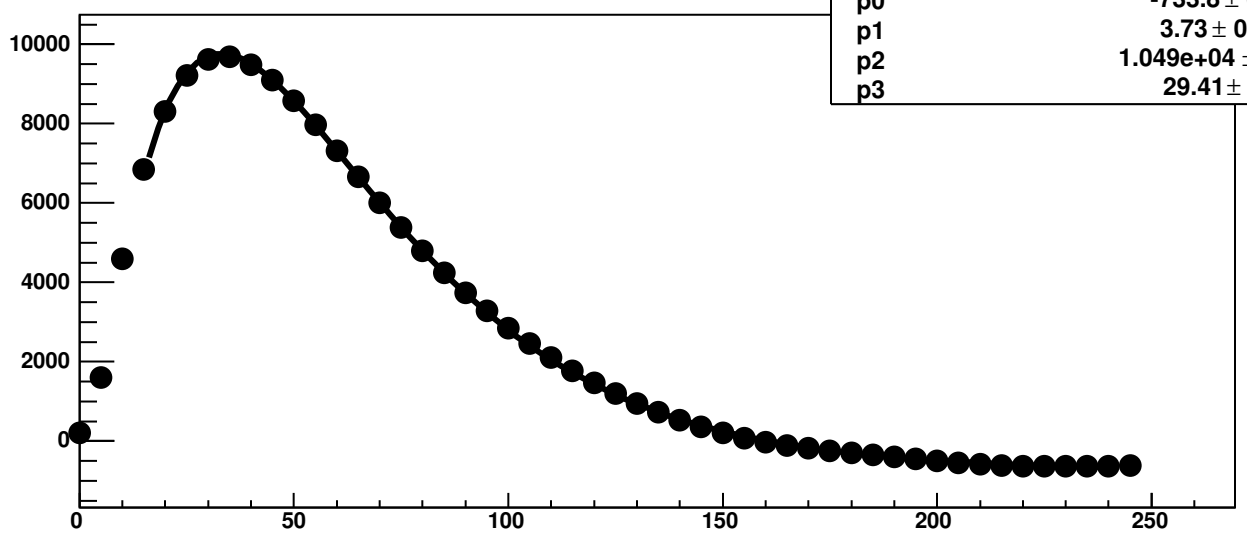

Chip 8, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.359e+04 / 41
p0 373.8 ± 0.2464
p1 244.7 ± 0.5838
p2 56.71 ± 84.85
p3 36.55 ± 1.414
p4 0.422 ± 0.001852
360
340
320
300
280
260
240
220
200
180
0
50
100
150
200
250


Chip 8, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

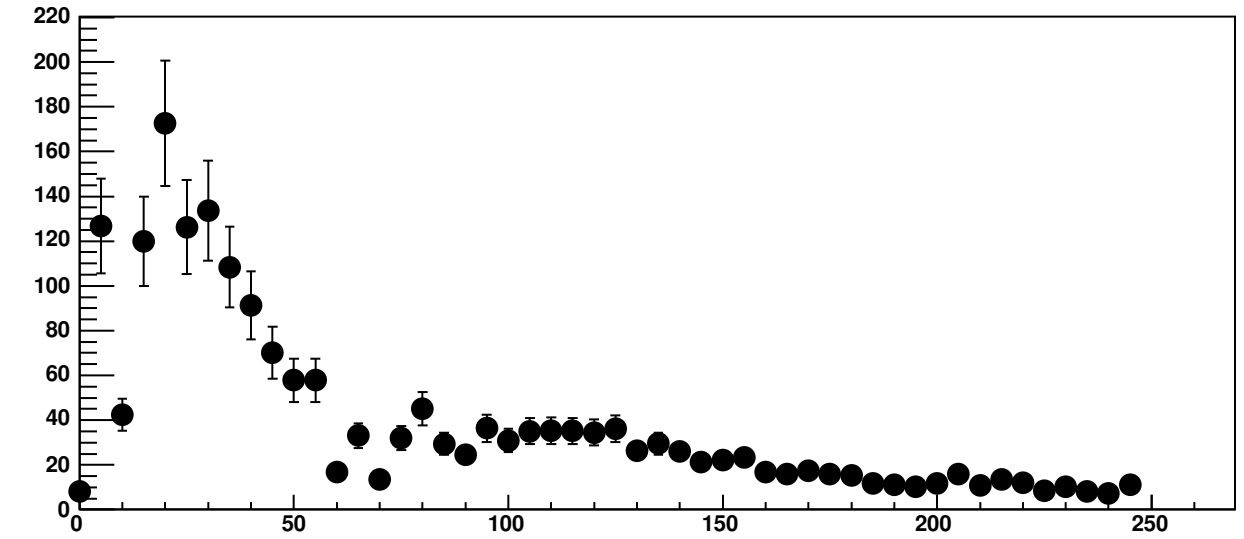

Chip 8, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
0
50
100
150
200
250

Chip 8, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold

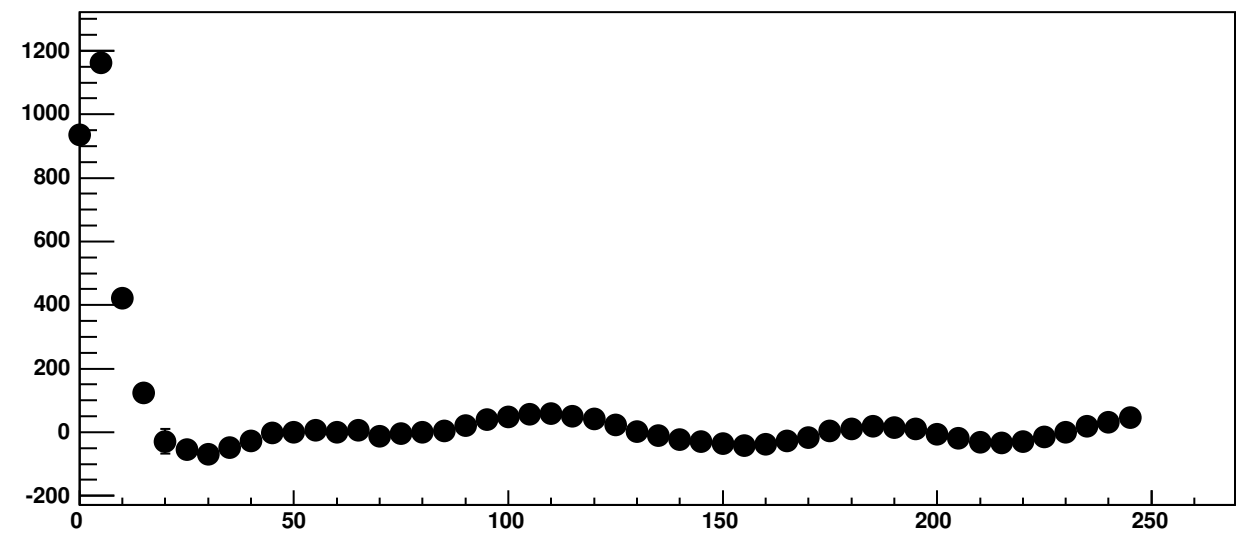
χ^2 / ndf	1728 / 42
p0	-733.8 ± 0.9135
p1	3.73 ± 0.04954
p2	1.049e+04 ± 6.561
p3	29.41 ± 0.0188

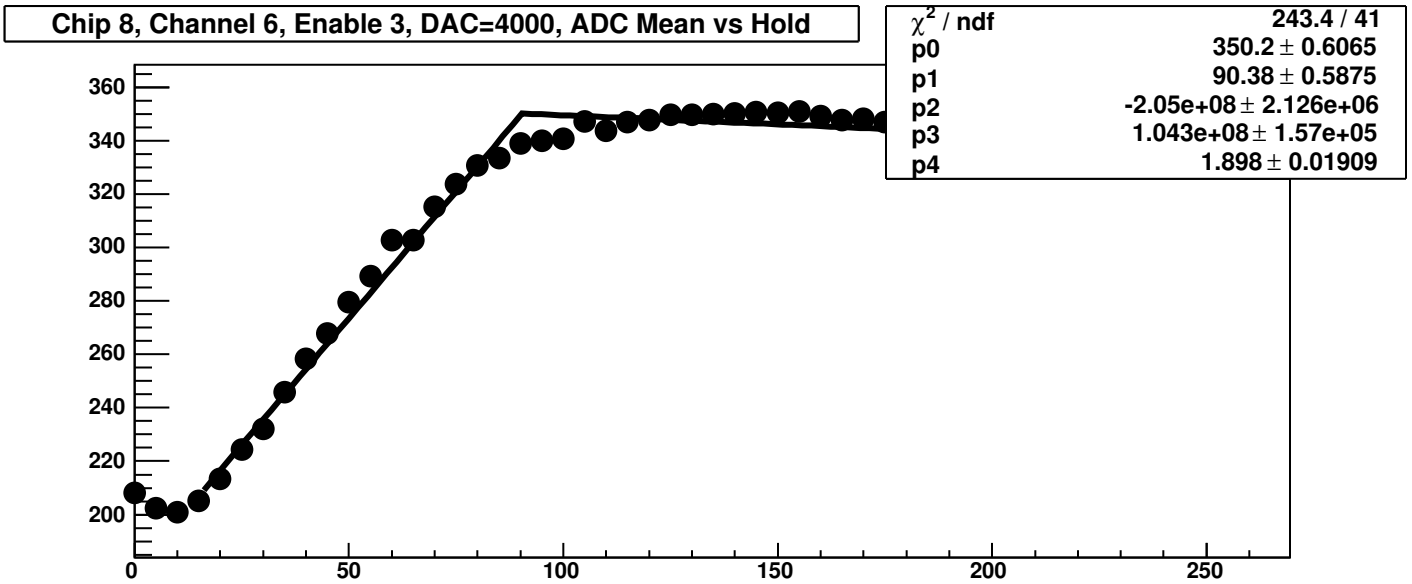


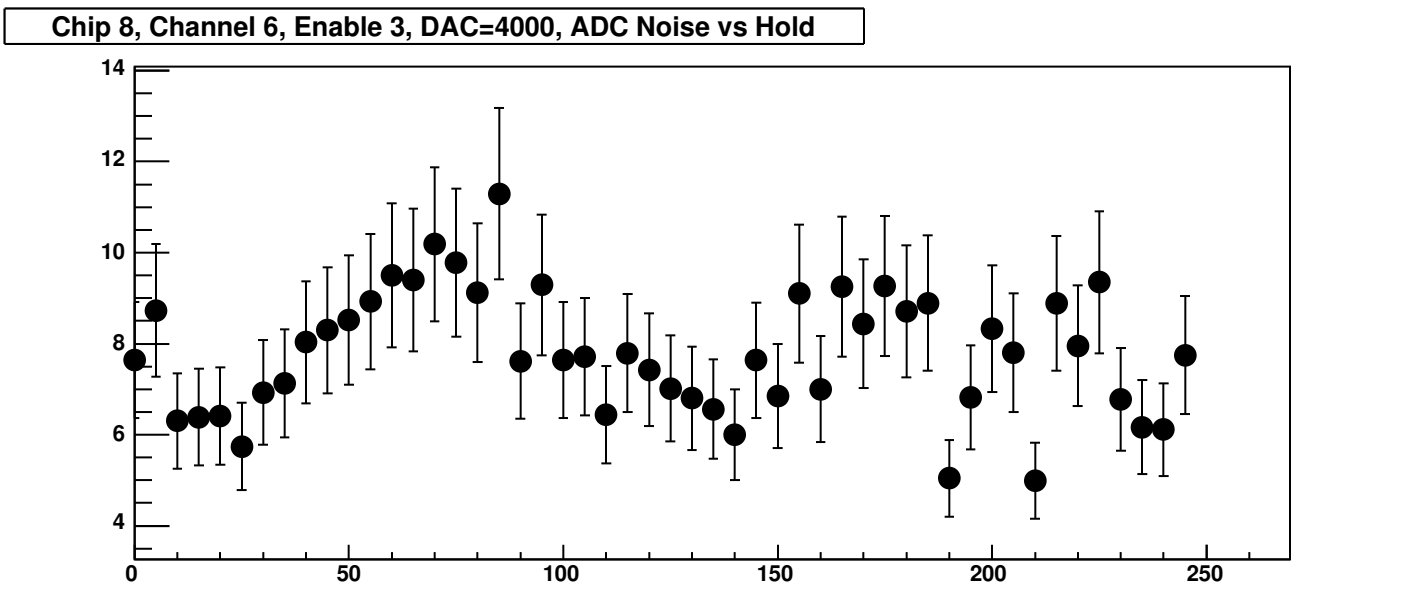
Chip 8, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold

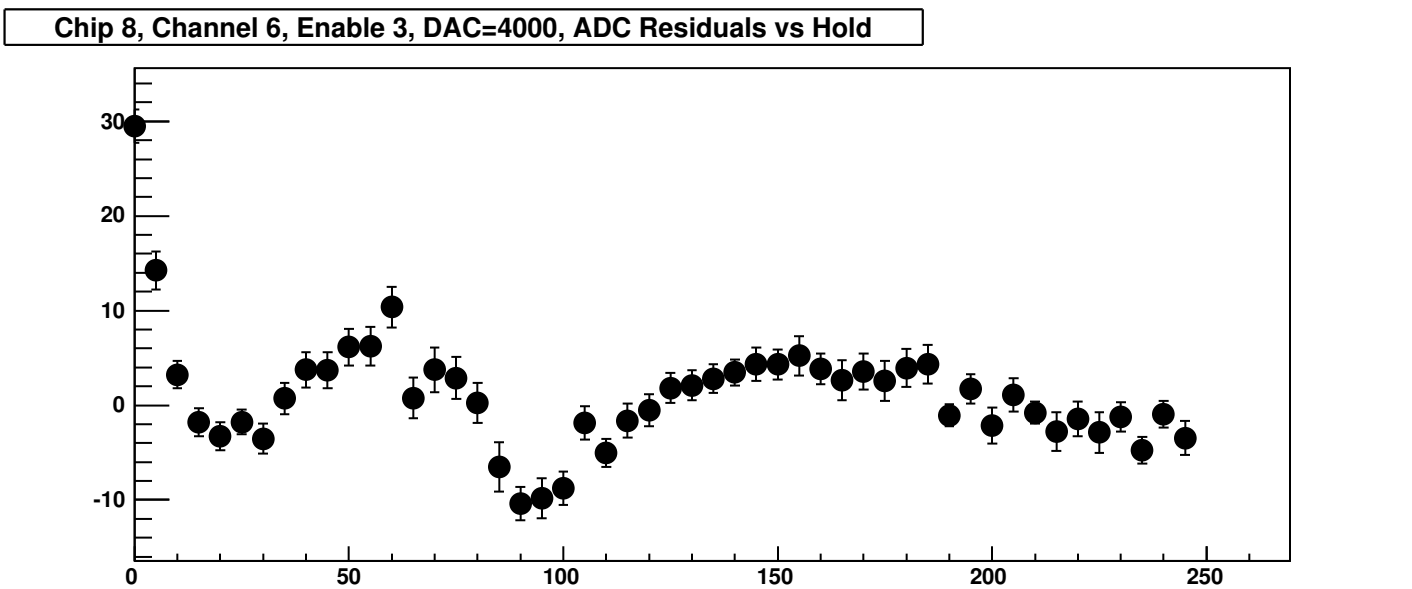


Chip 8, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold

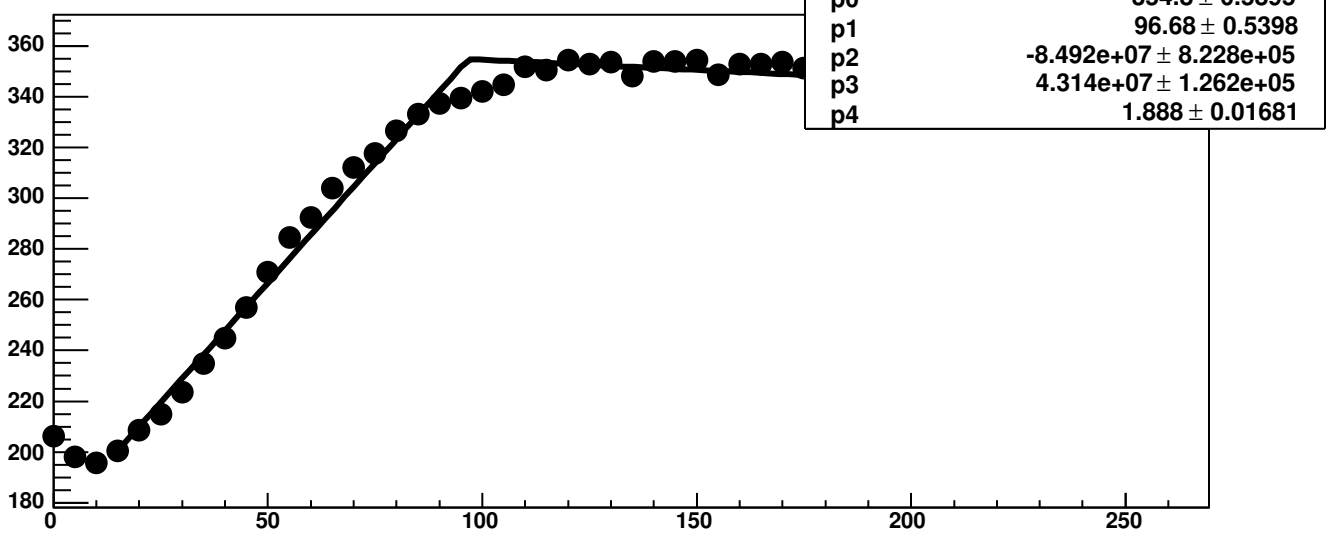



Chip 8, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 243.4 / 41
p0 350.2 ± 0.6065
p1 90.38 ± 0.5875
p2 -2.05e+08 ± 2.126e+06
p3 1.043e+08 ± 1.57e+05
p4 1.898 ± 0.01909
360
340
320
300
280
260
240
220
200
0
50
100
150
200
250

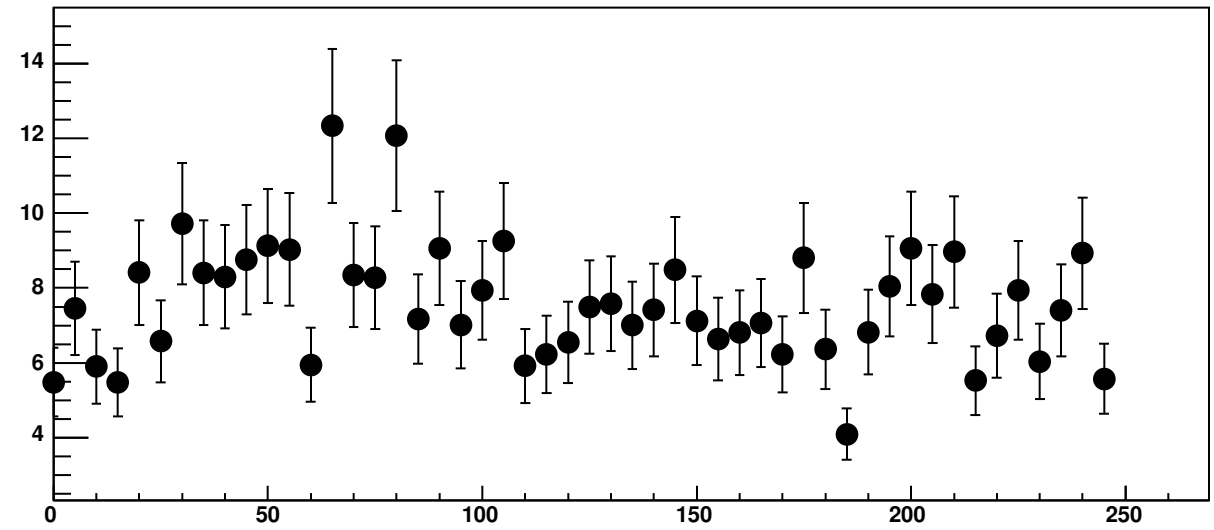

Chip 8, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 8, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
0
50
100
150
200
250

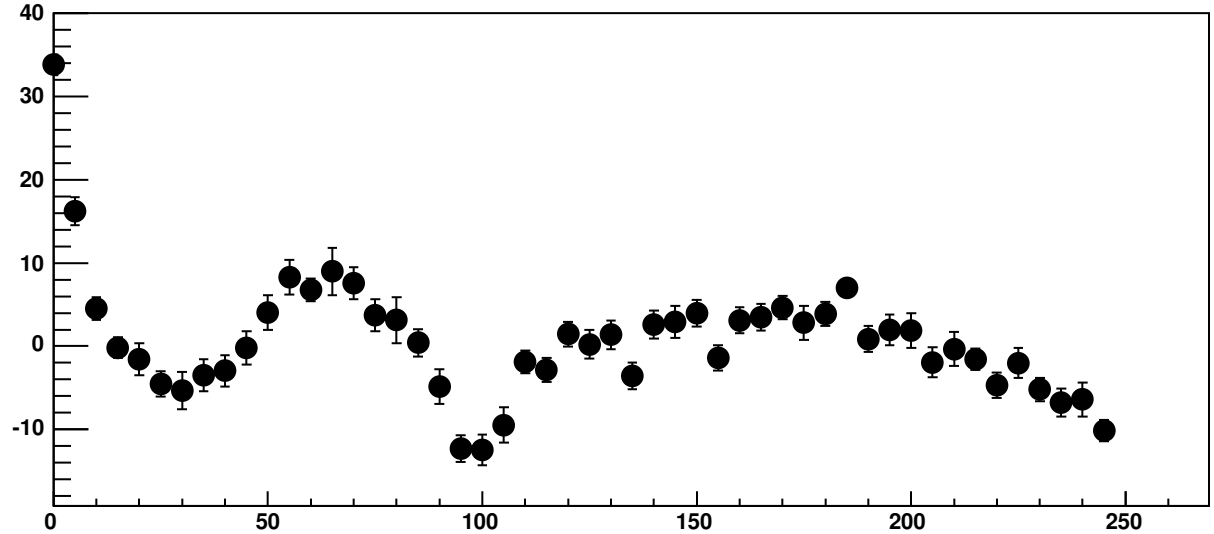
Chip 8, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold



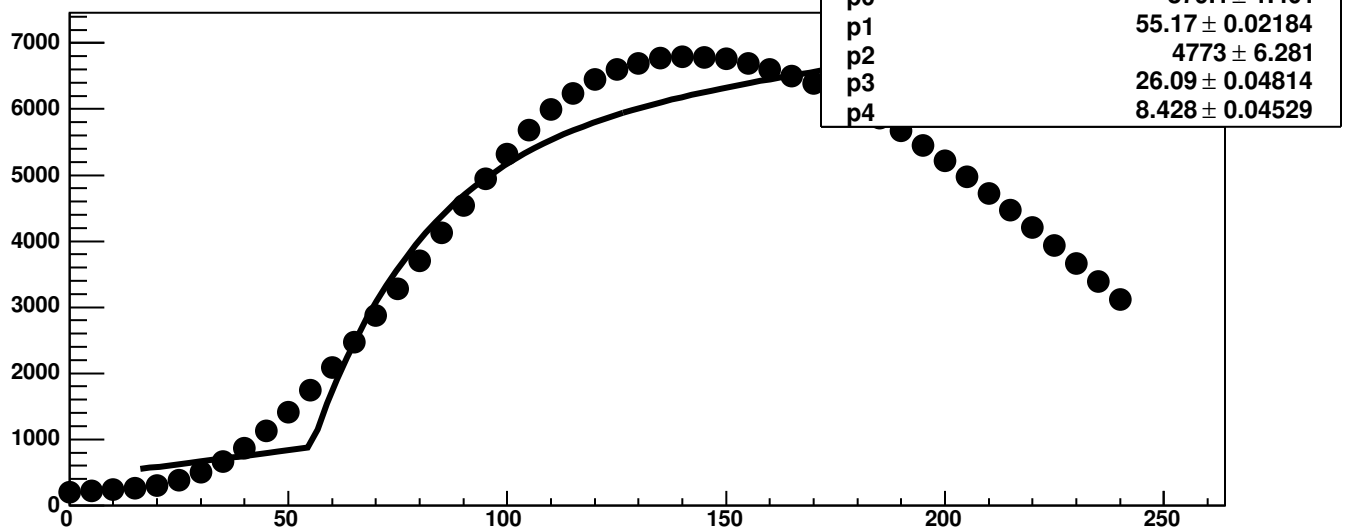
Chip 8, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



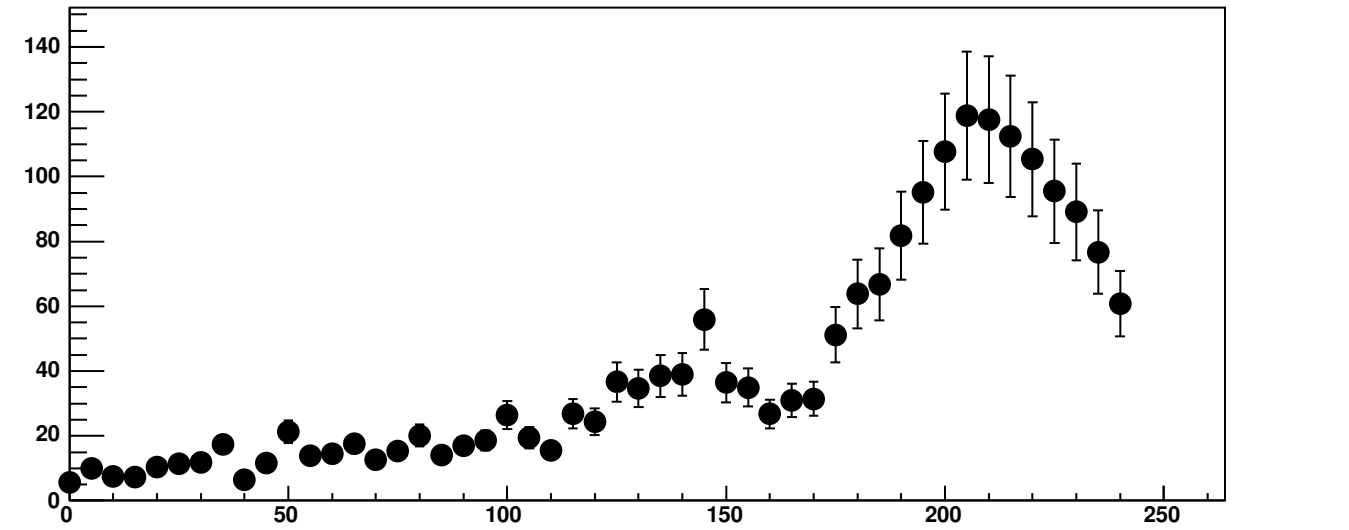
Chip 8, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold



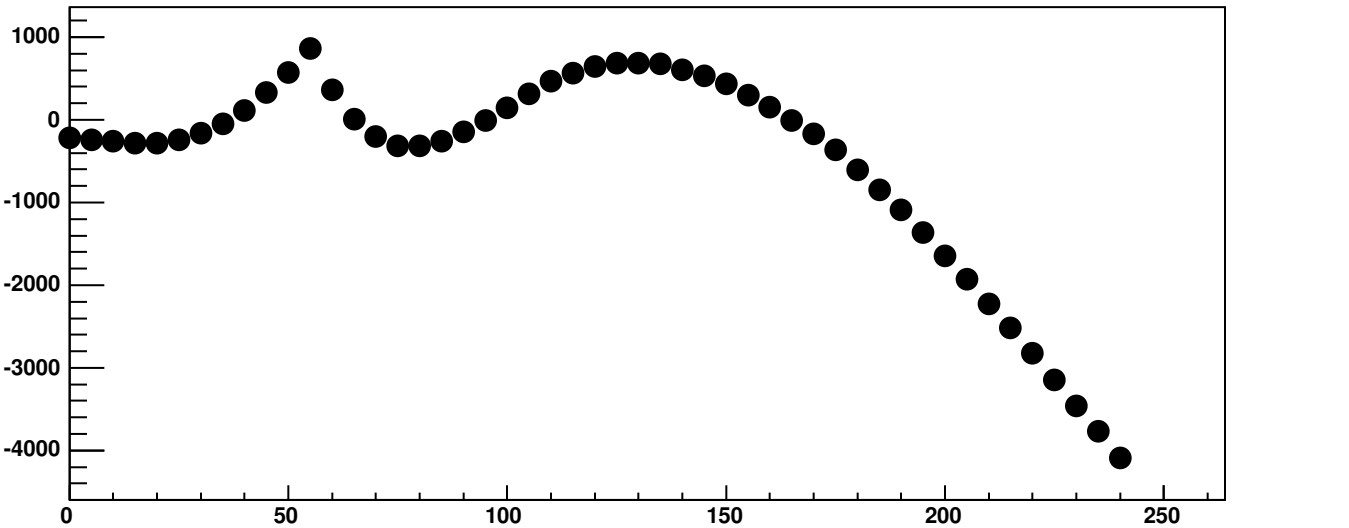
Chip 8, Channel 6, Enable 5, DAC=4000, ADC Mean vs Hold



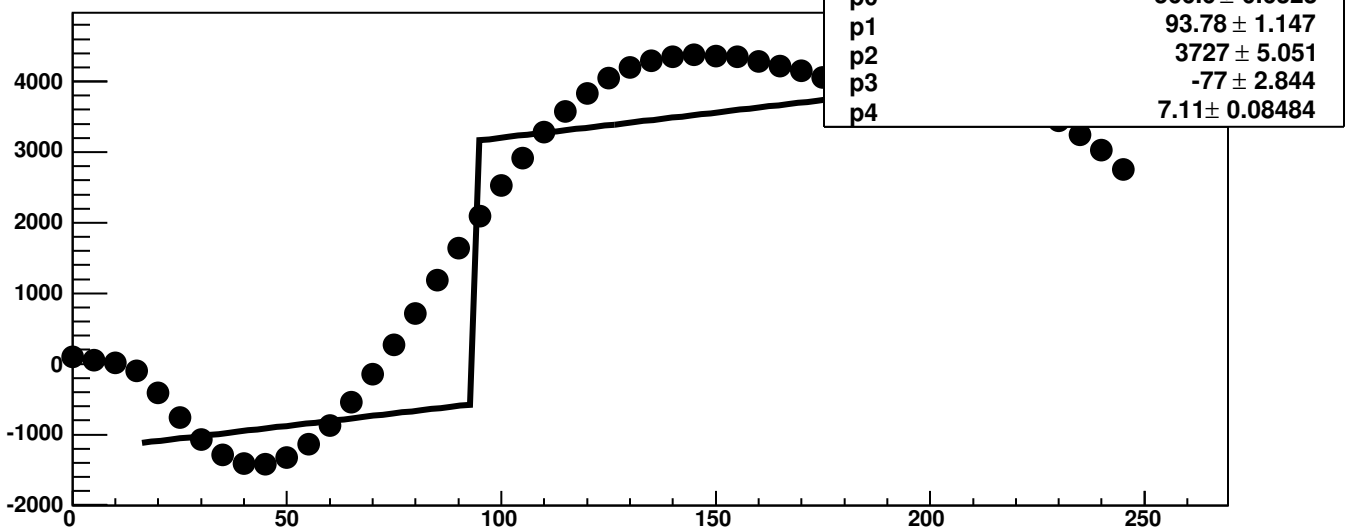
Chip 8, Channel 6, Enable 5, DAC=4000, ADC Noise vs Hold



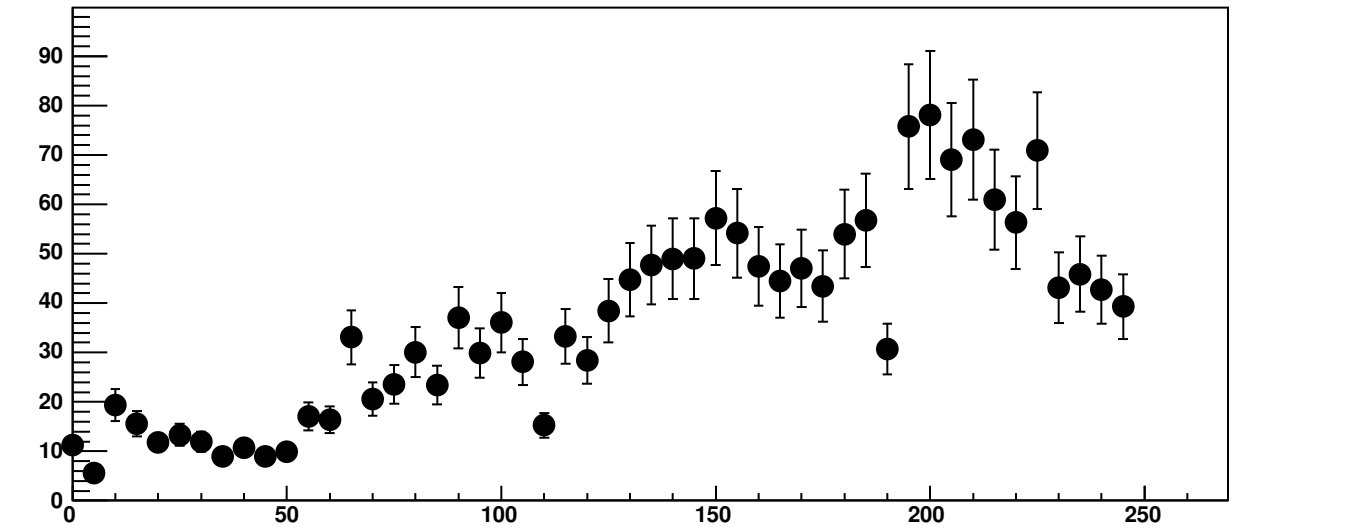
Chip 8, Channel 6, Enable 5, DAC=4000, ADC Residuals vs Hold



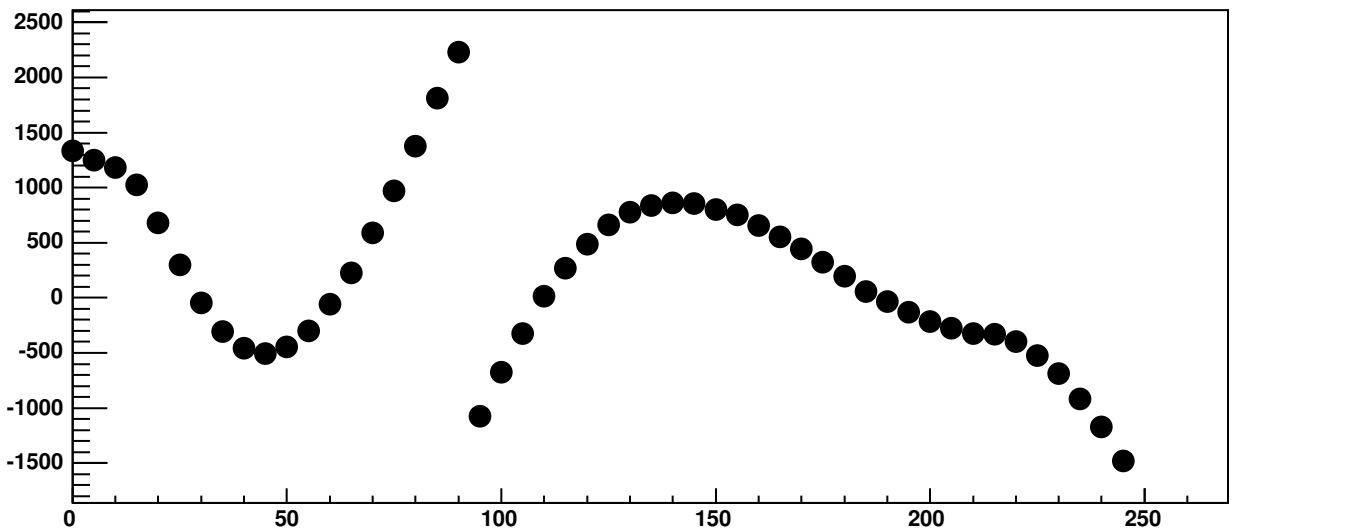
Chip 8, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold



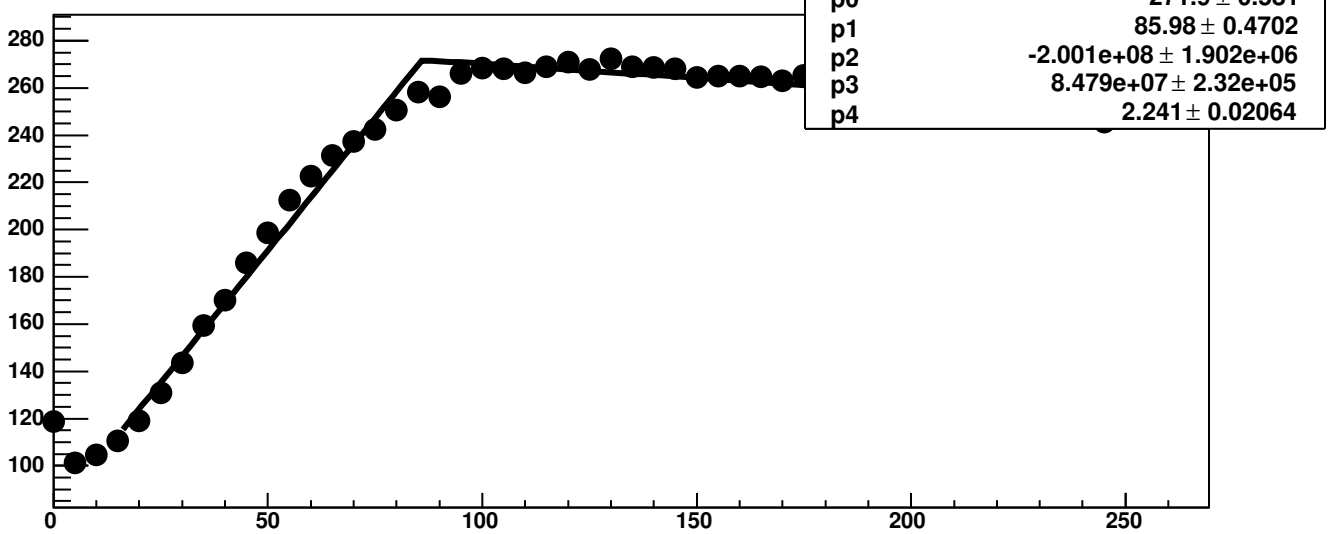
Chip 8, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



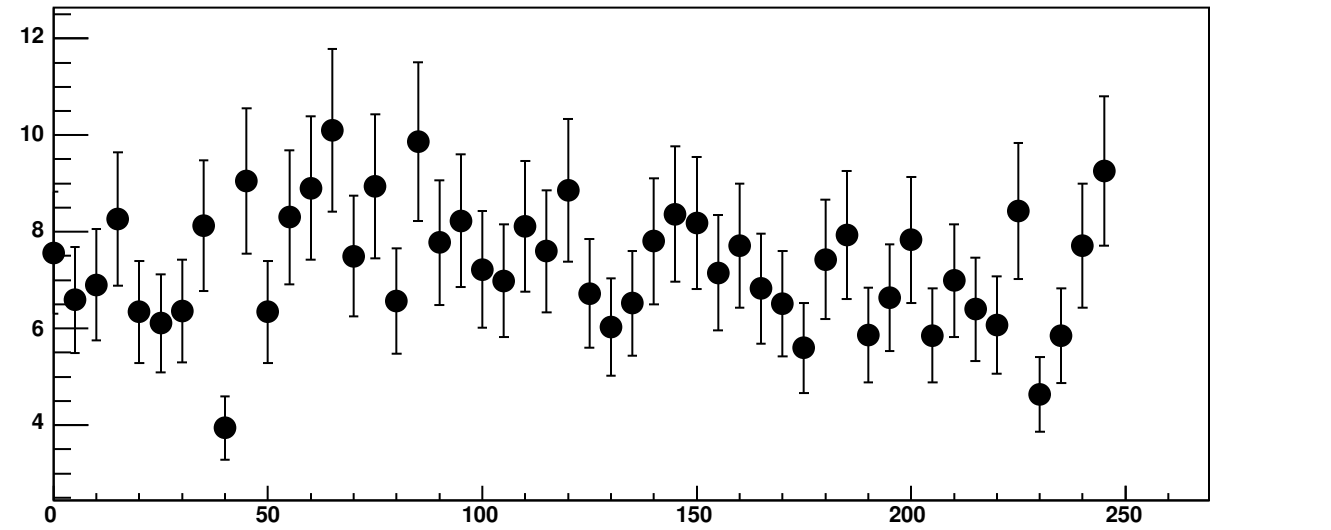
Chip 8, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold



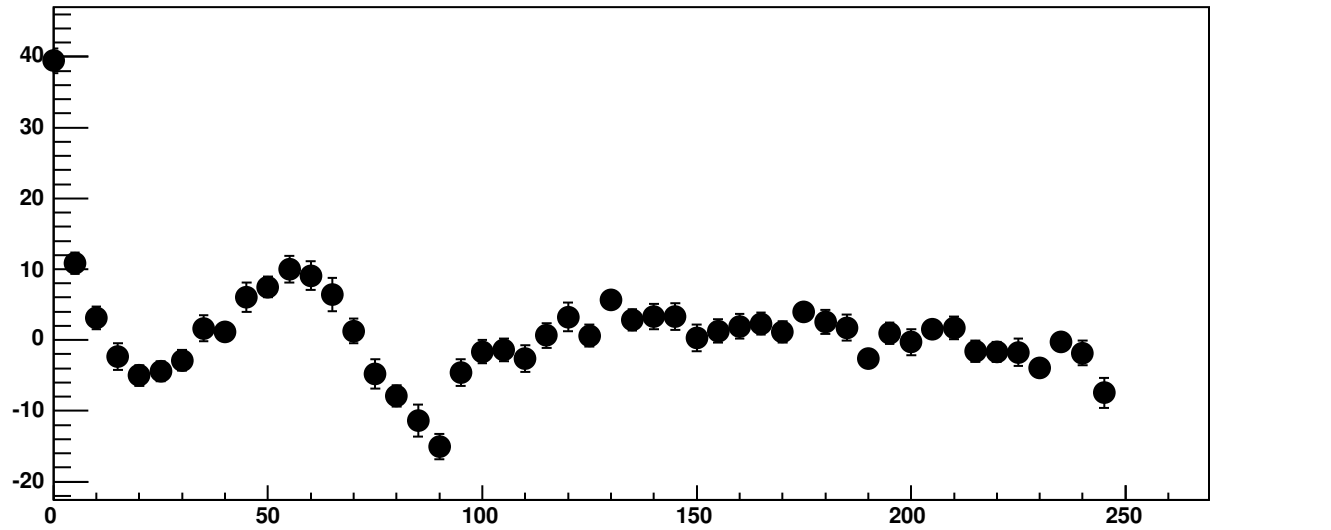
Chip 8, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold



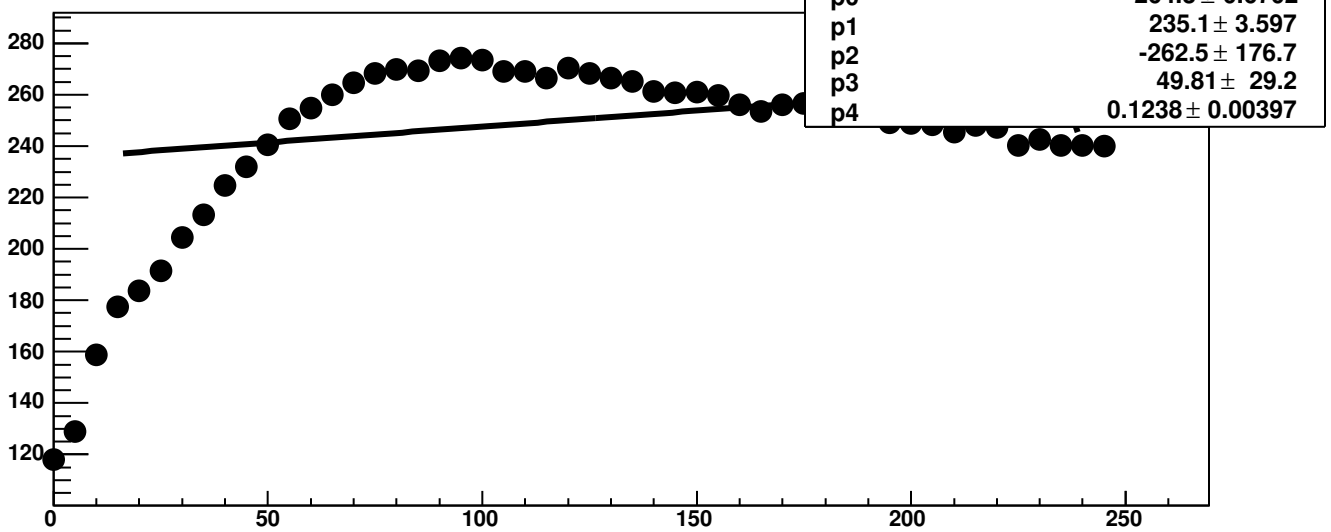
Chip 8, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



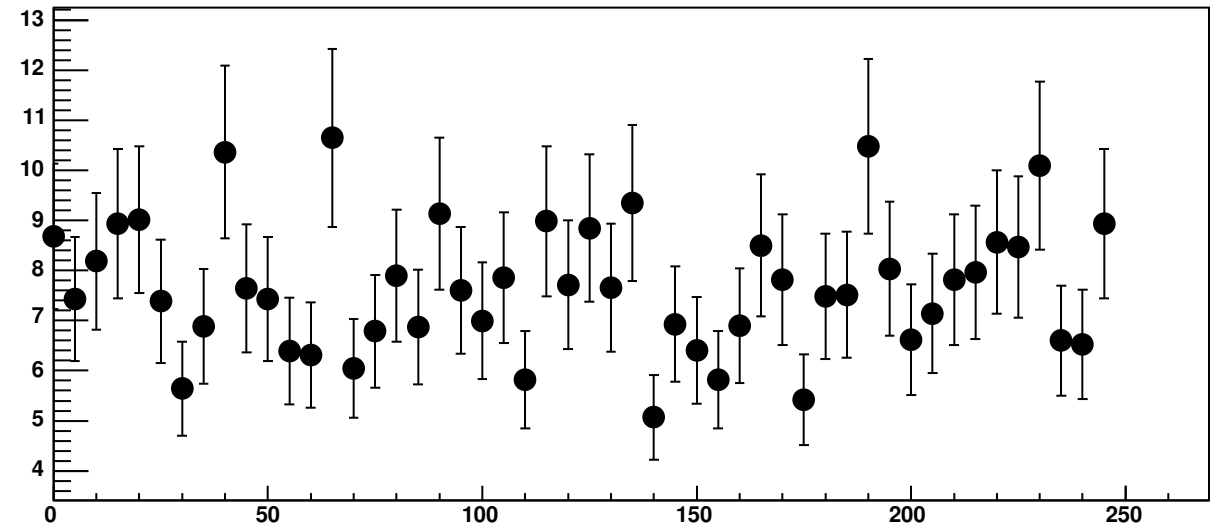
Chip 8, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold



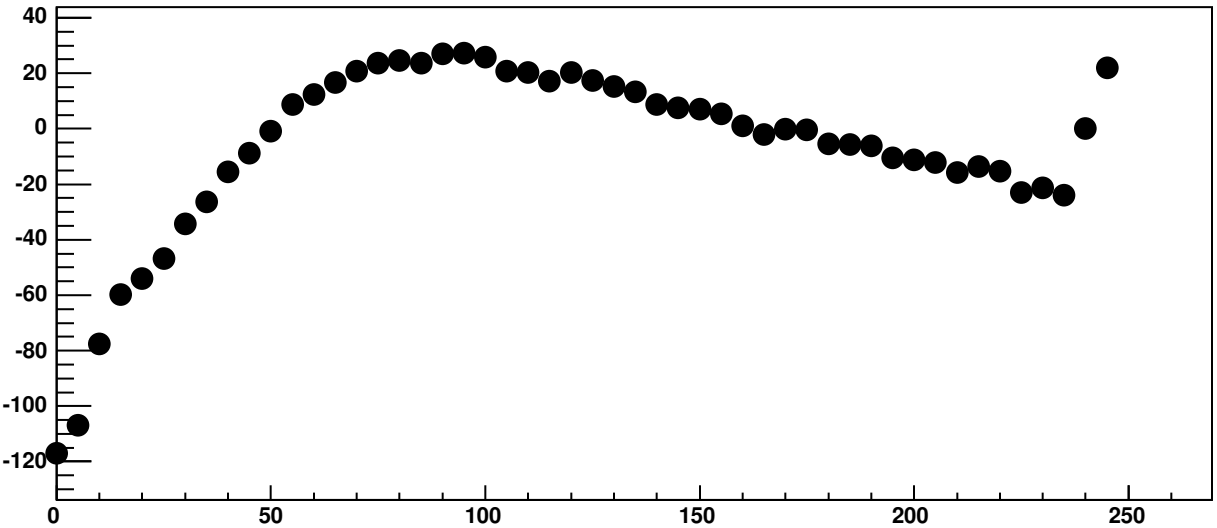
Chip 8, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold

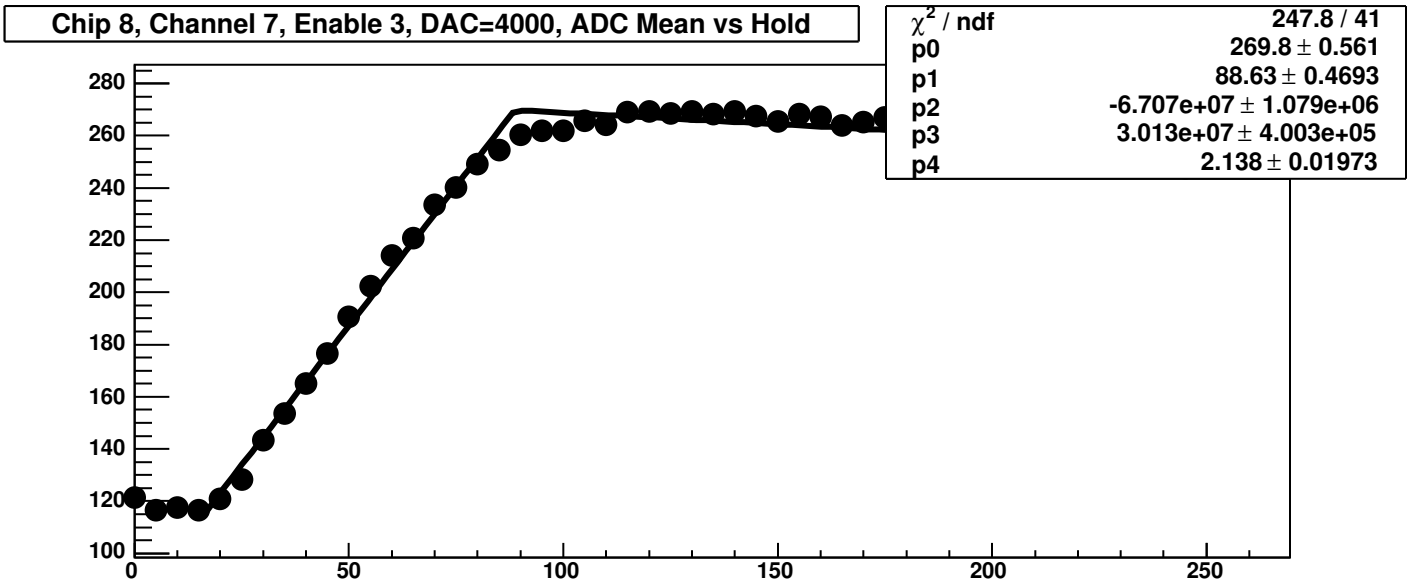


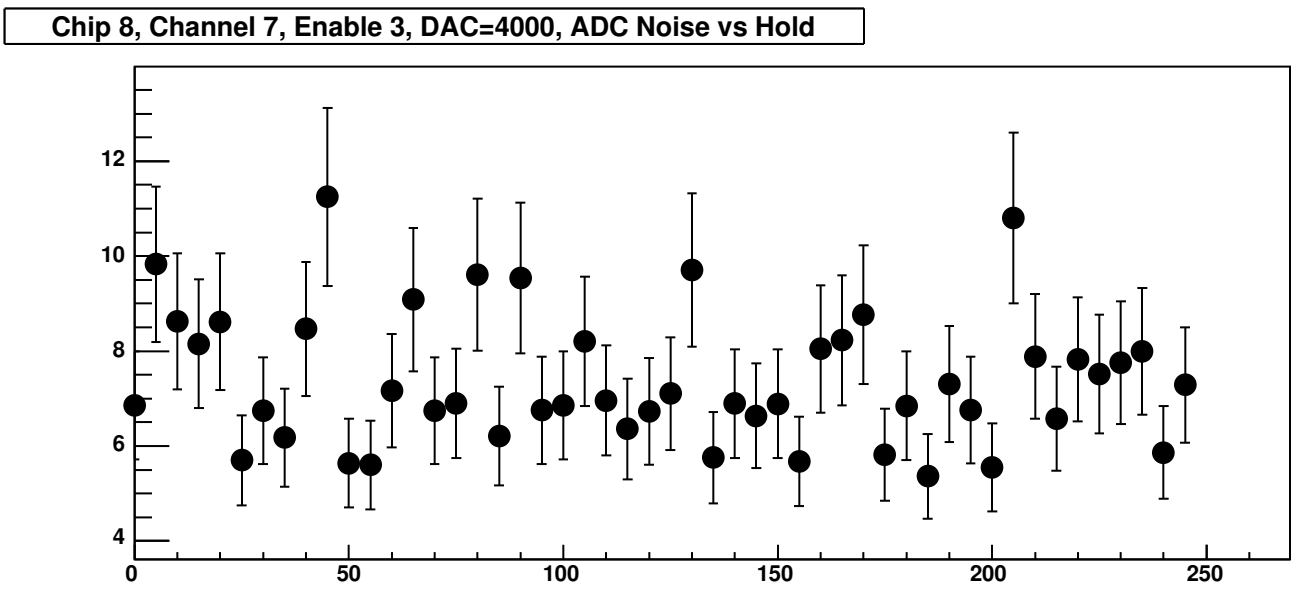
Chip 8, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold

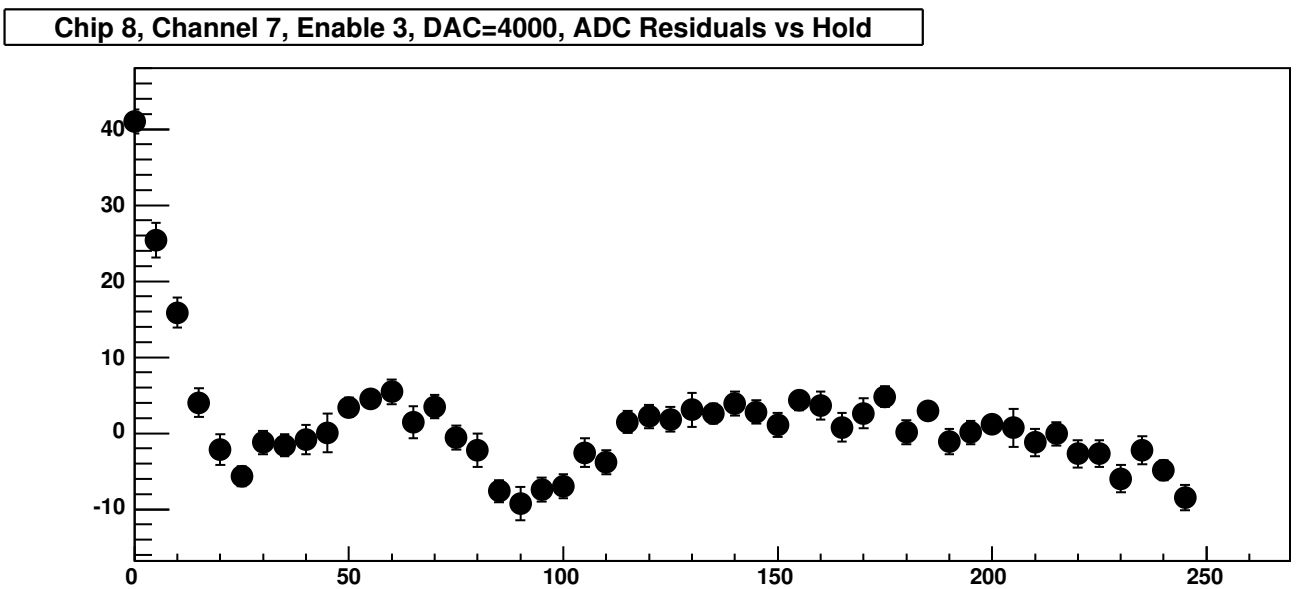


Chip 8, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold

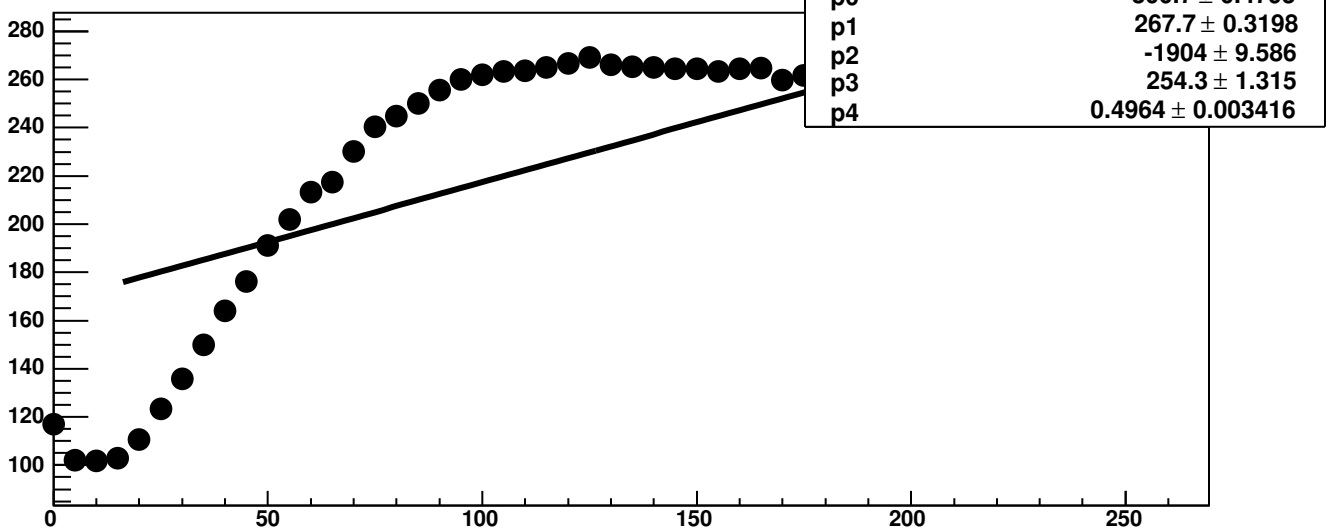



Chip 8, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 247.8 / 41
p0 269.8 ± 0.561
p1 88.63 ± 0.4693
p2 -6.707e+07 ± 1.079e+06
p3 3.013e+07 ± 4.003e+05
p4 2.138 ± 0.01973
280
260
240
220
200
180
160
140
120
100
0
50
100
150
200
250

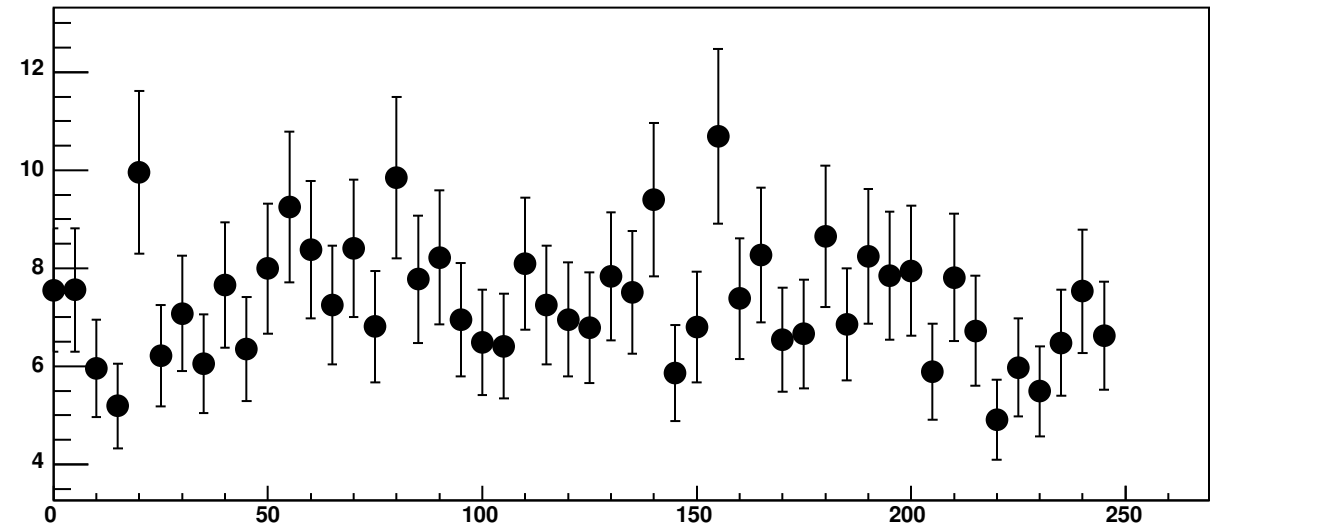

Chip 8, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 8, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

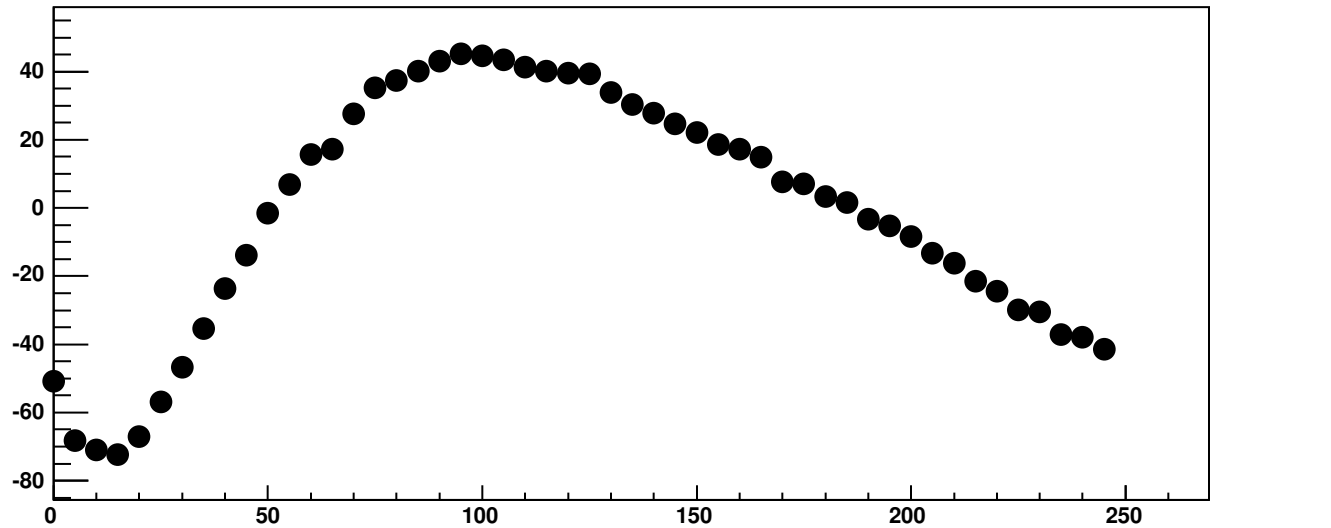
Chip 8, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold



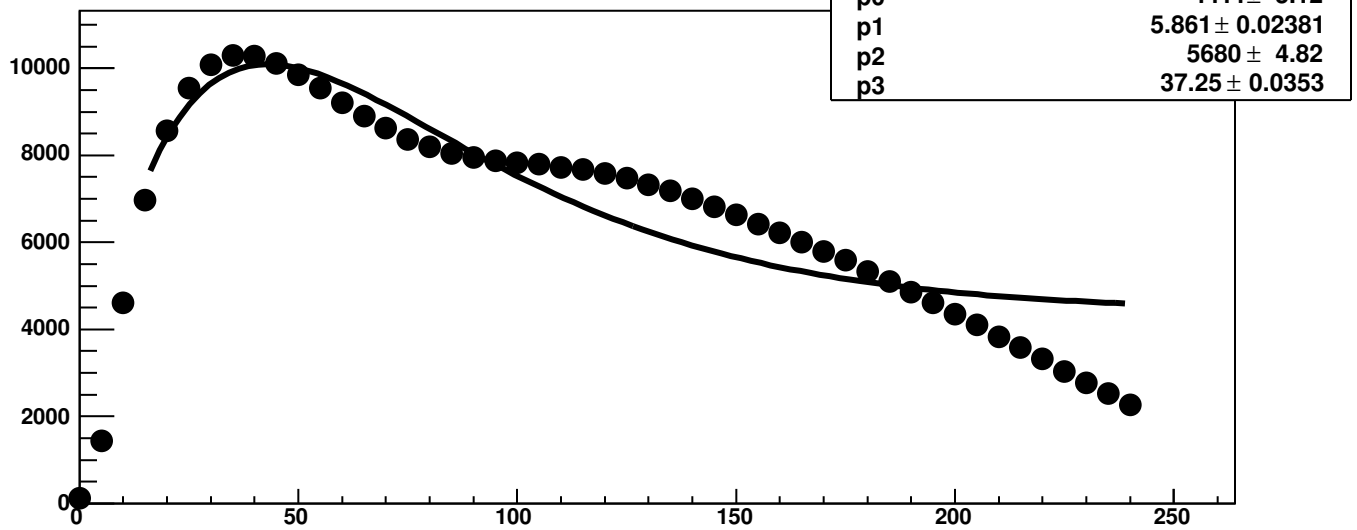
Chip 8, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



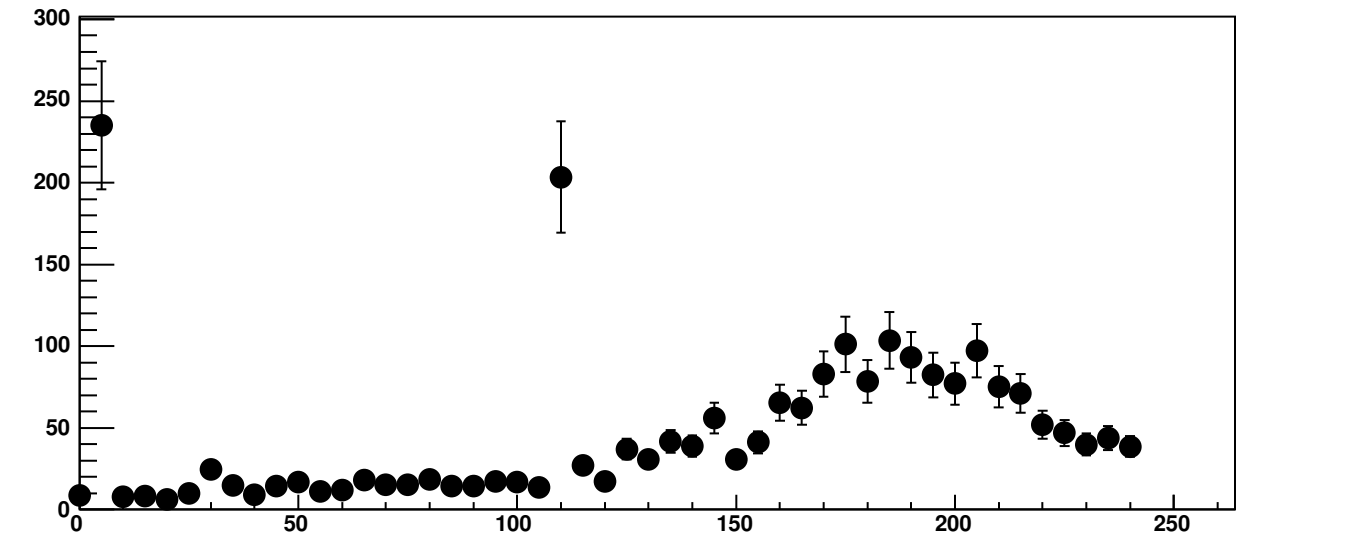
Chip 8, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold



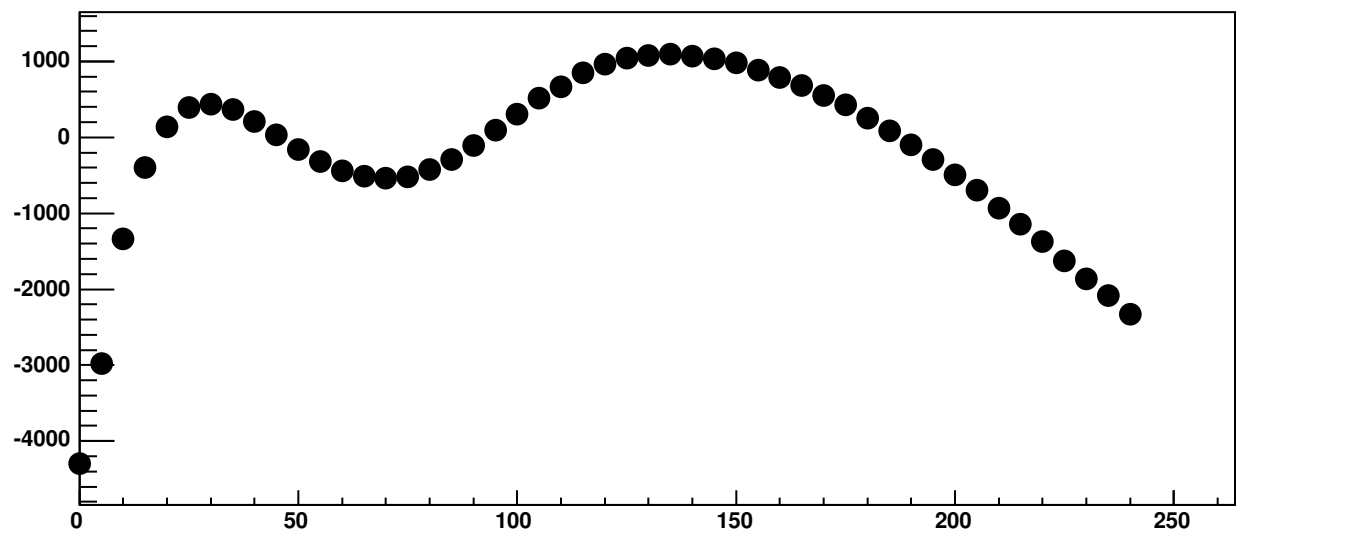
Chip 8, Channel 7, Enable 5!, DAC=4000, ADC Mean vs Hold



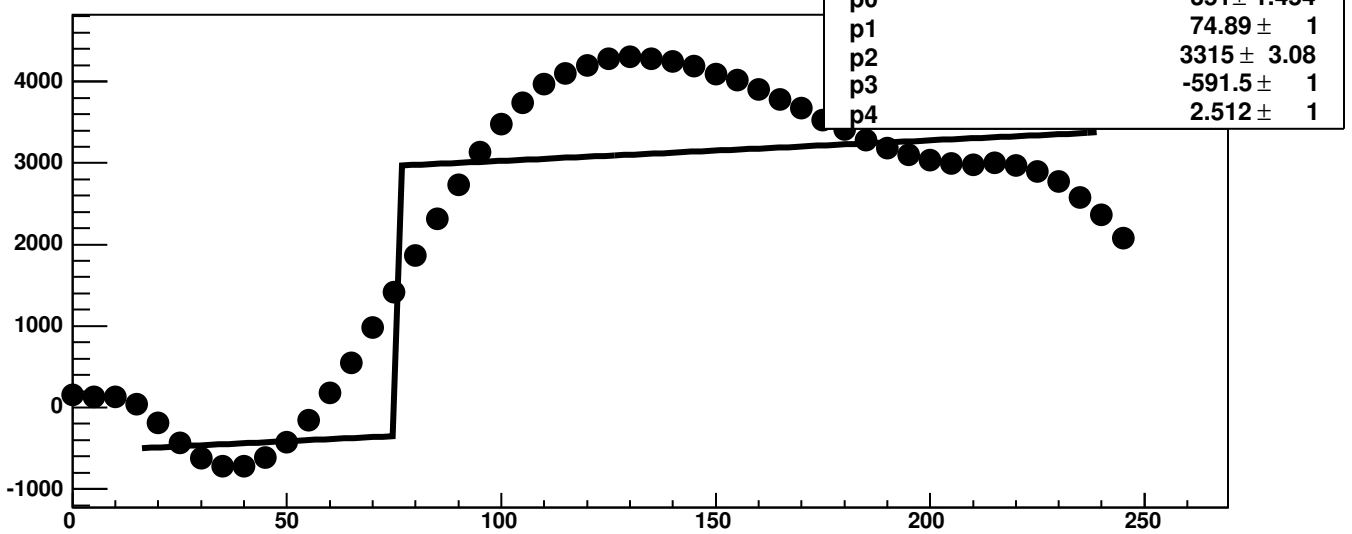
Chip 8, Channel 7, Enable 5!, DAC=4000, ADC Noise vs Hold



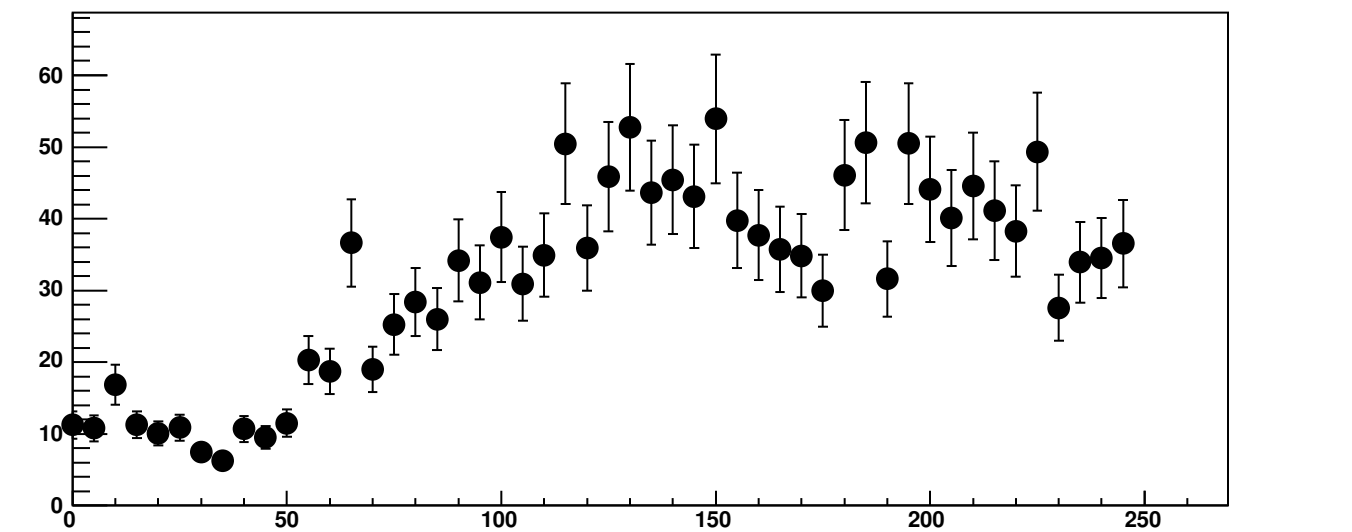
Chip 8, Channel 7, Enable 5!, DAC=4000, ADC Residuals vs Hold



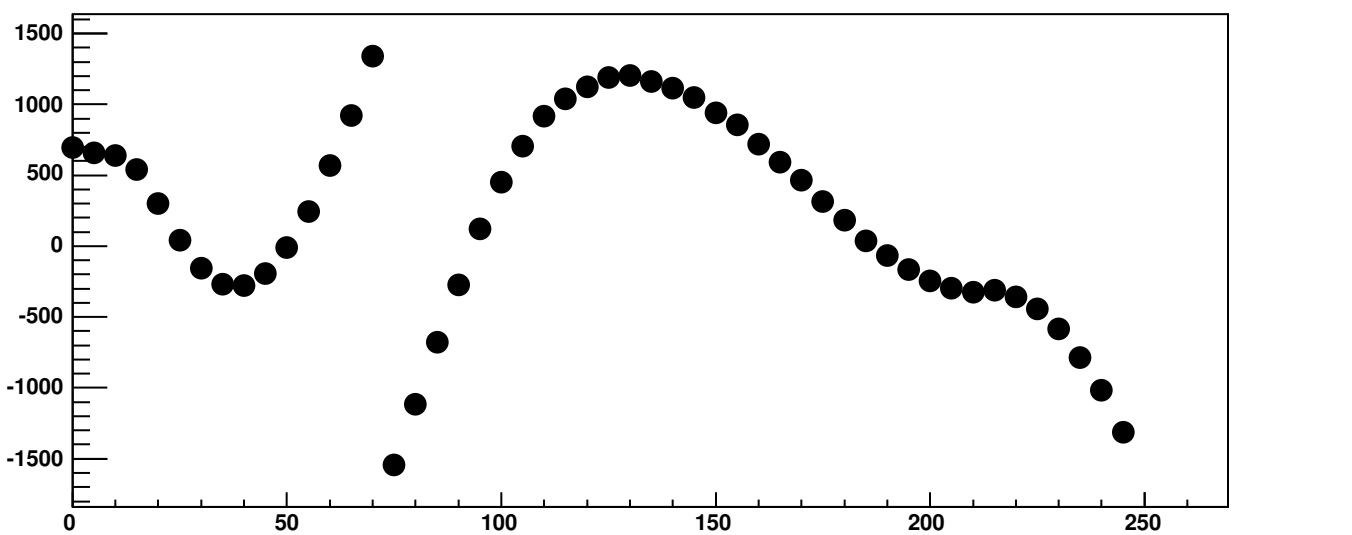
Chip 8, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold



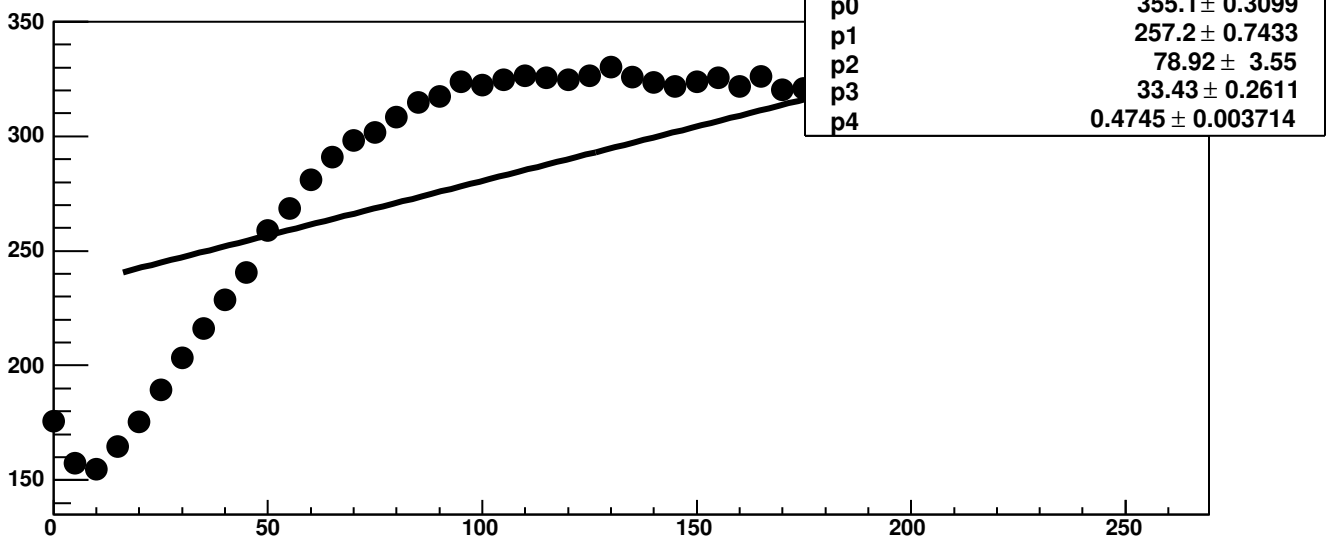
Chip 8, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



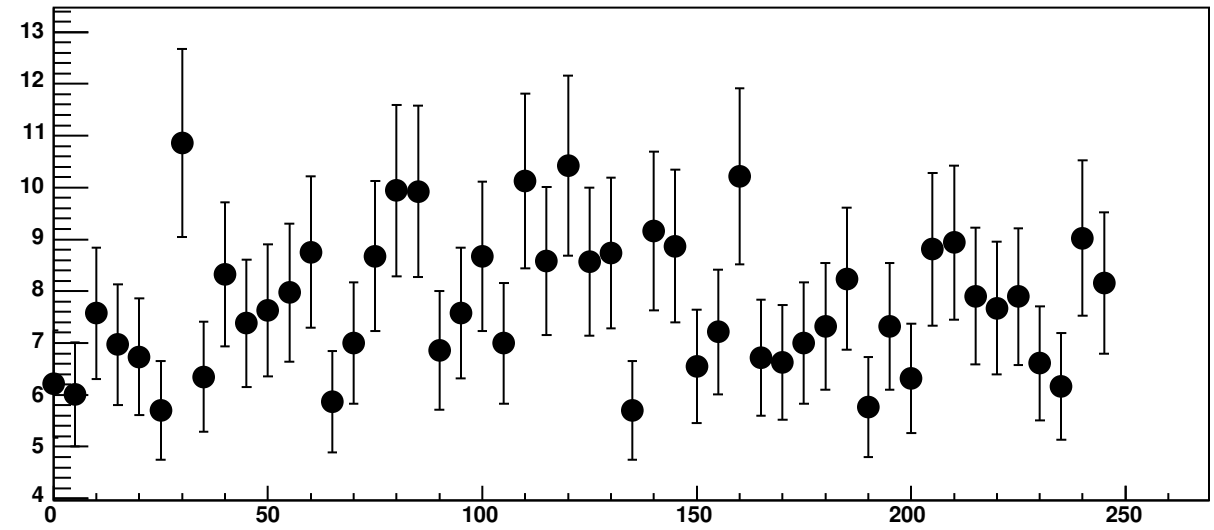
Chip 8, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold



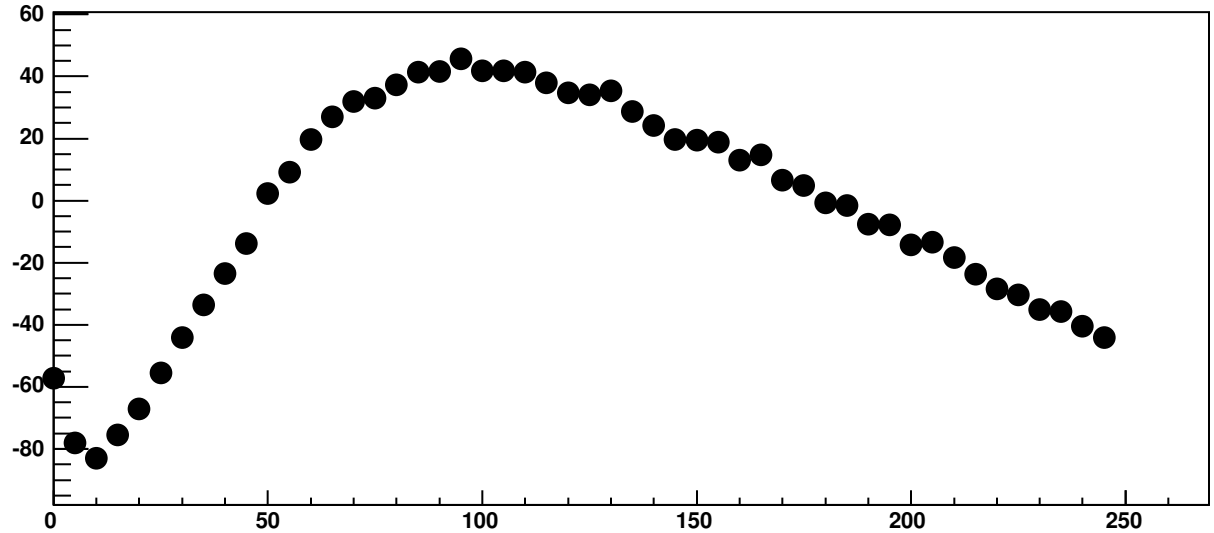
Chip 8, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold



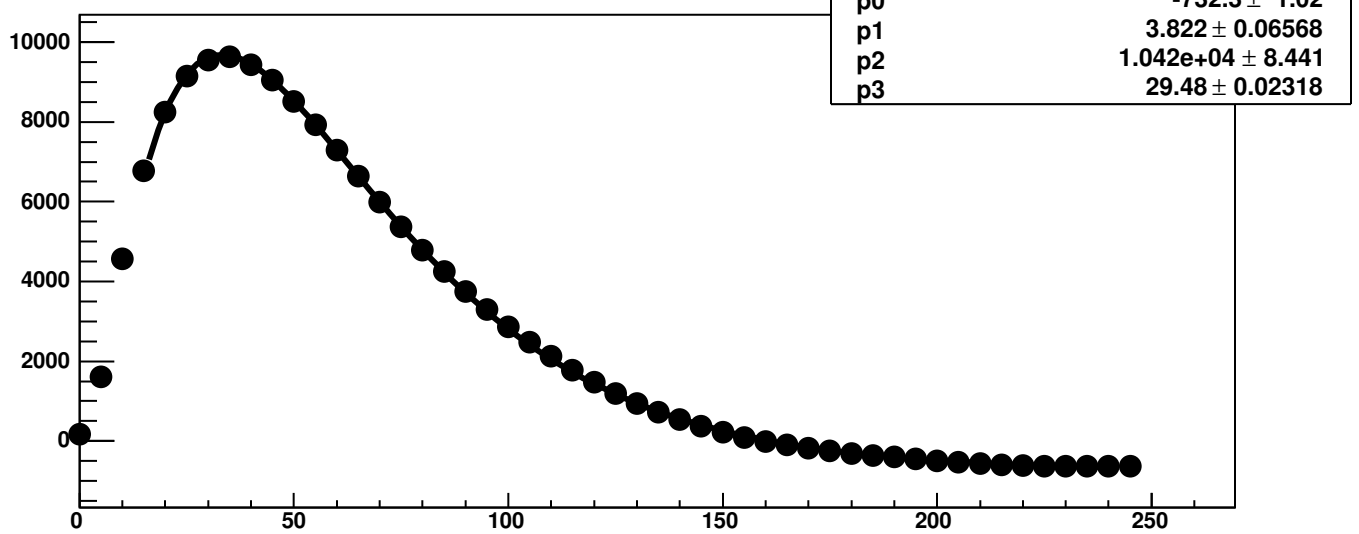
Chip 8, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



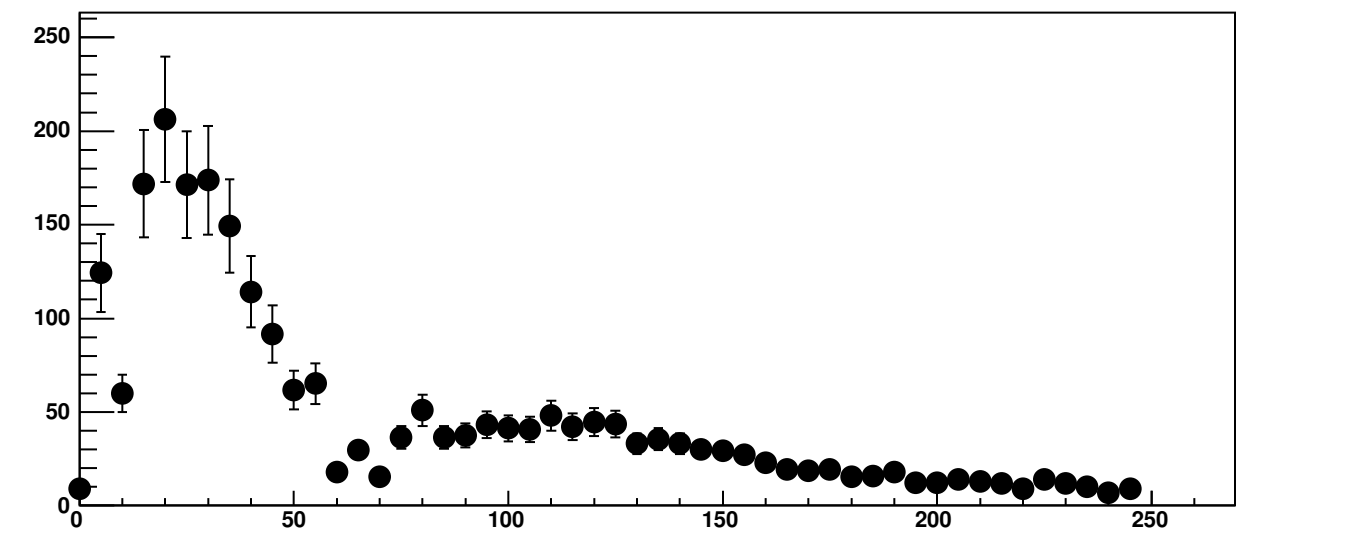
Chip 8, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold



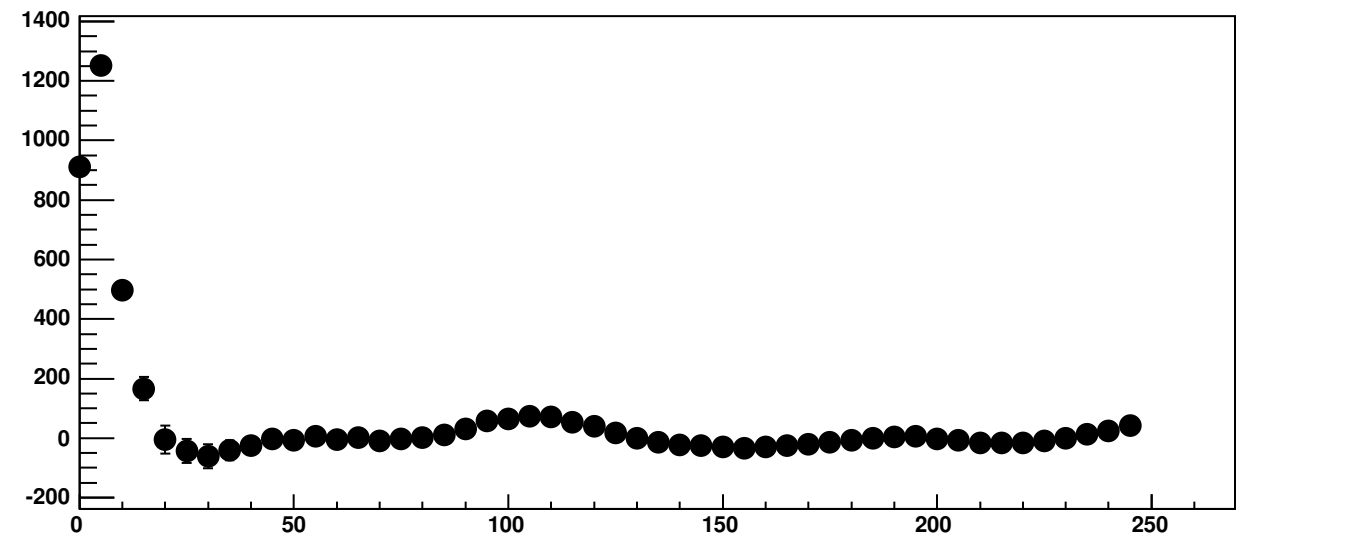
Chip 8, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold



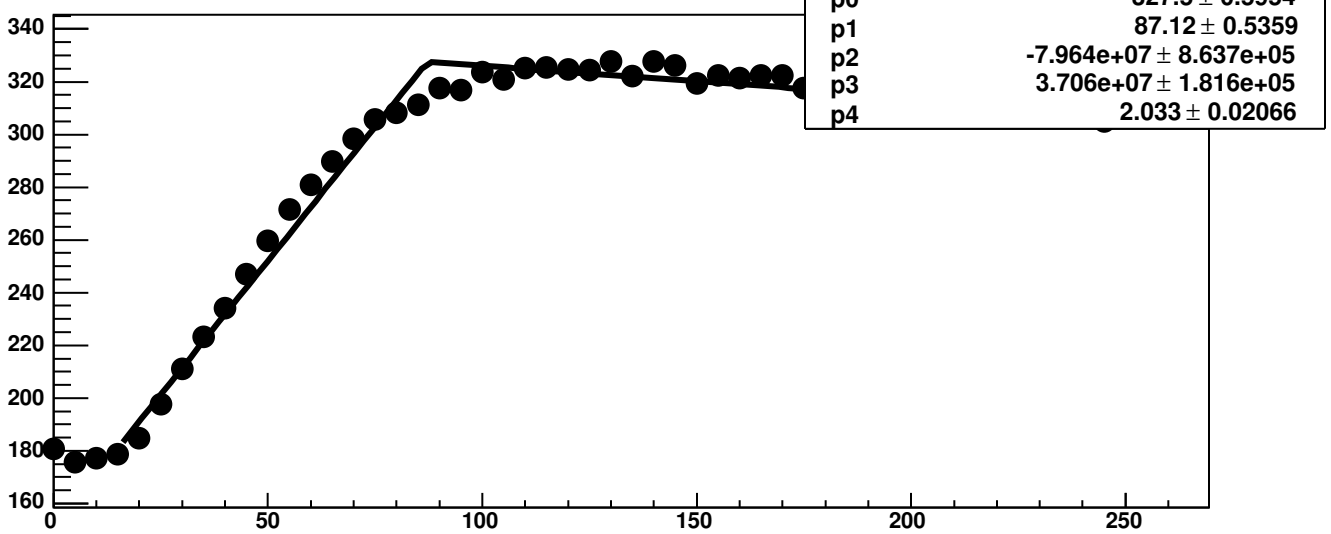
Chip 8, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold



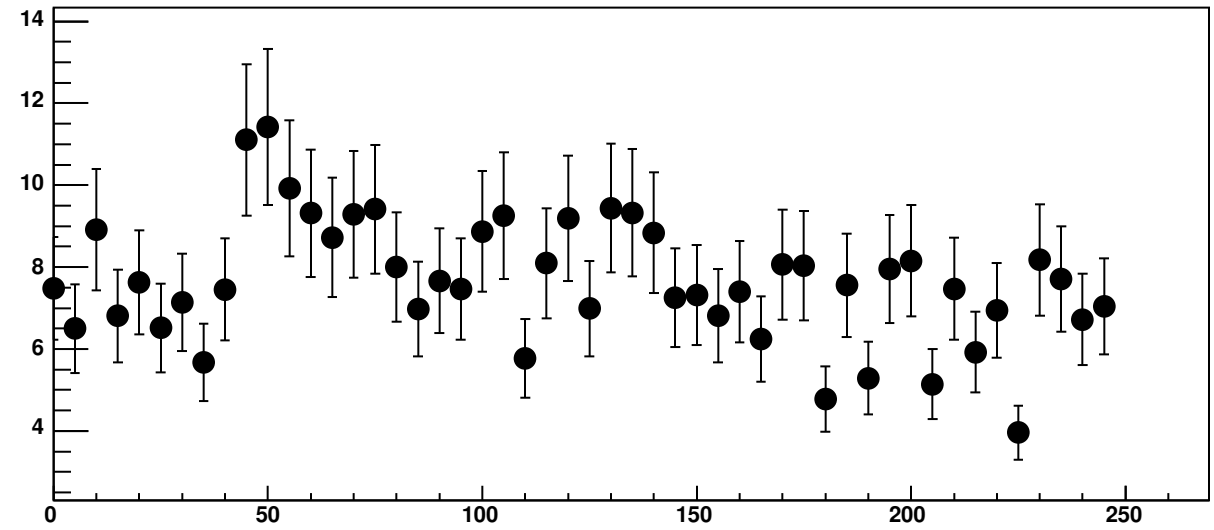
Chip 8, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold



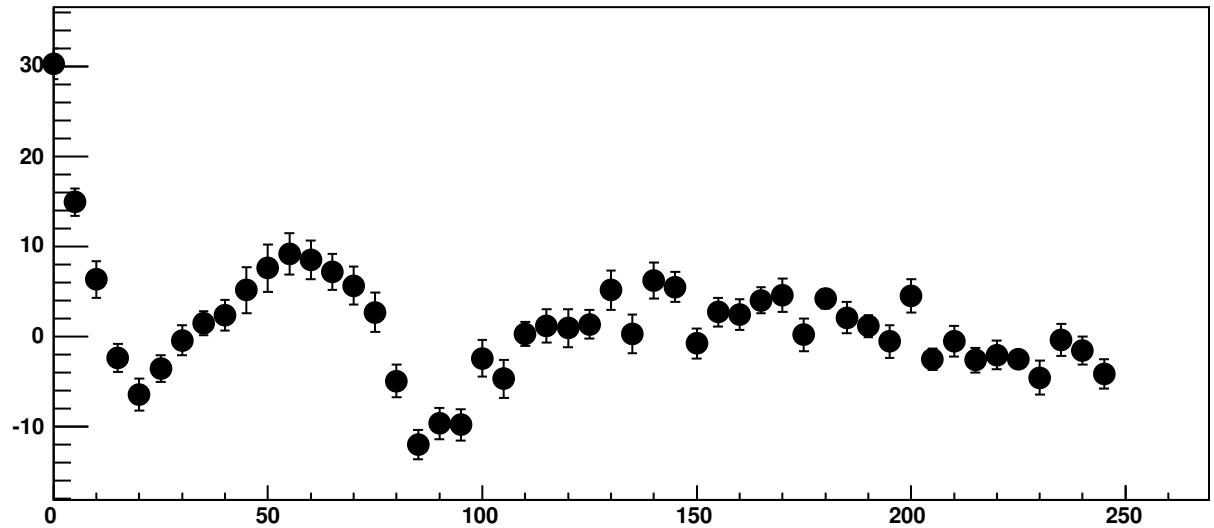
Chip 8, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

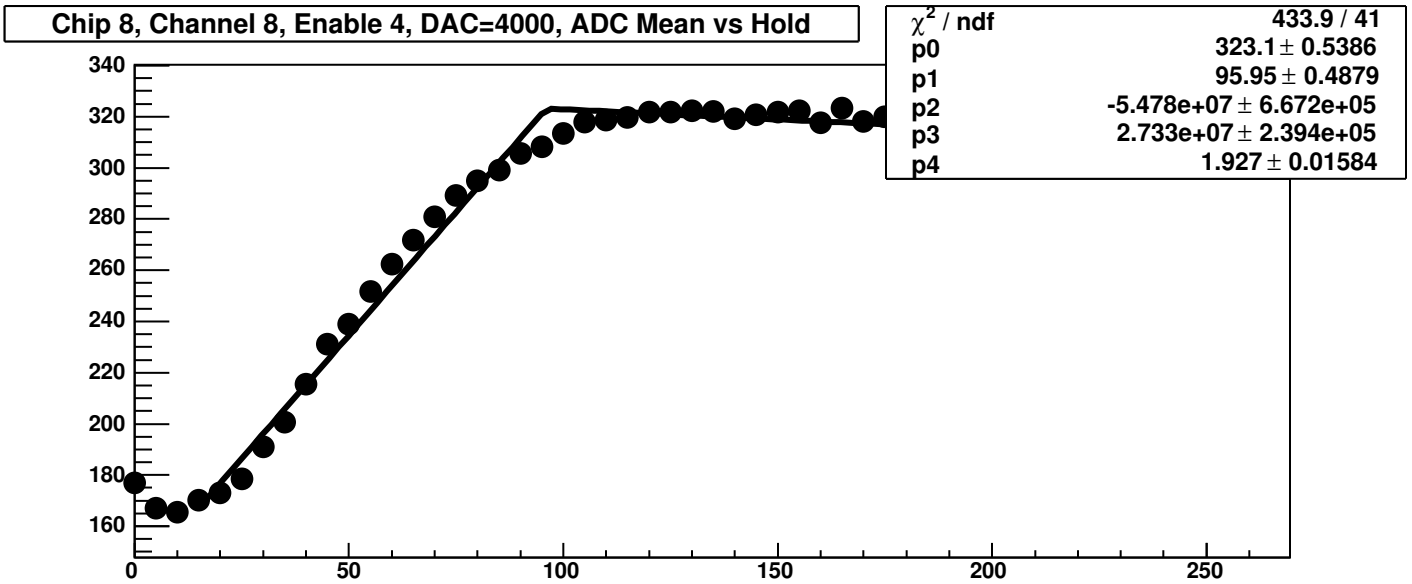


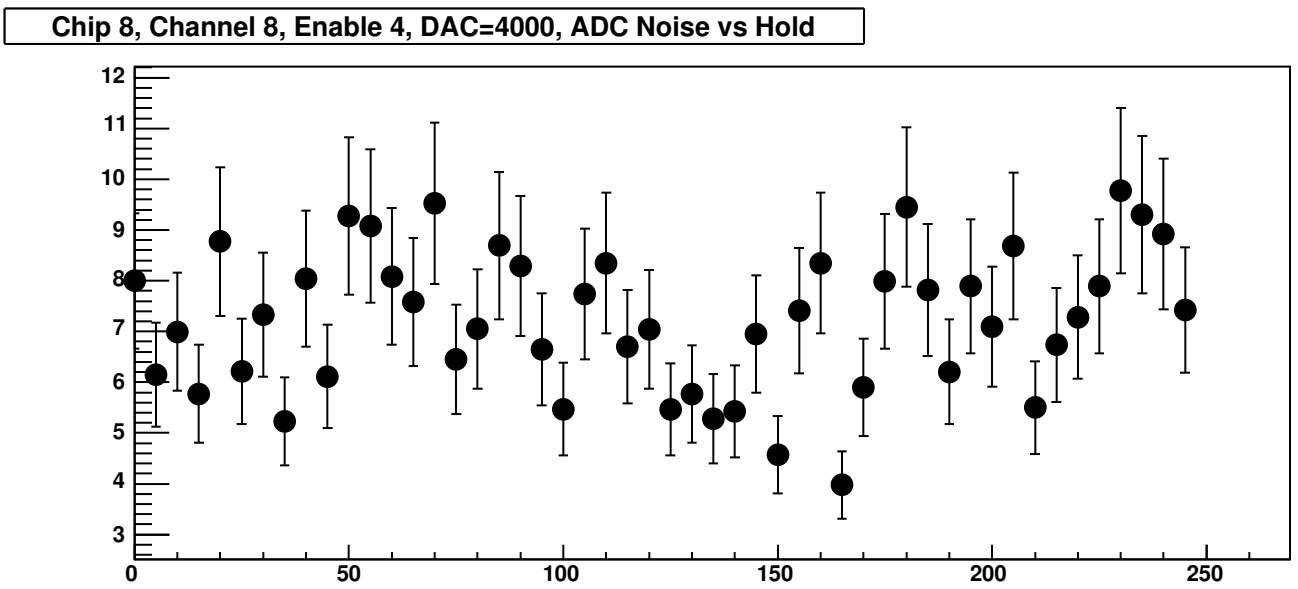
Chip 8, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold

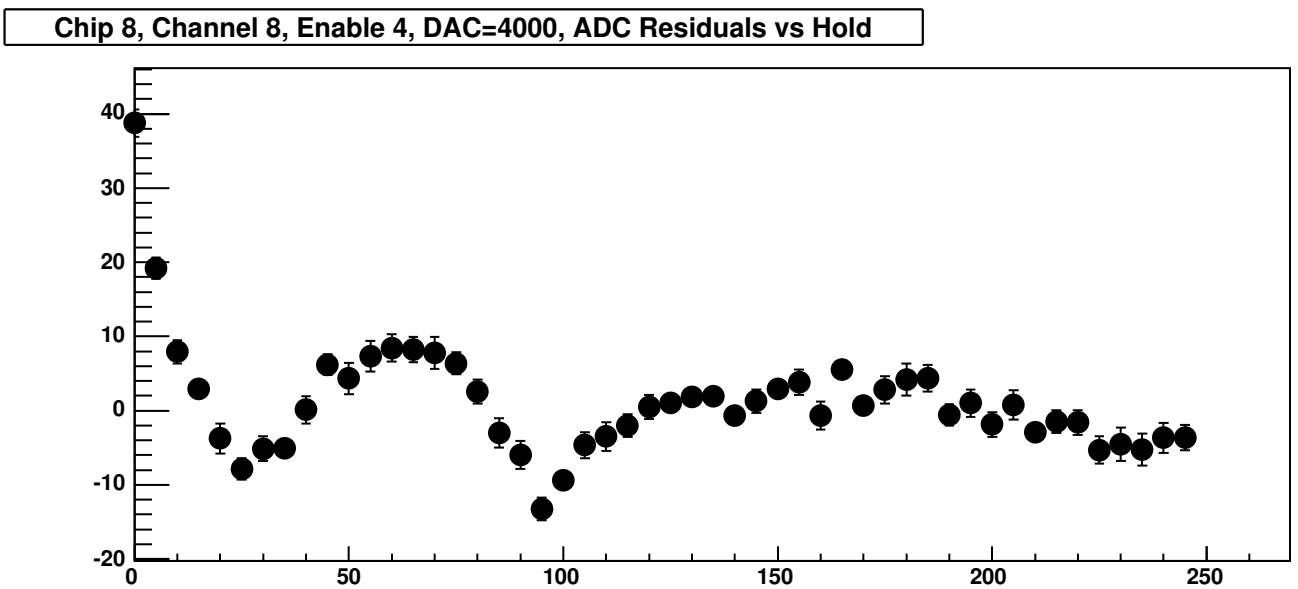


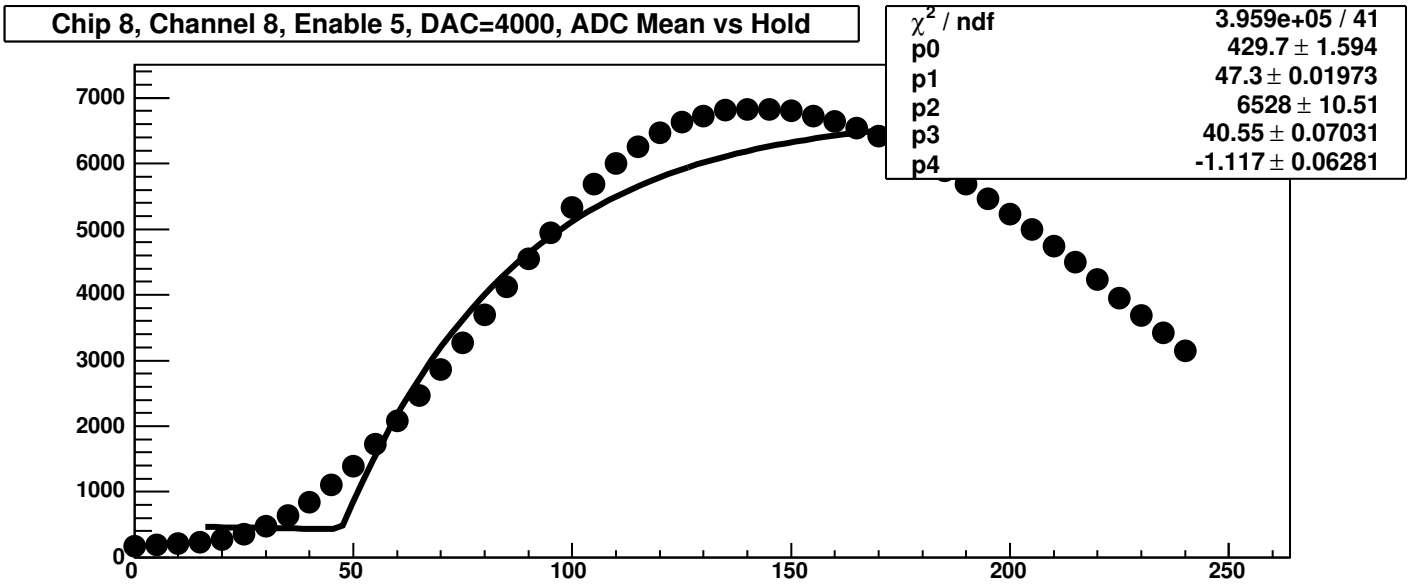
Chip 8, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

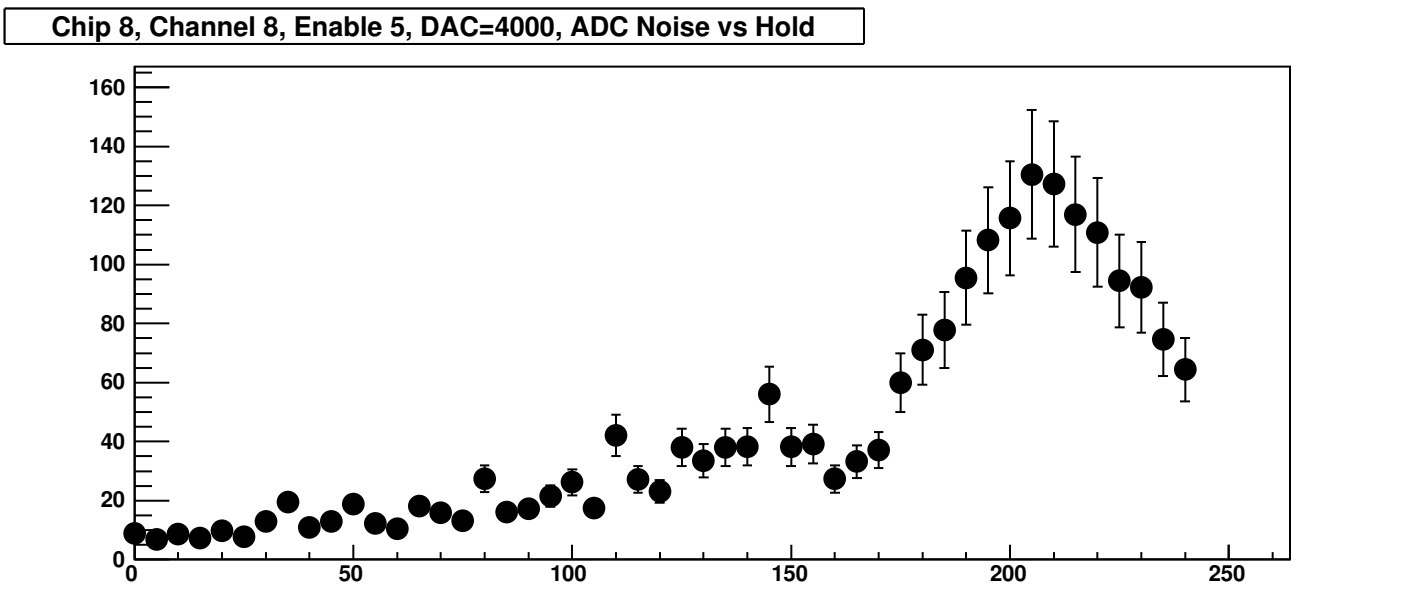


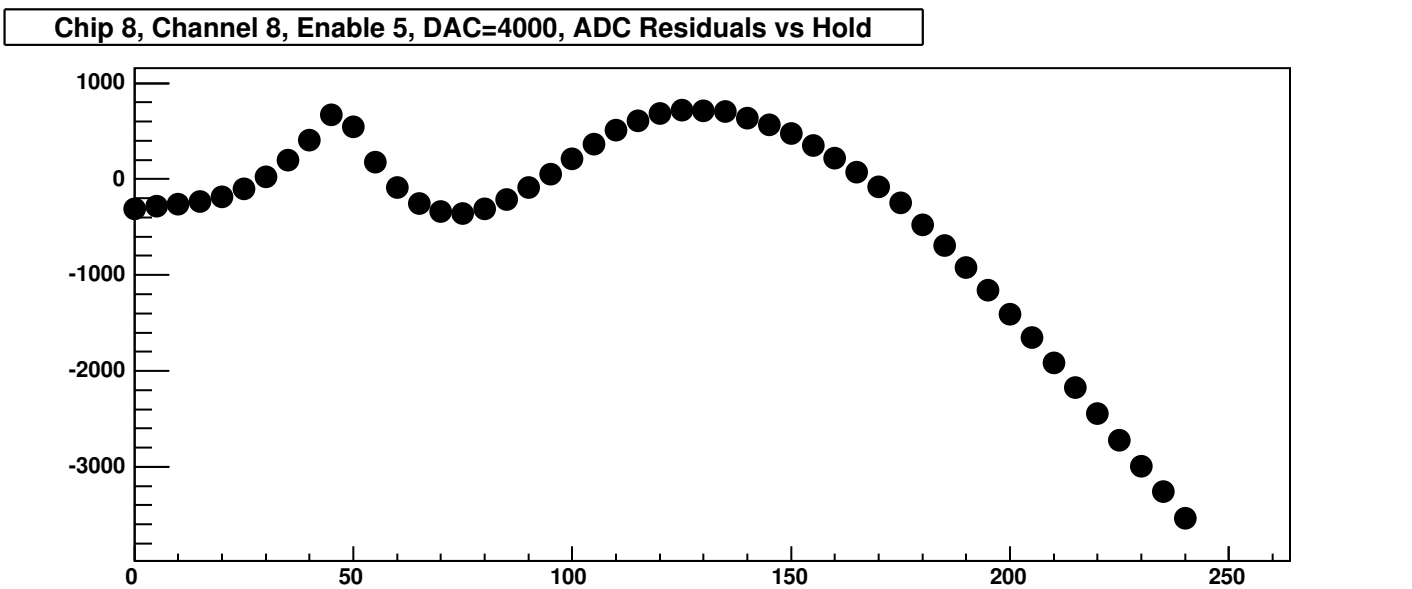

Chip 8, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 433.9 / 41
p0 323.1 ± 0.5386
p1 95.95 ± 0.4879
p2 -5.478e+07 ± 6.672e+05
p3 2.733e+07 ± 2.394e+05
p4 1.927 ± 0.01584


Chip 8, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold


Chip 8, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

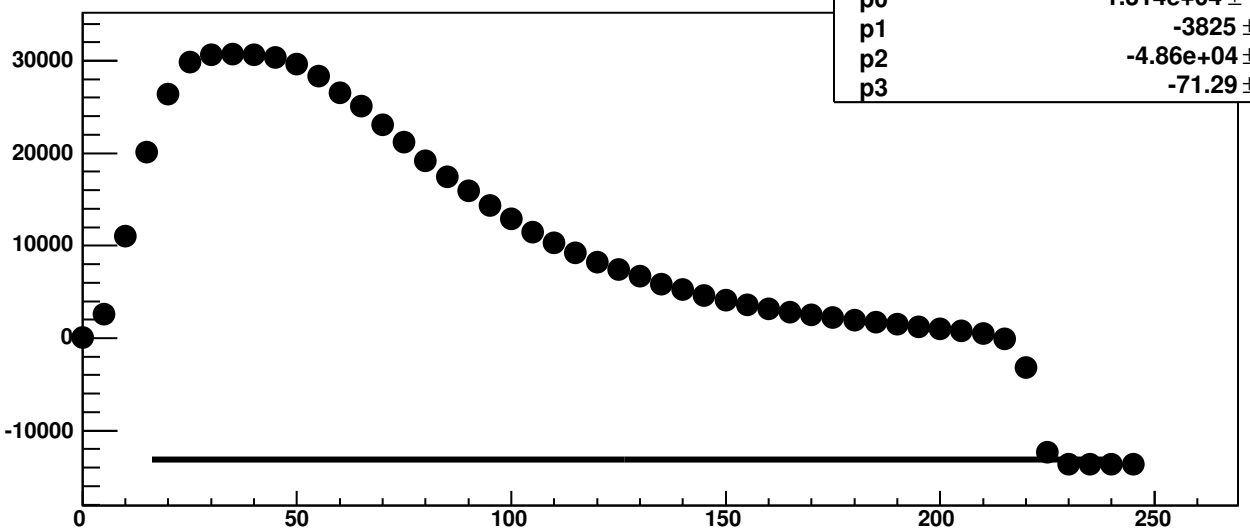

Chip 8, Channel 8, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.959e+05 / 41
p0 429.7 ± 1.594
p1 47.3 ± 0.01973
p2 6528 ± 10.51
p3 40.55 ± 0.07031
p4 -1.117 ± 0.06281
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 8, Channel 8, Enable 5, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

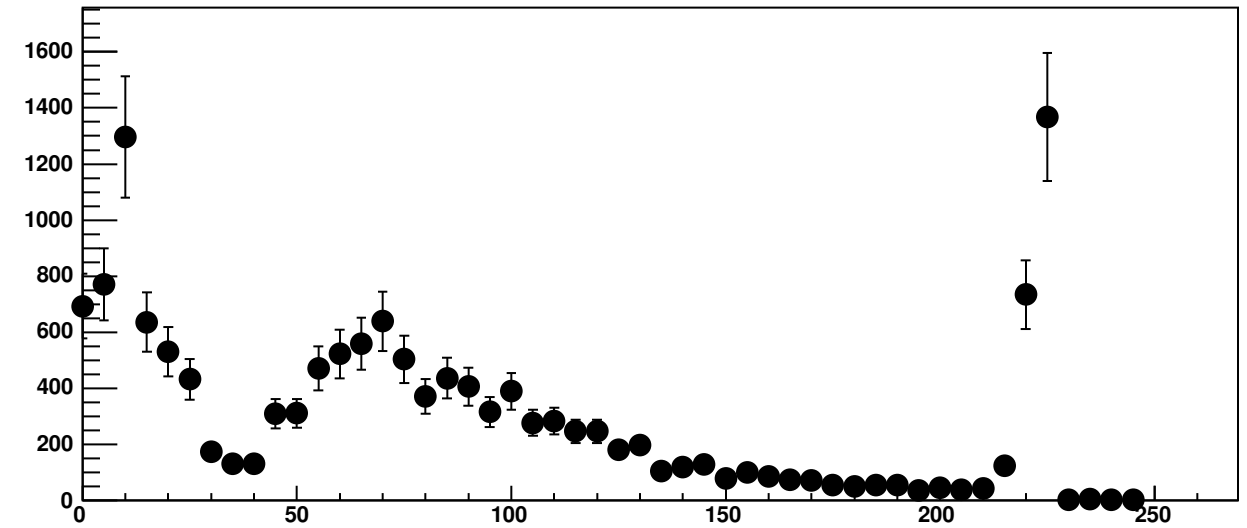

Chip 8, Channel 8, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

Chip 8, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold

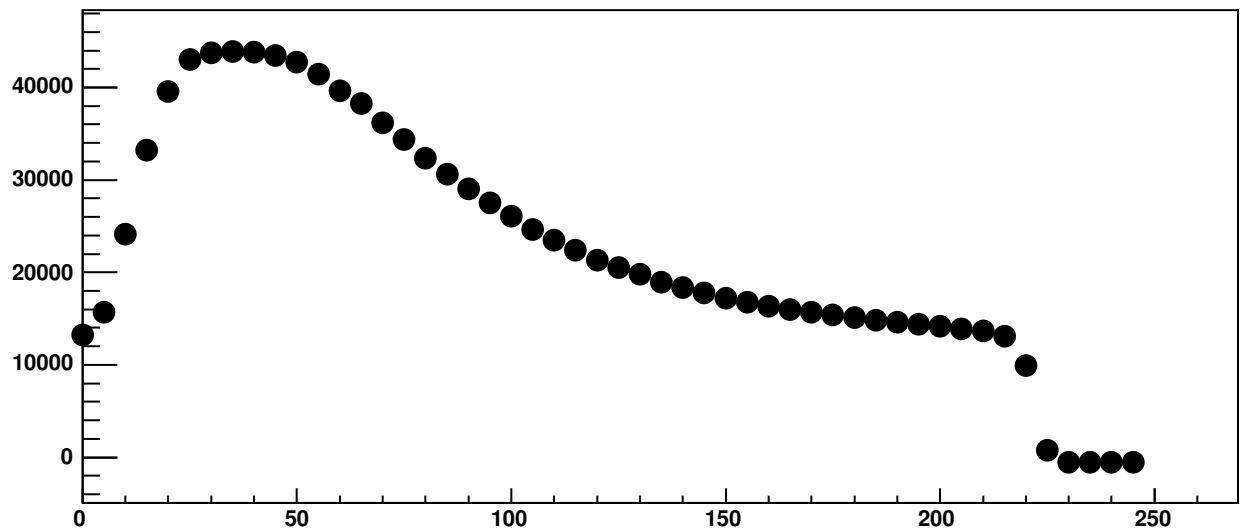
χ^2 / ndf	2.944e+07 / 42
p0	-1.314e+04 ± 0.5575
p1	-3825 ± 1.909
p2	-4.86e+04 ± 1.414
p3	-71.29 ± 1.414

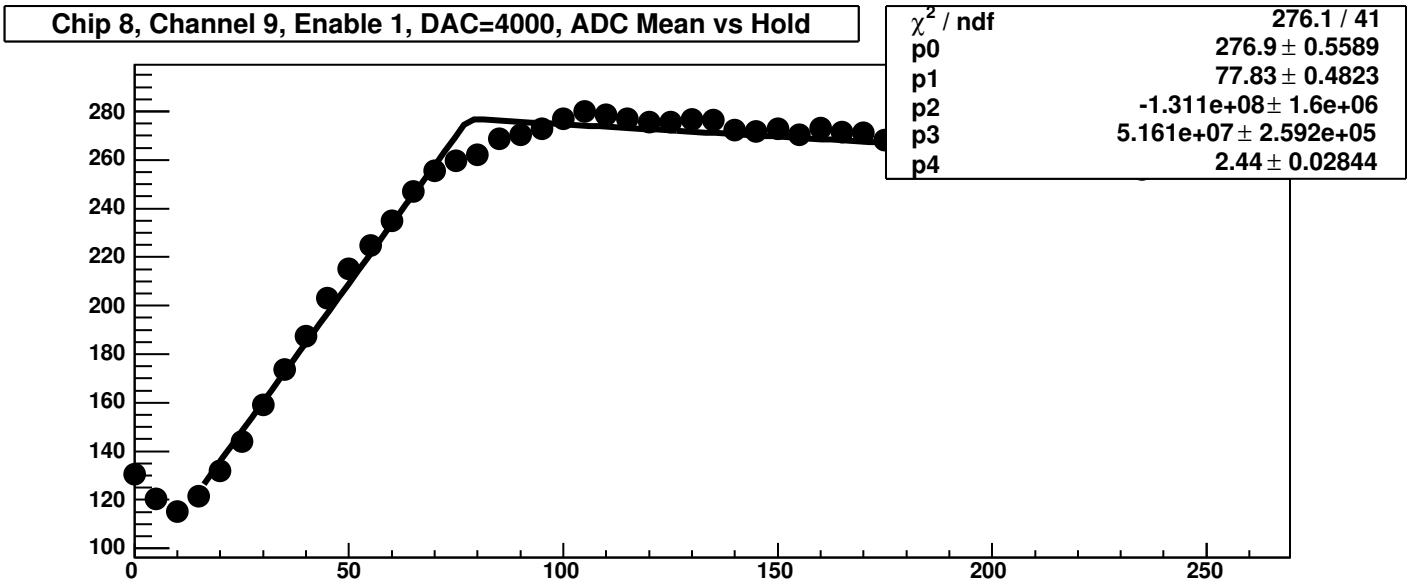


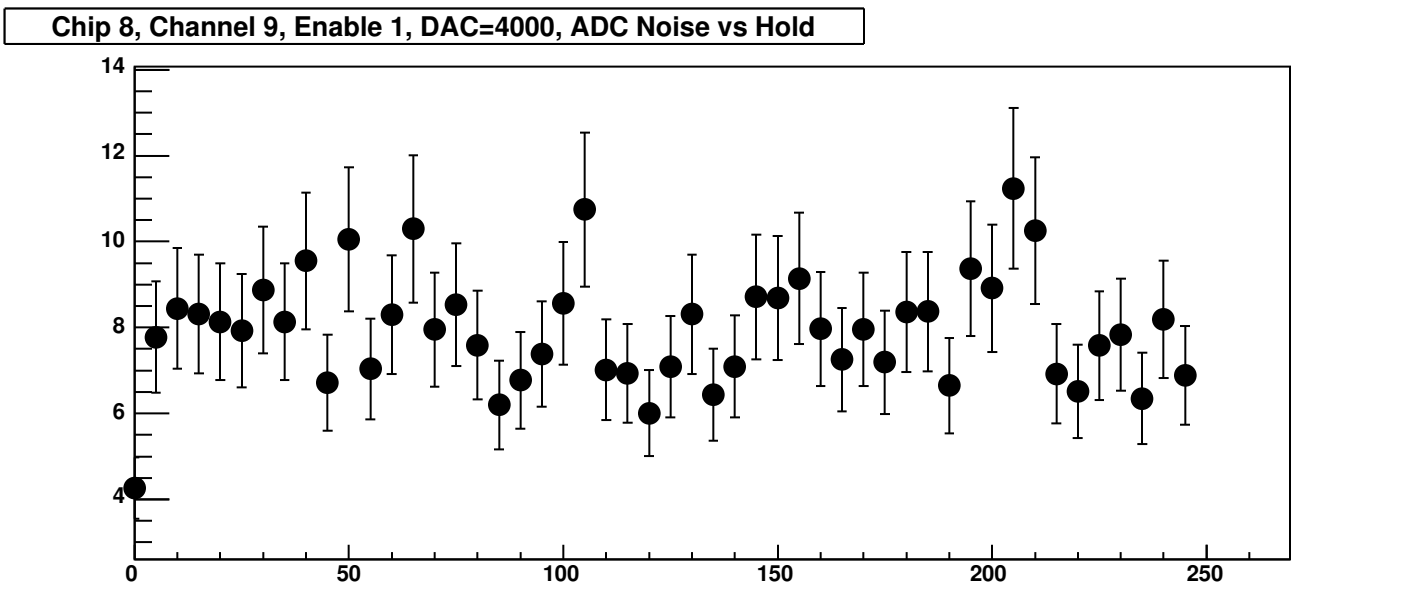
Chip 8, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold

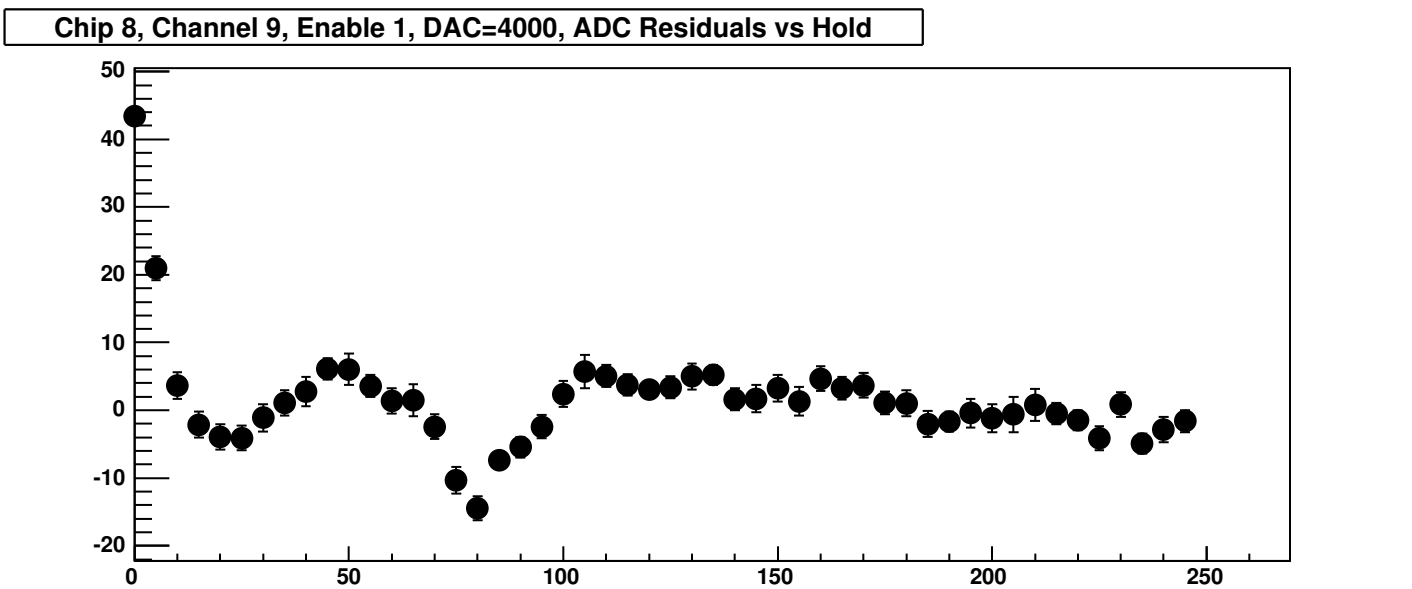


Chip 8, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold

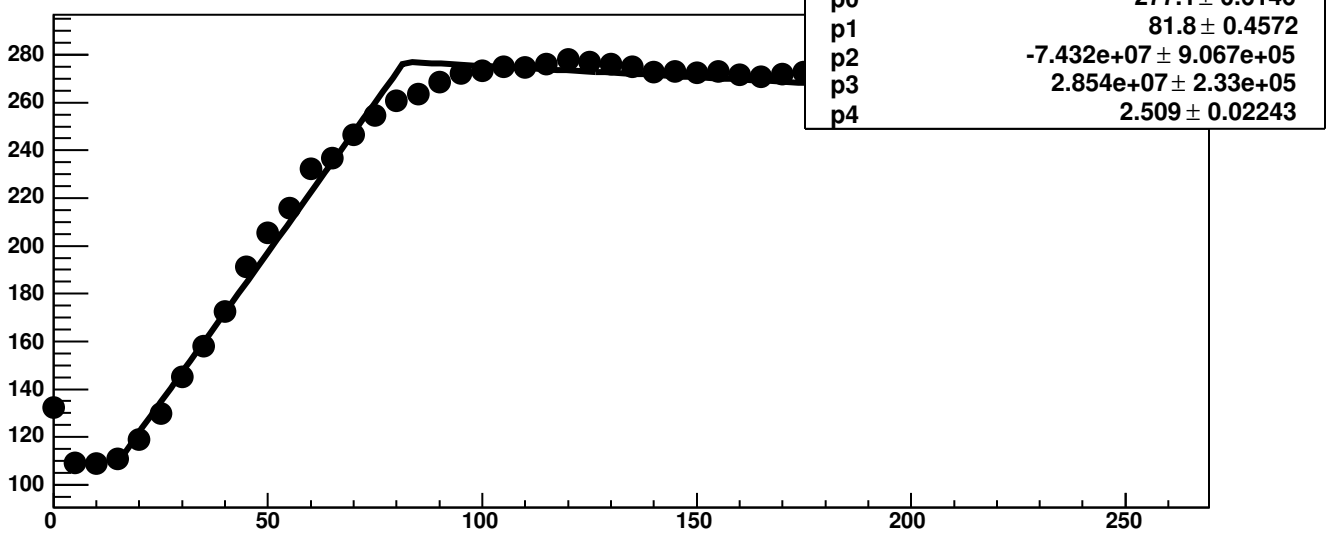



Chip 8, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 276.1 / 41
p0 276.9 ± 0.5589
p1 77.83 ± 0.4823
p2 -1.311e+08 ± 1.6e+06
p3 5.161e+07 ± 2.592e+05
p4 2.44 ± 0.02844
280
260
240
220
200
180
160
140
120
100
0
50
100
150
200
250

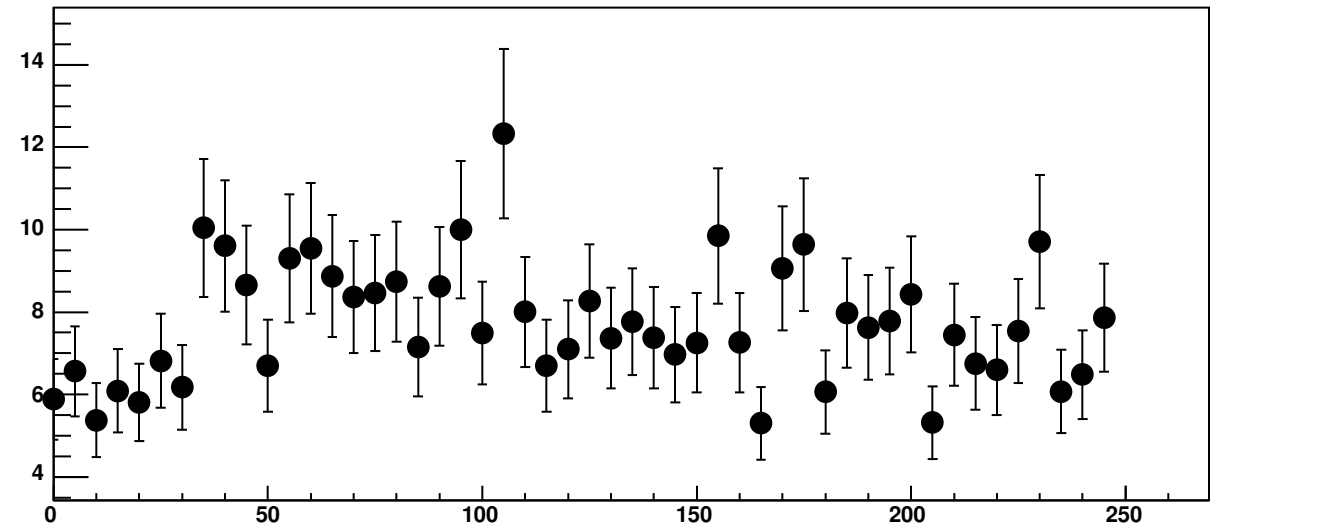

Chip 8, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 8, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
-20
0
50
100
150
200
250

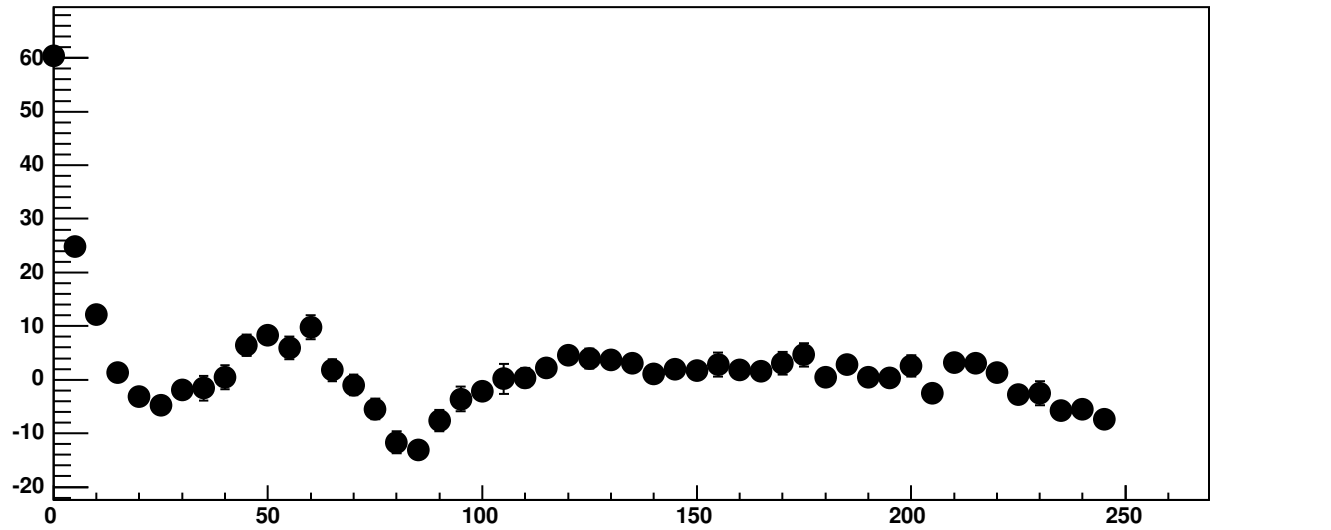
Chip 8, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold



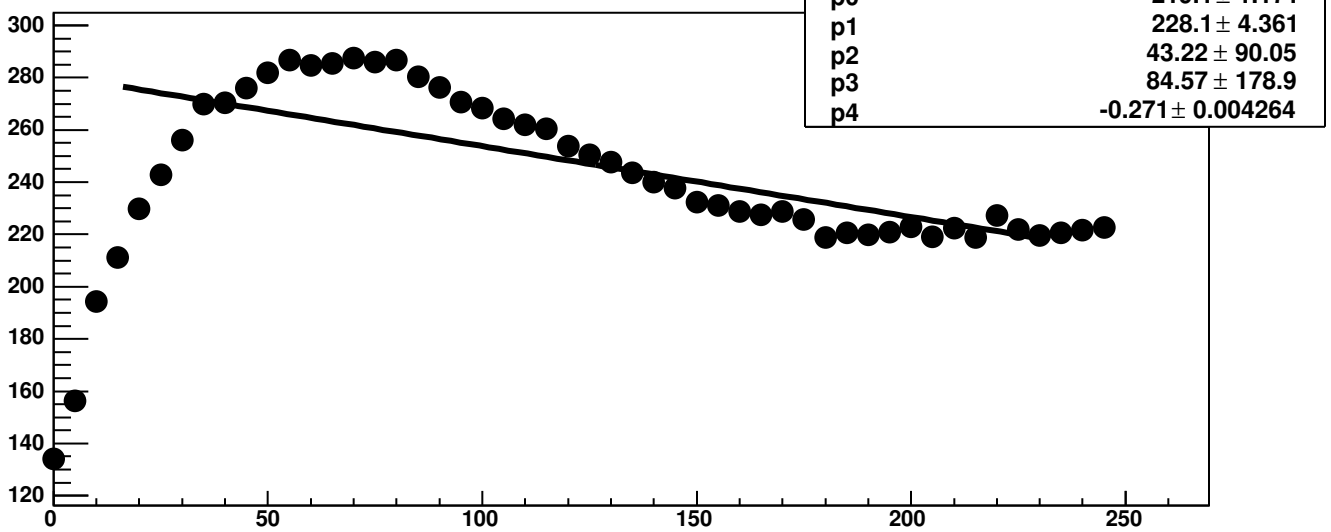
Chip 8, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



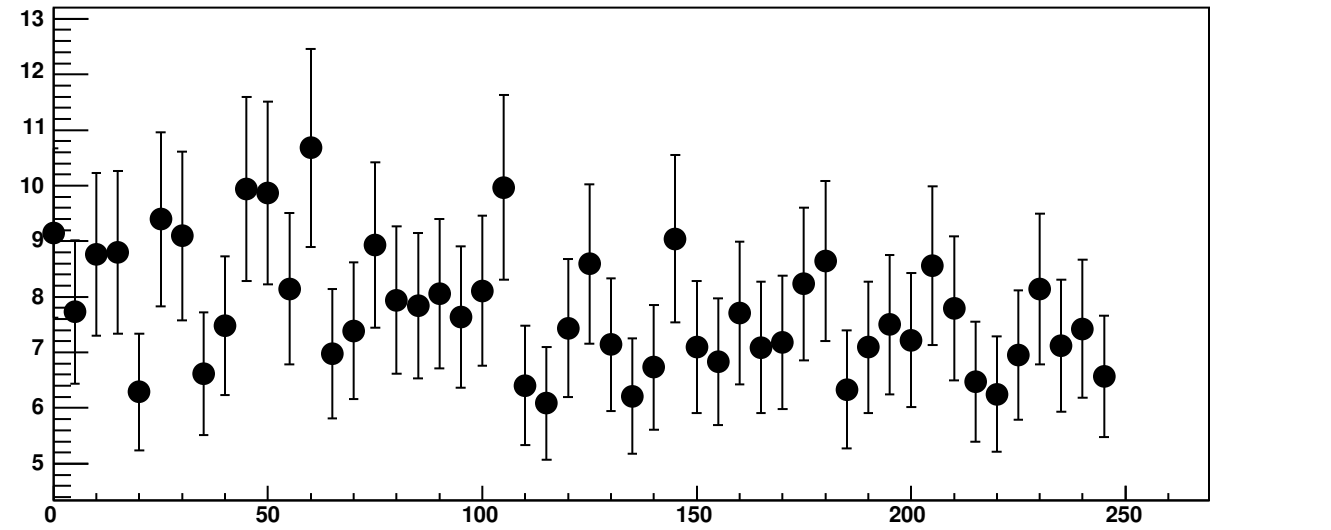
Chip 8, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold



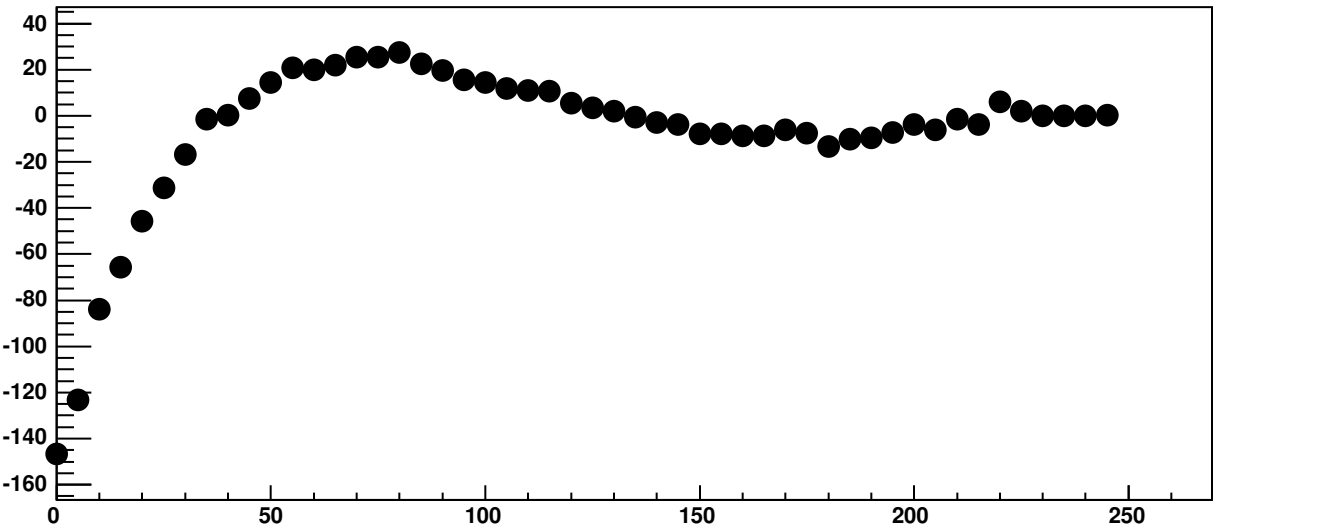
Chip 8, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold



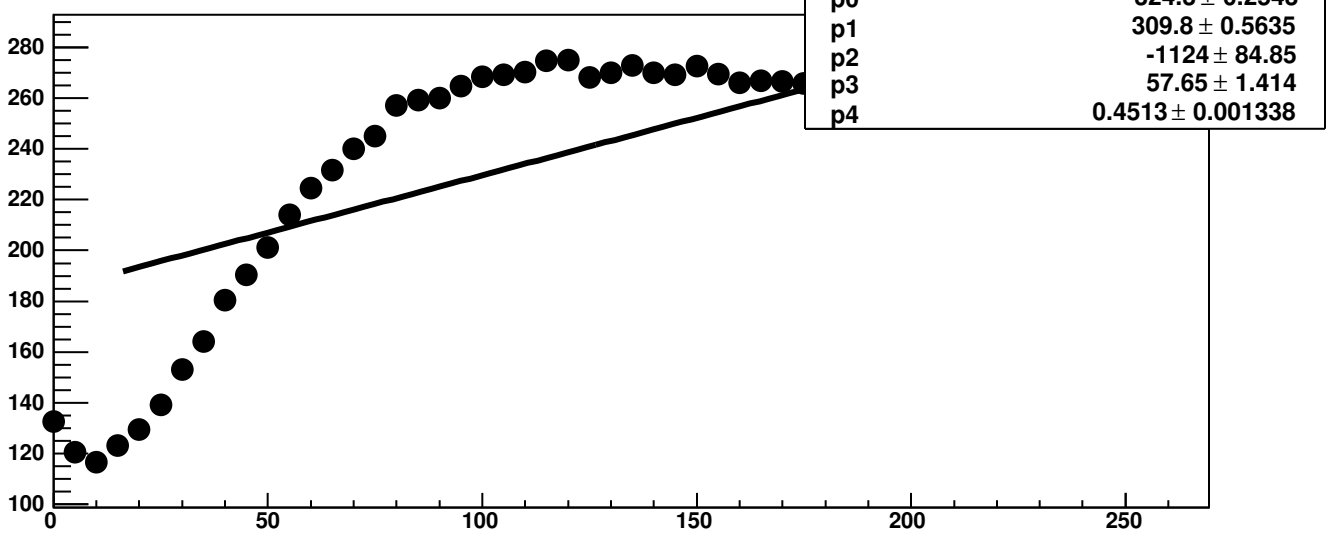
Chip 8, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold



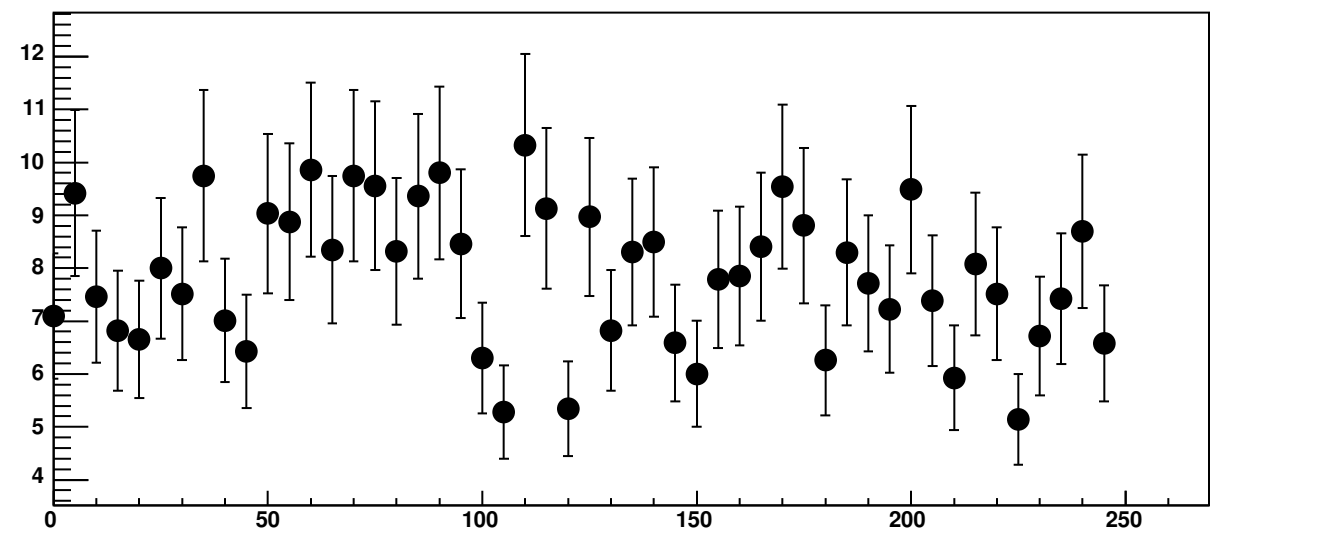
Chip 8, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold



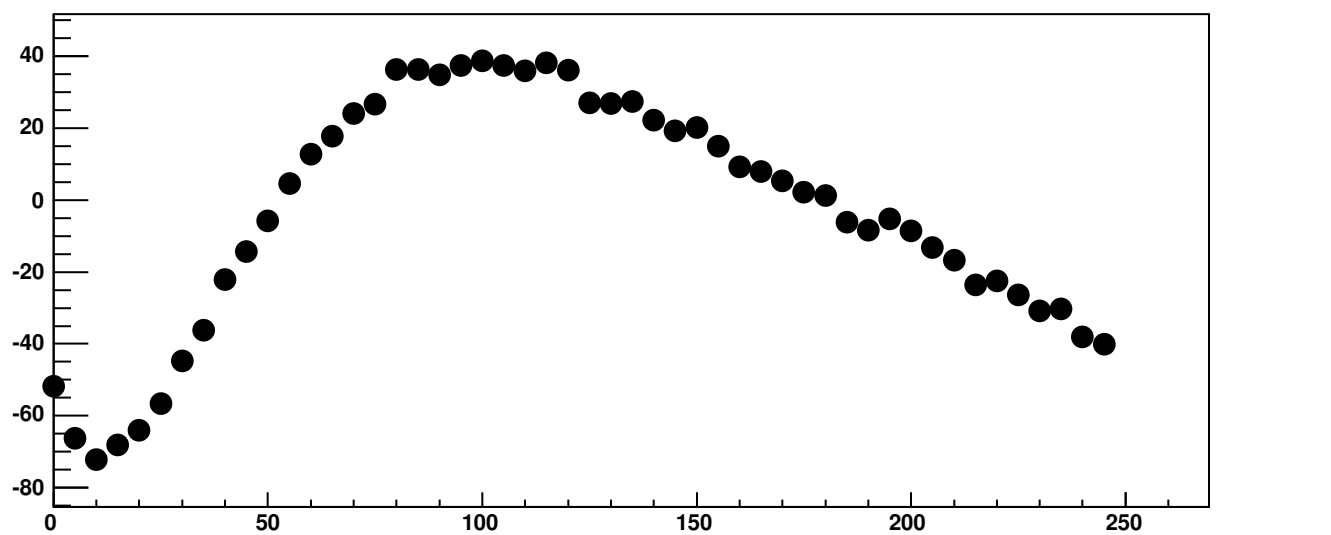
Chip 8, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

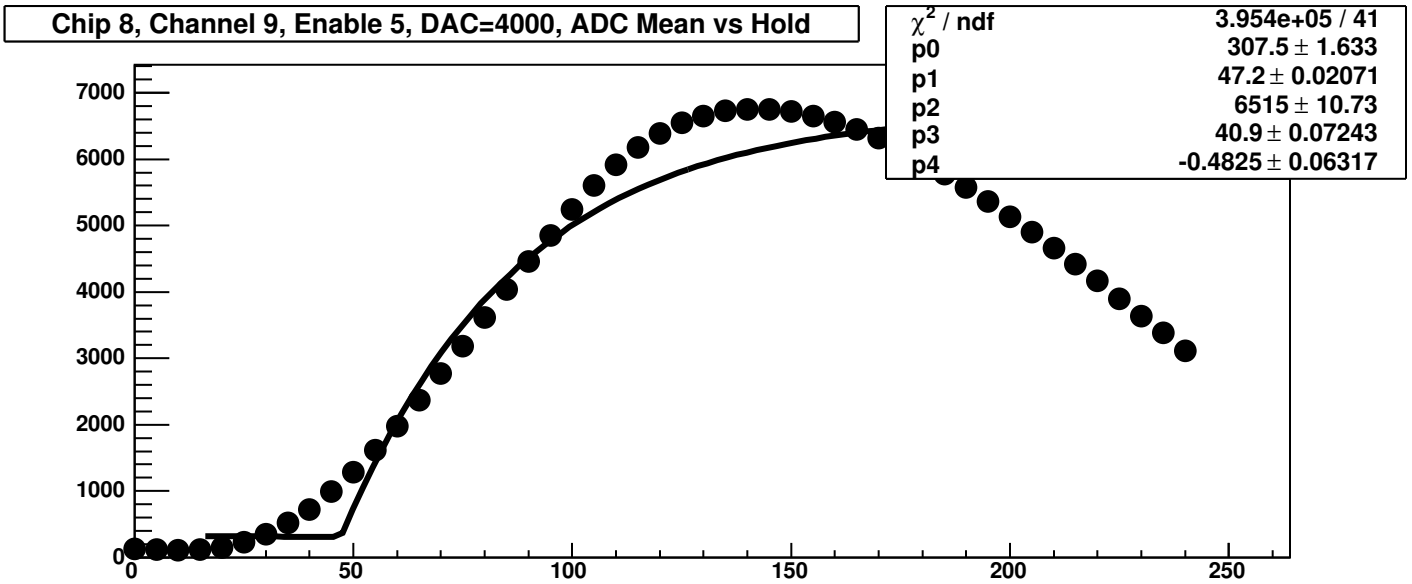


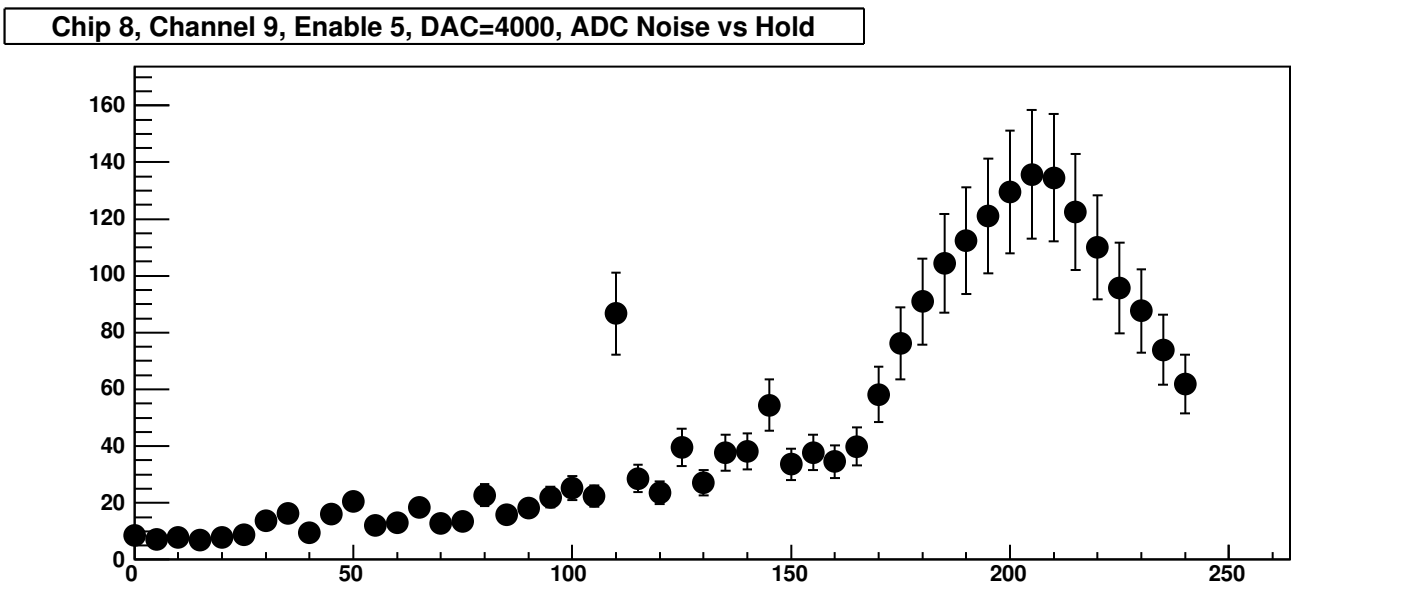
Chip 8, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold

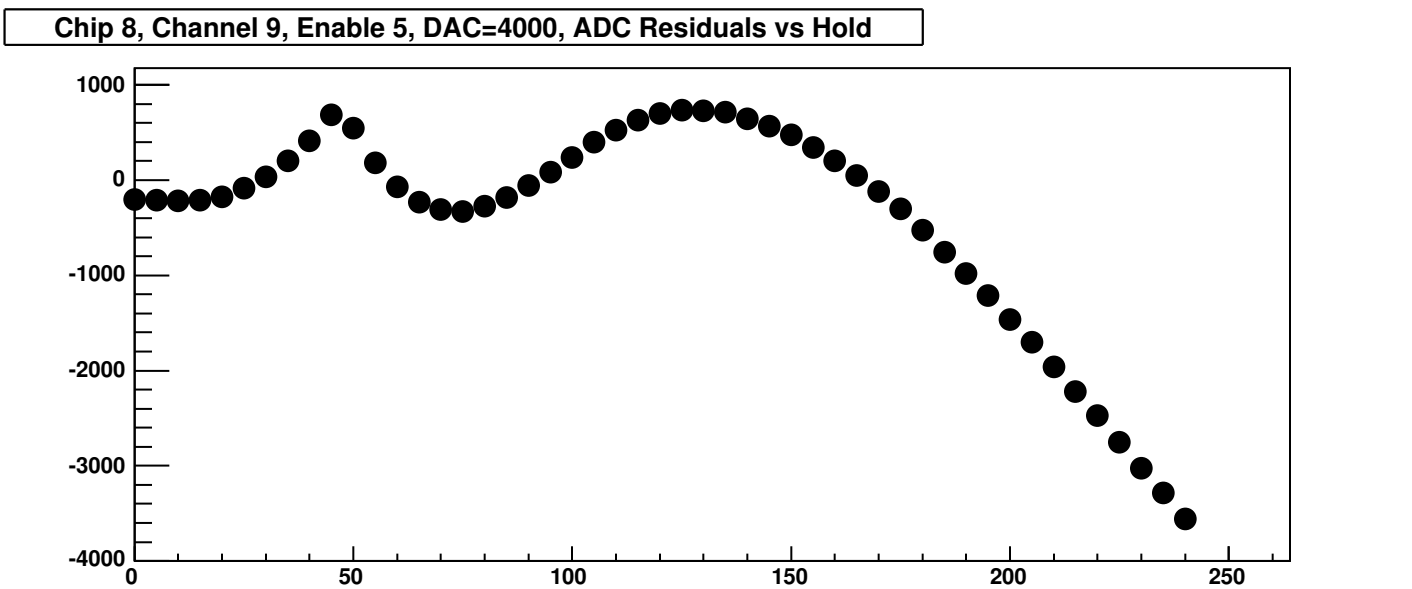


Chip 8, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

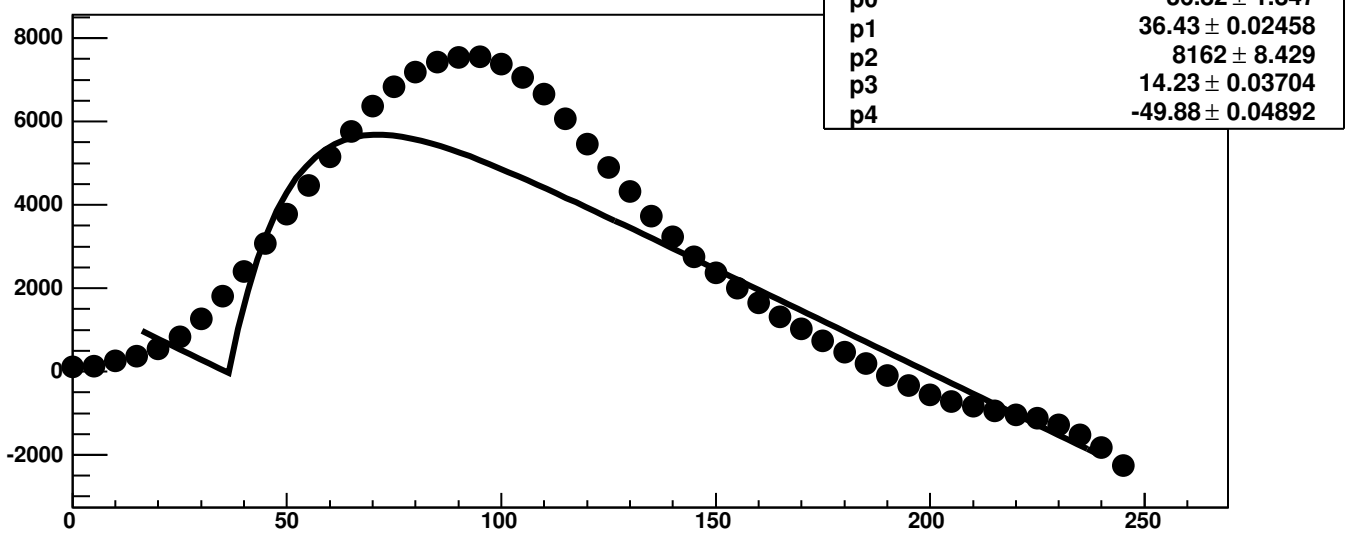



Chip 8, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.954e+05 / 41
p0 307.5 ± 1.633
p1 47.2 ± 0.02071
p2 6515 ± 10.73
p3 40.9 ± 0.07243
p4 -0.4825 ± 0.06317
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

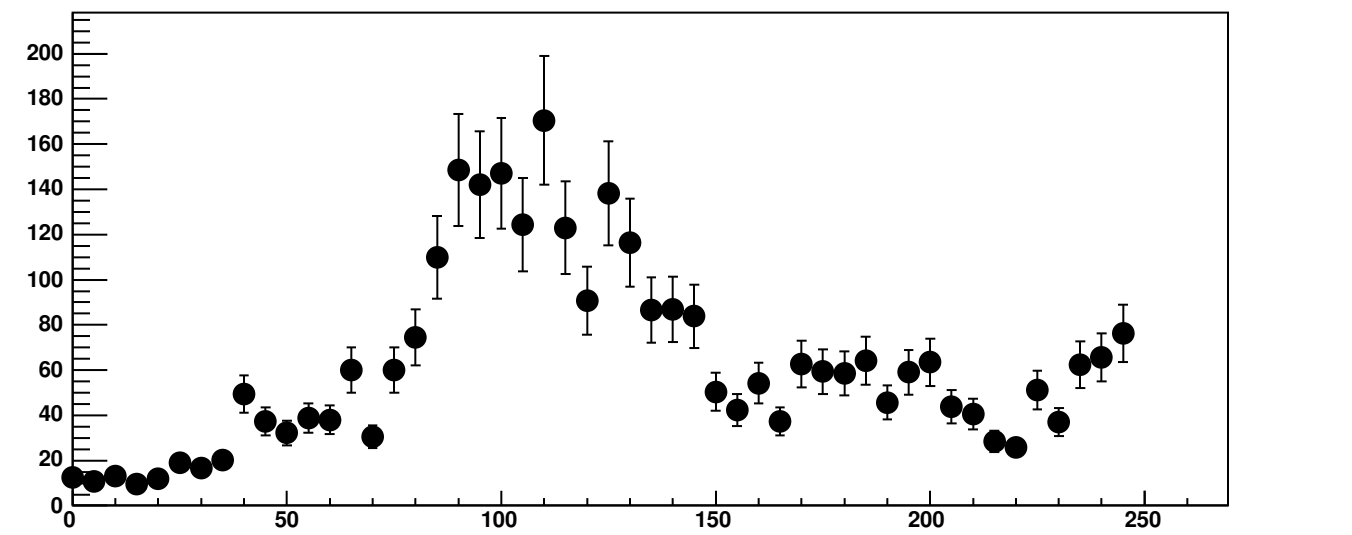

Chip 8, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 8, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

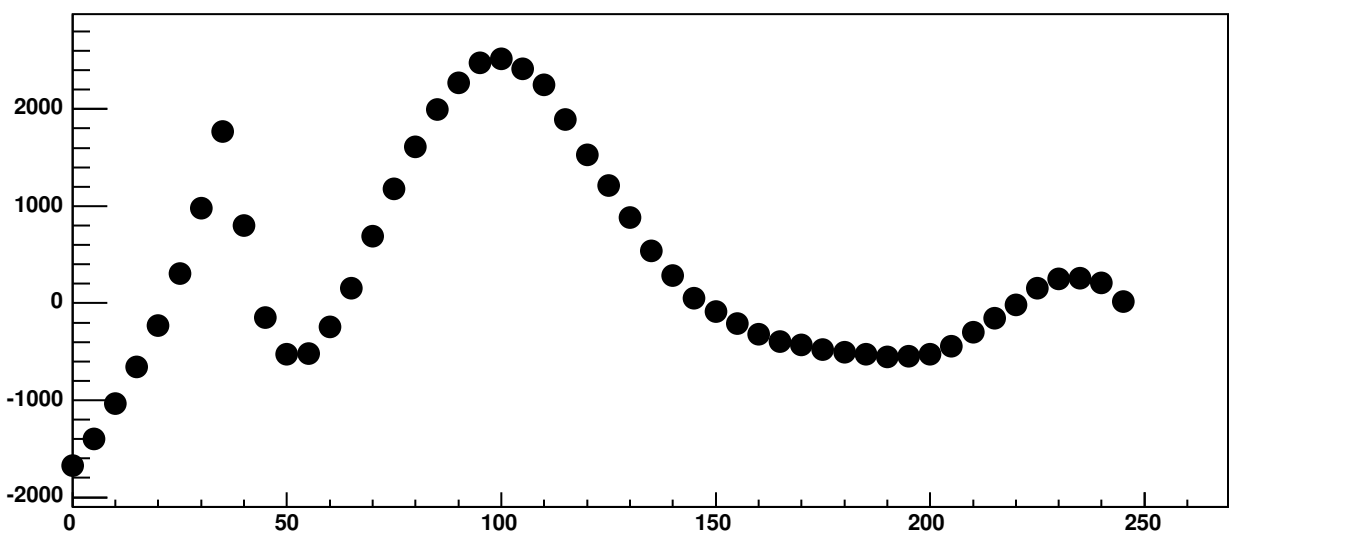
Chip 8, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold



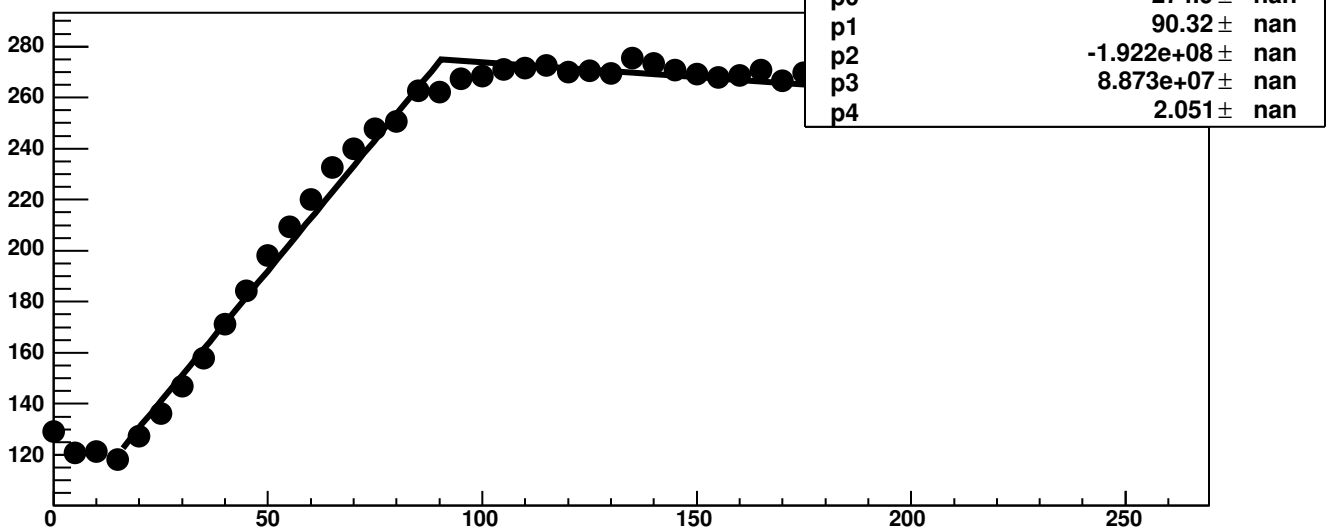
Chip 8, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold



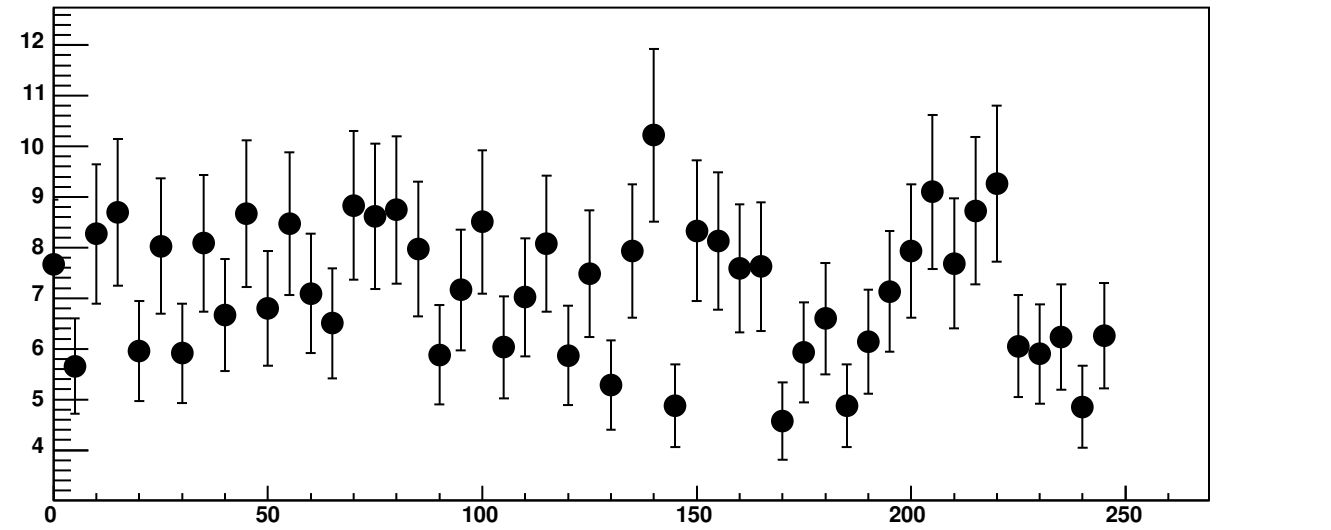
Chip 8, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold



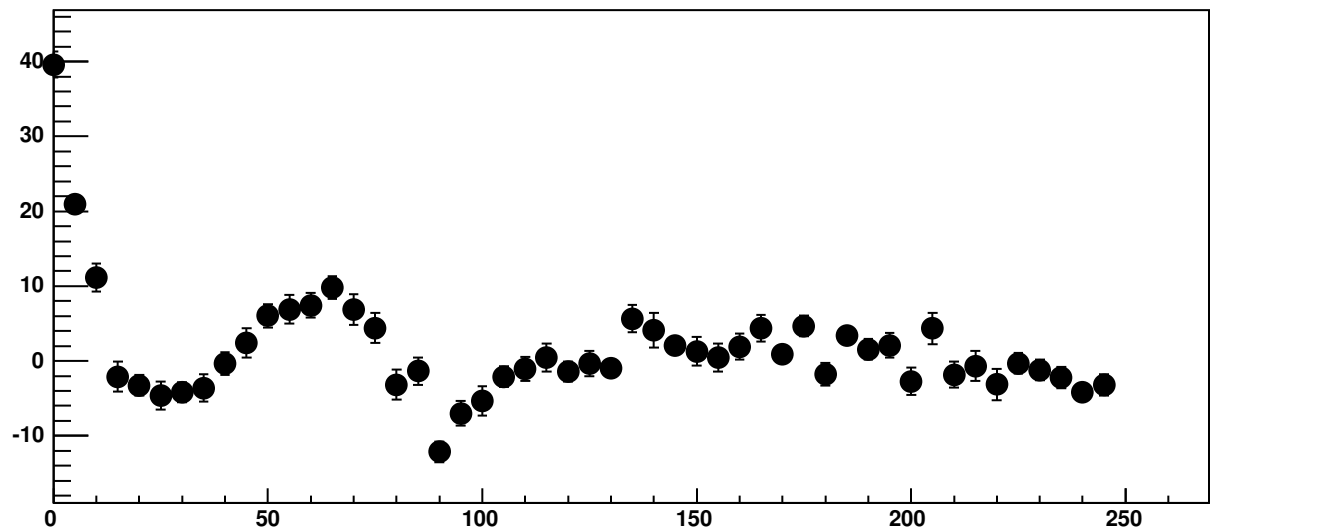
Chip 8, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold



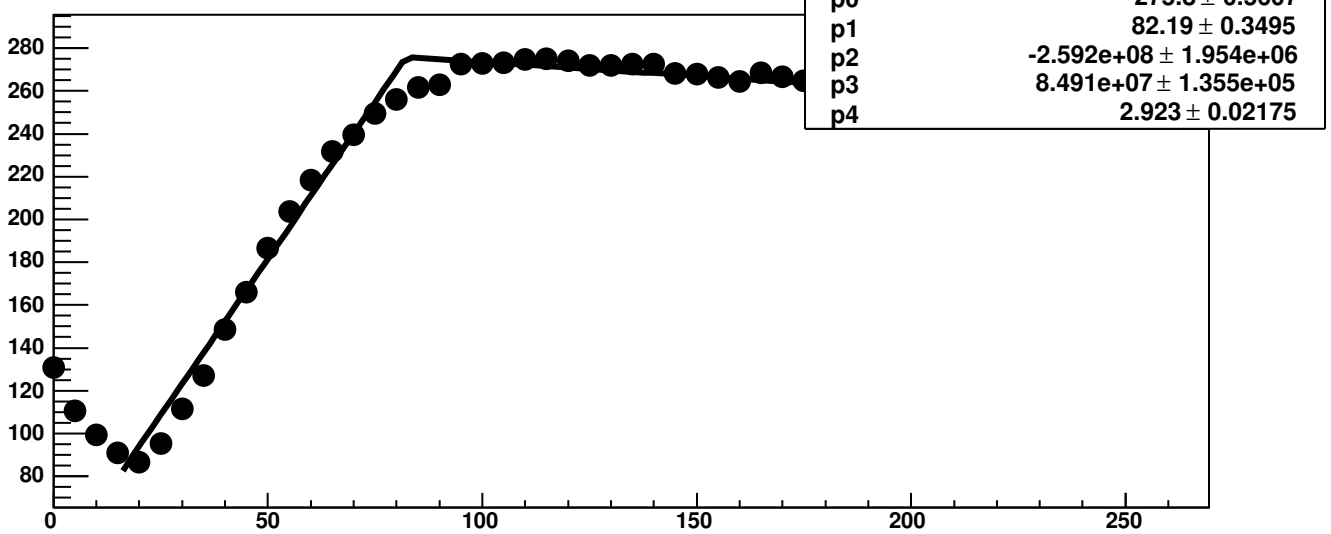
Chip 8, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



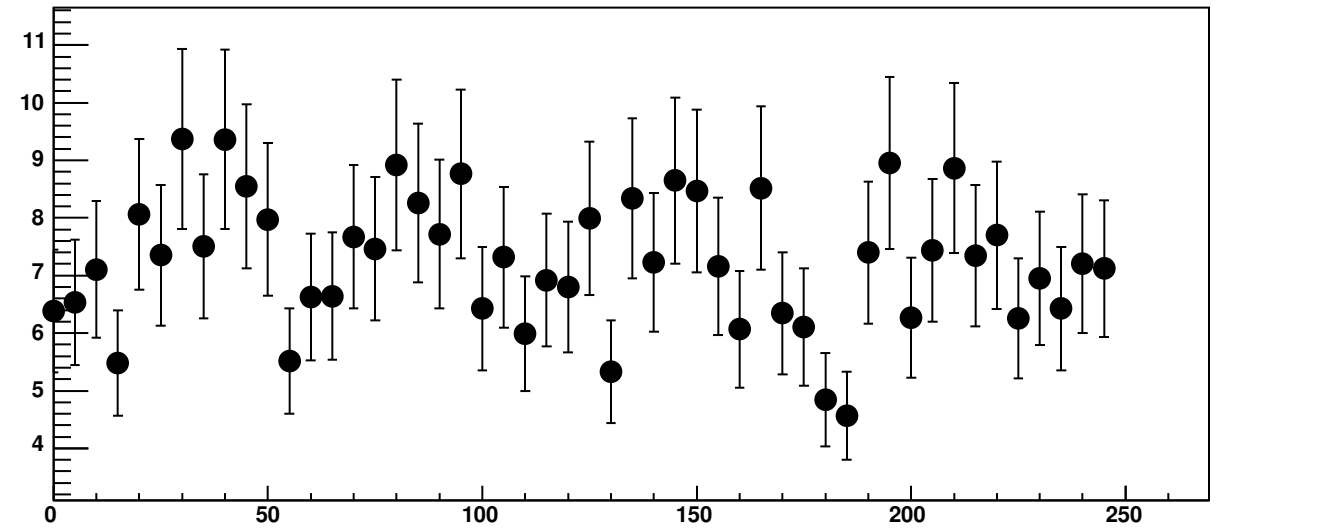
Chip 8, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold



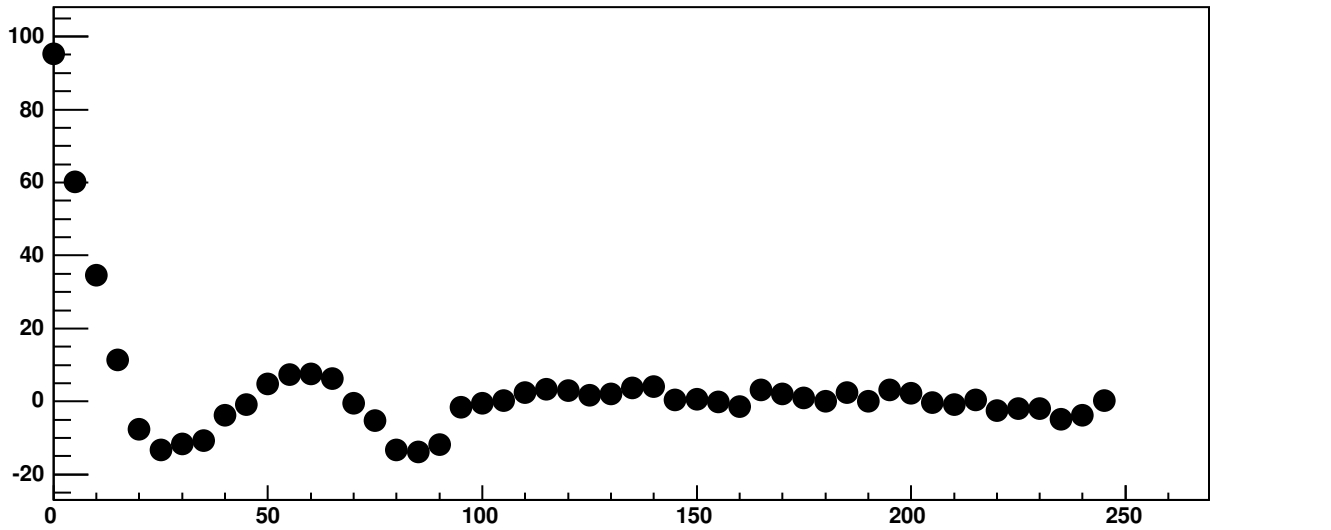
Chip 8, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold



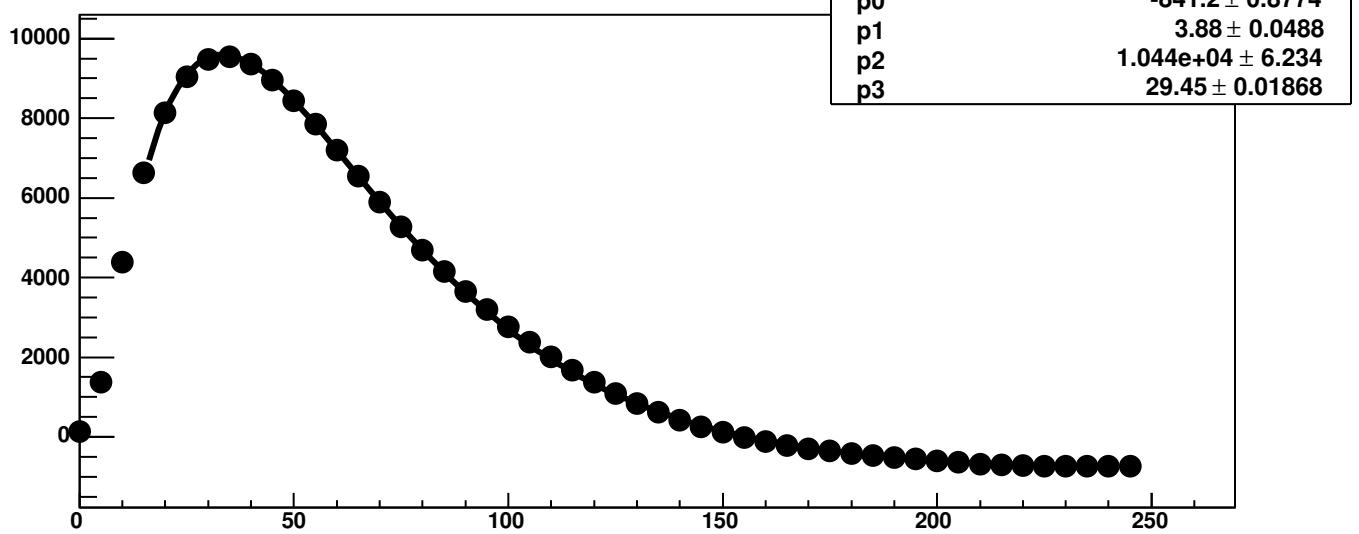
Chip 8, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



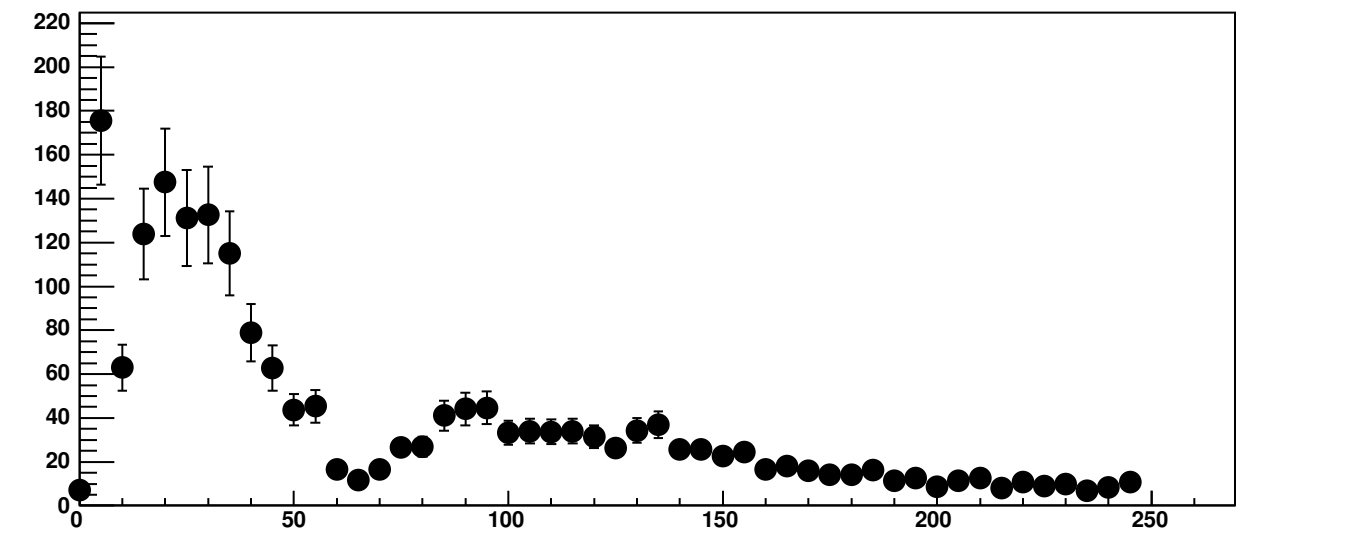
Chip 8, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold



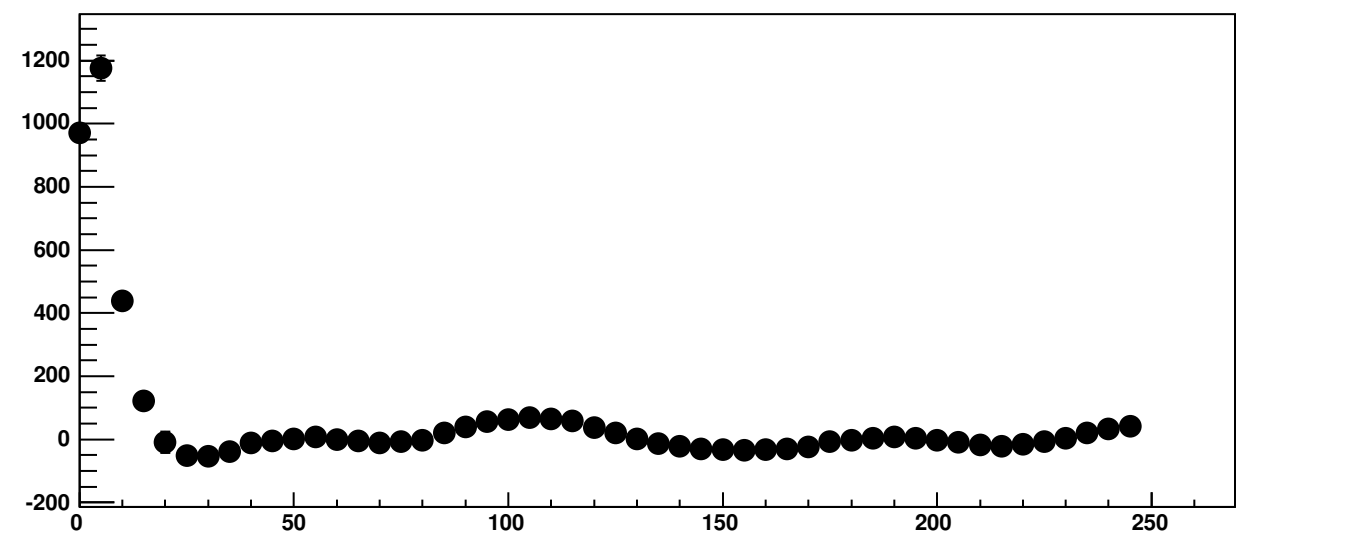
Chip 8, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold



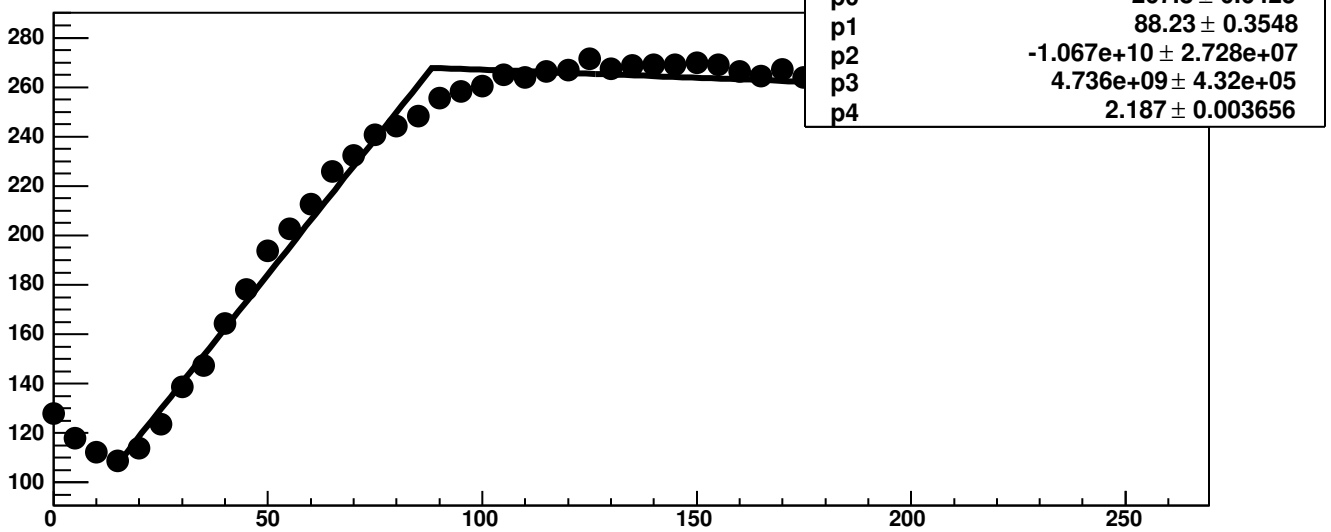
Chip 8, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold



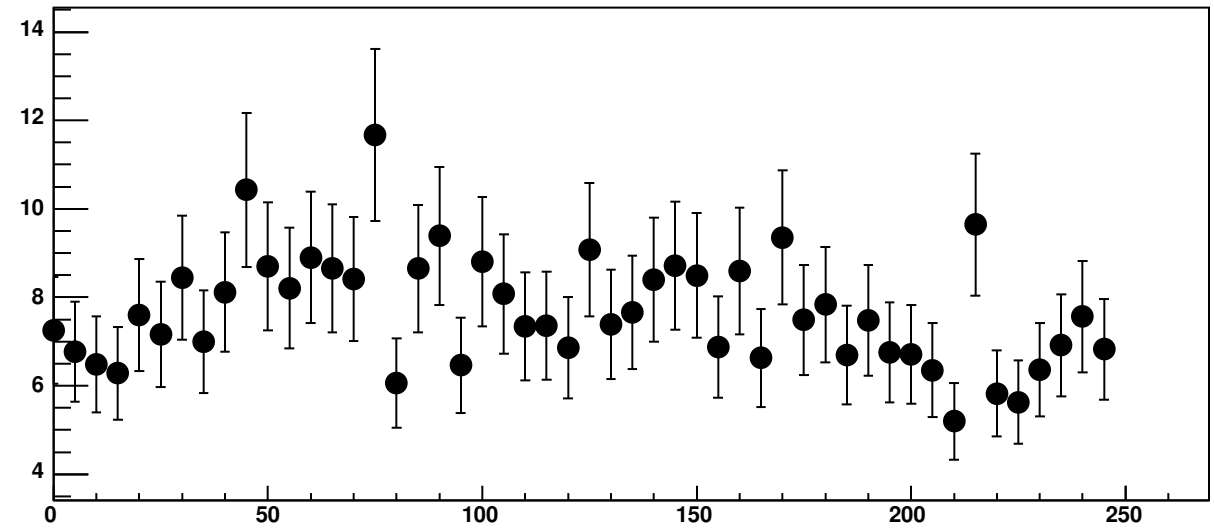
Chip 8, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold



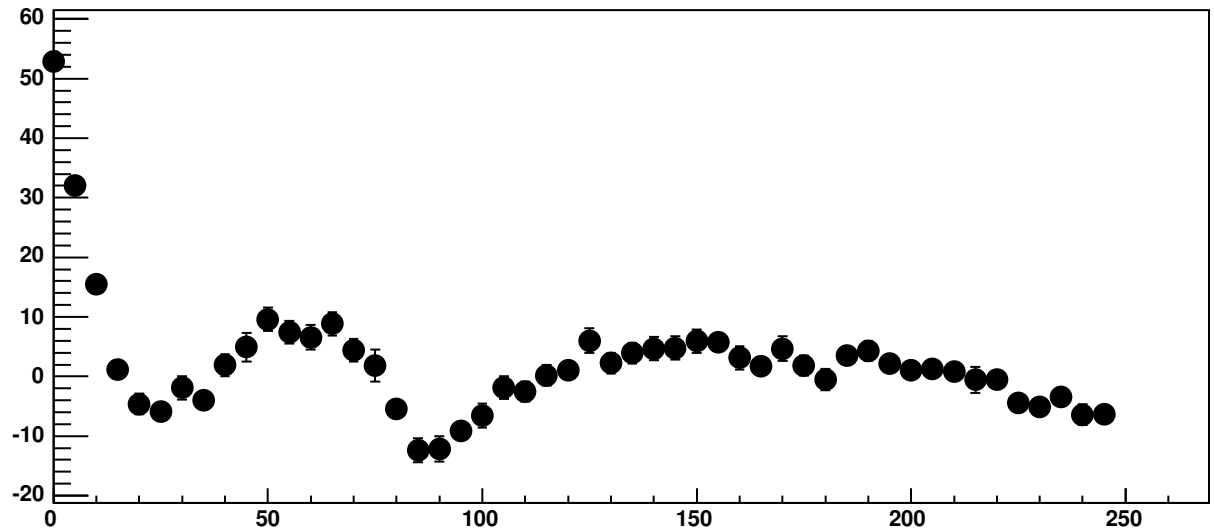
Chip 8, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold



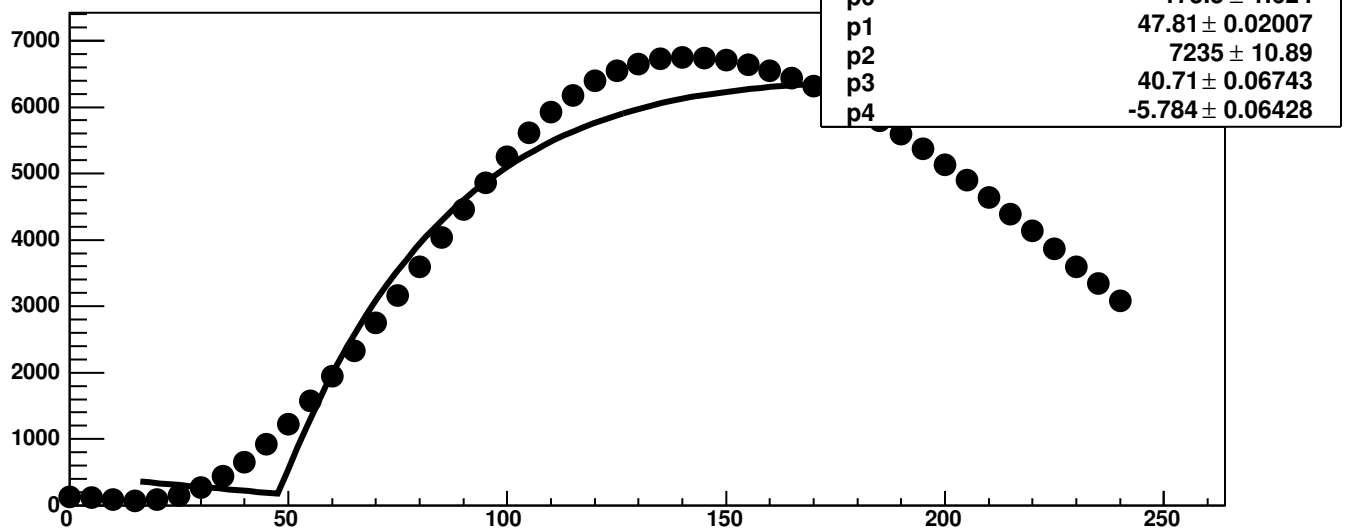
Chip 8, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



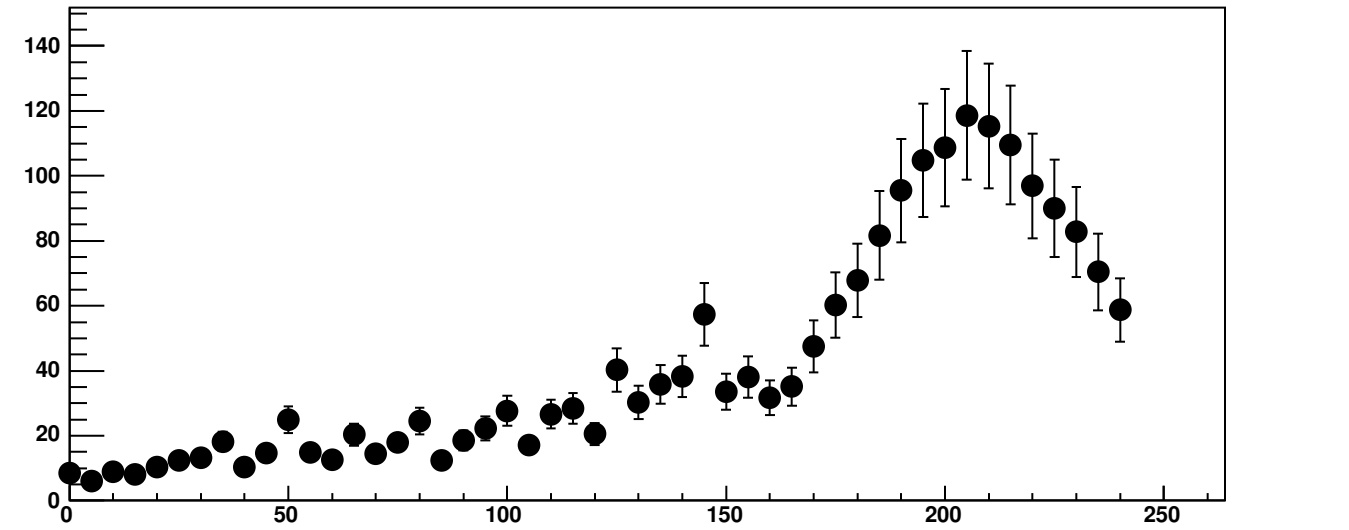
Chip 8, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold



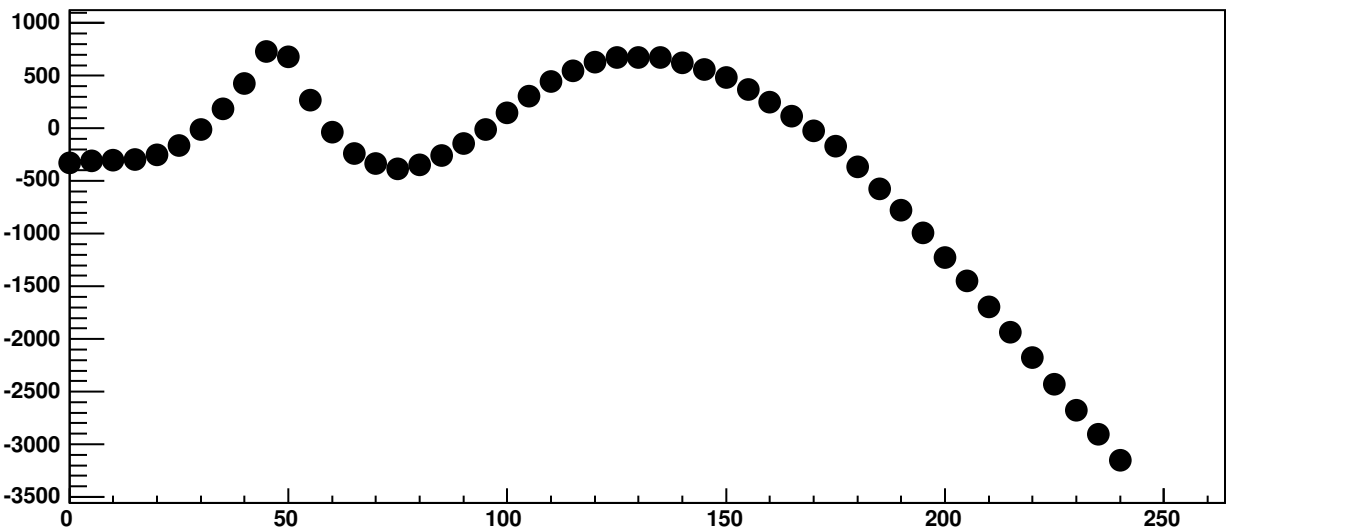
Chip 8, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold

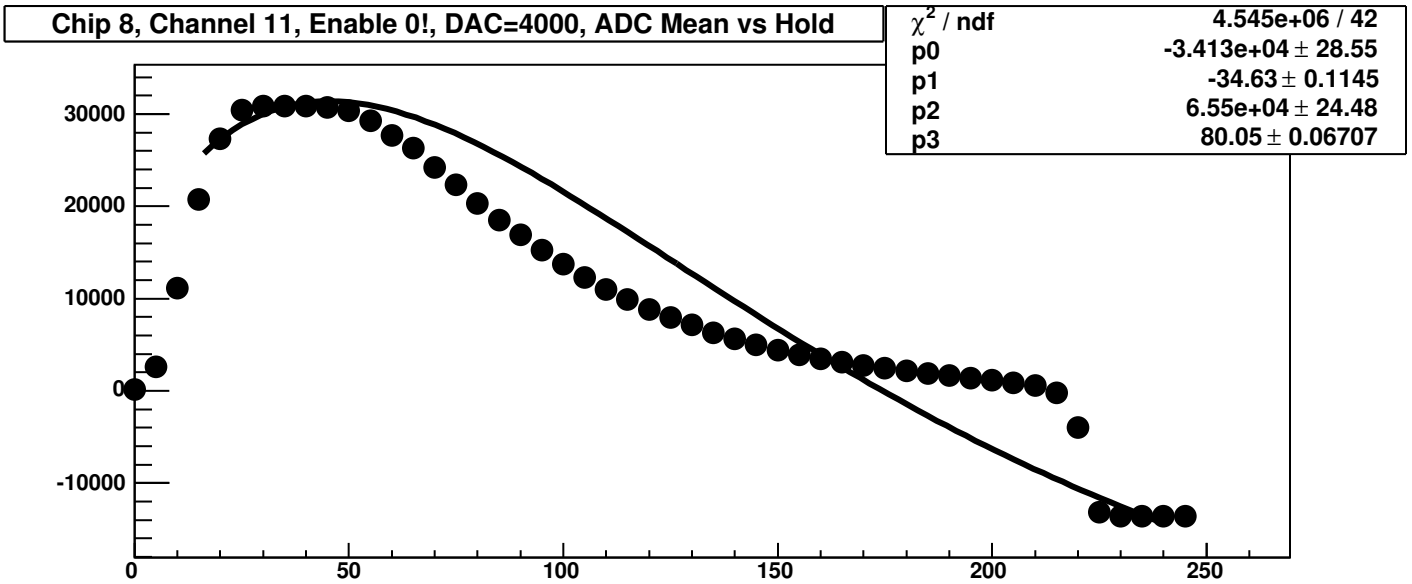


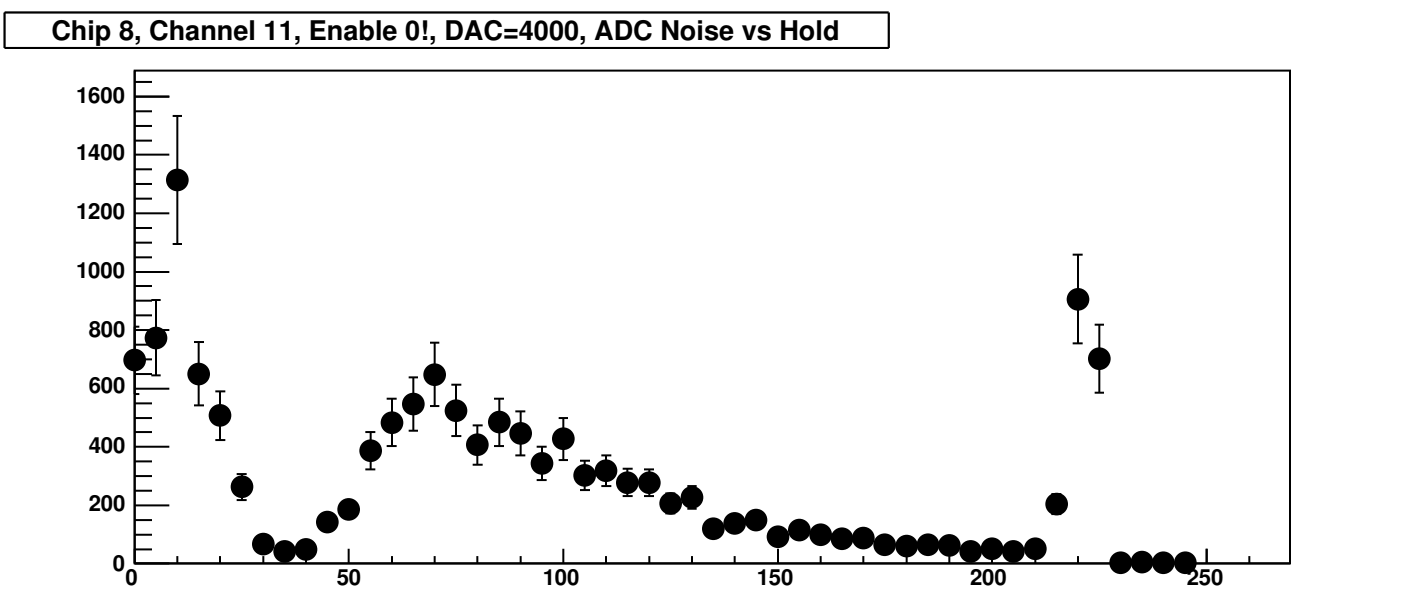
Chip 8, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold

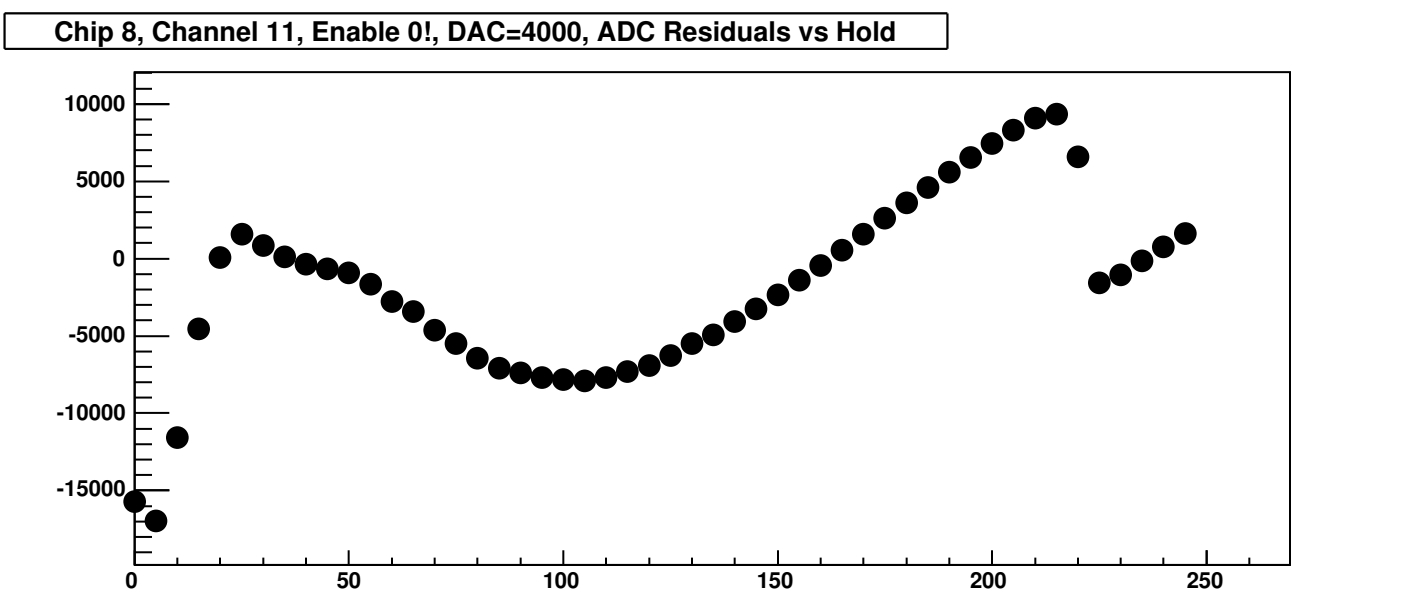


Chip 8, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

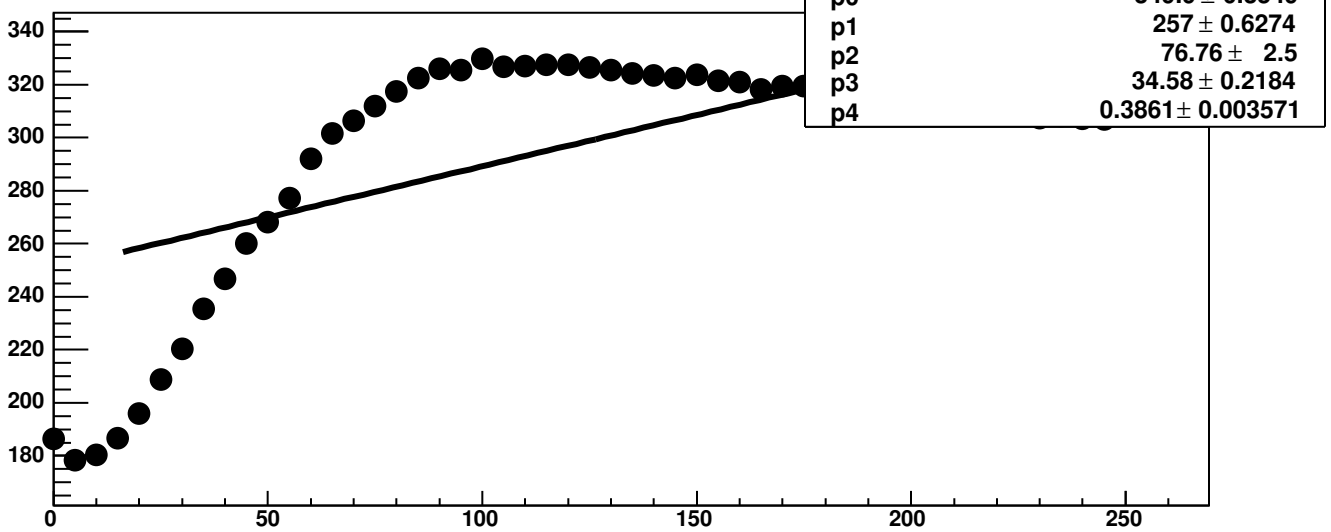



Chip 8, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.545e+06 / 42
p0 -3.413e+04 ± 28.55
p1 -34.63 ± 0.1145
p2 6.55e+04 ± 24.48
p3 80.05 ± 0.06707
30000
20000
10000
0
-10000
0
50
100
150
200
250

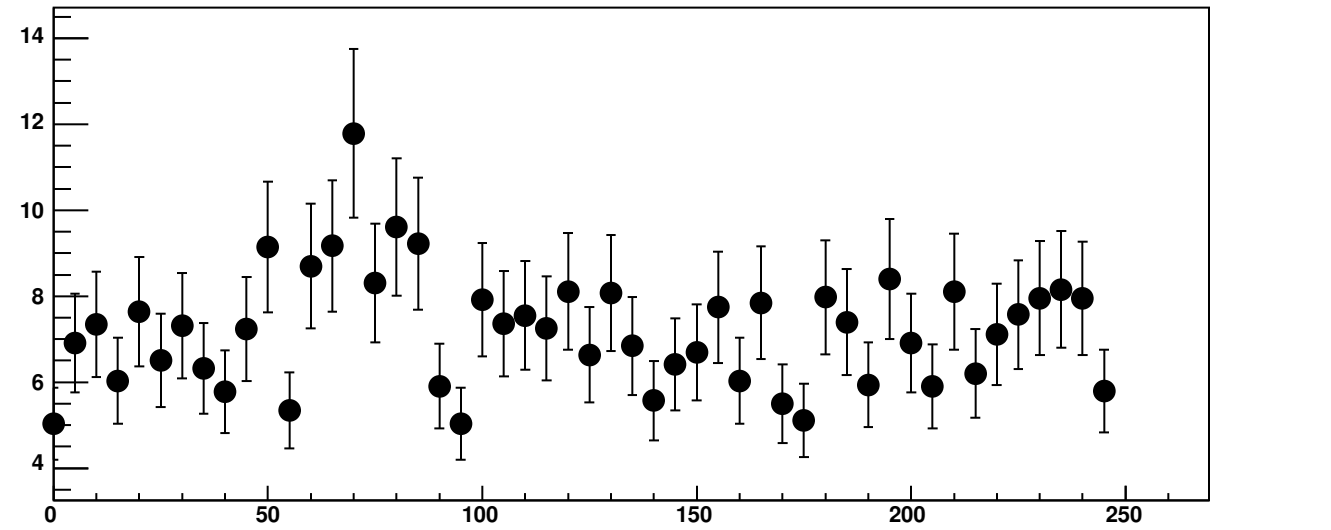

Chip 8, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 8, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold
10000
5000
0
-5000
-10000
-15000
0
50
100
150
200
250

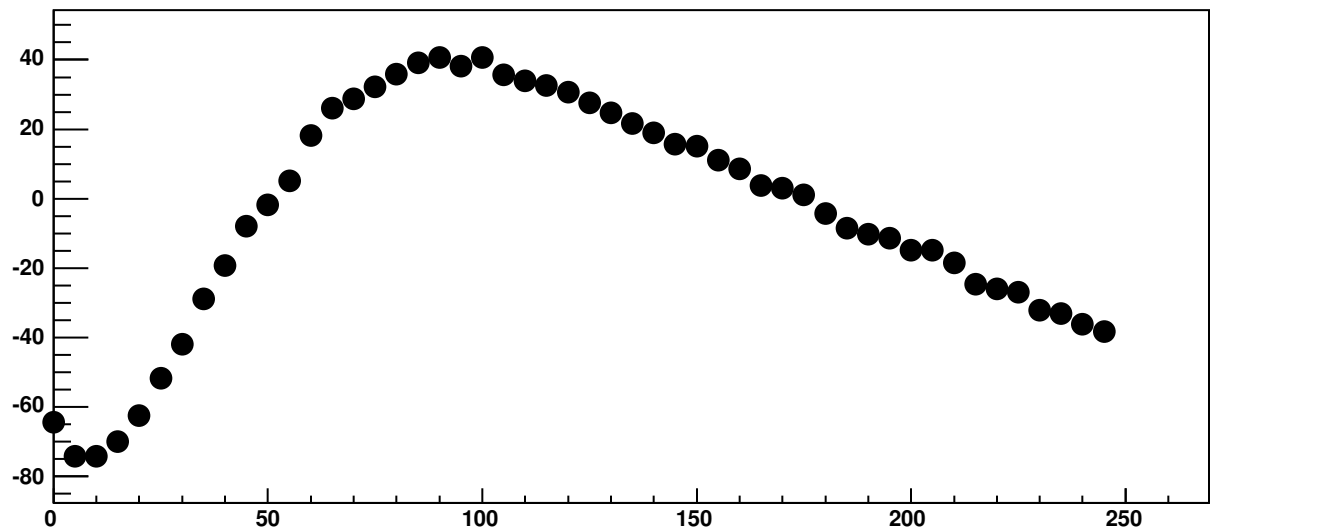
Chip 8, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold



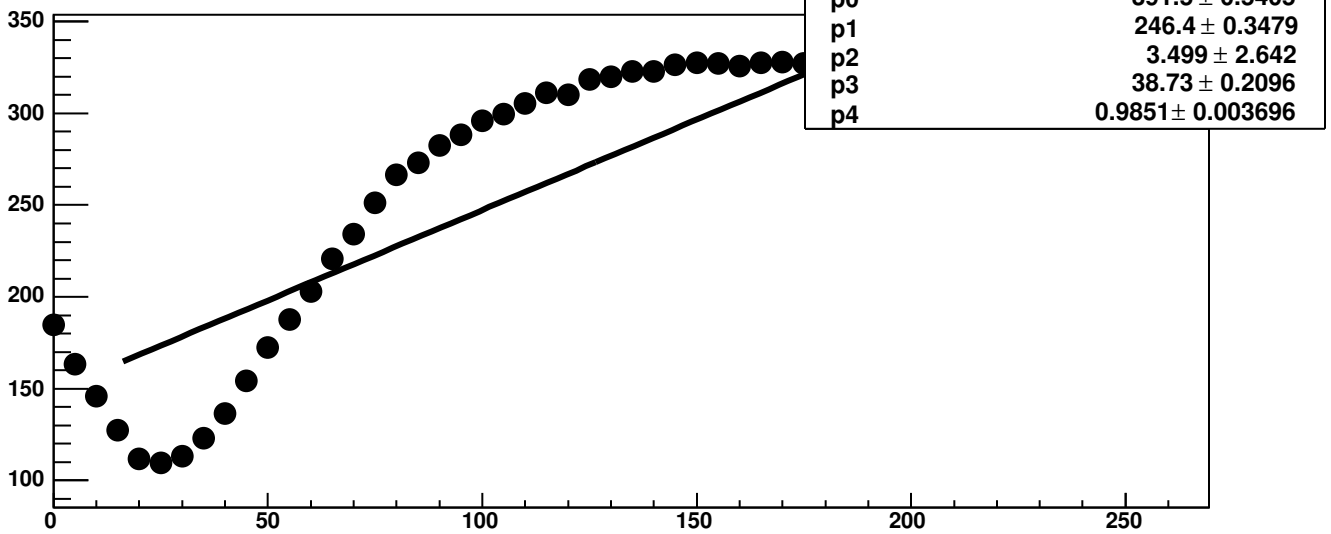
Chip 8, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



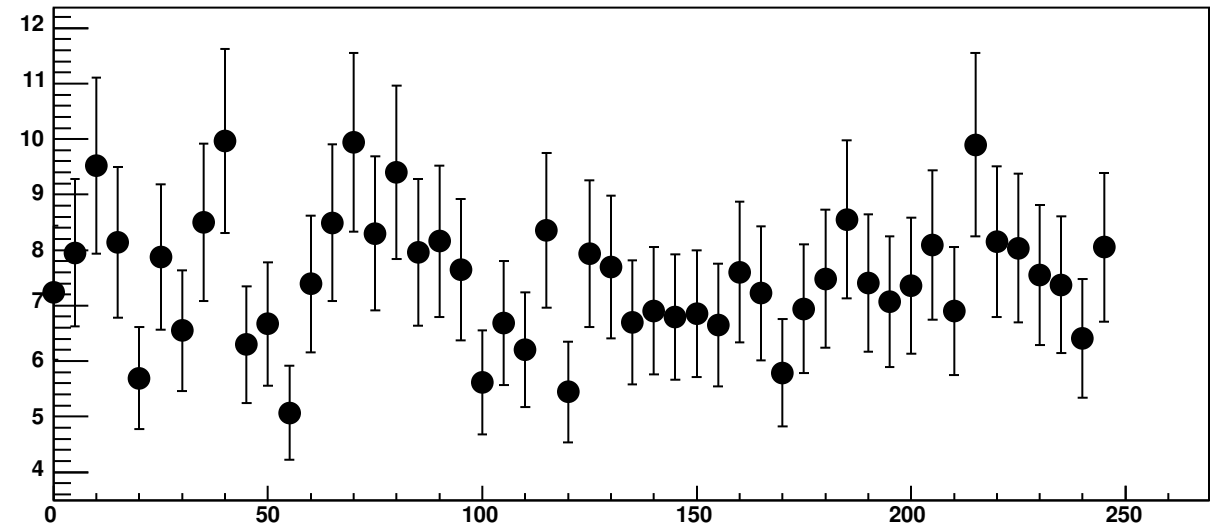
Chip 8, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold



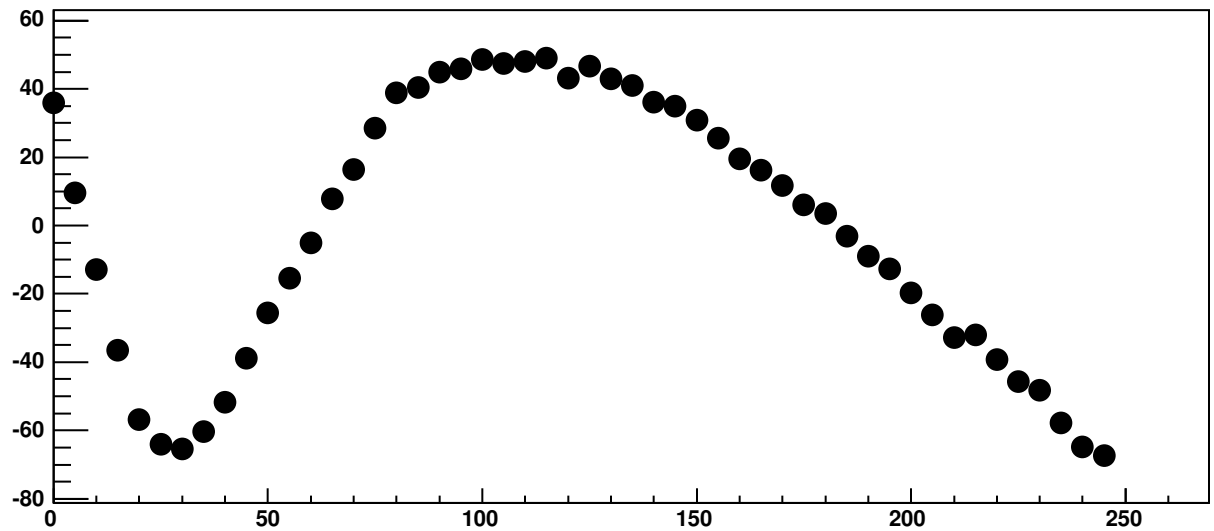
Chip 8, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold



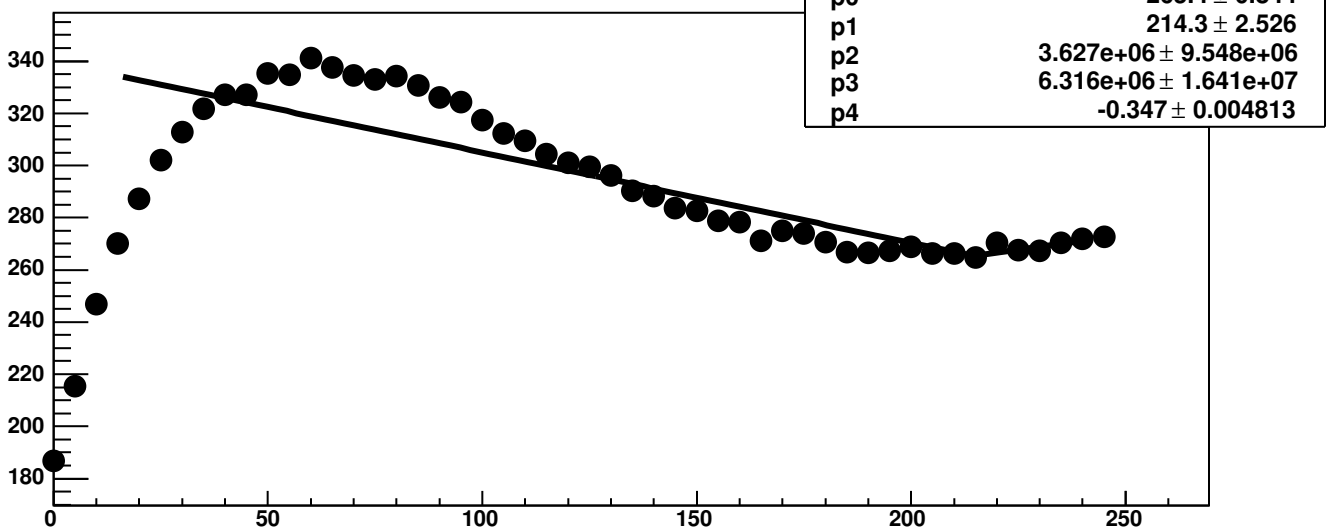
Chip 8, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



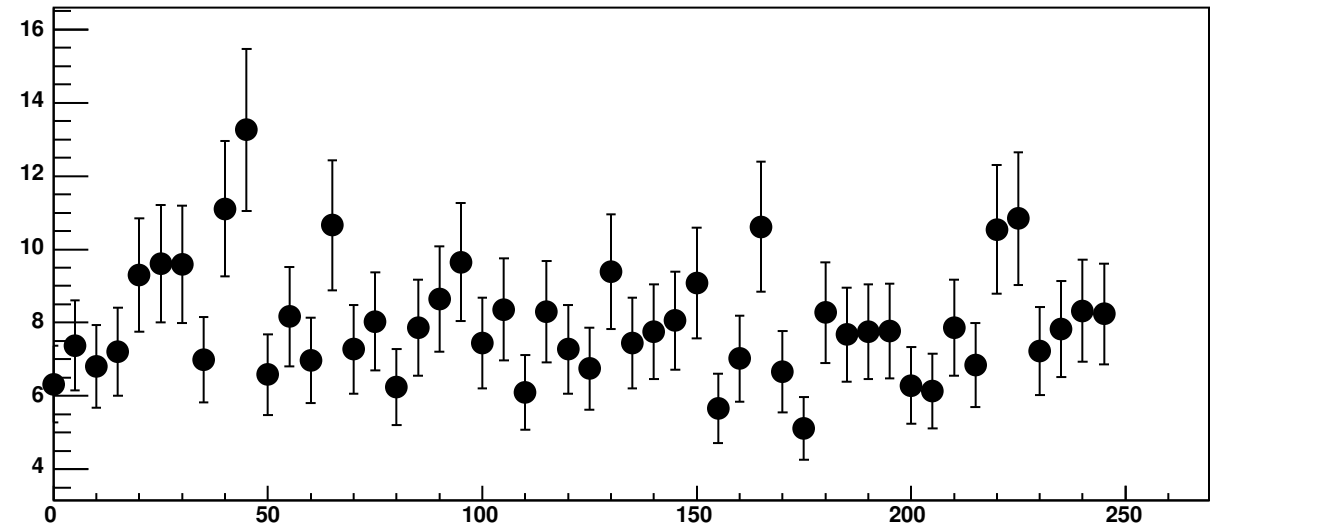
Chip 8, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold



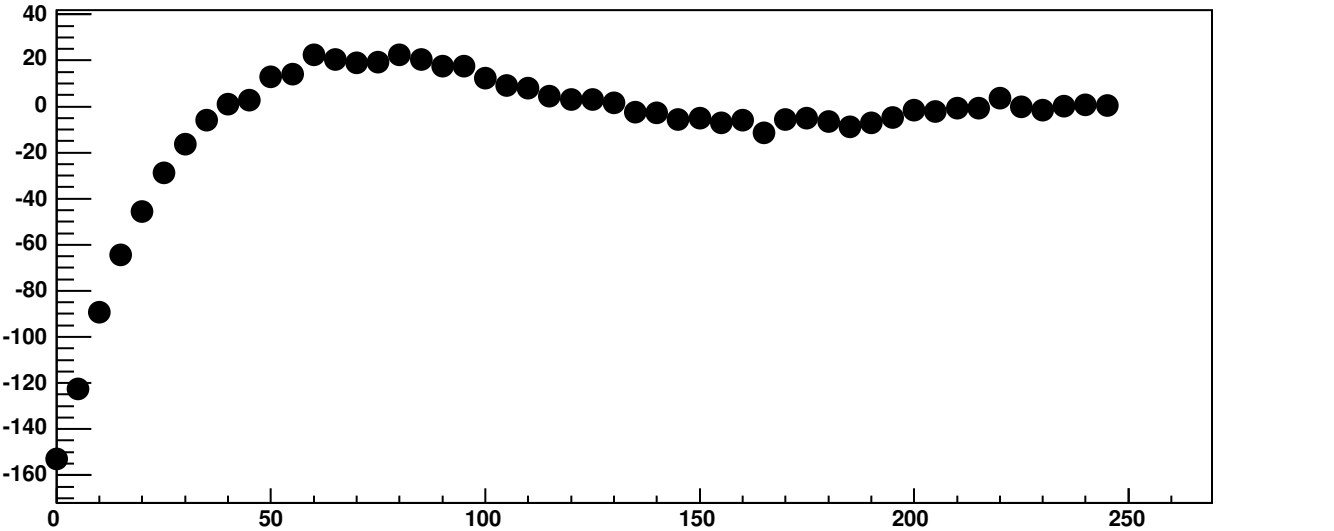
Chip 8, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold



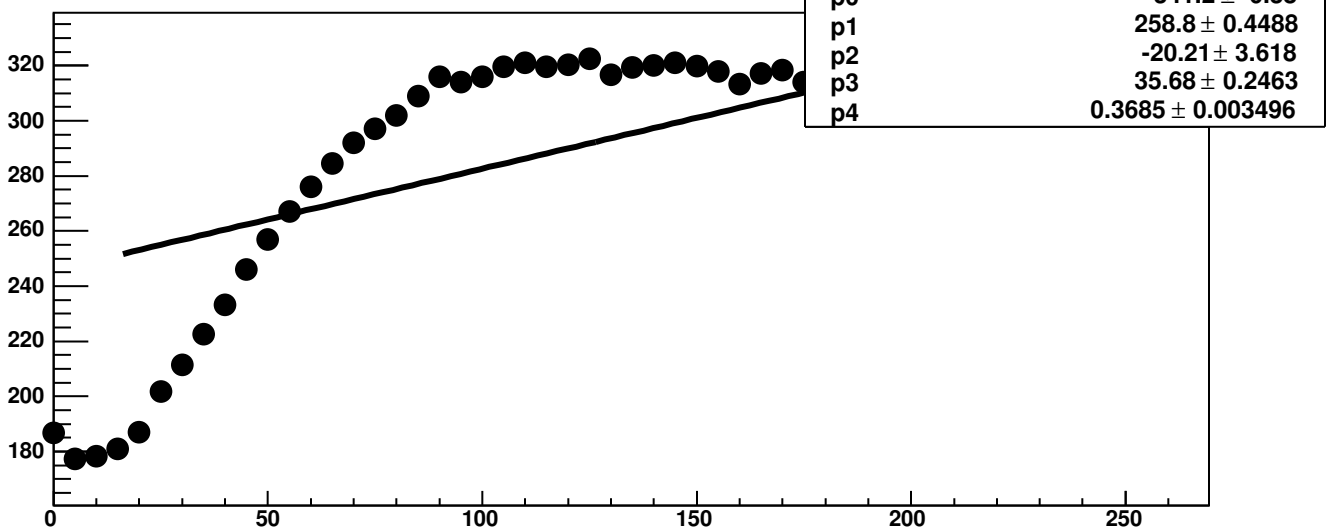
Chip 8, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold



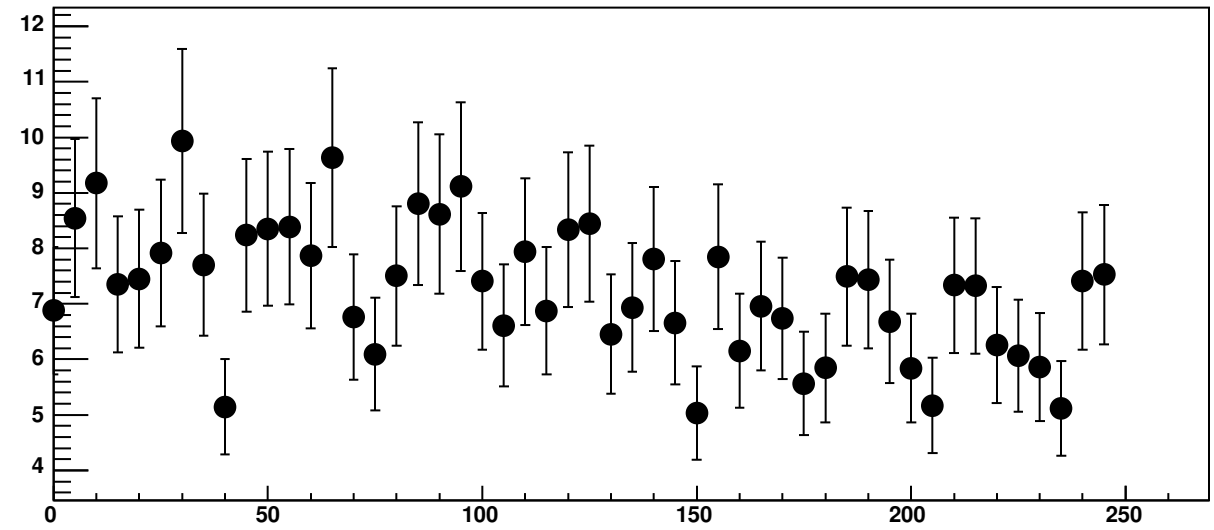
Chip 8, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold



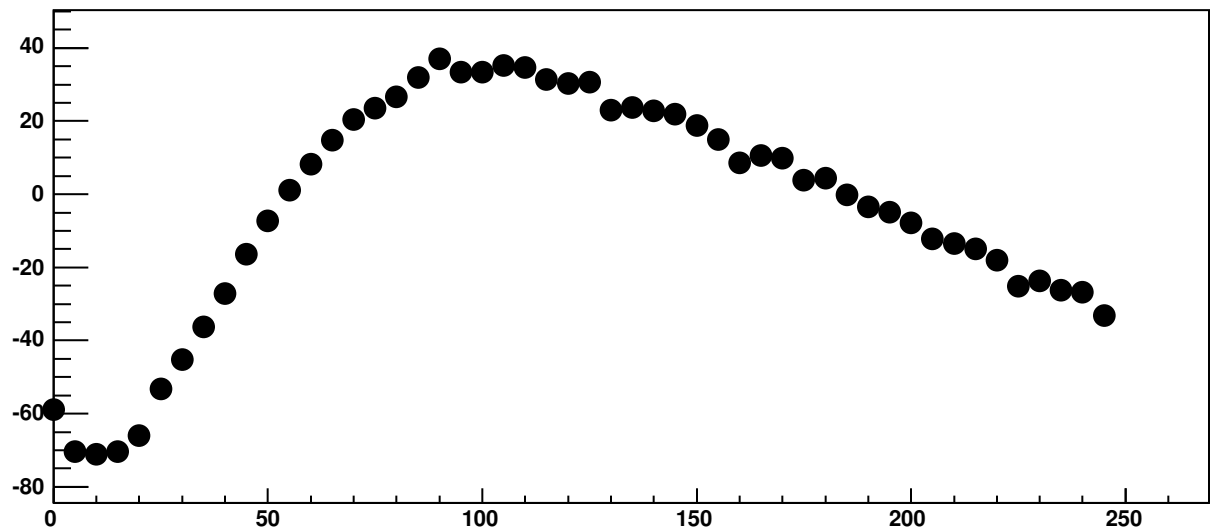
Chip 8, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

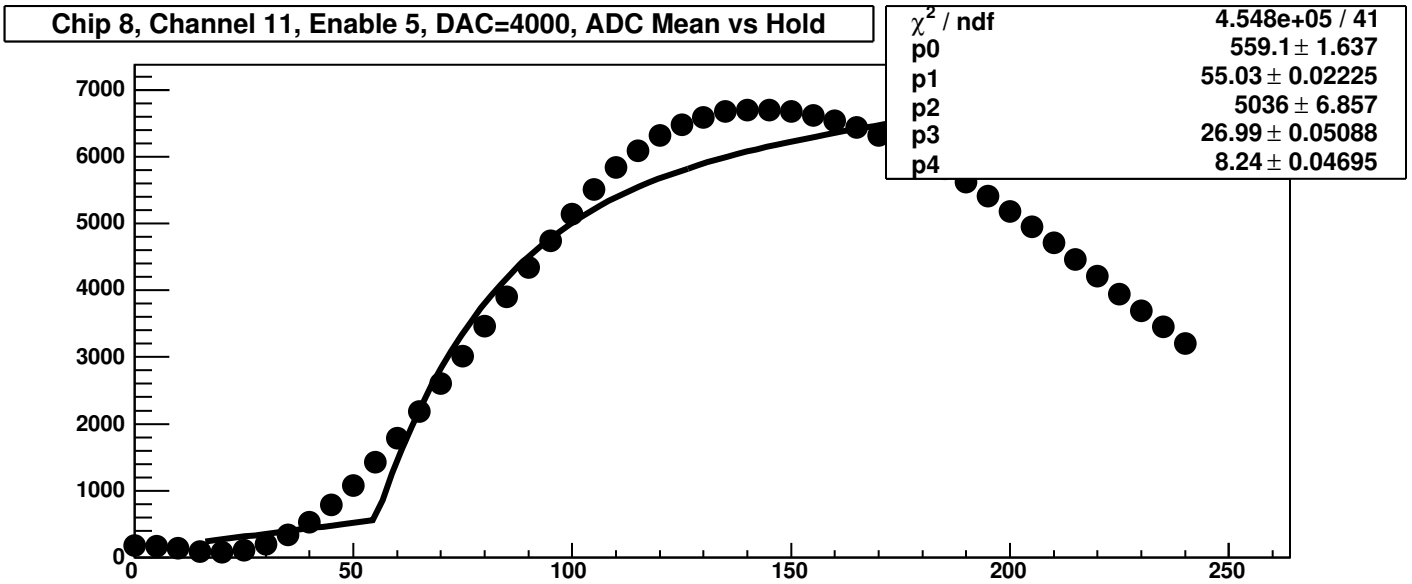


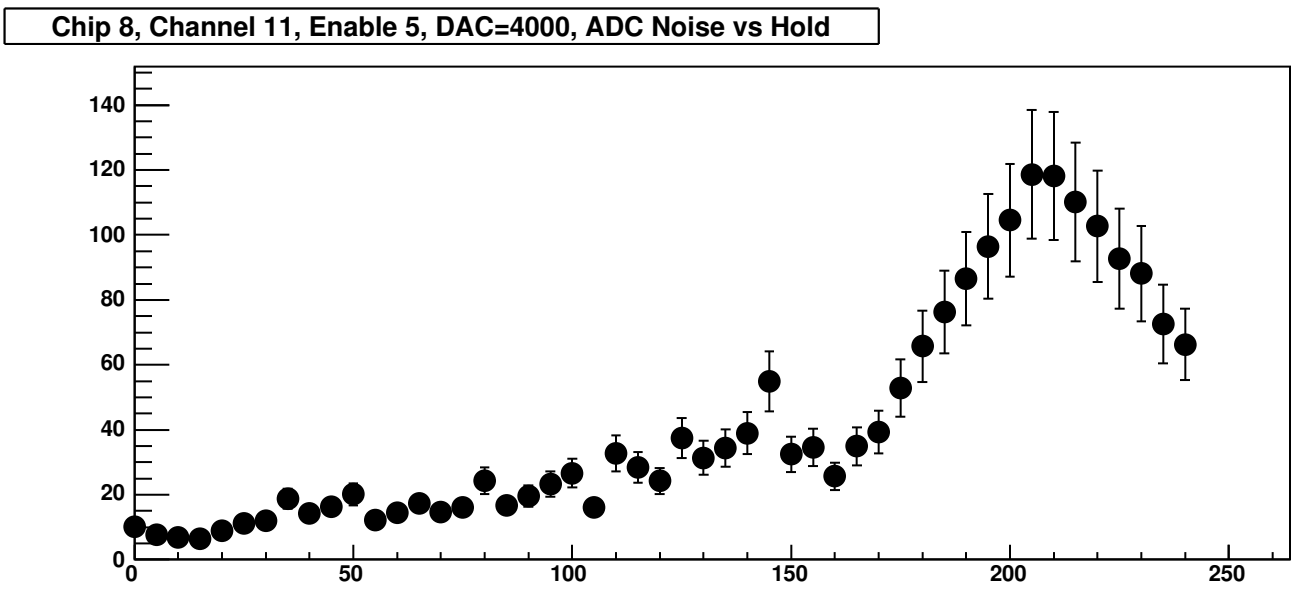
Chip 8, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold

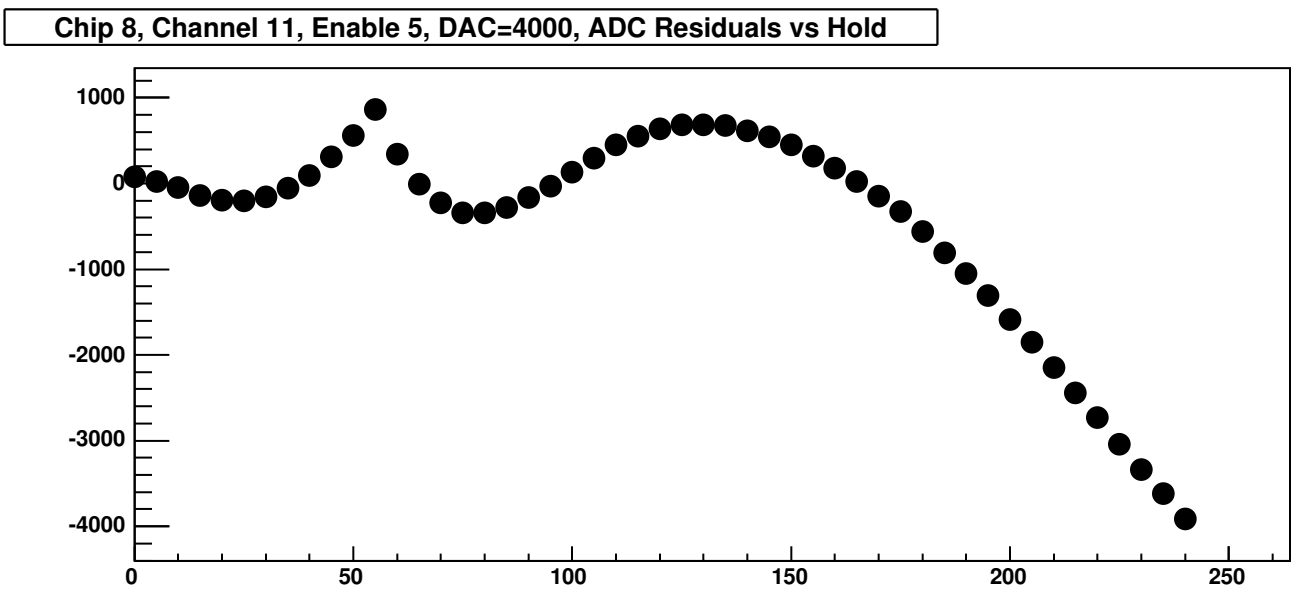


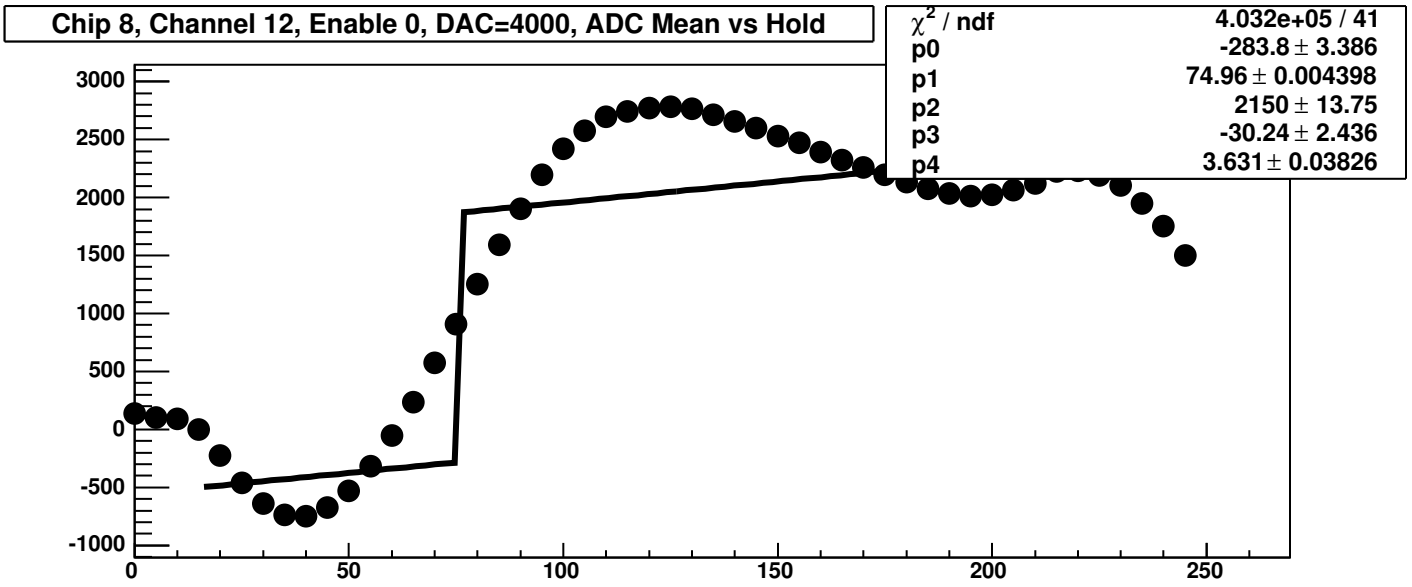
Chip 8, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

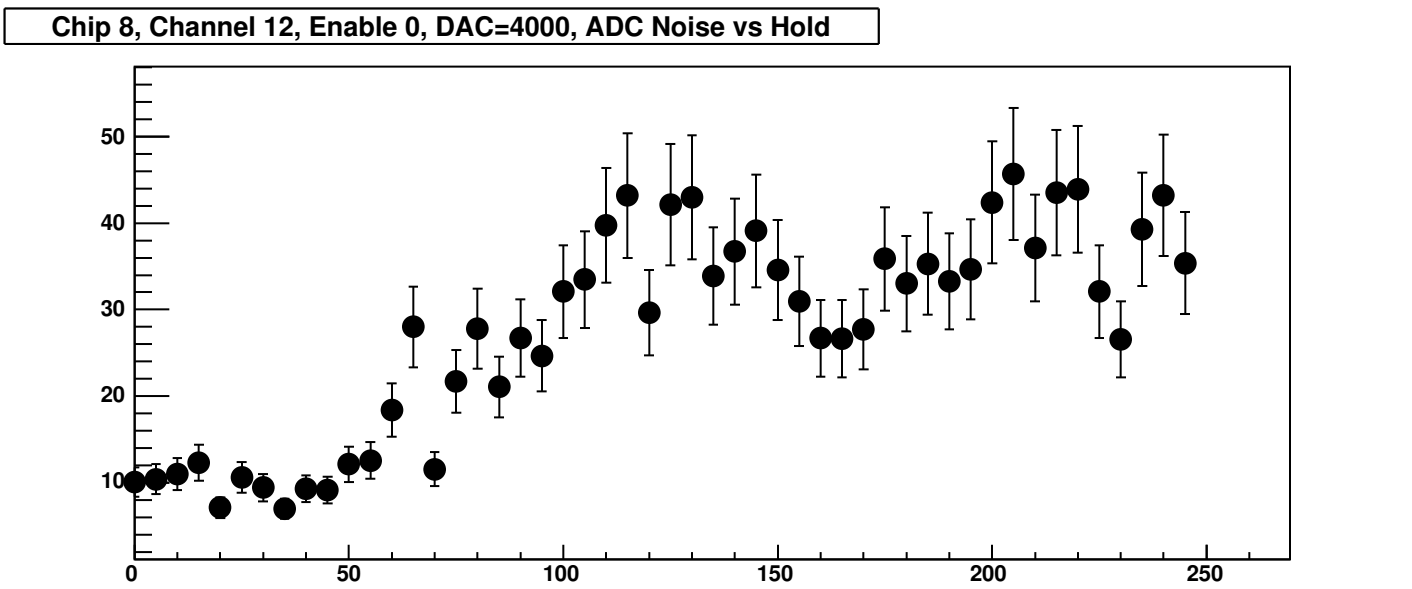


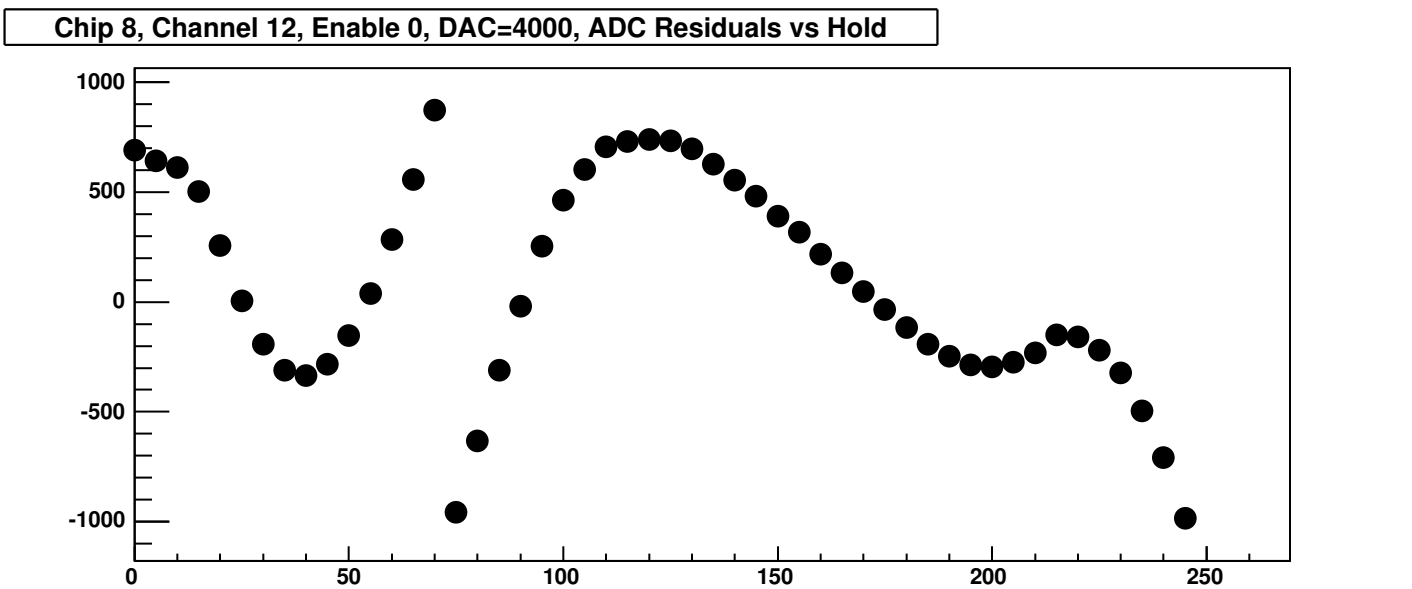

Chip 8, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.548e+05 / 41
p0 559.1 ± 1.637
p1 55.03 ± 0.02225
p2 5036 ± 6.857
p3 26.99 ± 0.05088
p4 8.24 ± 0.04695
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

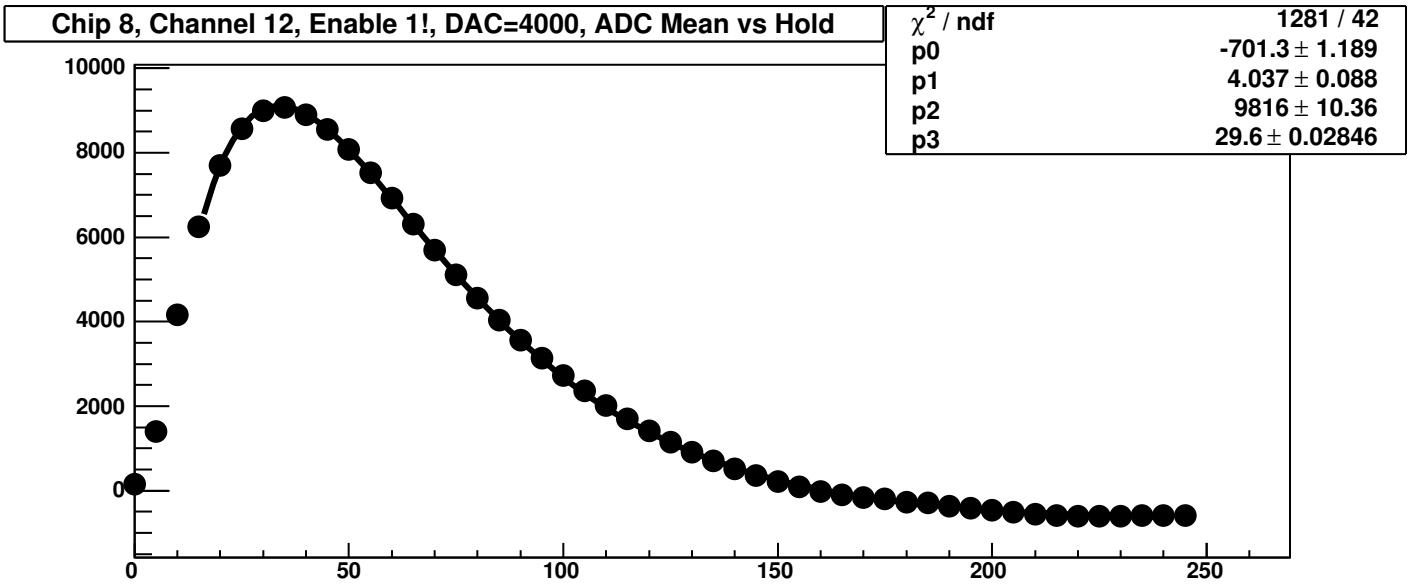

Chip 8, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250

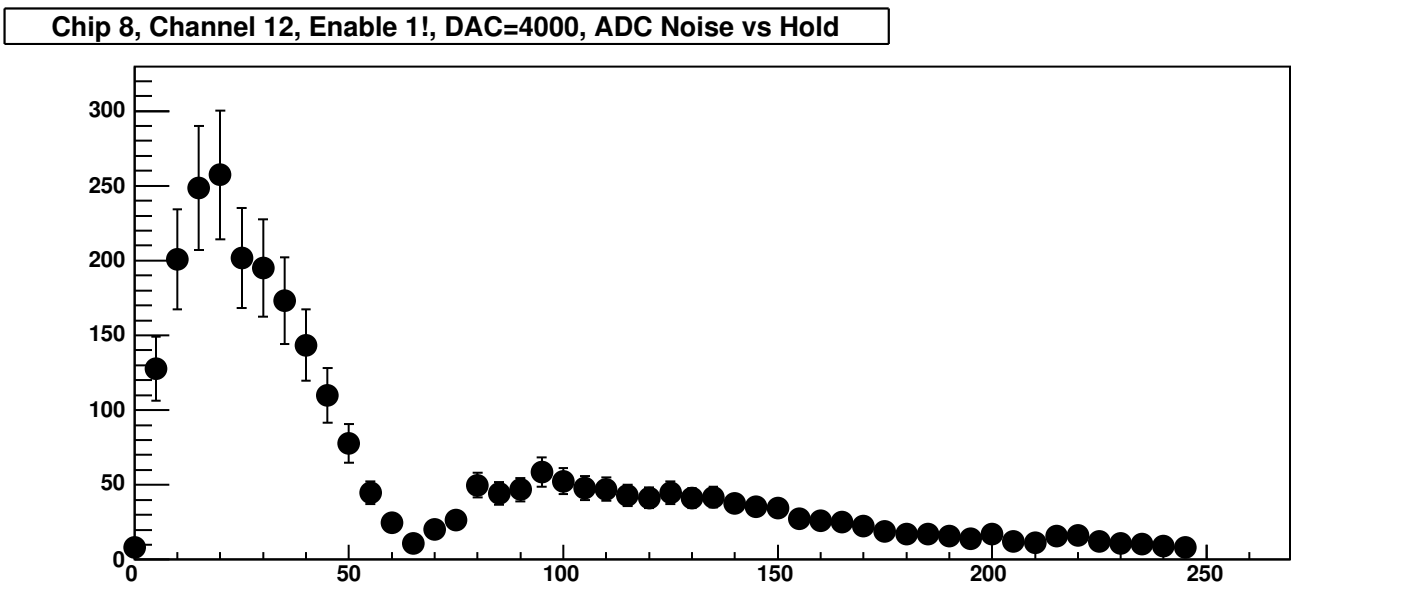

Chip 8, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

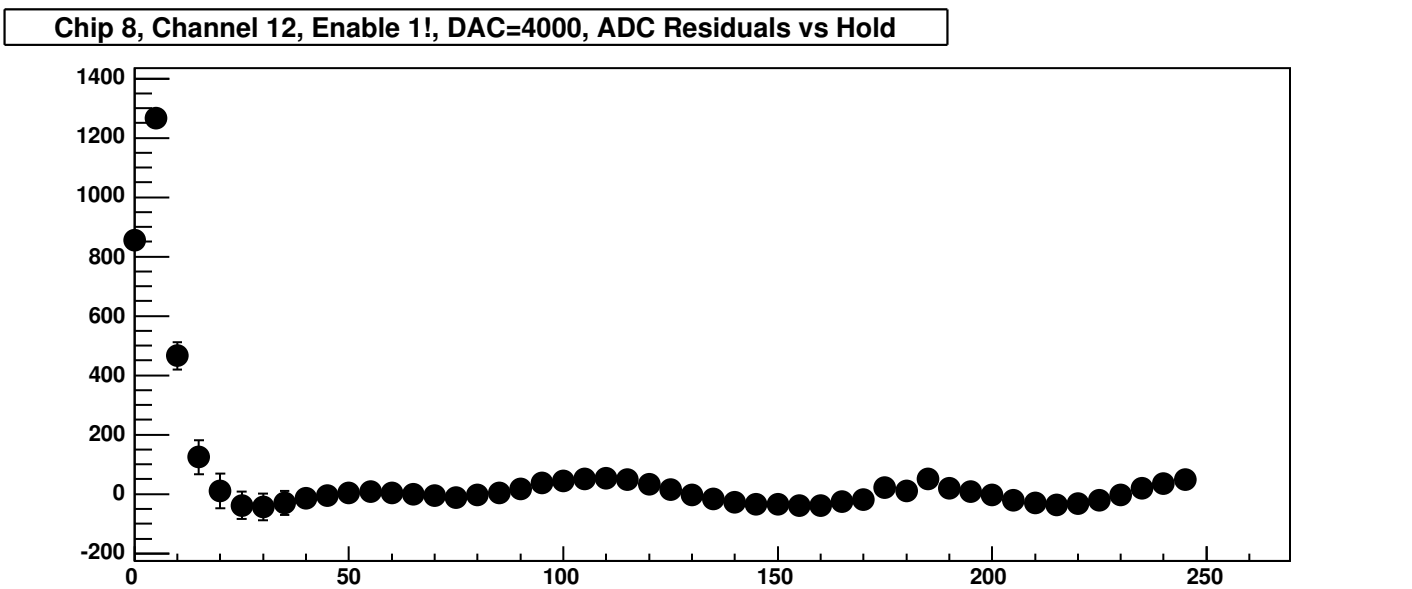

Chip 8, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.032e+05 / 41
p0 -283.8 ± 3.386
p1 74.96 ± 0.004398
p2 2150 ± 13.75
p3 -30.24 ± 2.436
p4 3.631 ± 0.03826
3000
2500
2000
1500
1000
500
0
-500
-1000
0
50
100
150
200
250

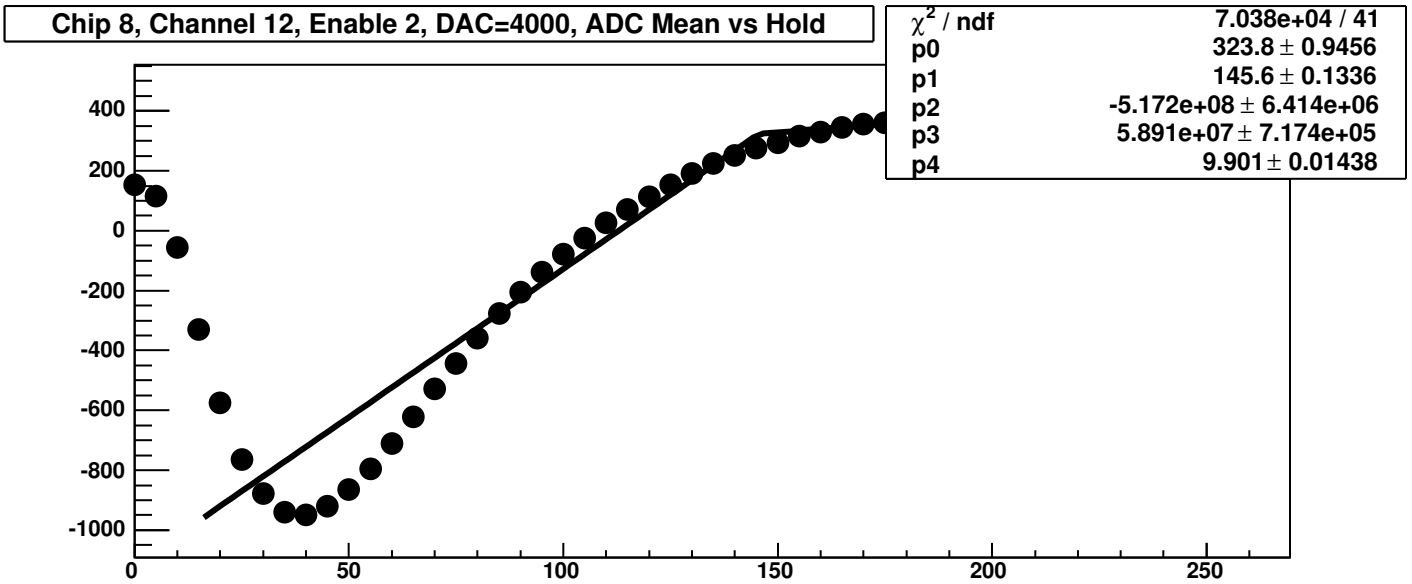

Chip 8, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
0
50
100
150
200
250

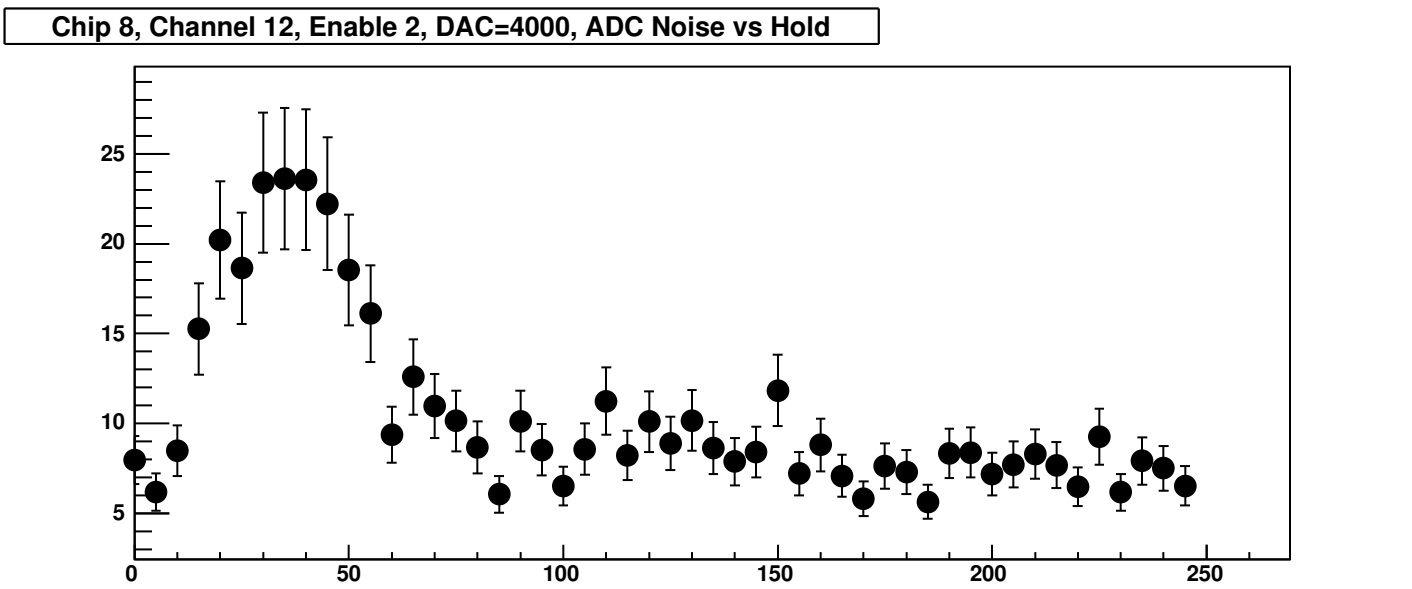

Chip 8, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
0
50
100
150
200
250

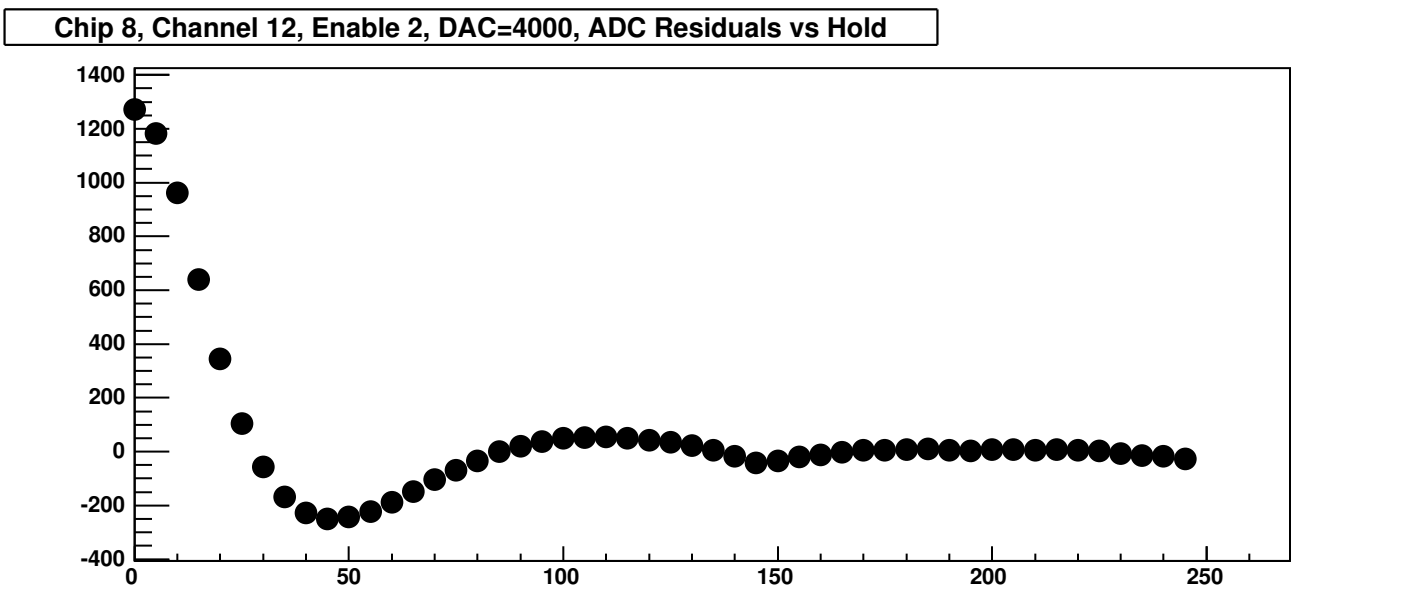

Chip 8, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1281 / 42
p0 -701.3 ± 1.189
p1 4.037 ± 0.088
p2 9816 ± 10.36
p3 29.6 ± 0.02846
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 8, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold
300
250
200
150
100
50
0
0
50
100
150
200
250

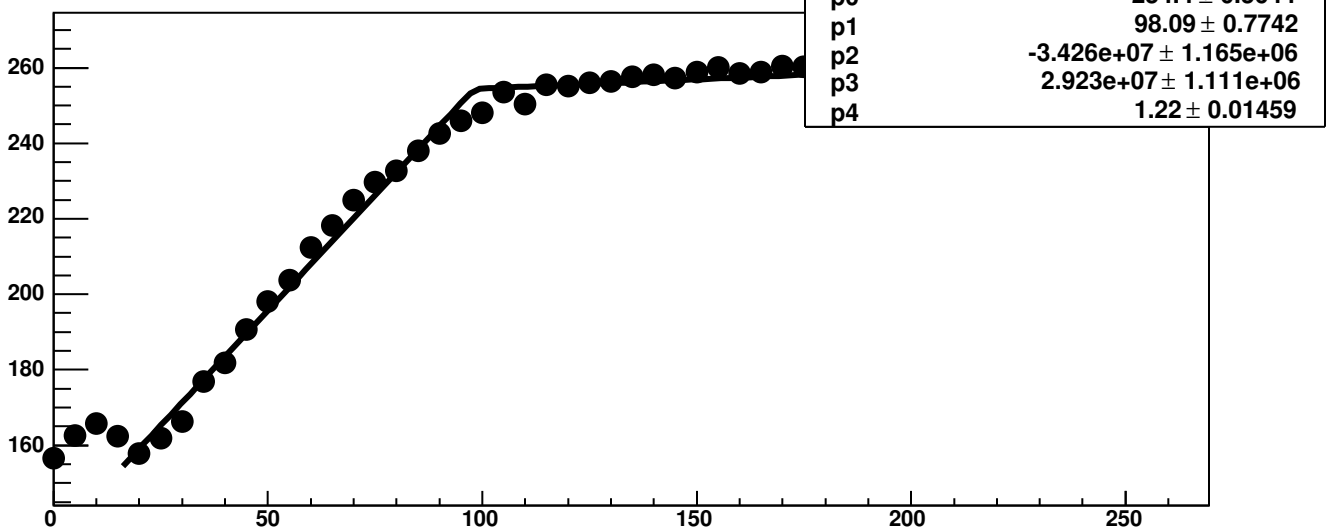

Chip 8, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250


Chip 8, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 7.038e+04 / 41
p0 323.8 ± 0.9456
p1 145.6 ± 0.1336
p2 -5.172e+08 ± 6.414e+06
p3 5.891e+07 ± 7.174e+05
p4 9.901 ± 0.01438
400
200
0
-200
-400
-600
-800
-1000
0
50
100
150
200
250

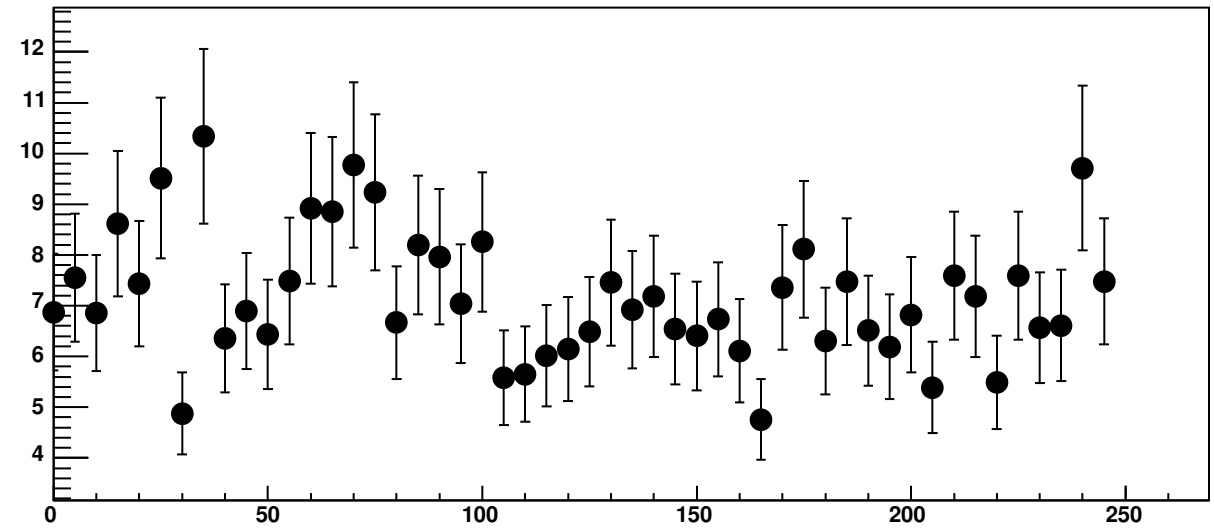

Chip 8, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold
25
20
15
10
5
0
50
100
150
200
250


Chip 8, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
-200
-400
0
50
100
150
200
250

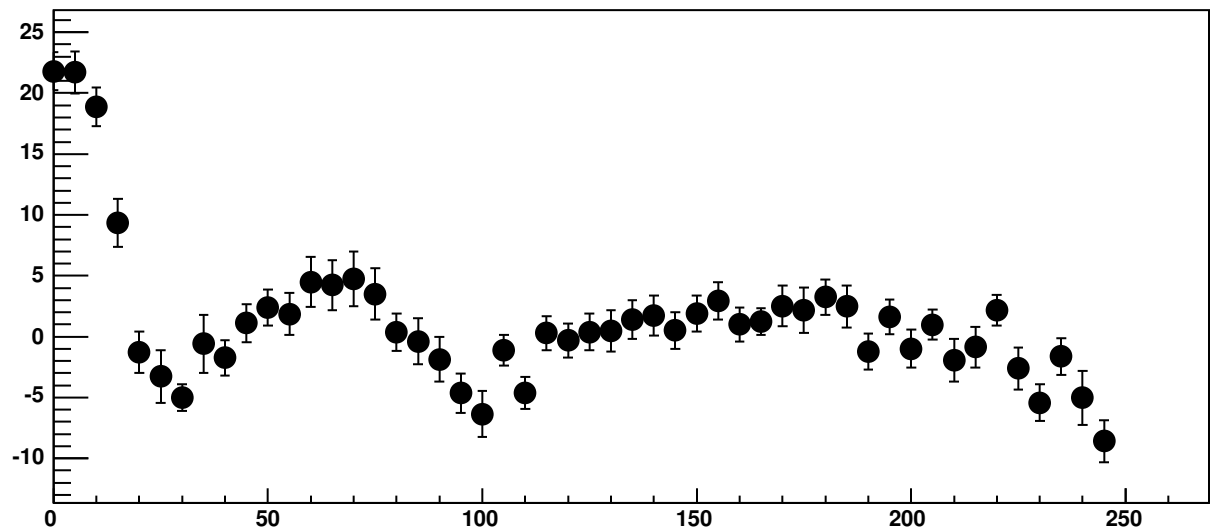
Chip 8, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold



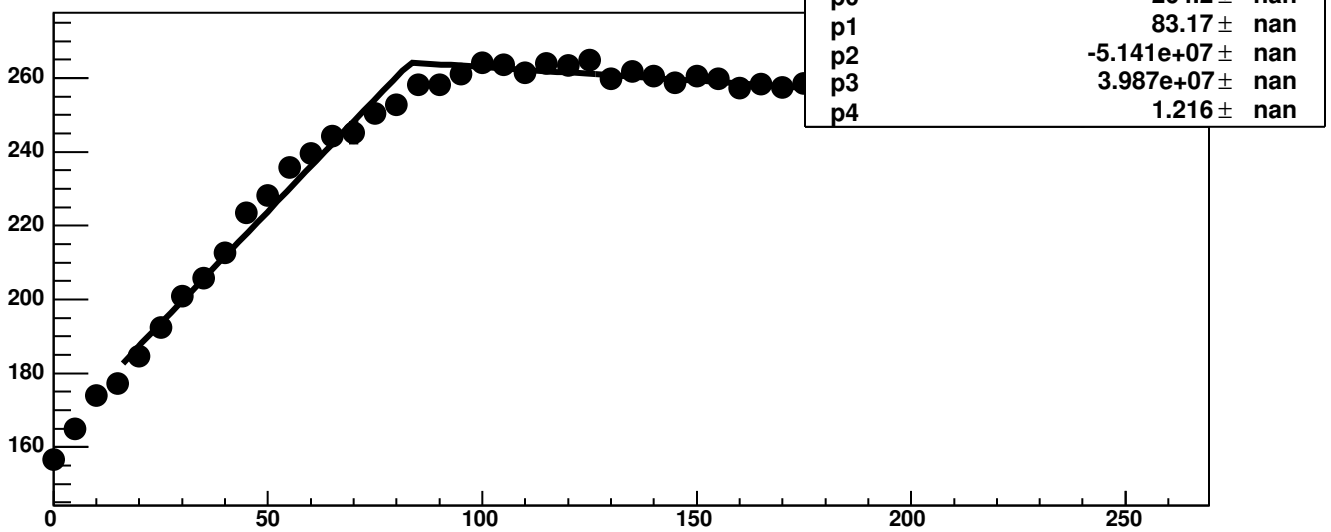
Chip 8, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



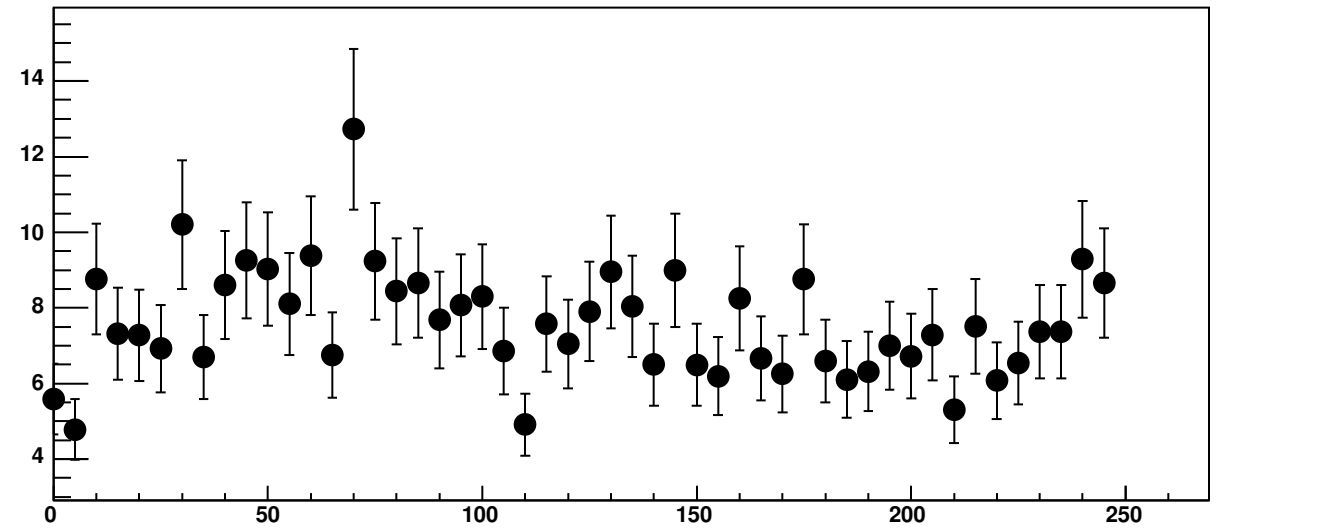
Chip 8, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold



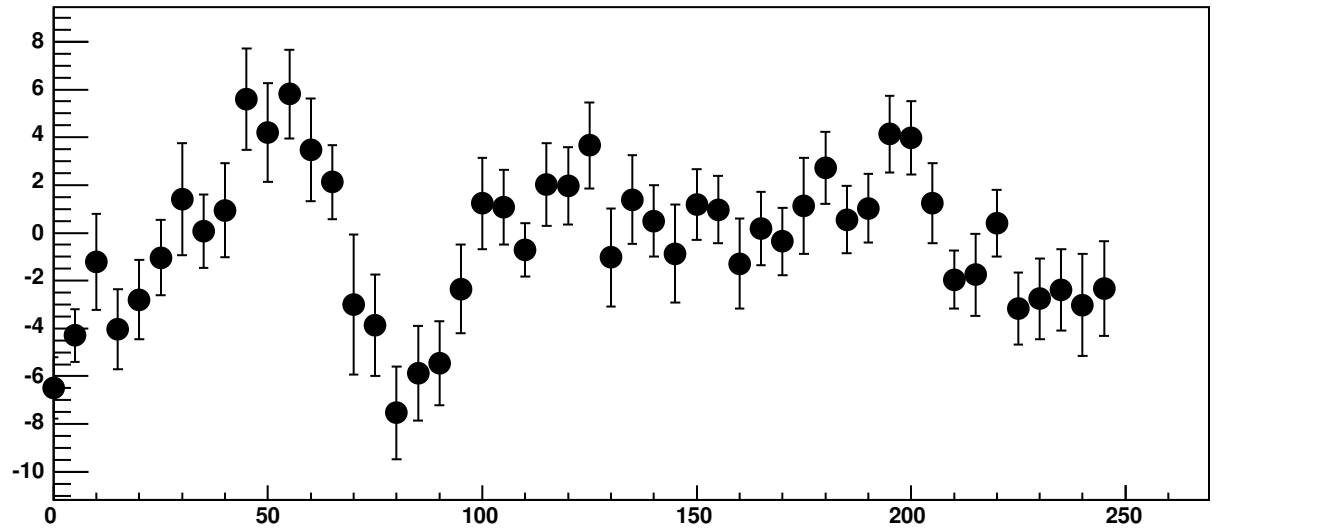
Chip 8, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold

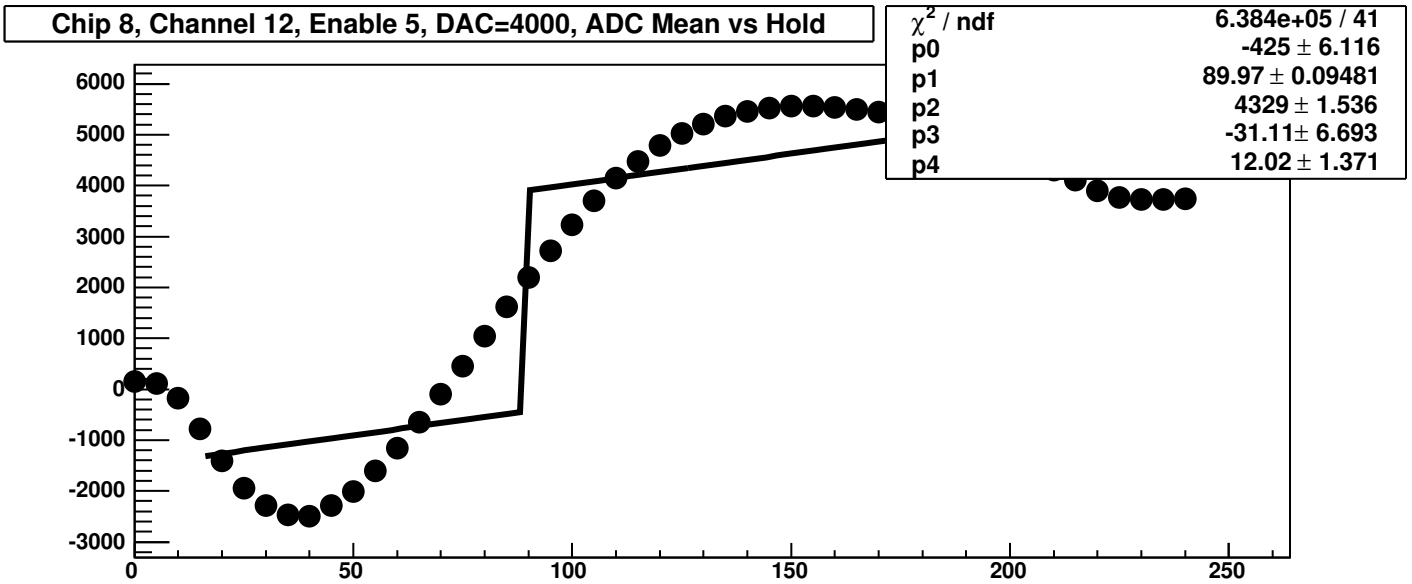


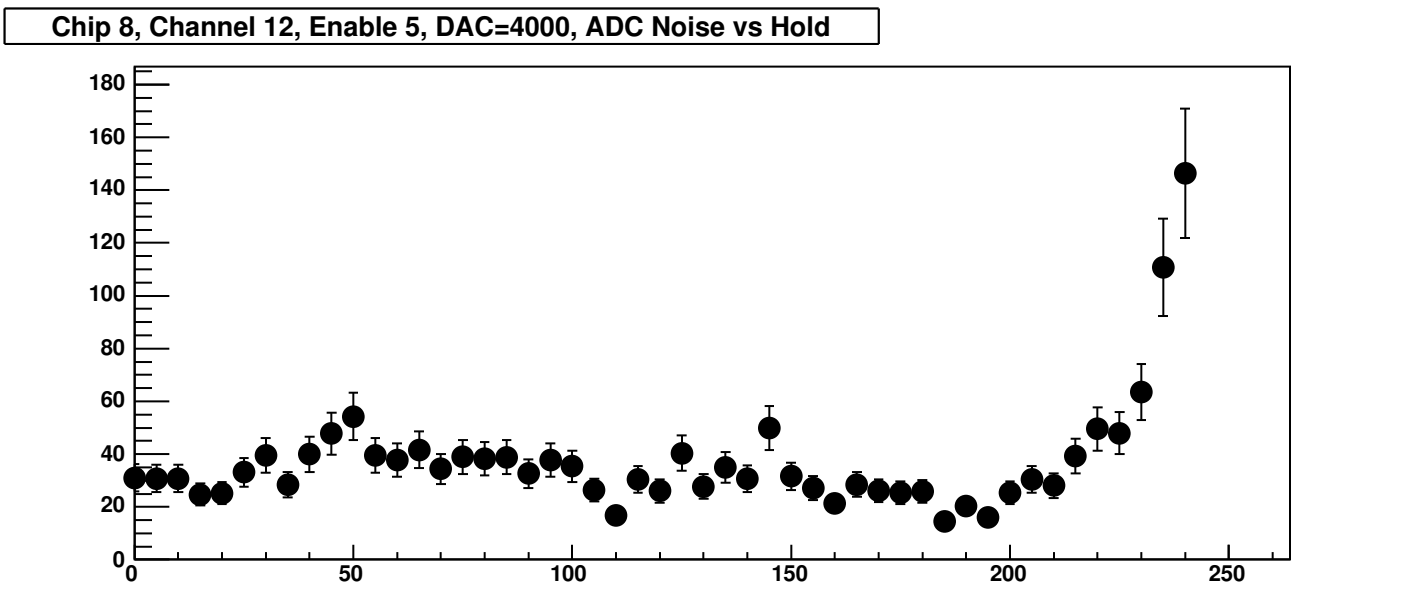
Chip 8, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold

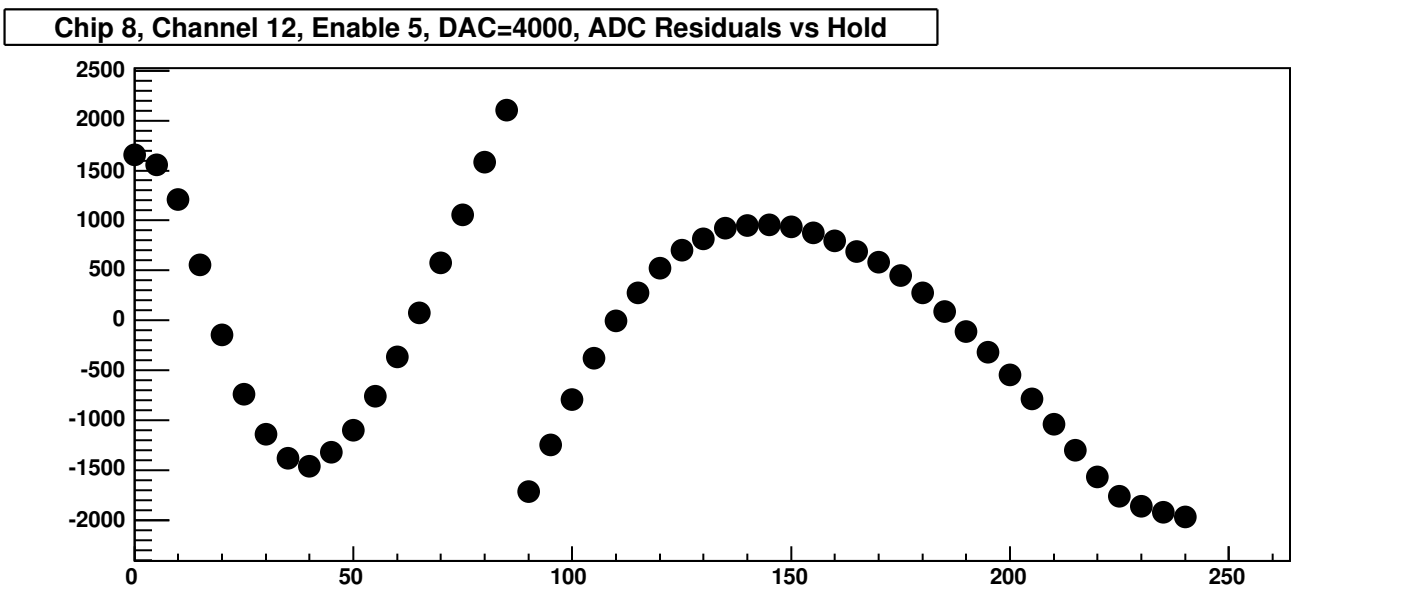


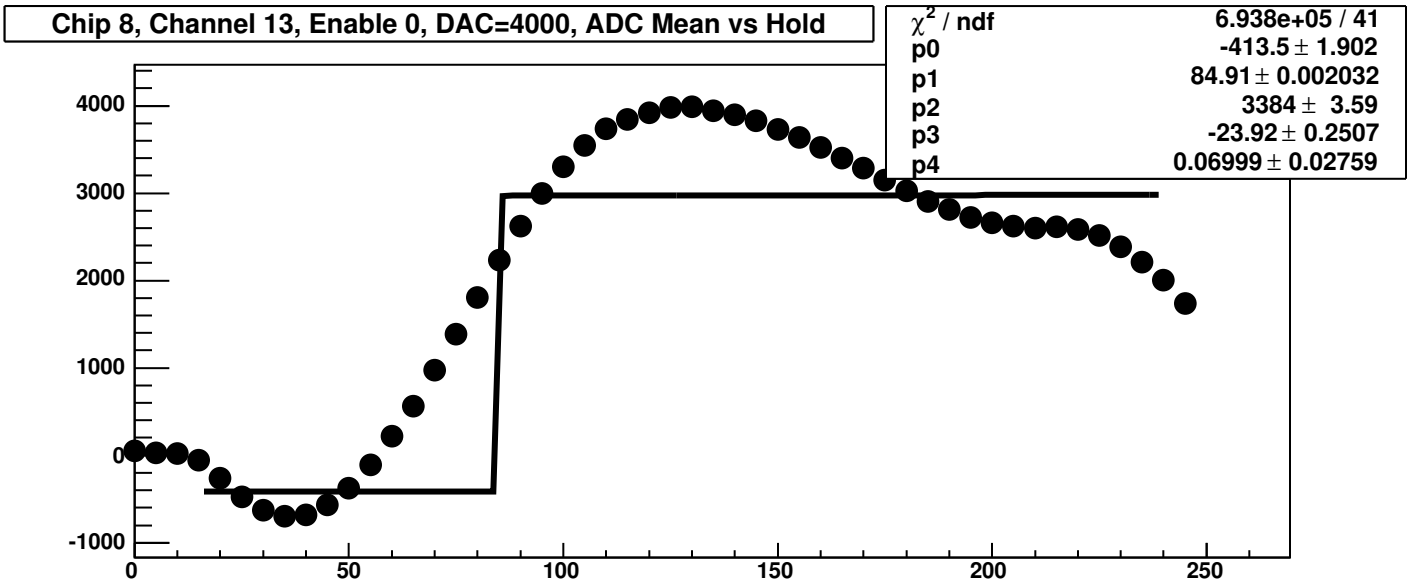
Chip 8, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold

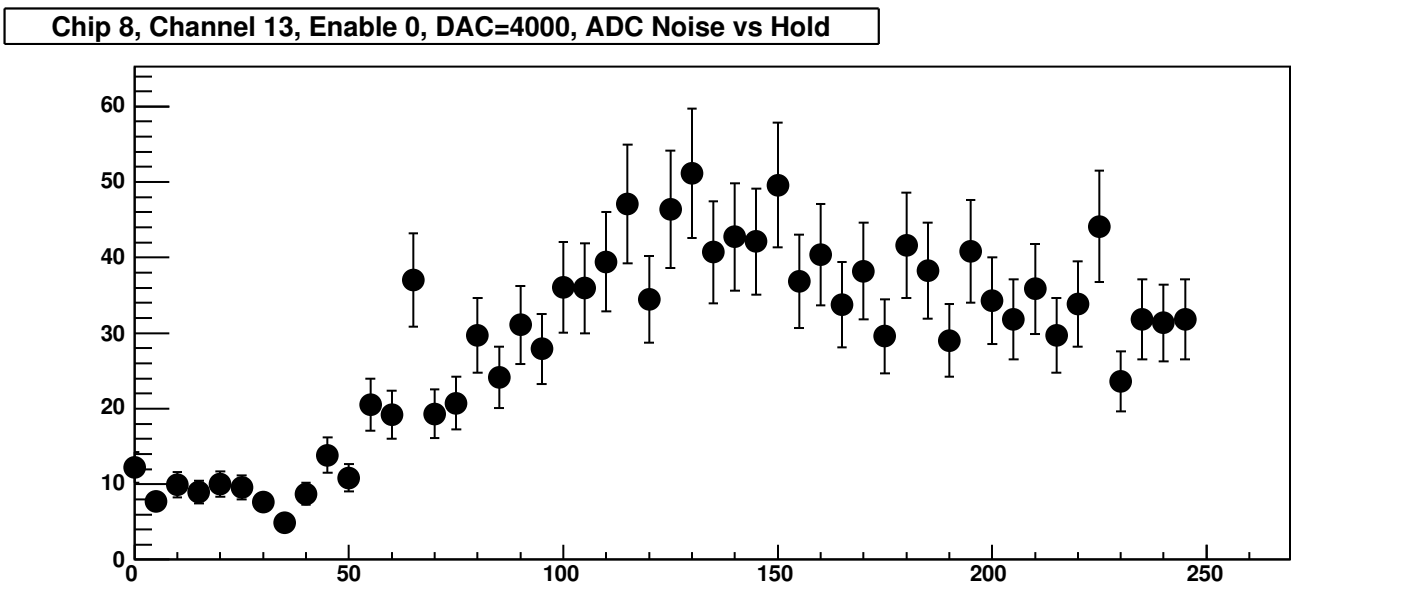


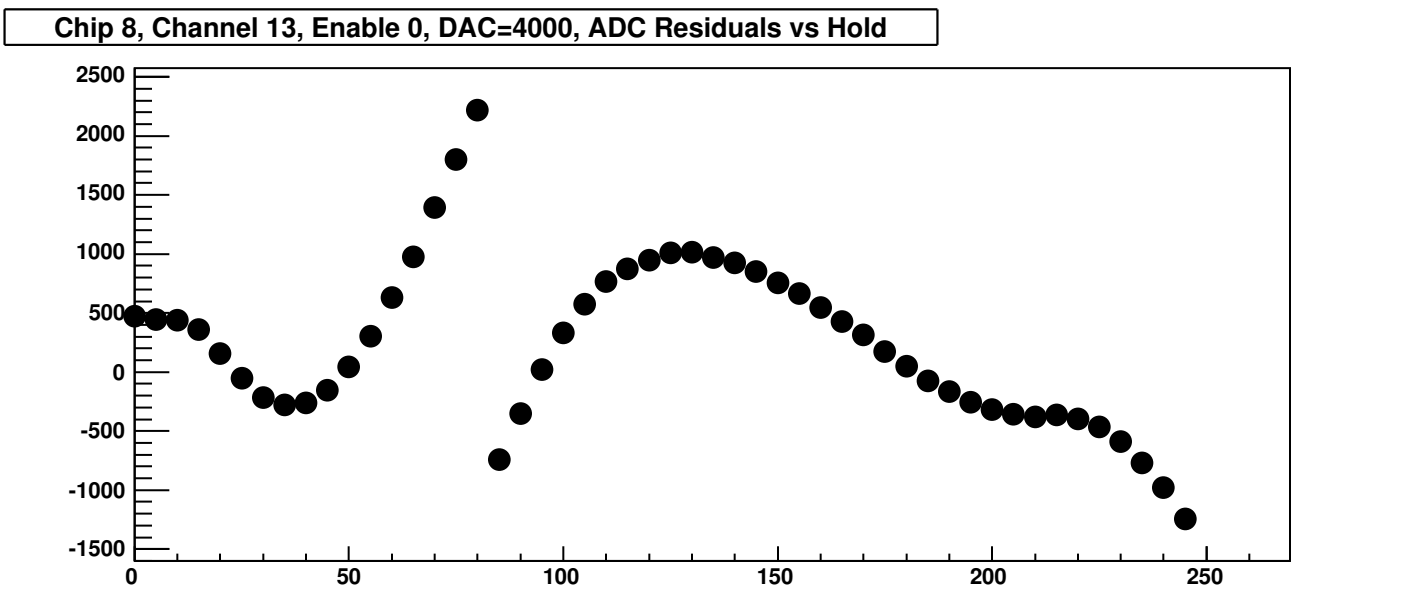

Chip 8, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 6.384e+05 / 41
p0 -425 ± 6.116
p1 89.97 ± 0.09481
p2 4329 ± 1.536
p3 -31.11± 6.693
p4 12.02 ± 1.371
6000
5000
4000
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250


Chip 8, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

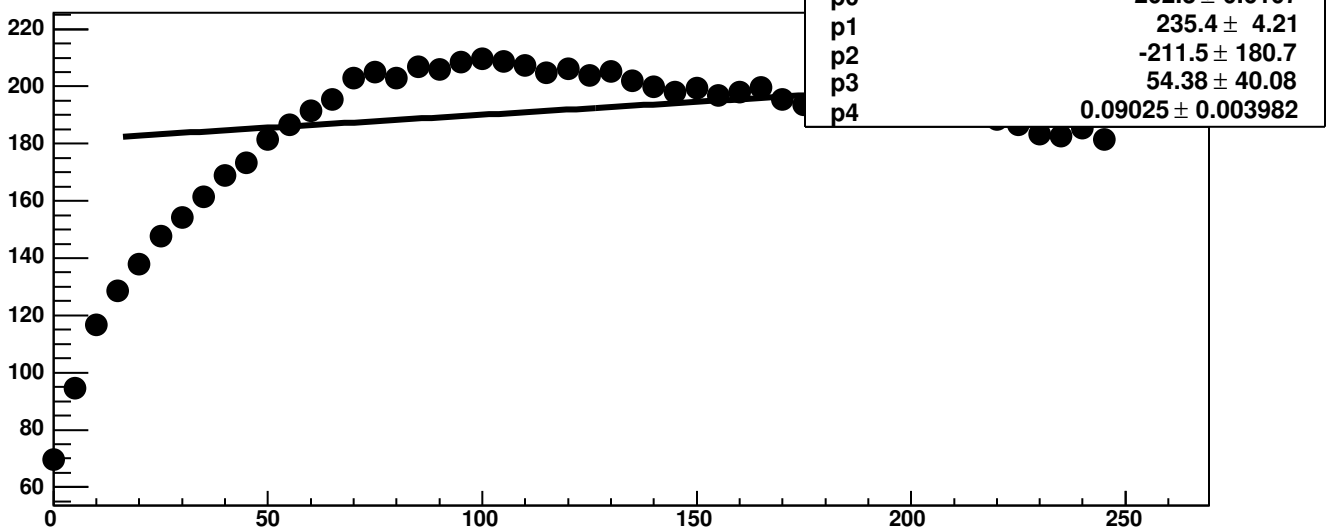

Chip 8, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold
2500
2000
1500
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250


Chip 8, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 6.938e+05 / 41
p0 -413.5 ± 1.902
p1 84.91 ± 0.002032
p2 3384 ± 3.59
p3 -23.92 ± 0.2507
p4 0.06999 ± 0.02759
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

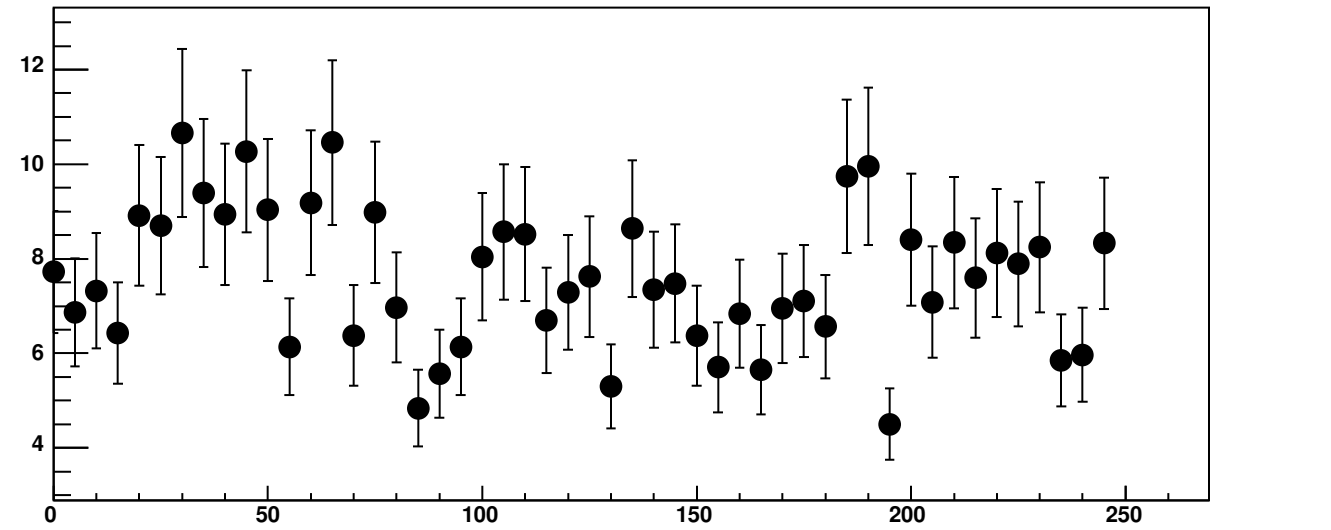

Chip 8, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold
60
50
40
30
20
10
0
0
50
100
150
200
250


Chip 8, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold
2500
2000
1500
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

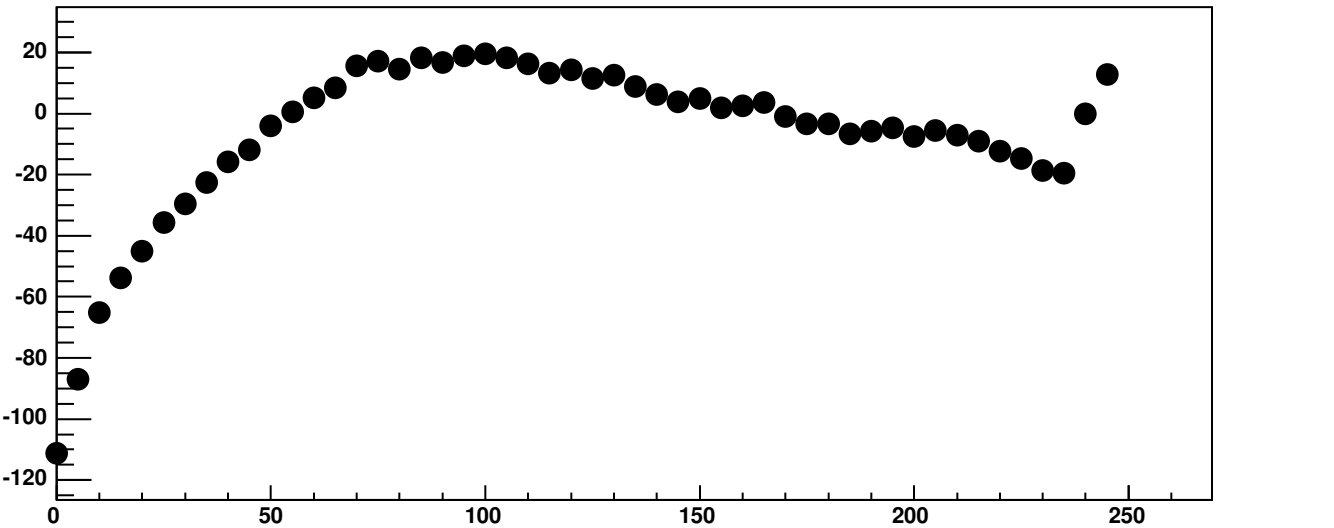
Chip 8, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold



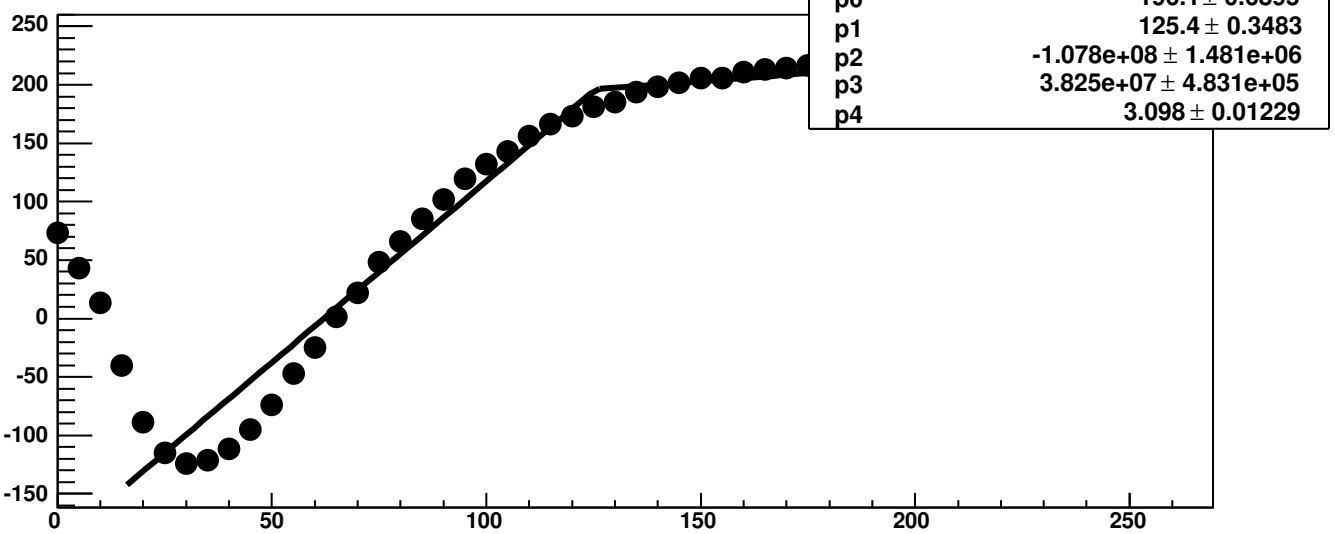
Chip 8, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold



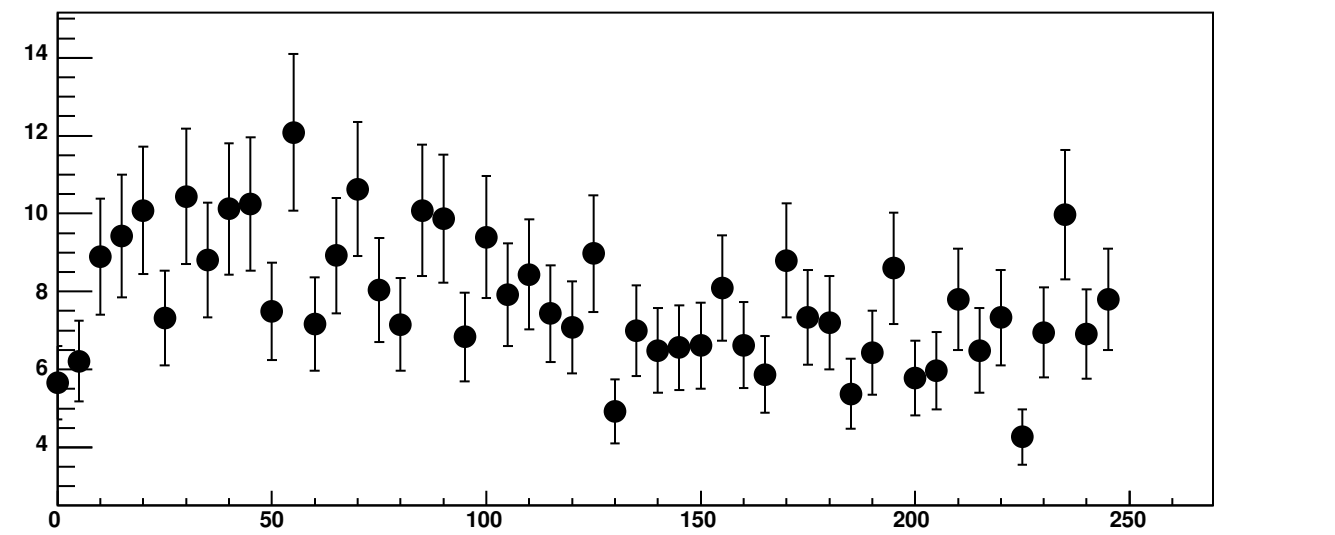
Chip 8, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold



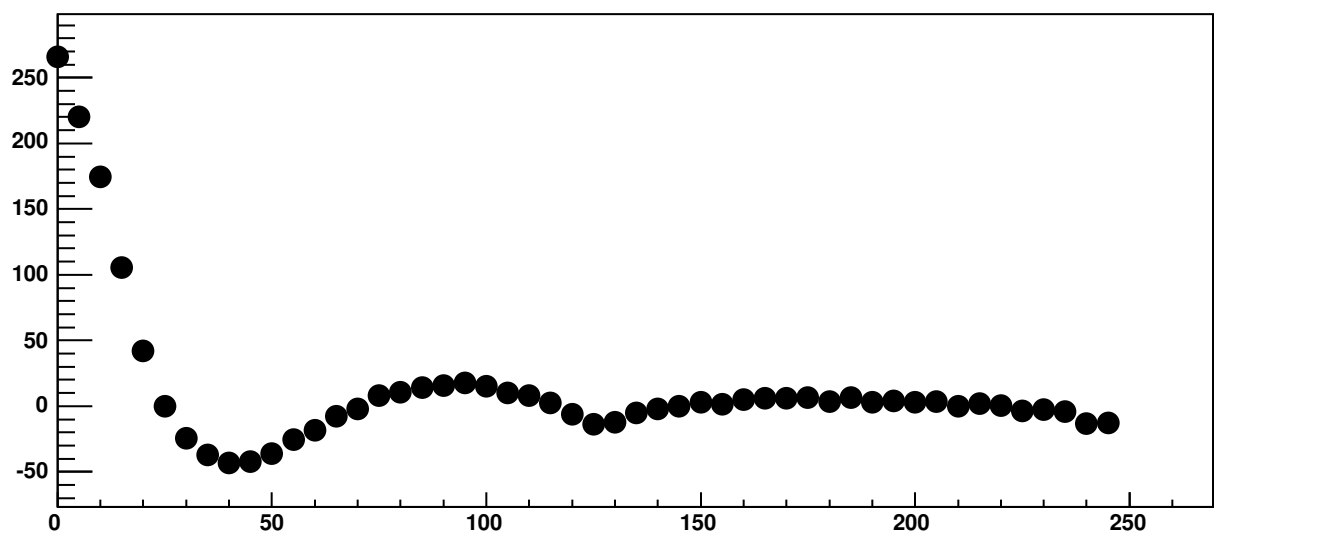
Chip 8, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold



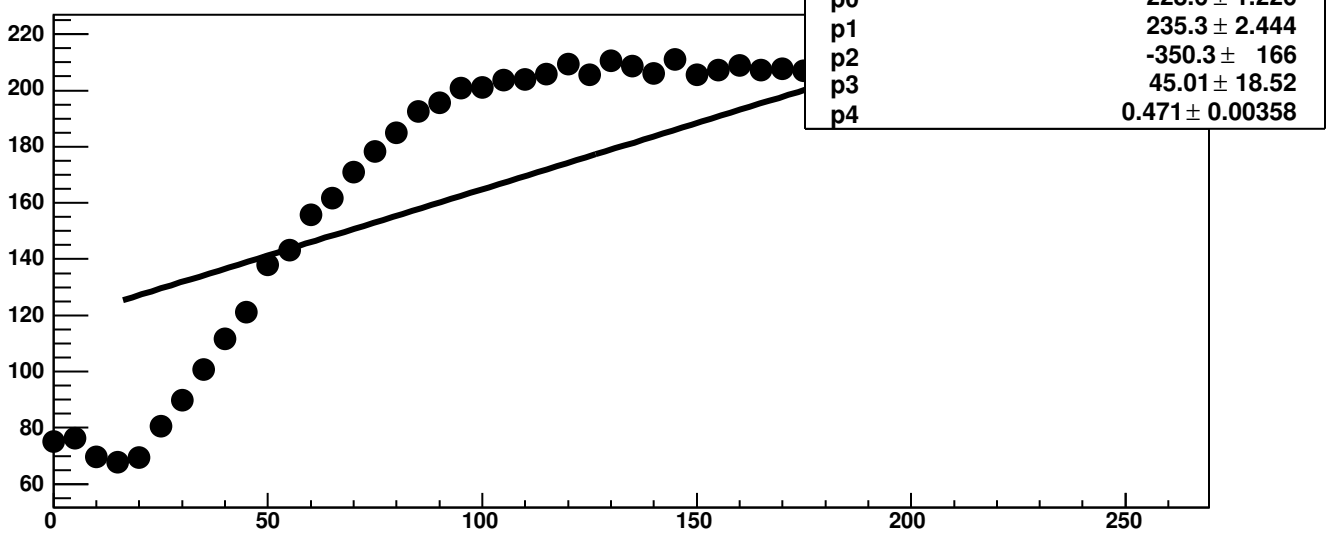
Chip 8, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



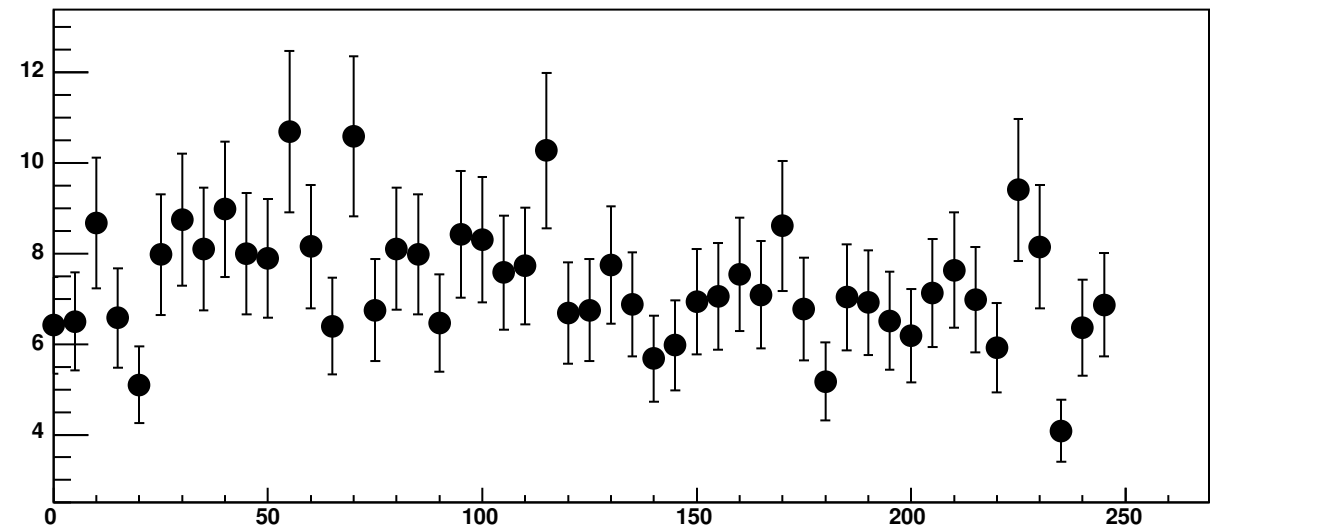
Chip 8, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold



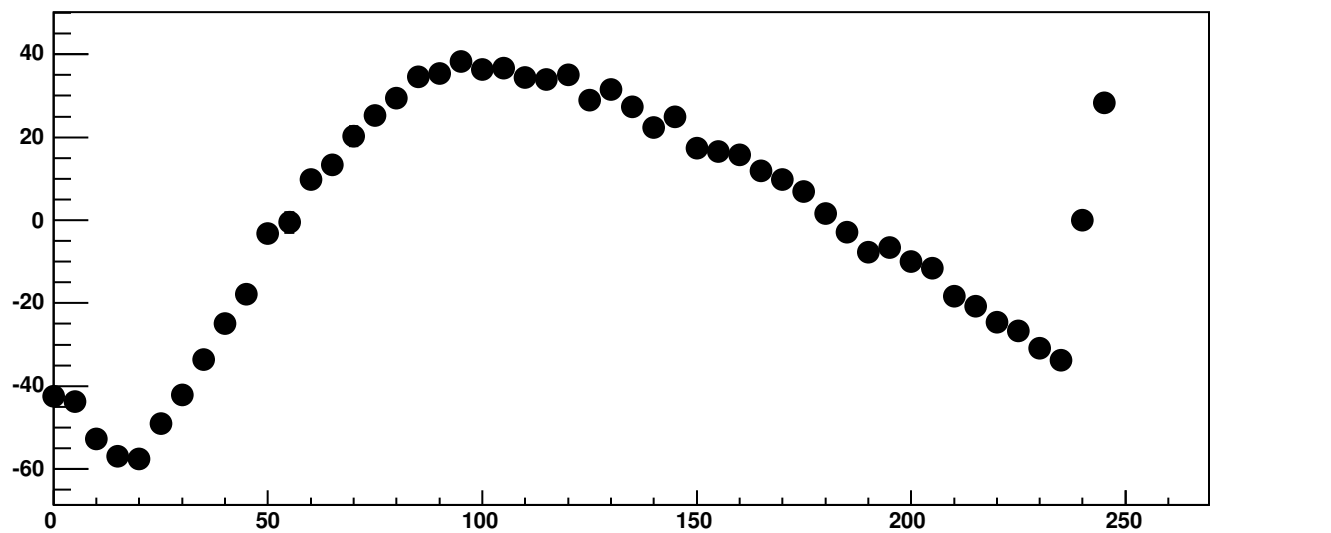
Chip 8, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold



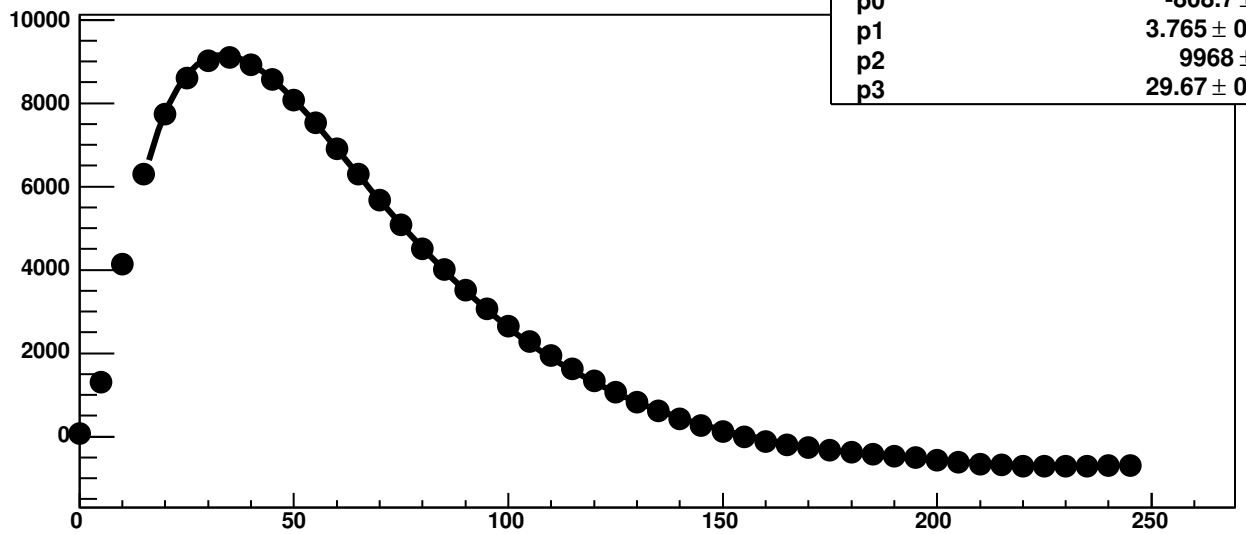
Chip 8, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



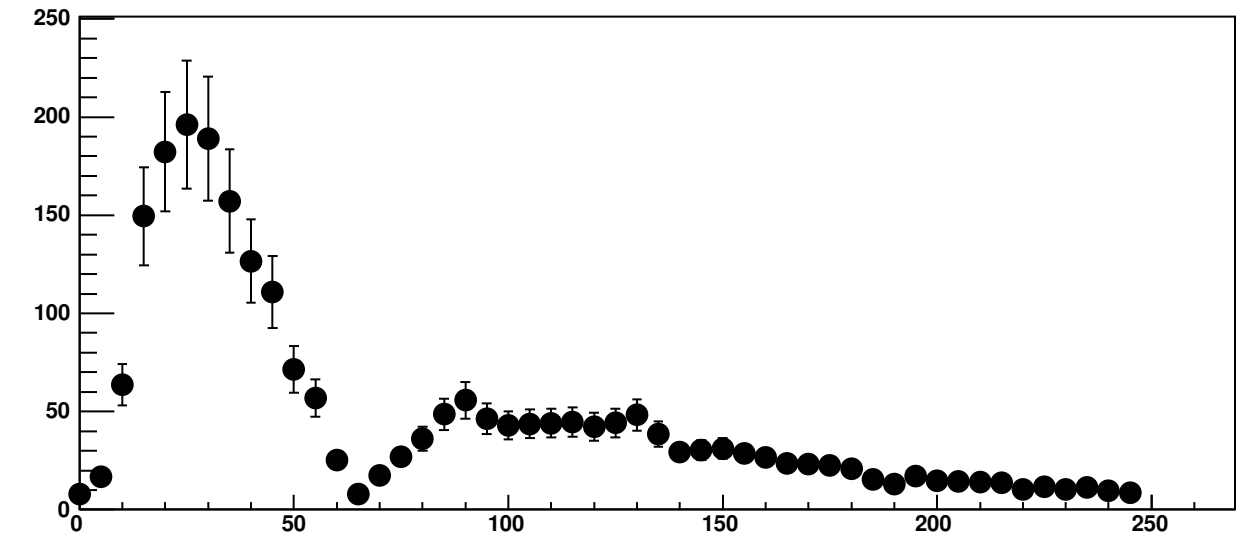
Chip 8, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold



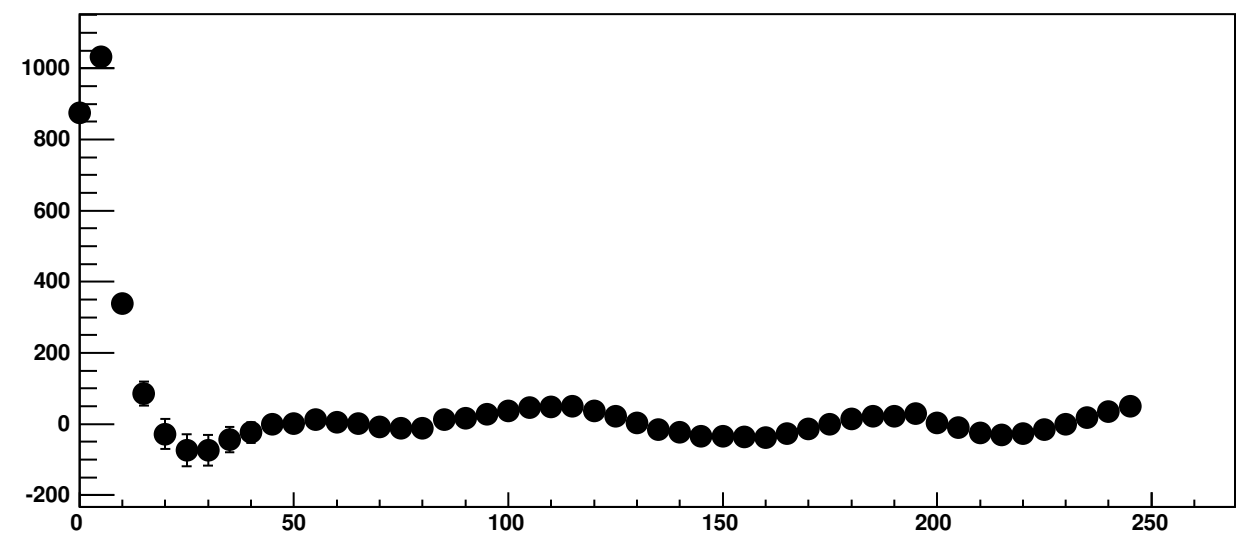
Chip 8, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold



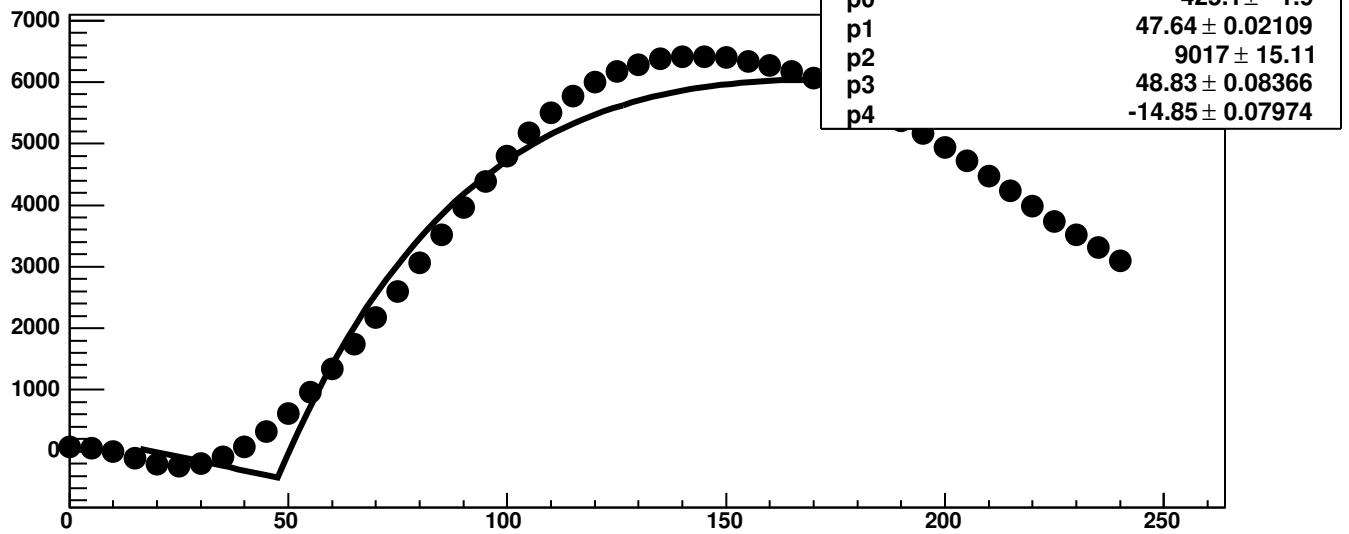
Chip 8, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold



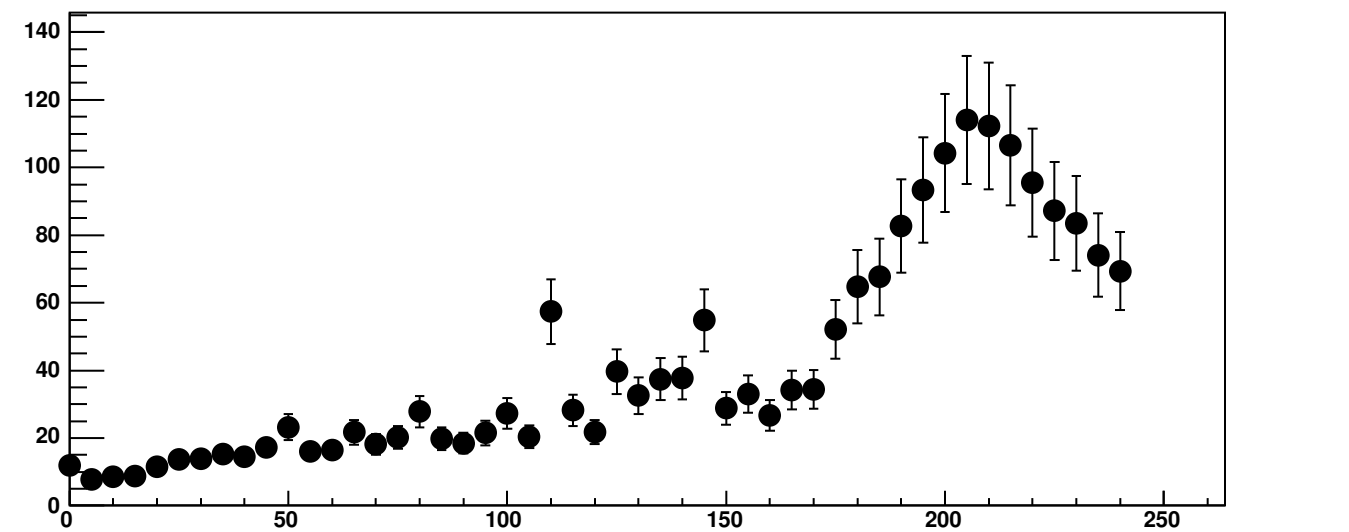
Chip 8, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold



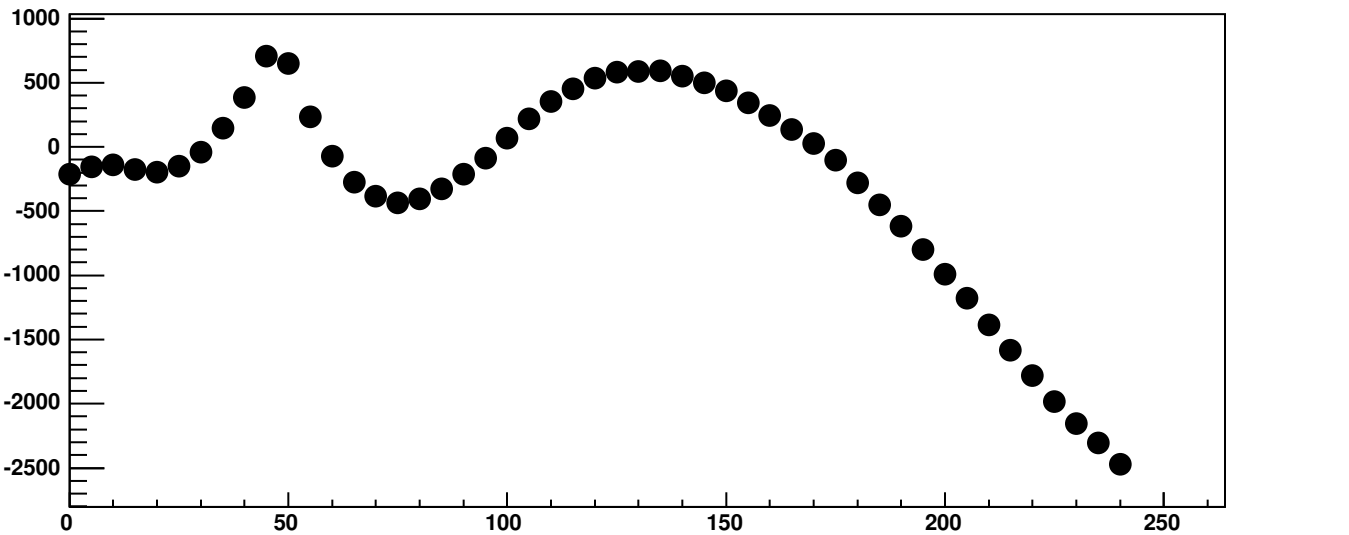
Chip 8, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold



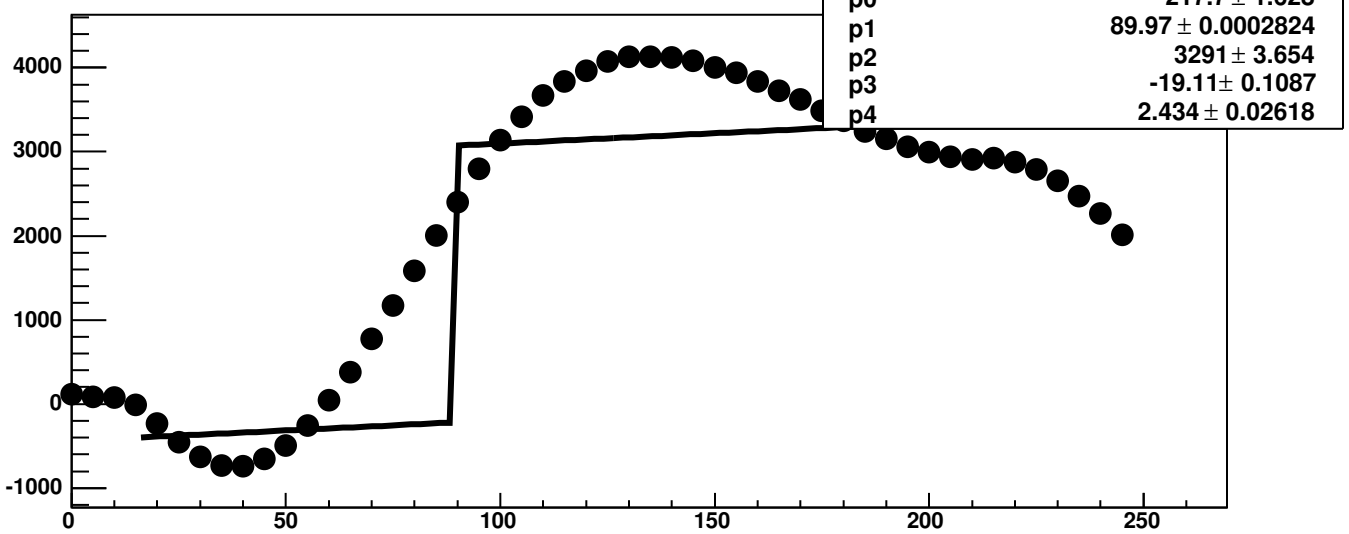
Chip 8, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



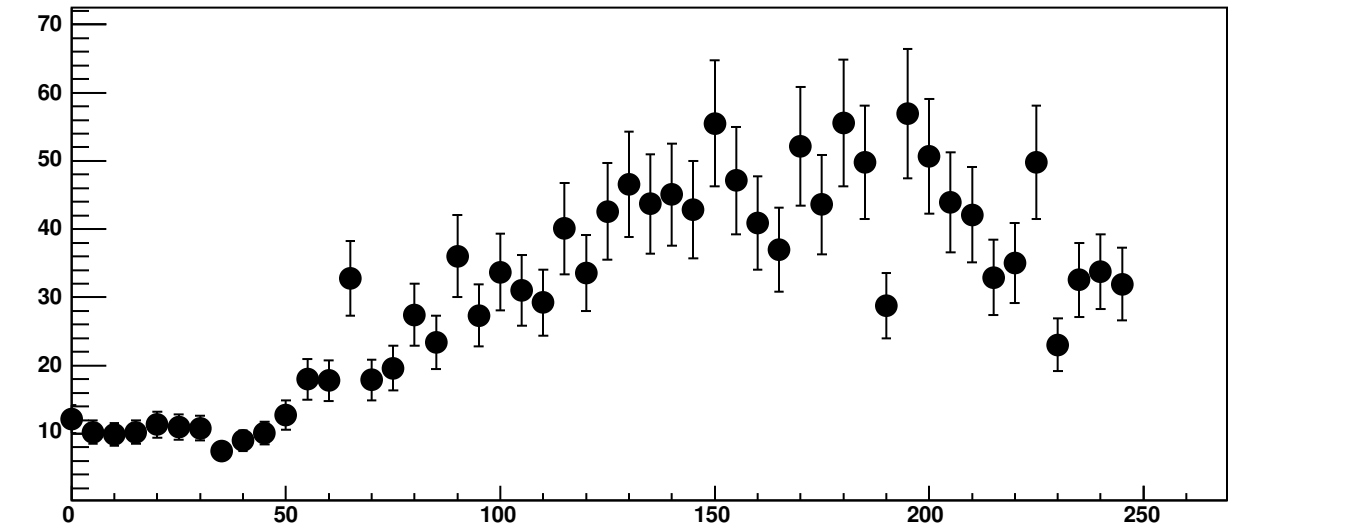
Chip 8, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold



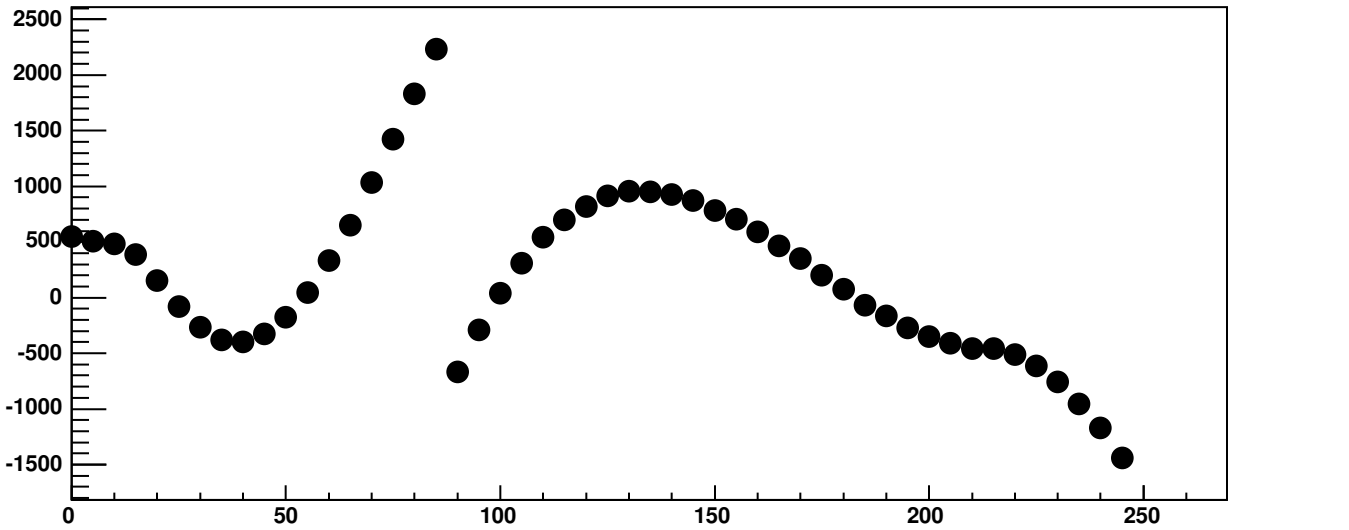
Chip 8, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold



Chip 8, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold

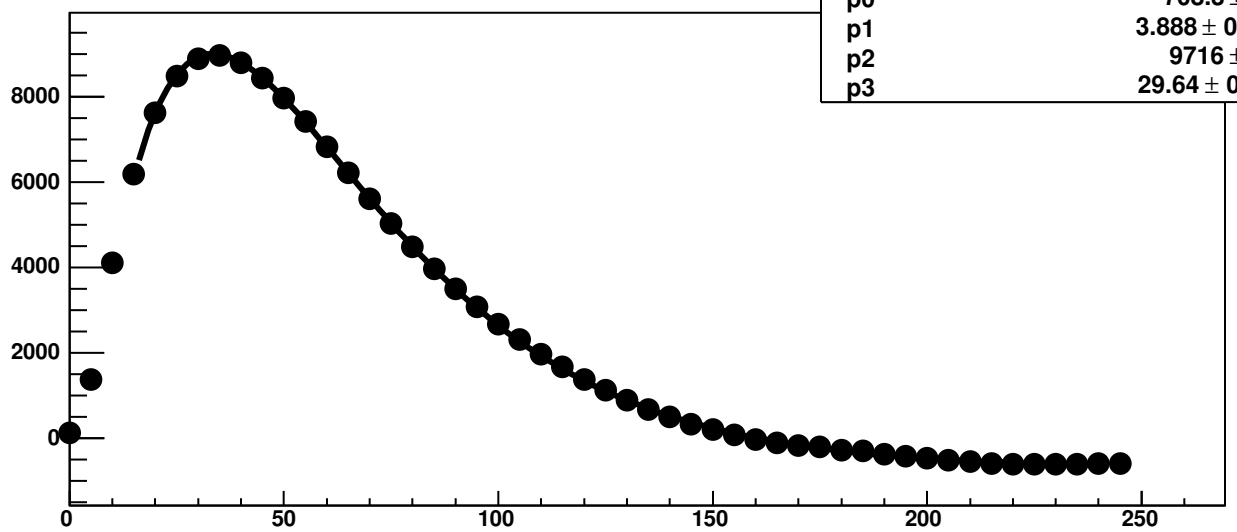


Chip 8, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

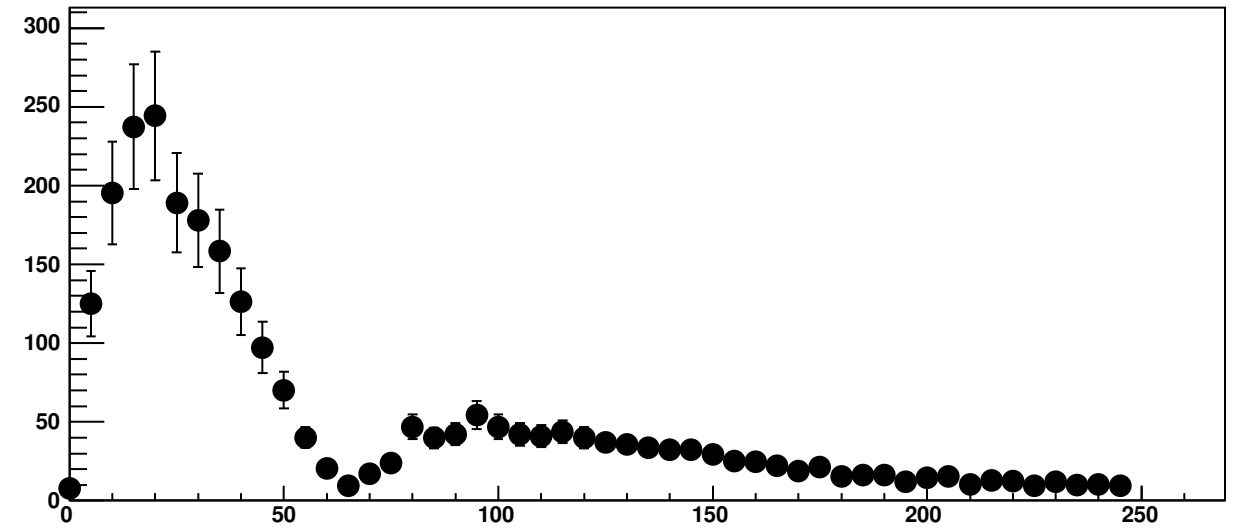


Chip 8, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold

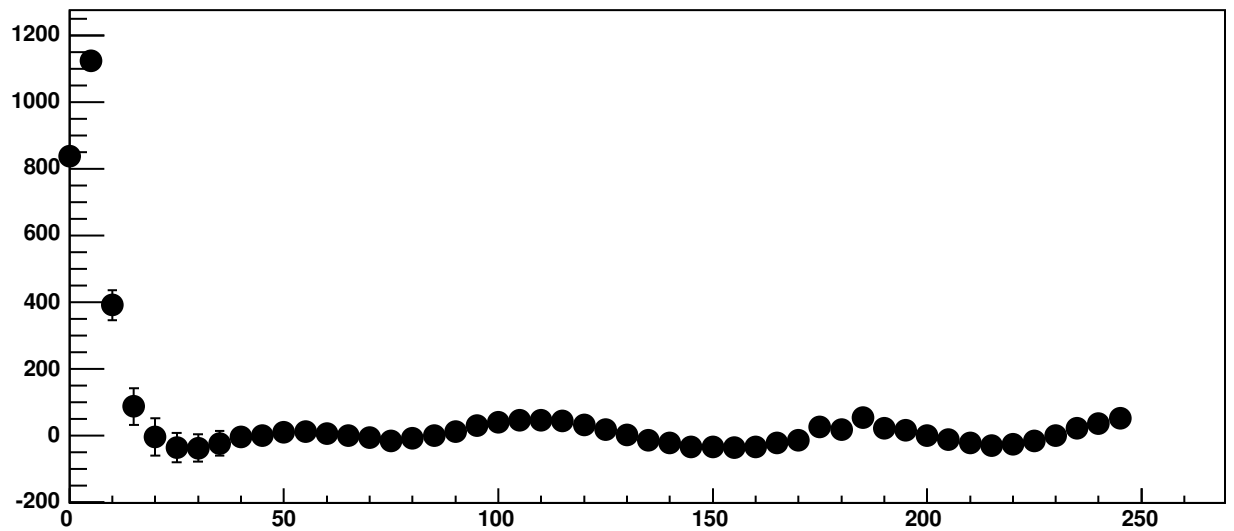
χ^2 / ndf	1275 / 42
p0	-708.3 ± 1.124
p1	3.888 ± 0.08433
p2	9716 ± 9.712
p3	29.64 ± 0.02661



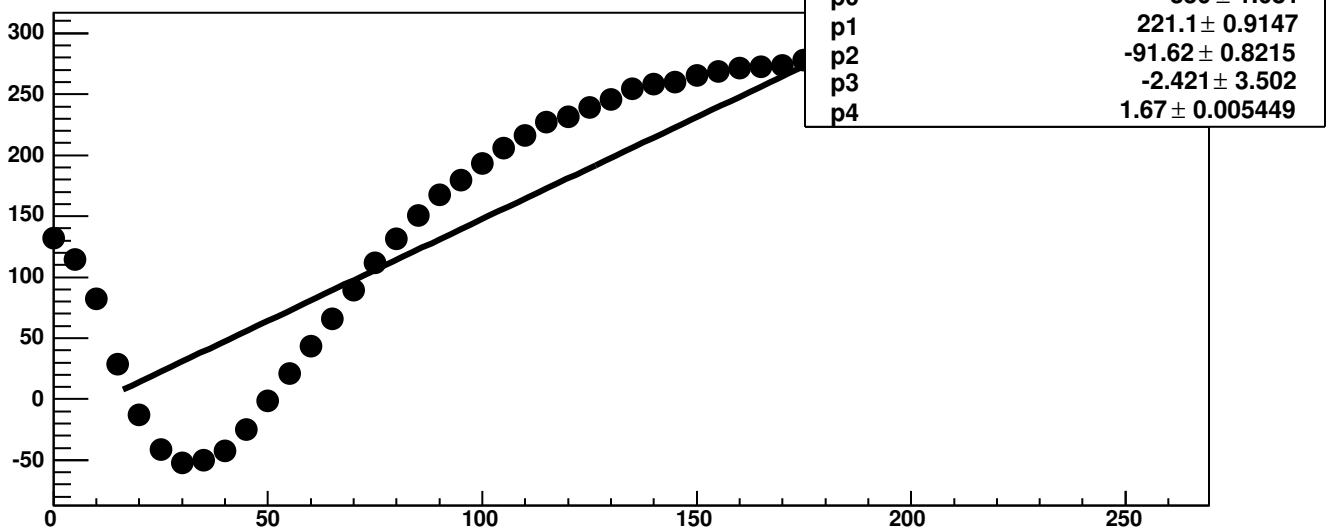
Chip 8, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold



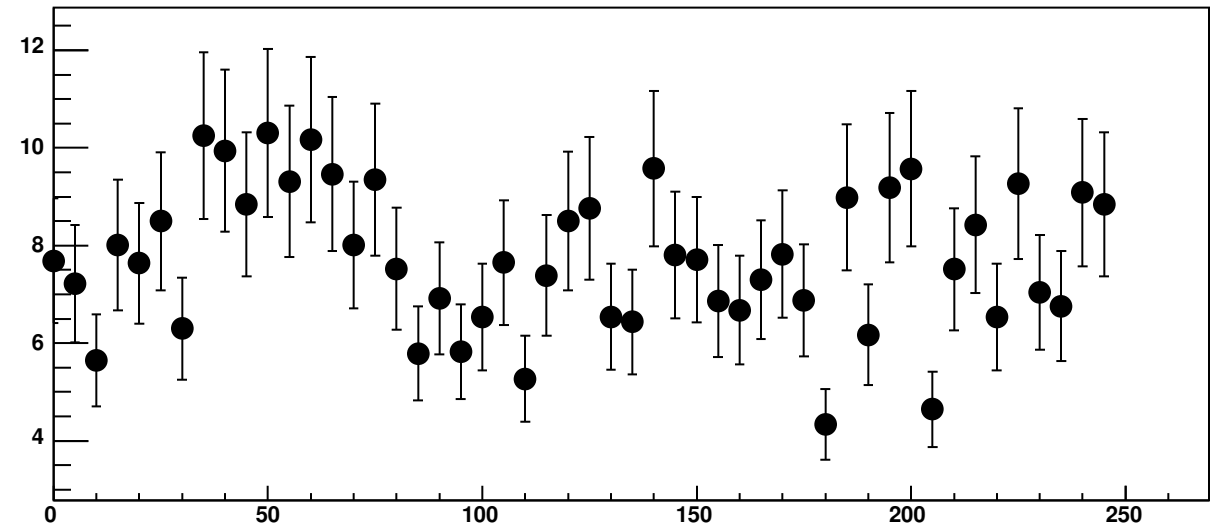
Chip 8, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold



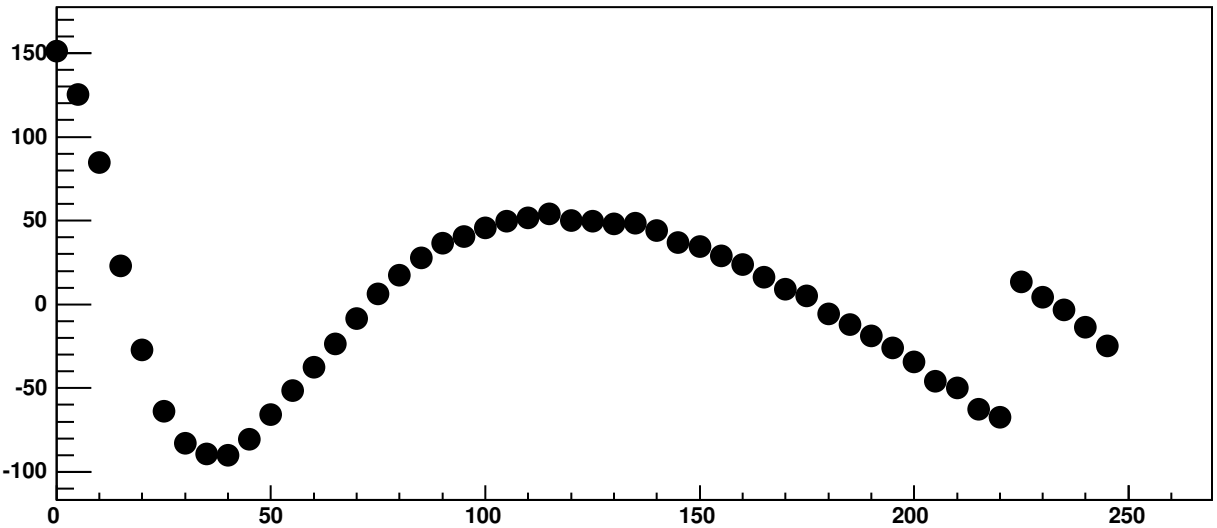
Chip 8, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold



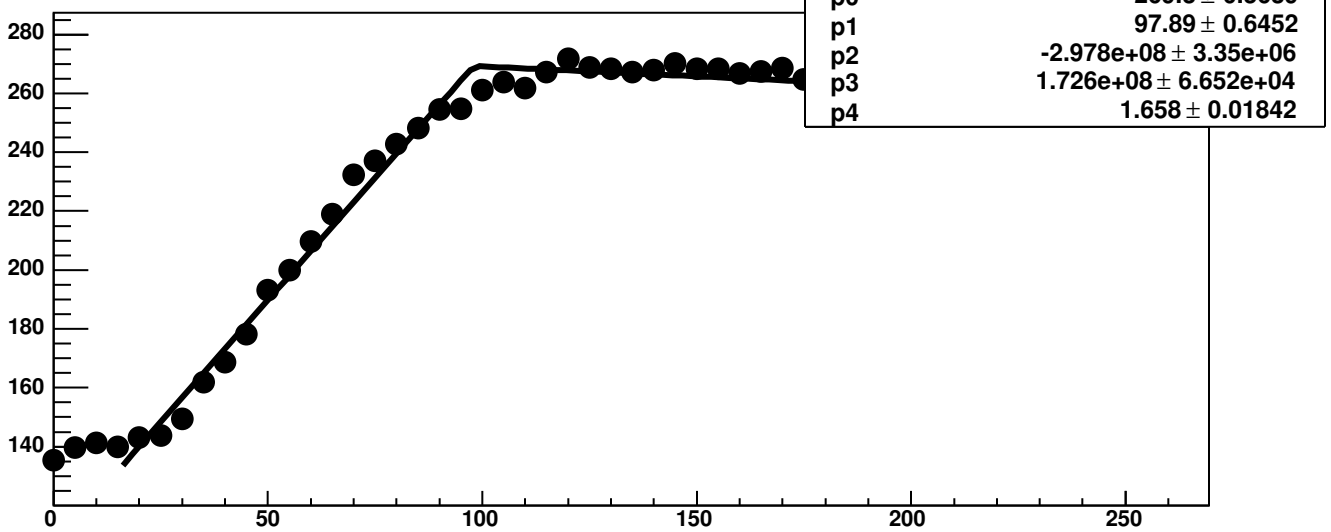
Chip 8, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



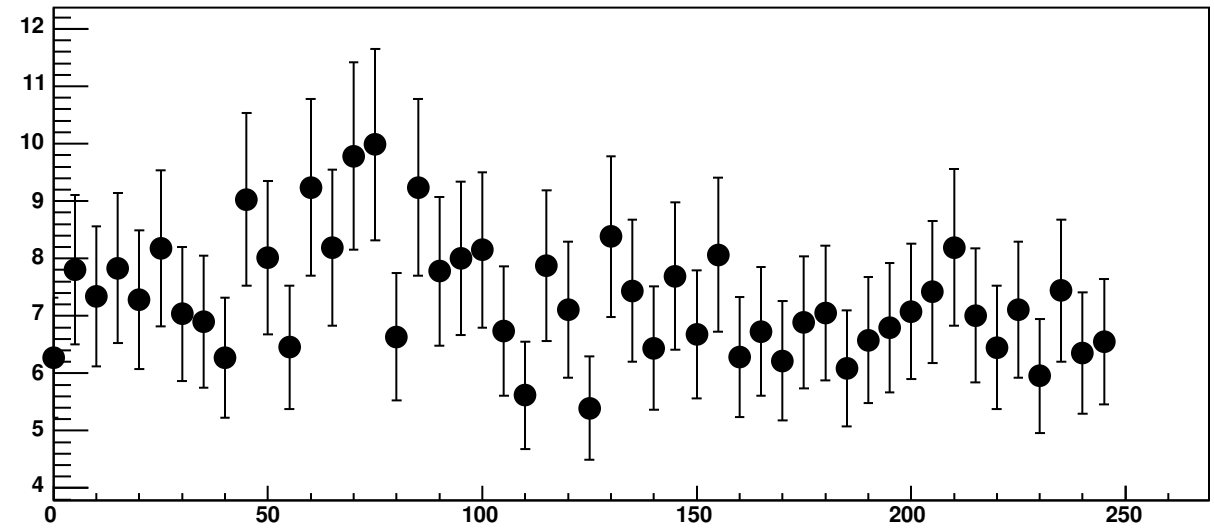
Chip 8, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold



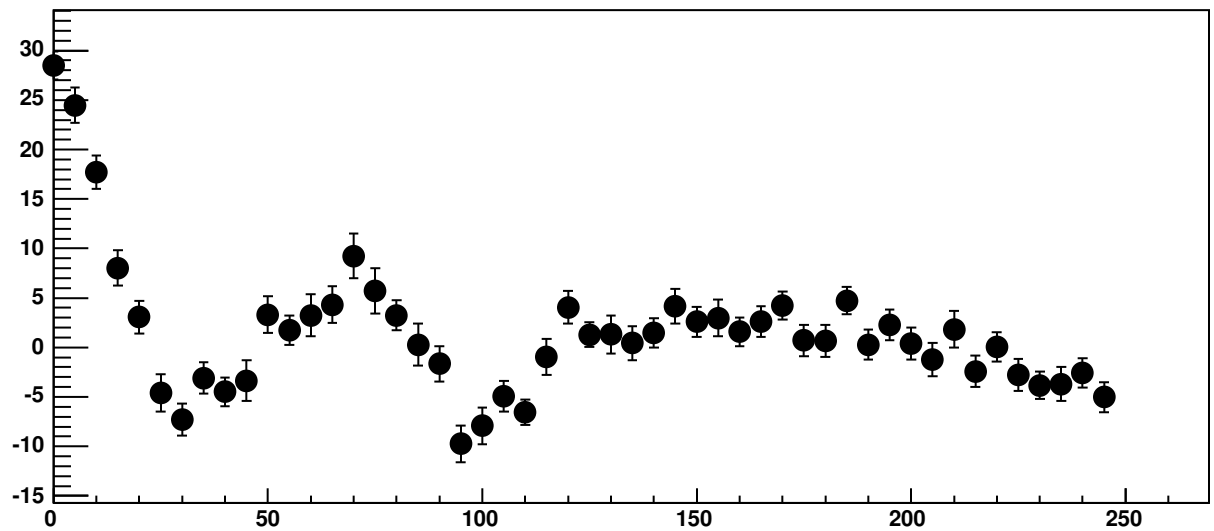
Chip 8, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold



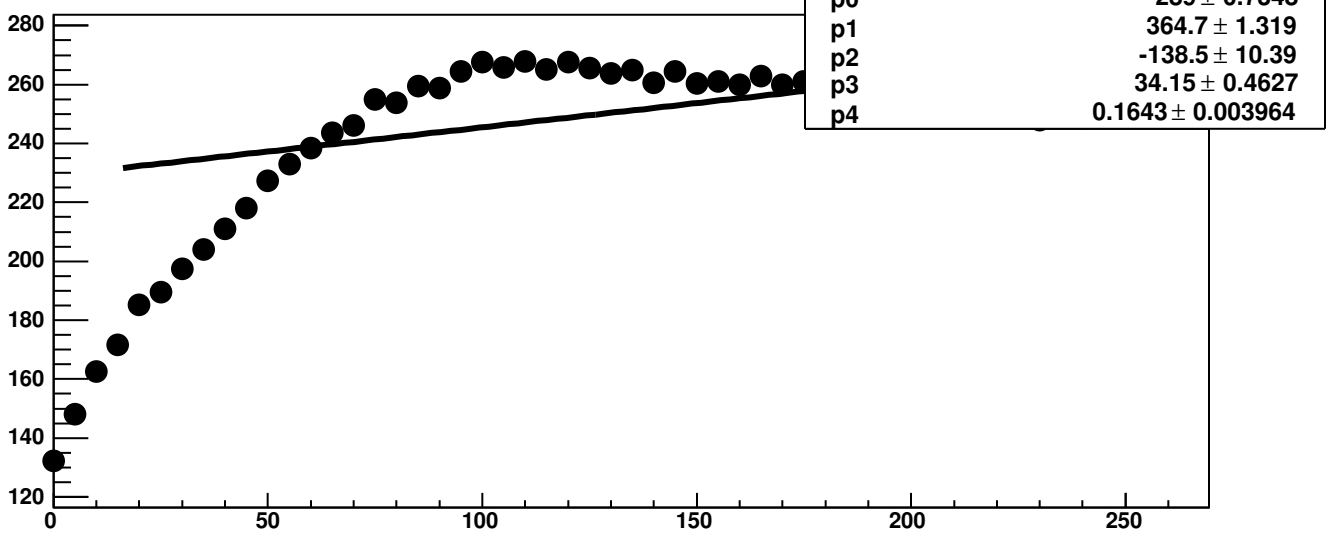
Chip 8, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



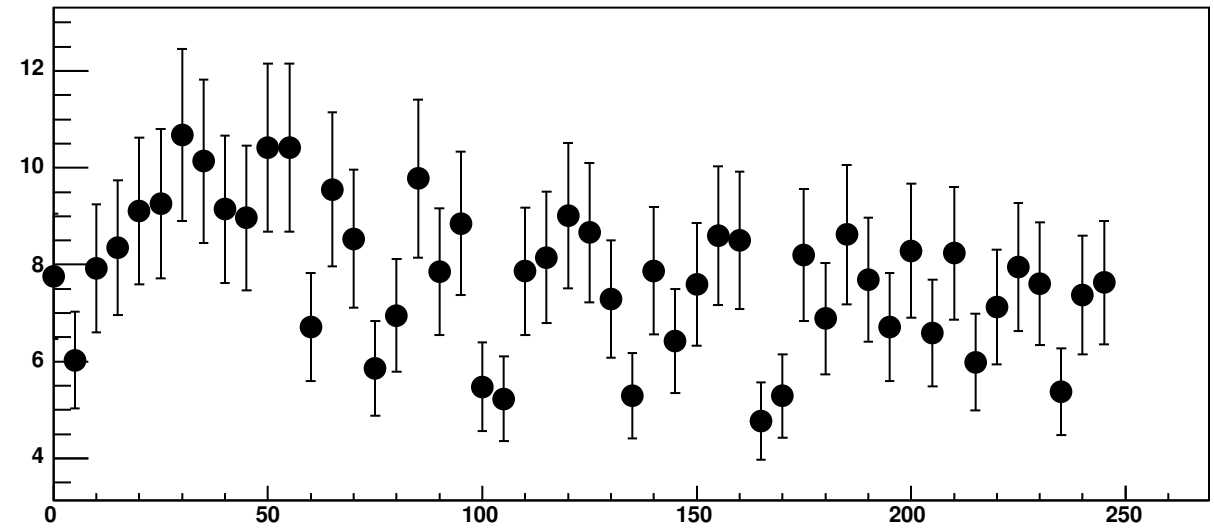
Chip 8, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold



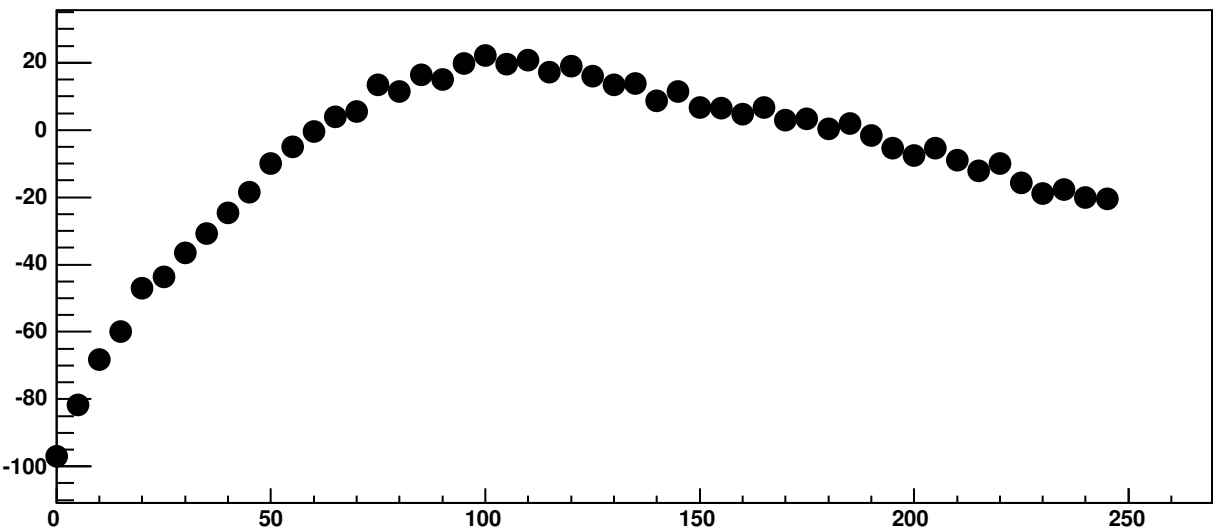
Chip 8, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold

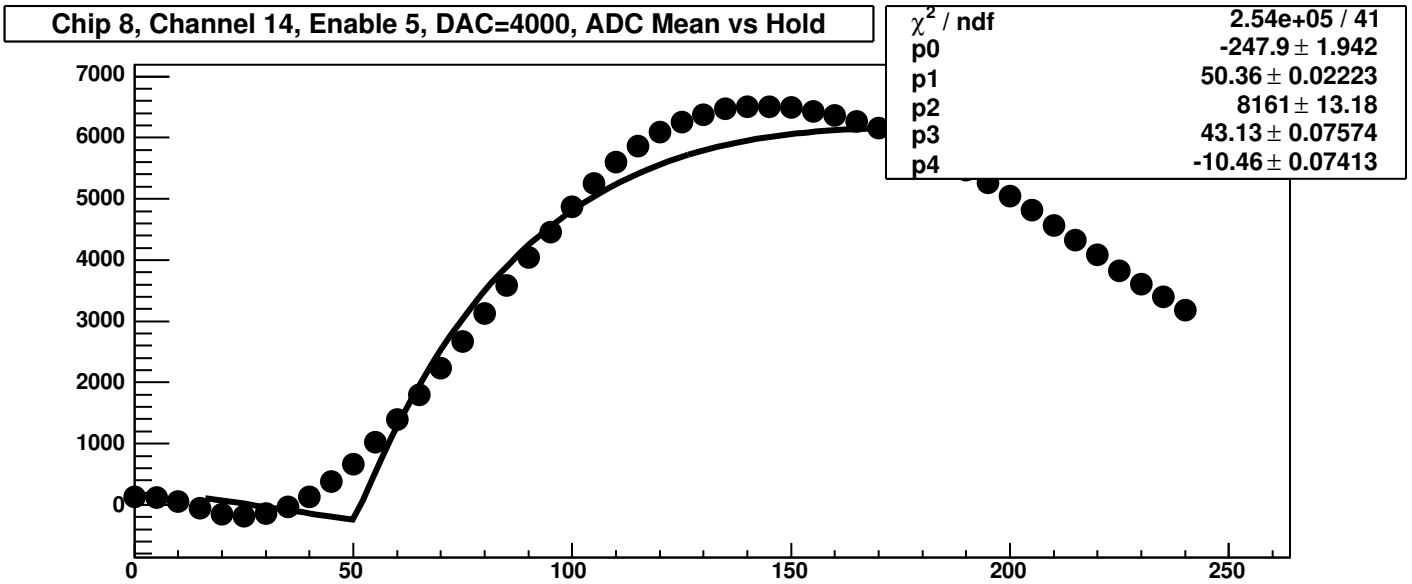


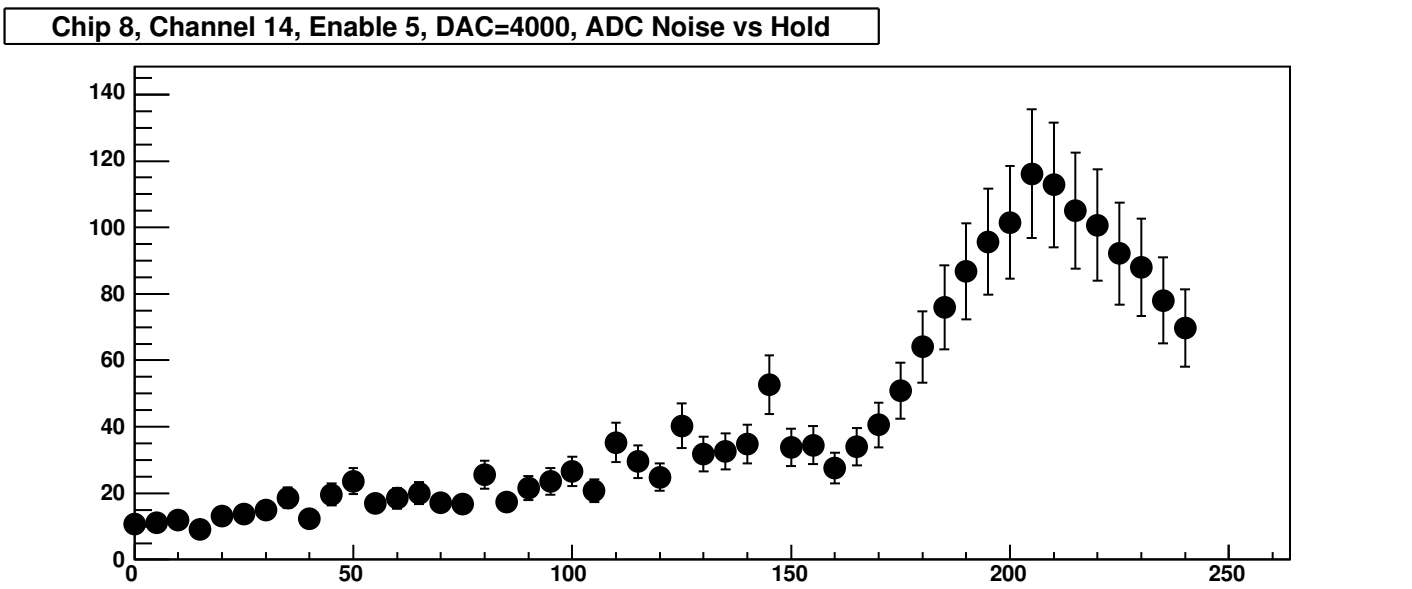
Chip 8, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold

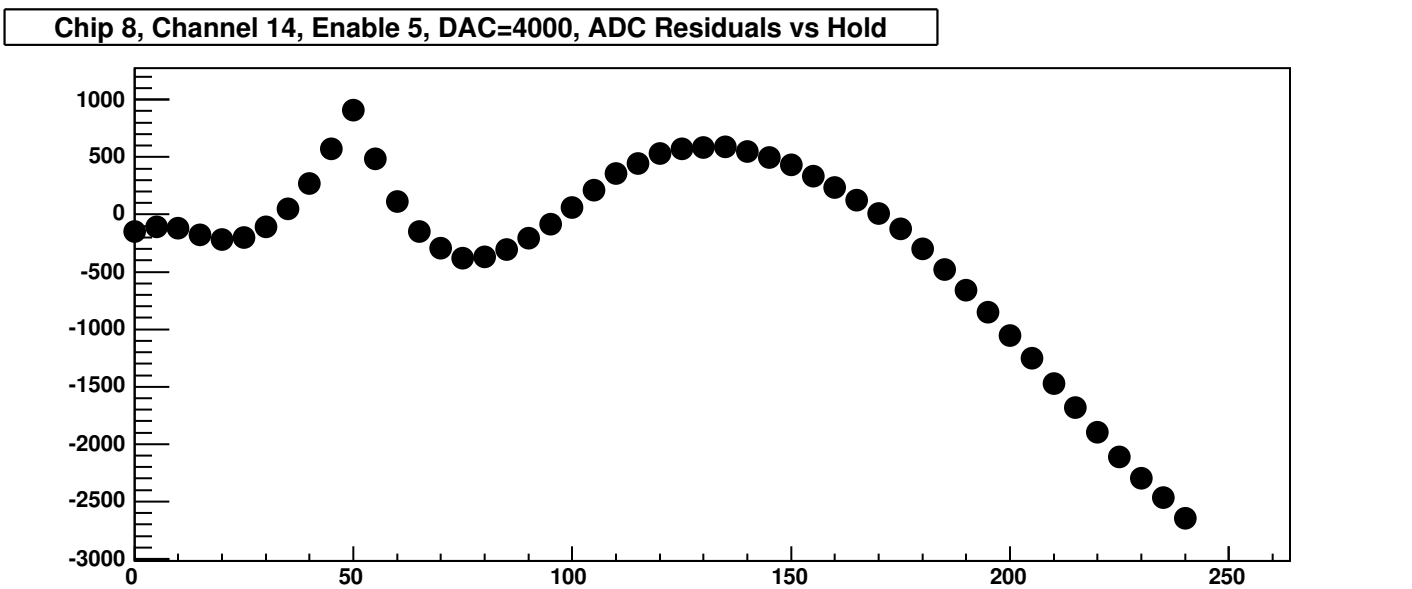


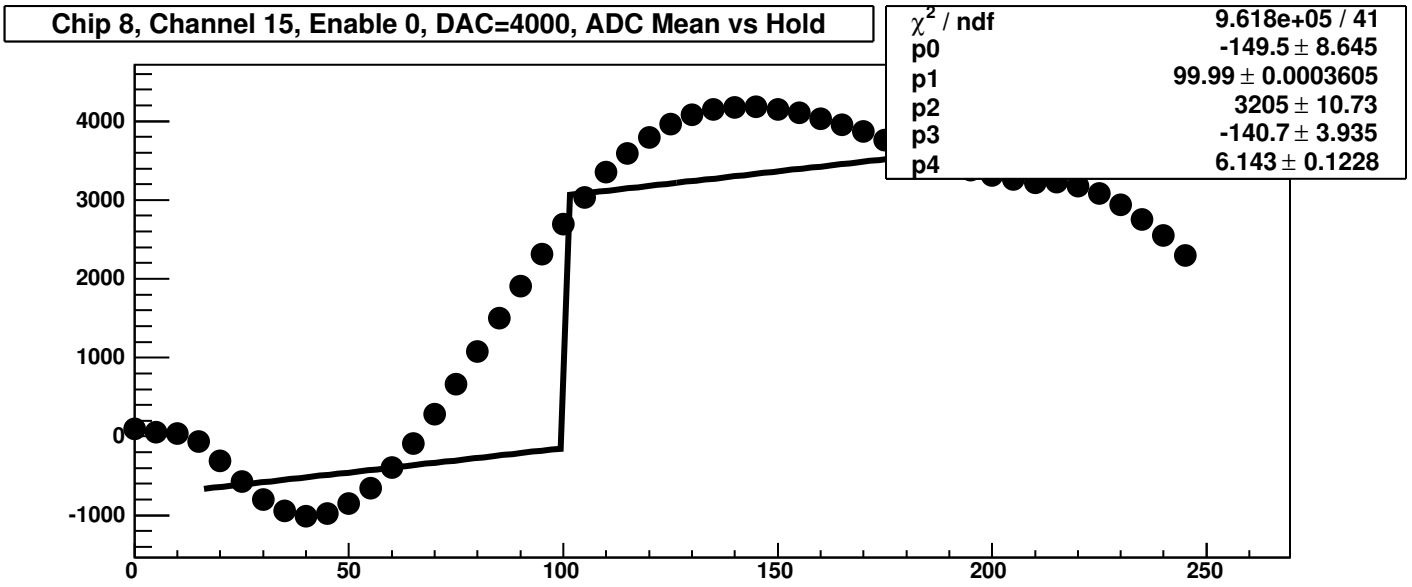
Chip 8, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold

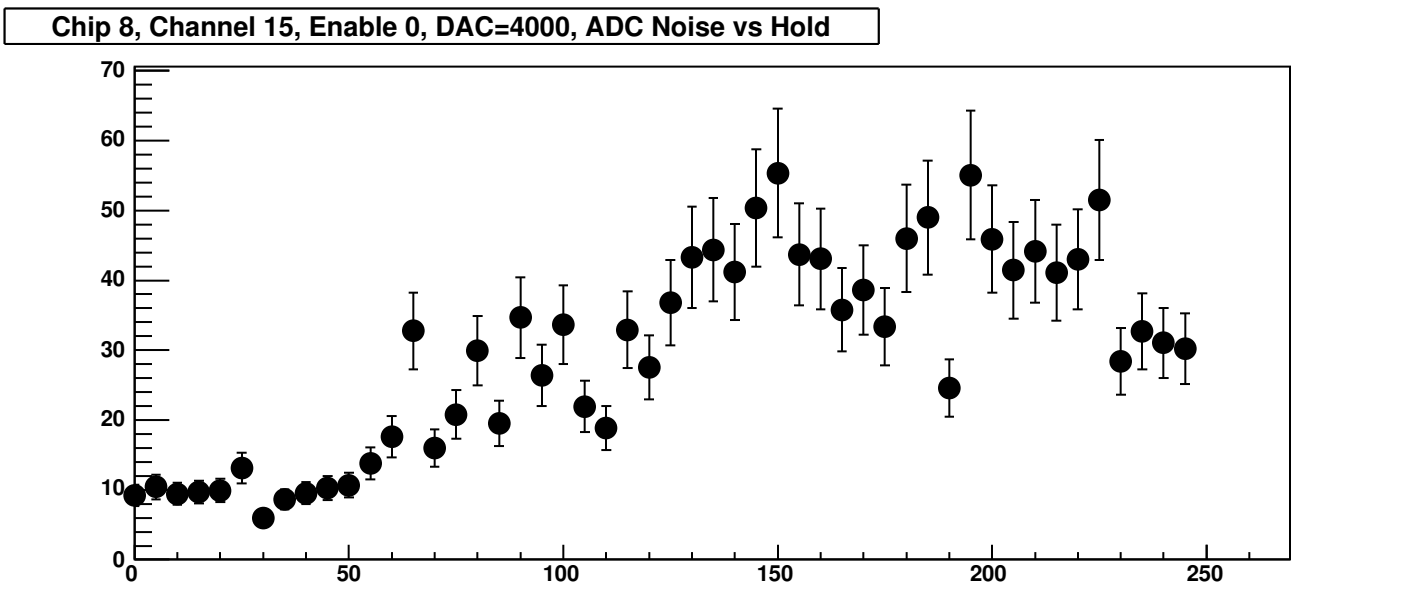


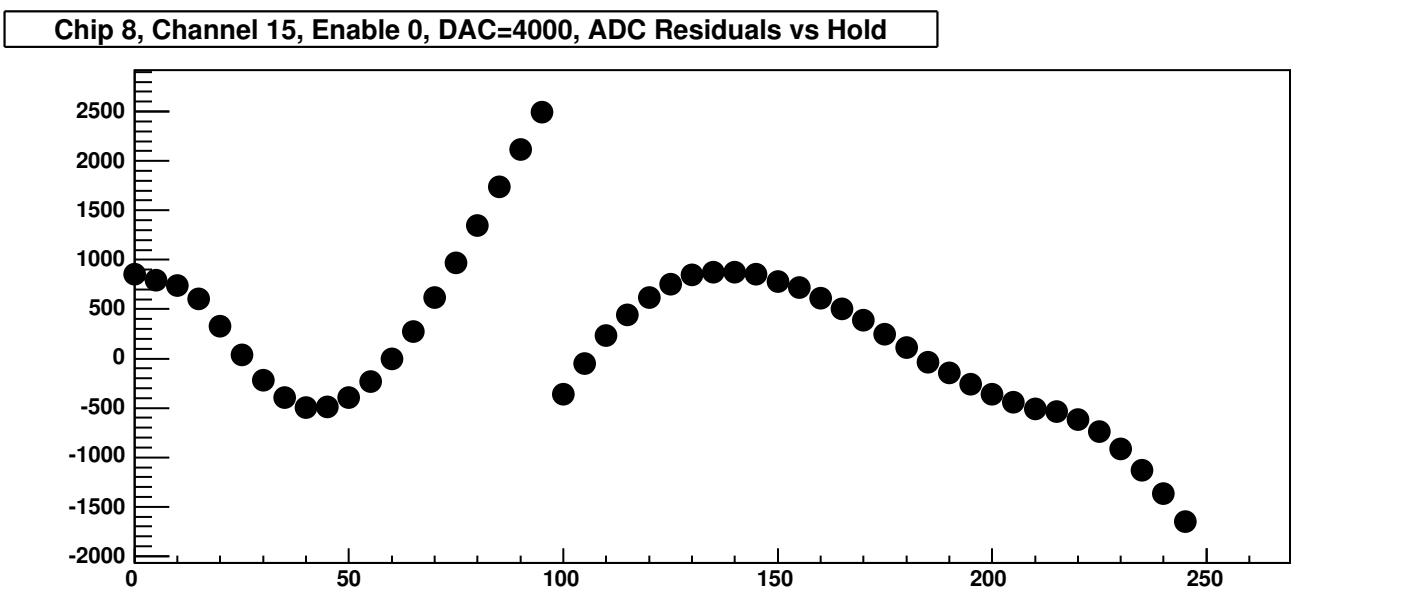

Chip 8, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.54e+05 / 41
p0 -247.9 ± 1.942
p1 50.36 ± 0.02223
p2 8161 ± 13.18
p3 43.13 ± 0.07574
p4 -10.46 ± 0.07413
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 8, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250

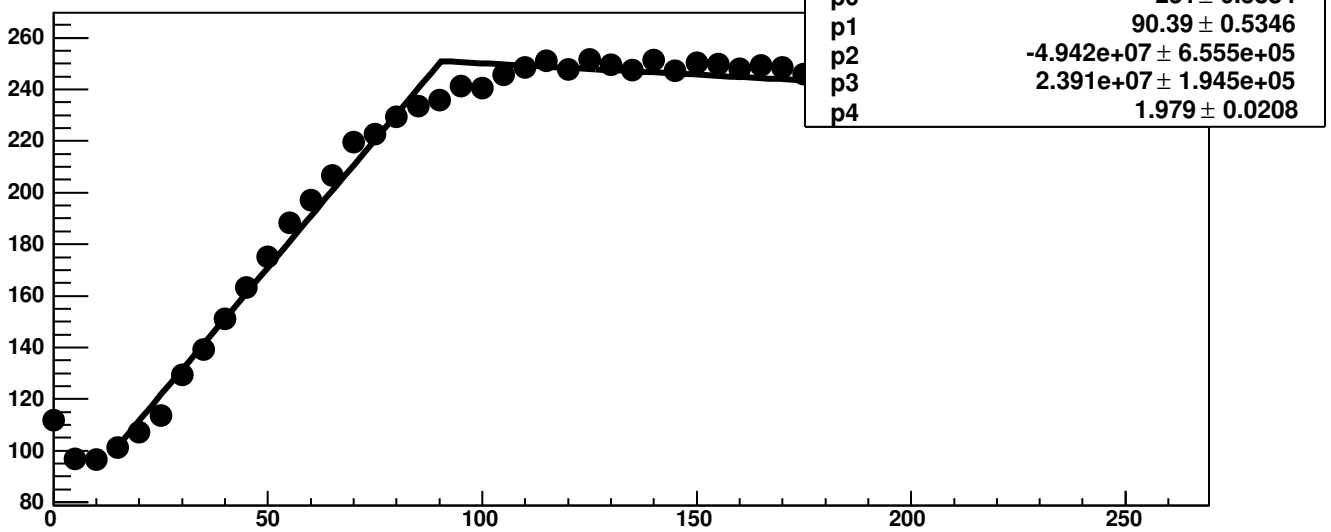

Chip 8, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250


Chip 8, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 9.618e+05 / 41
p0 -149.5 ± 8.645
p1 99.99 ± 0.0003605
p2 3205 ± 10.73
p3 -140.7 ± 3.935
p4 6.143 ± 0.1228
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

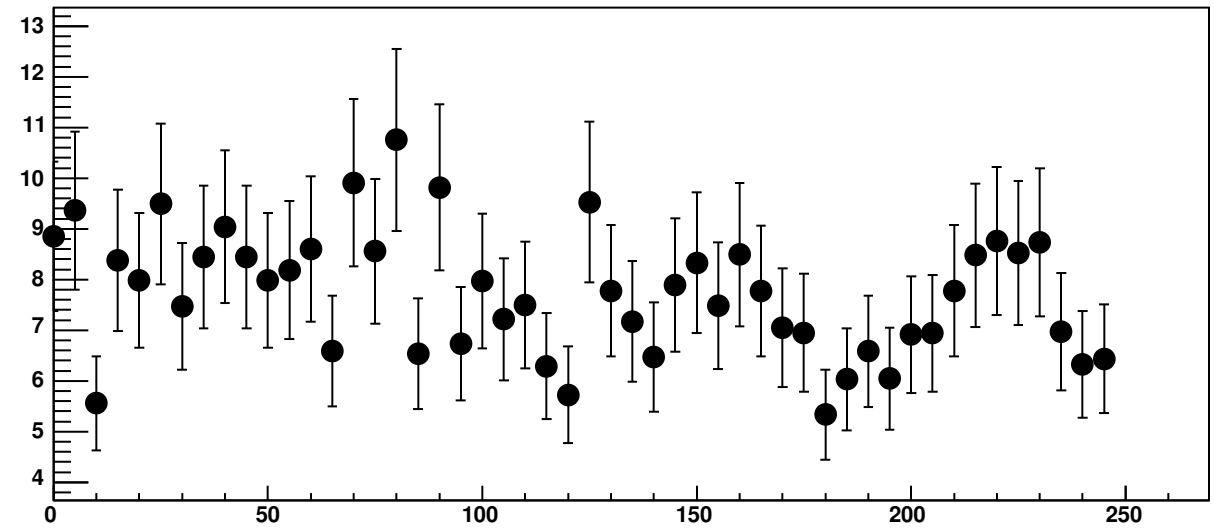

Chip 8, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold
70
60
50
40
30
20
10
0
0
50
100
150
200
250


Chip 8, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold
2500
2000
1500
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

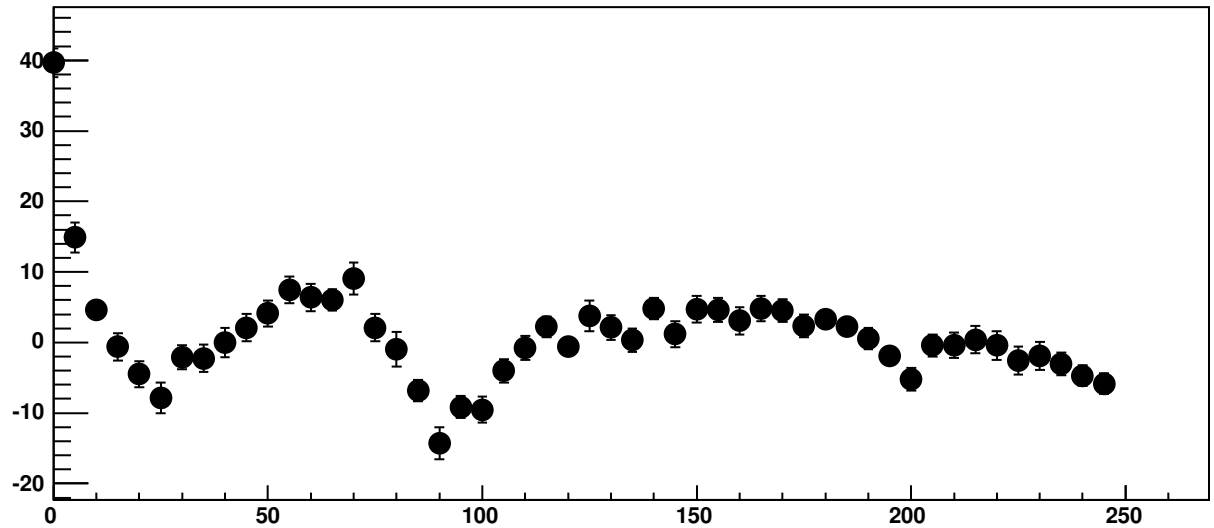
Chip 8, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

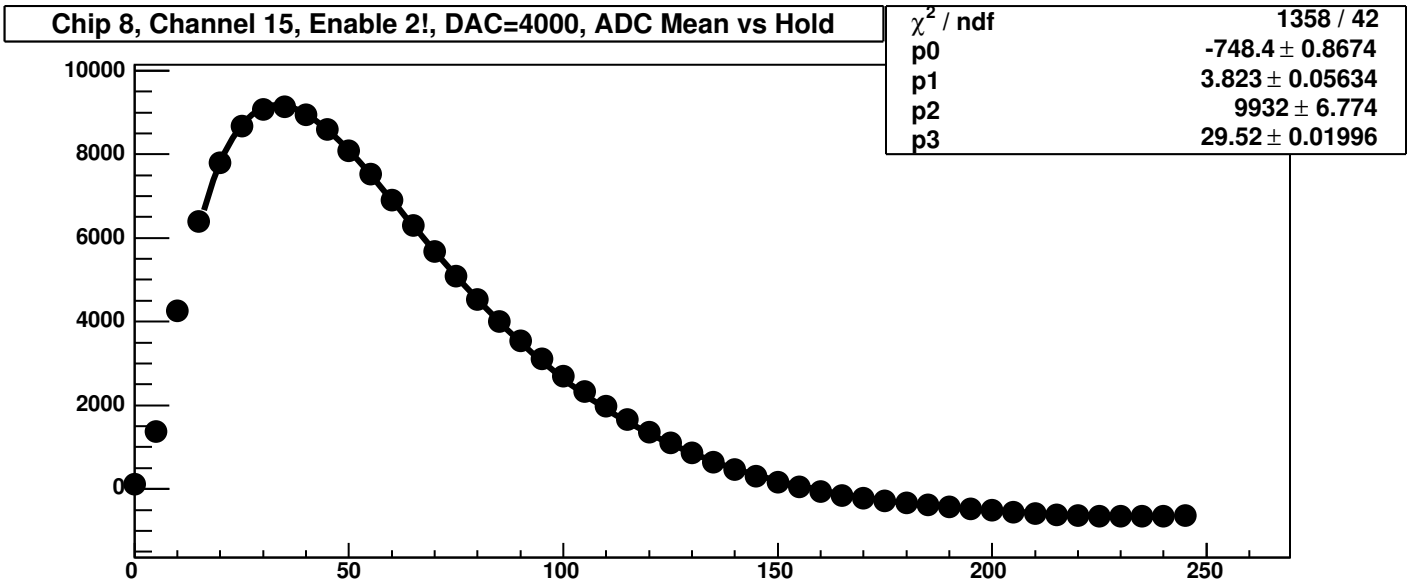


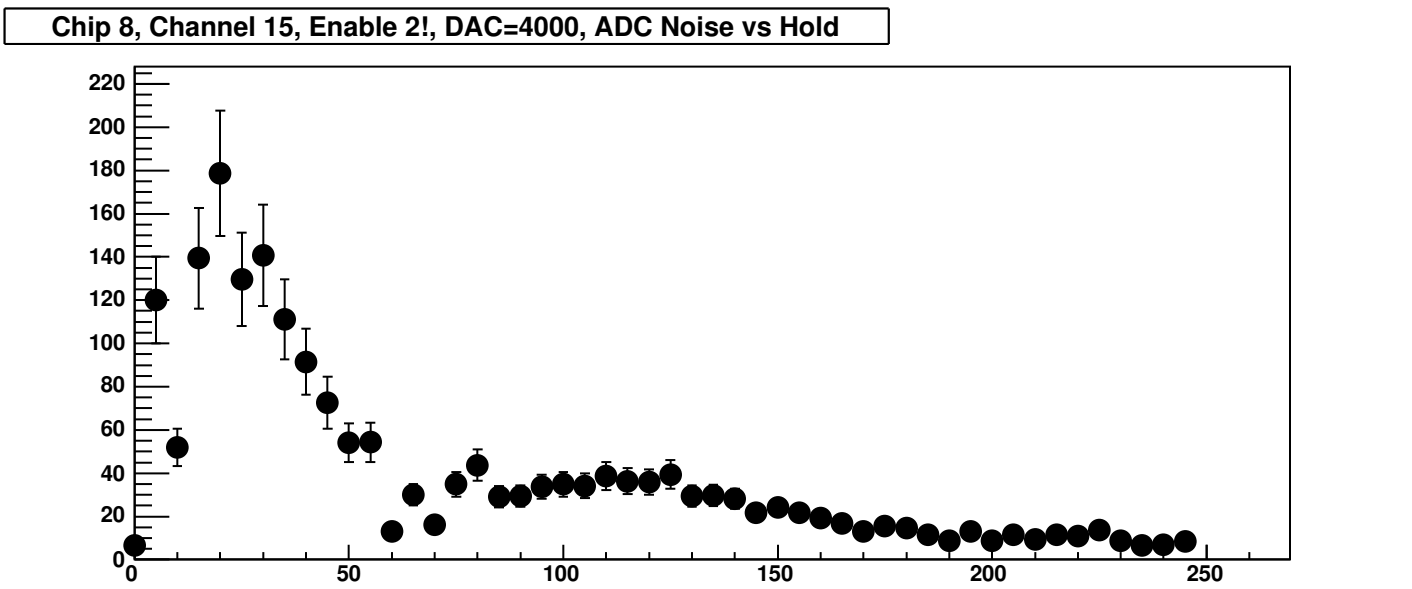
Chip 8, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold

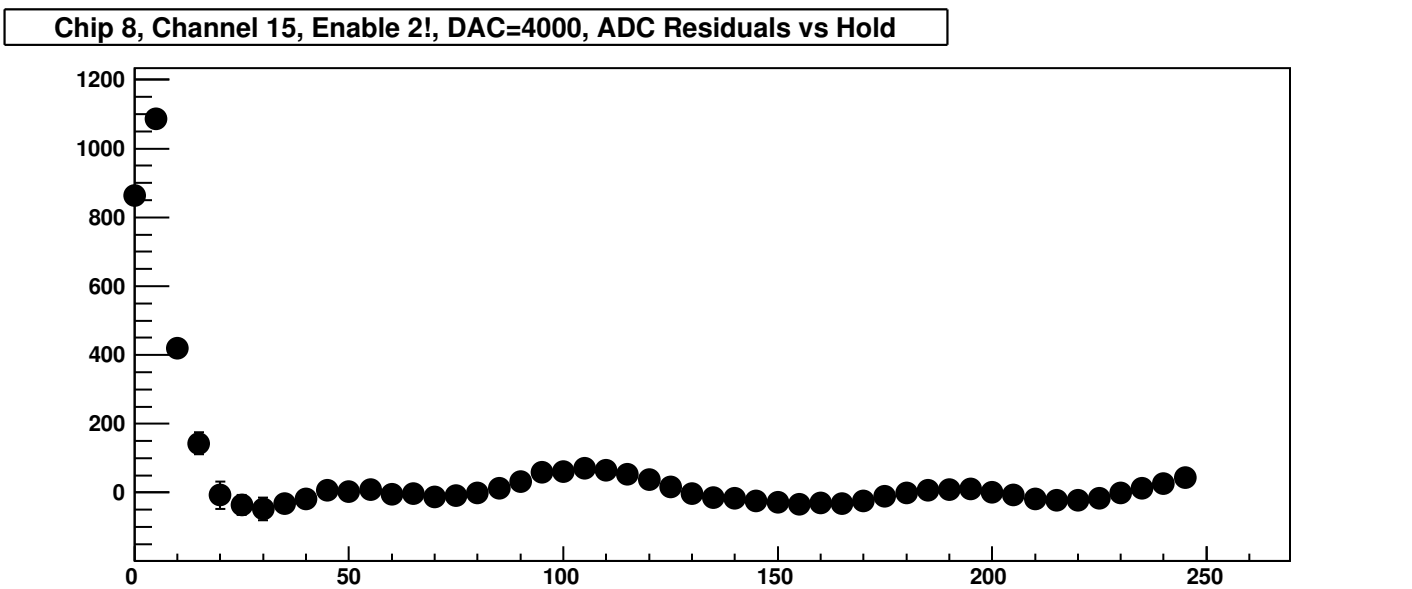


Chip 8, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

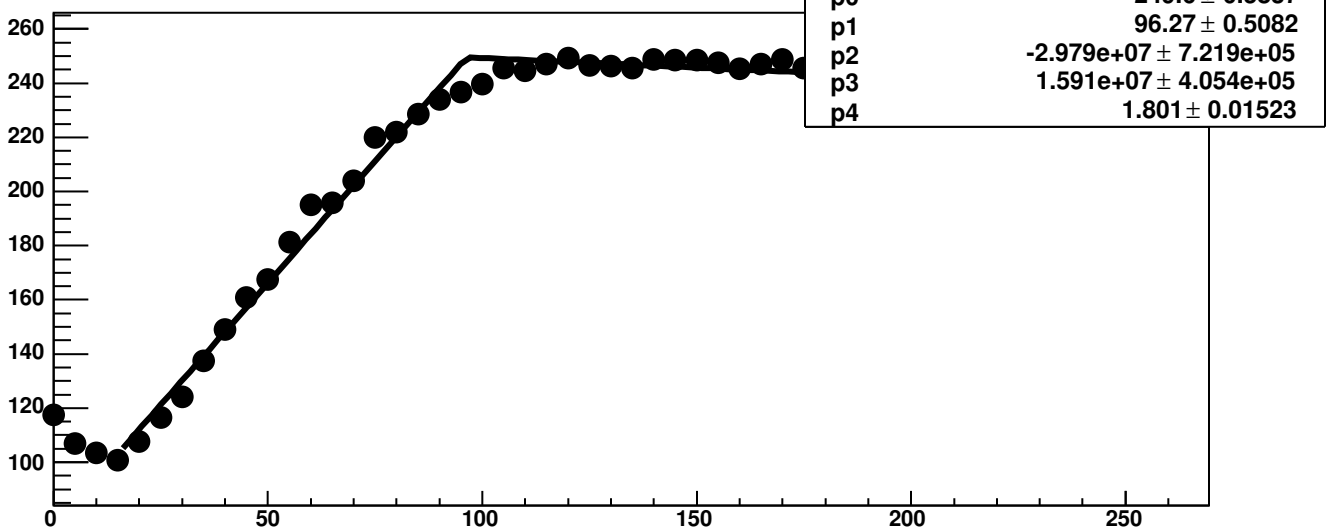



Chip 8, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1358 / 42
p0 -748.4 ± 0.8674
p1 3.823 ± 0.05634
p2 9932 ± 6.774
p3 29.52 ± 0.01996
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

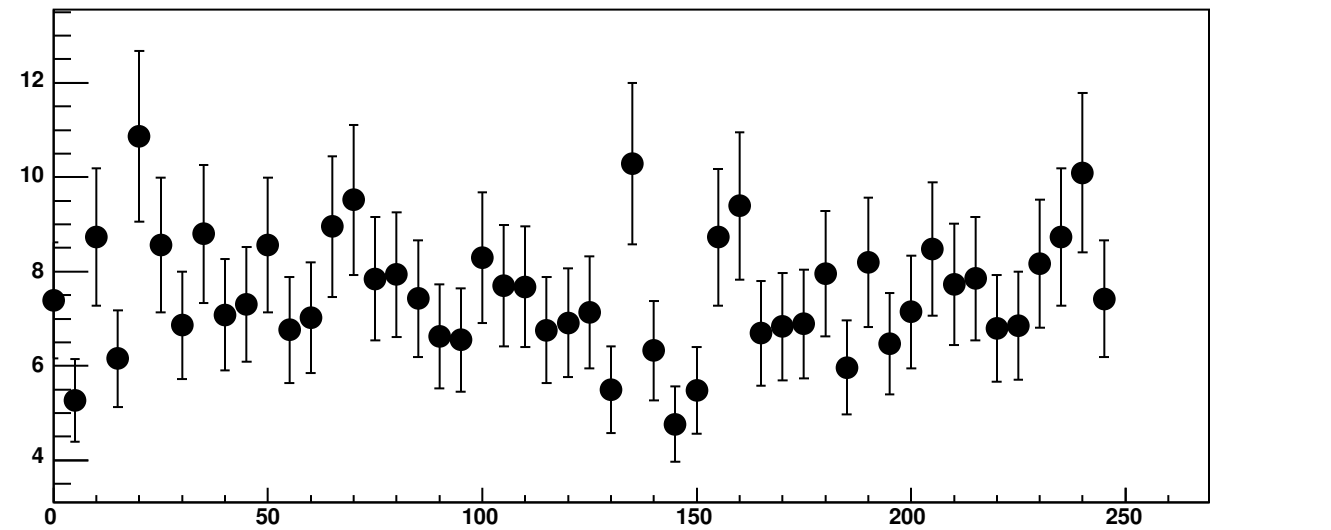

Chip 8, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 8, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold
1200
1000
800
600
400
200
0
0
50
100
150
200
250

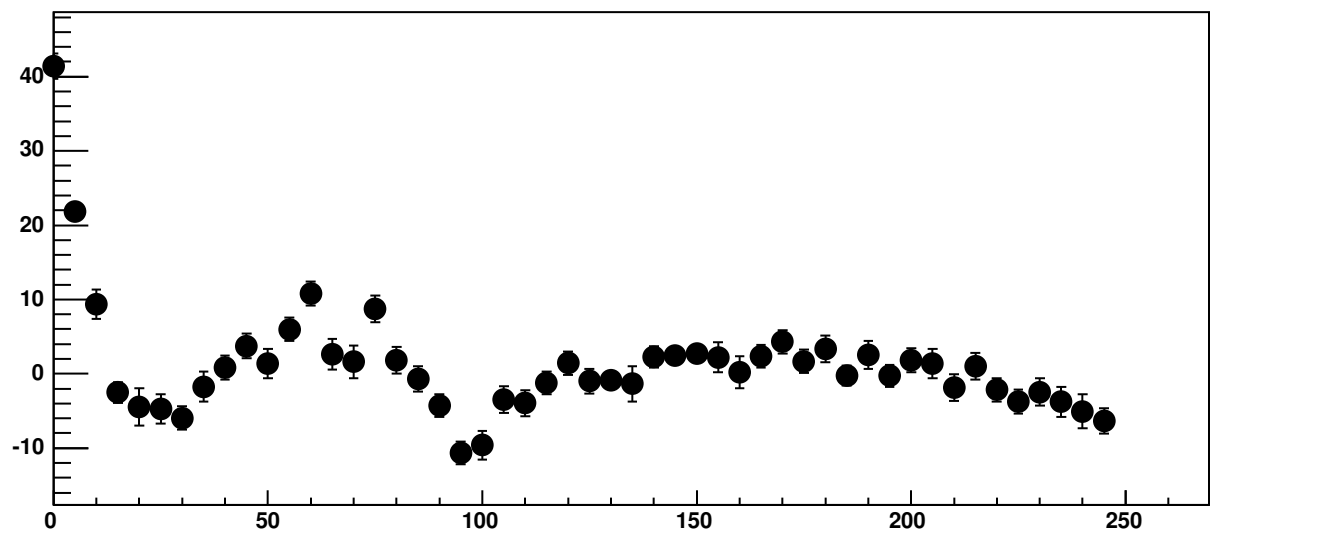
Chip 8, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

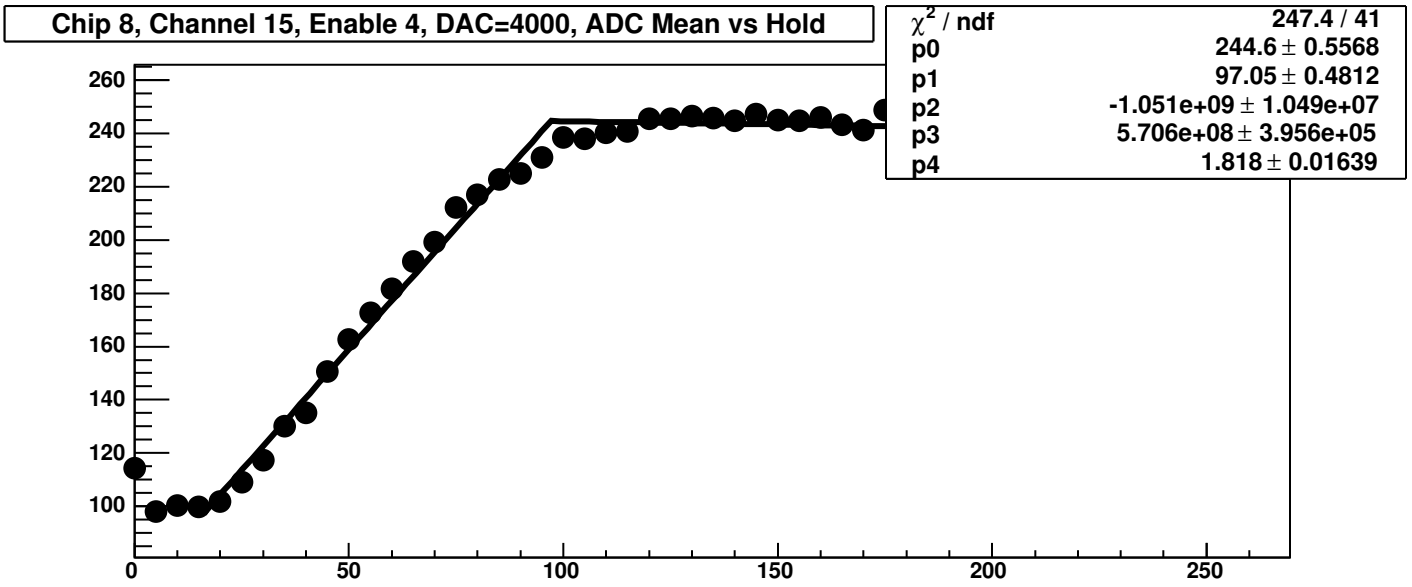


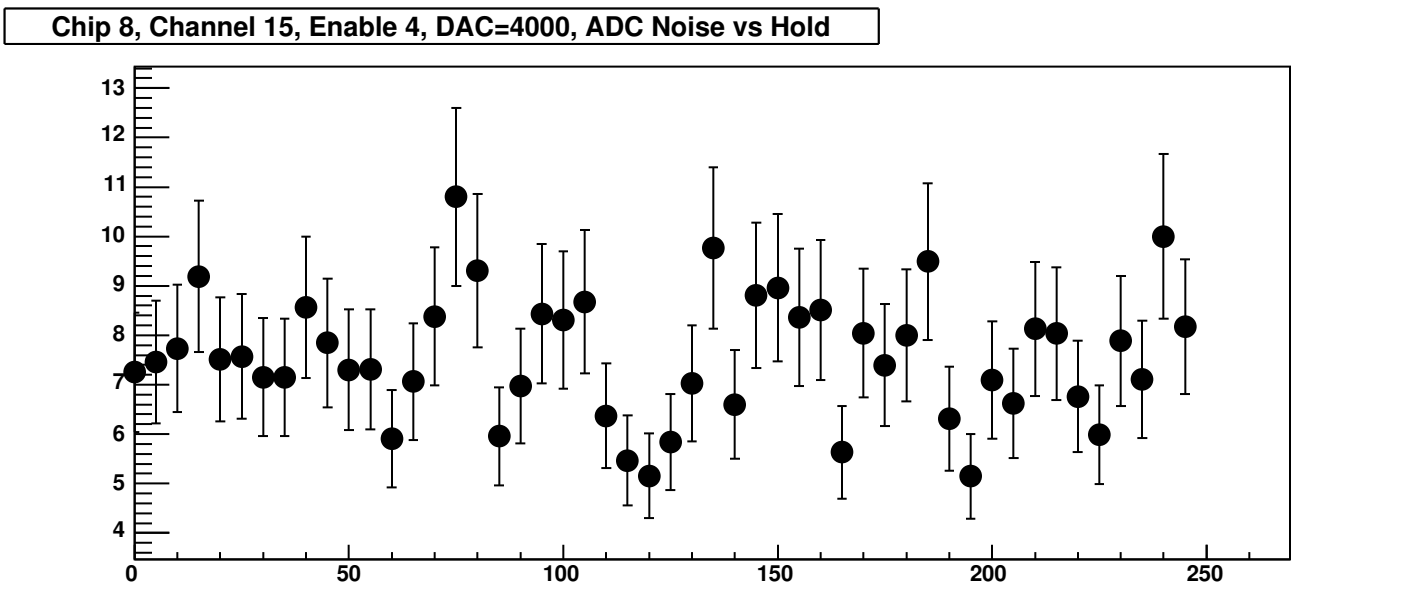
Chip 8, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold

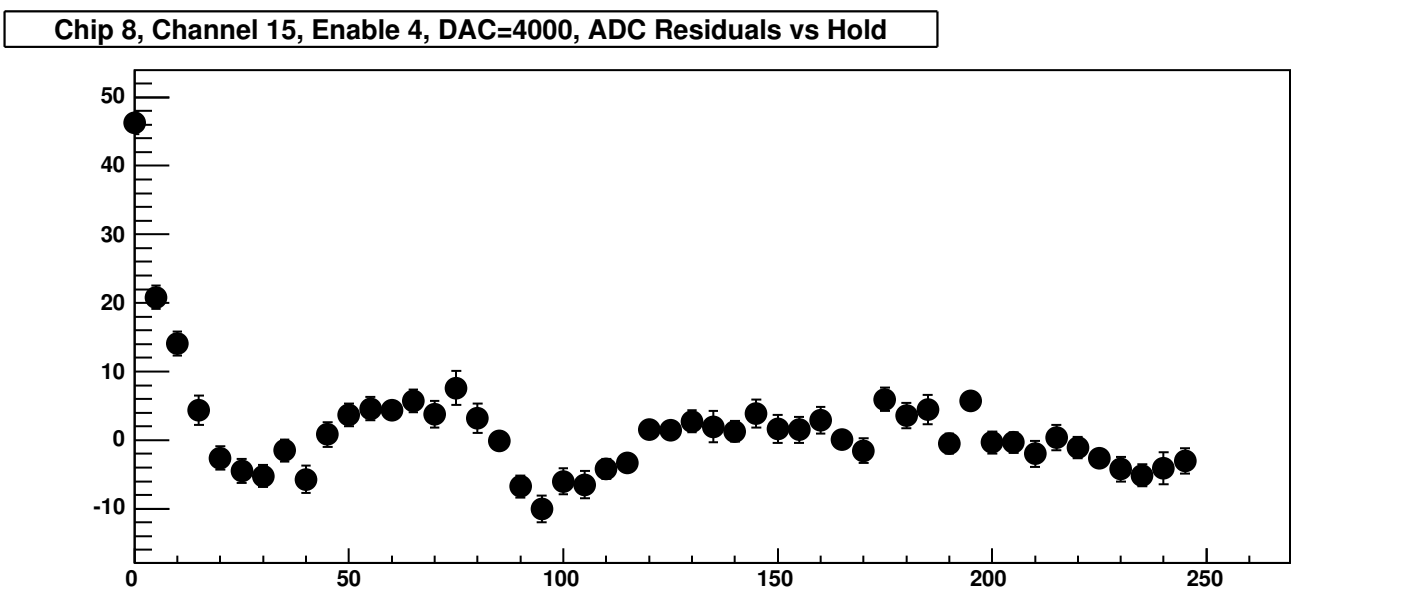


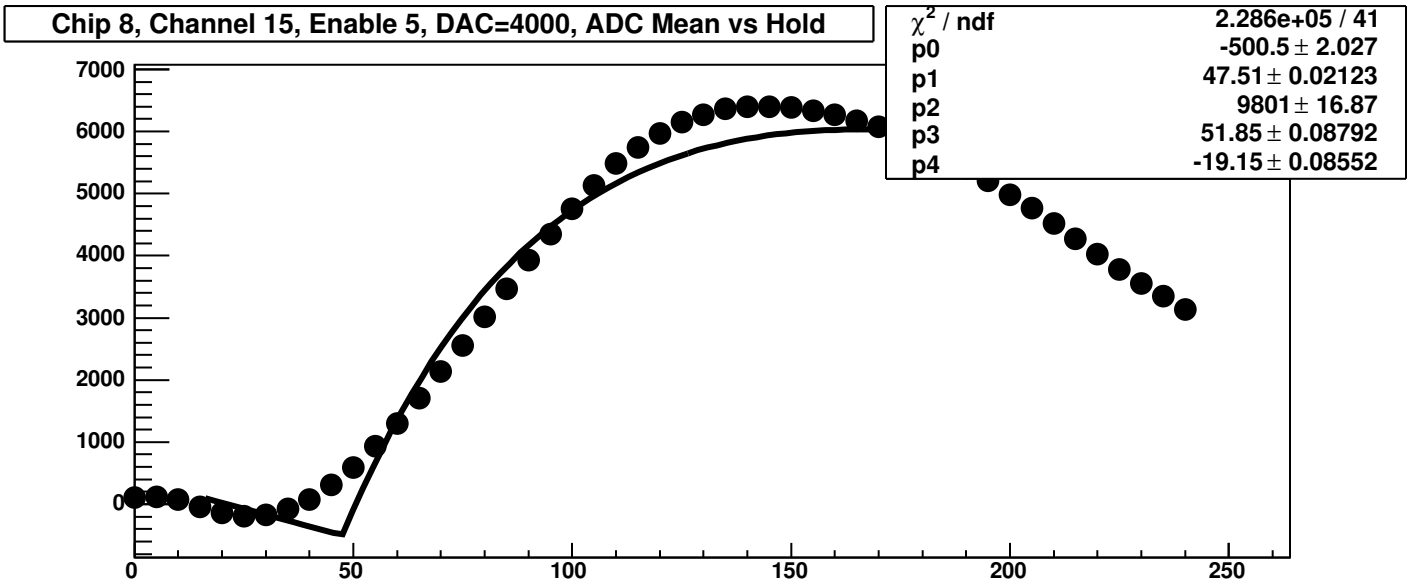
Chip 8, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold

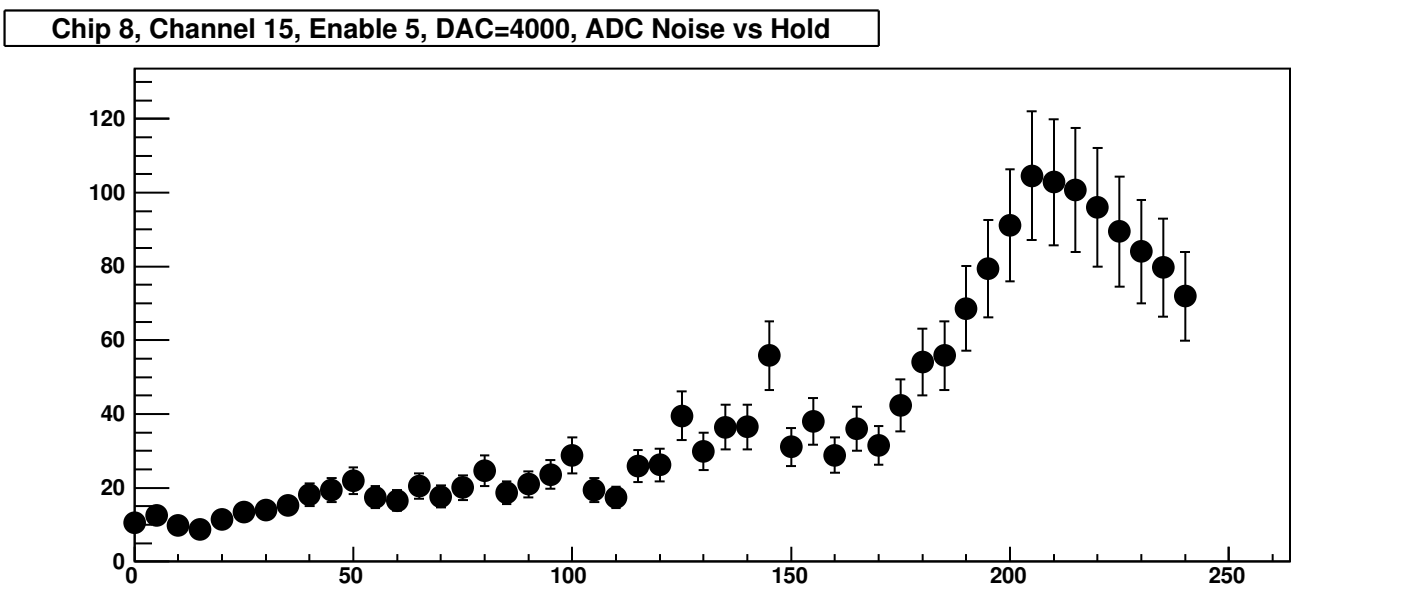


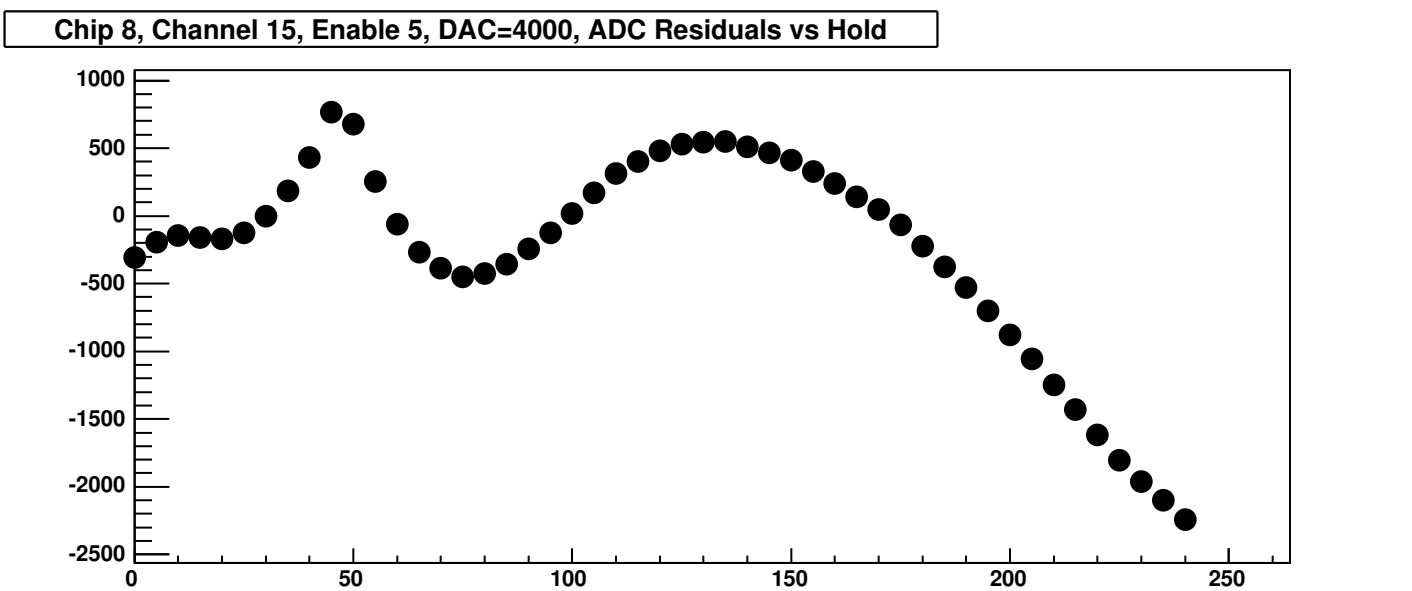

Chip 8, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 247.4 / 41
p0 244.6 ± 0.5568
p1 97.05 ± 0.4812
p2 -1.051e+09 ± 1.049e+07
p3 5.706e+08 ± 3.956e+05
p4 1.818 ± 0.01639
260
240
220
200
180
160
140
120
100
0
50
100
150
200
250

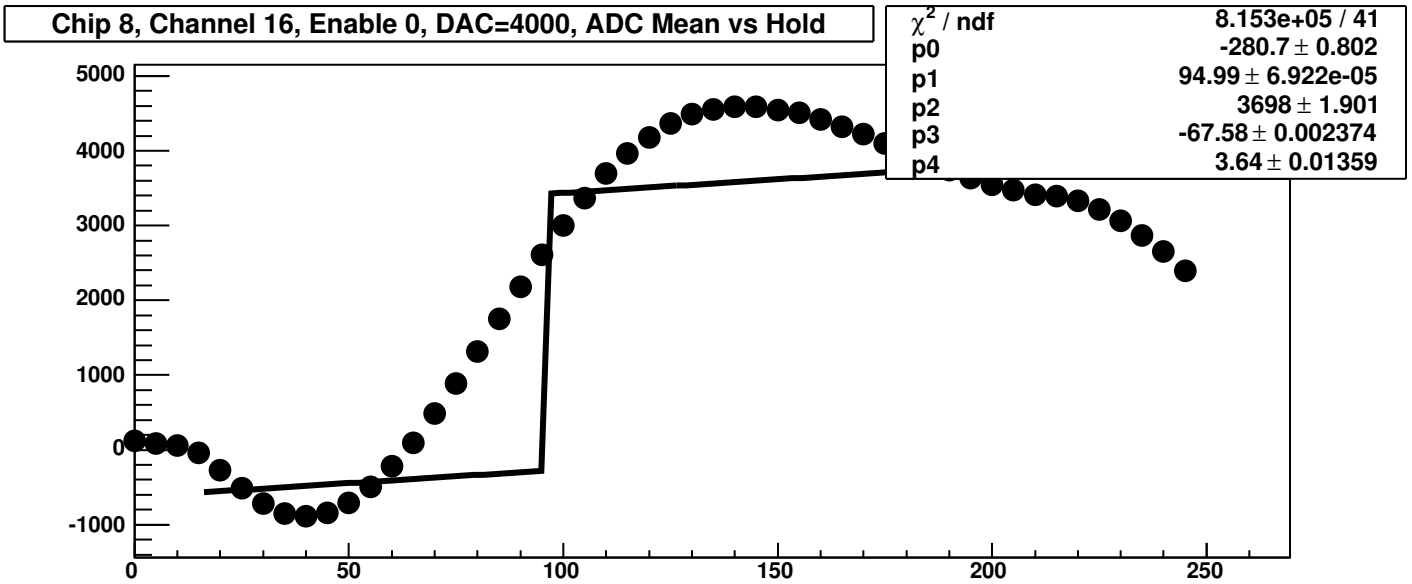

Chip 8, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

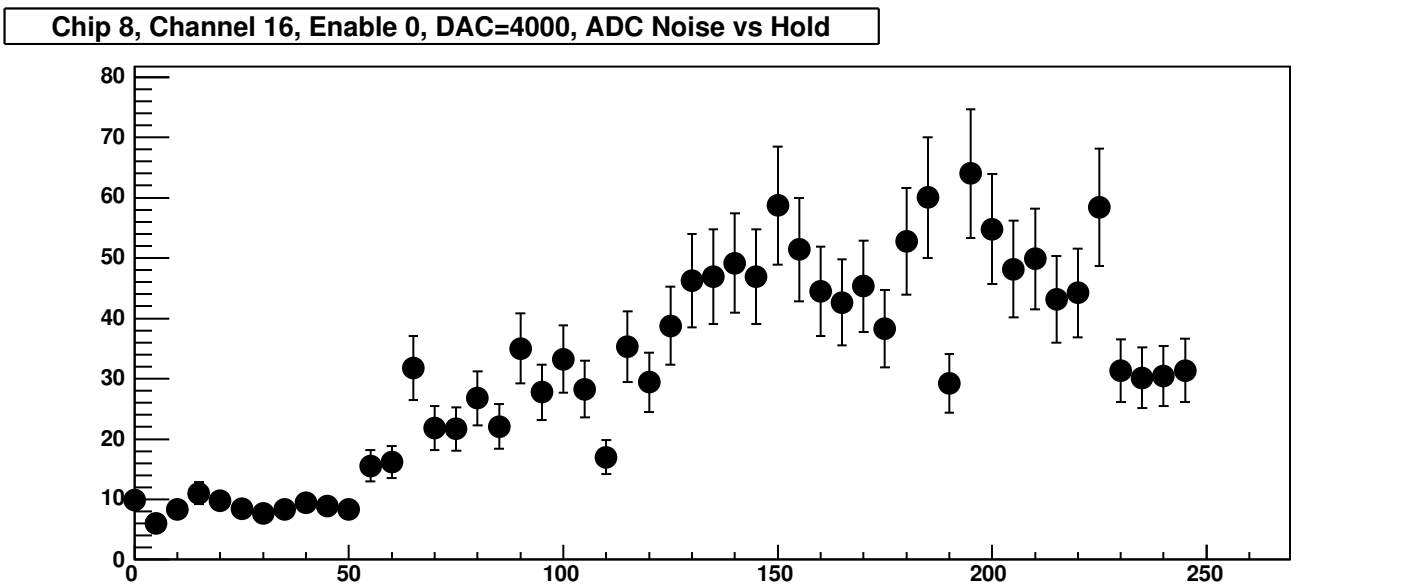

Chip 8, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250

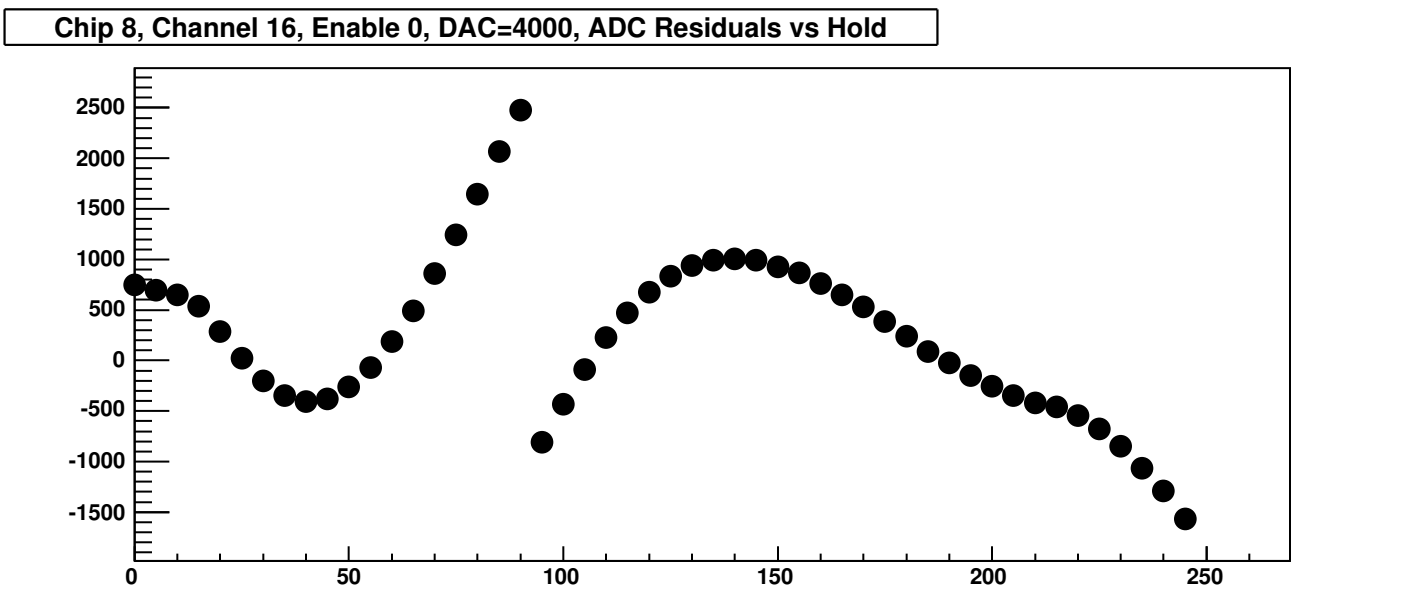

Chip 8, Channel 15, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.286e+05 / 41
p0 -500.5 ± 2.027
p1 47.51 ± 0.02123
p2 9801 ± 16.87
p3 51.85 ± 0.08792
p4 -19.15 ± 0.08552
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 8, Channel 15, Enable 5, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250

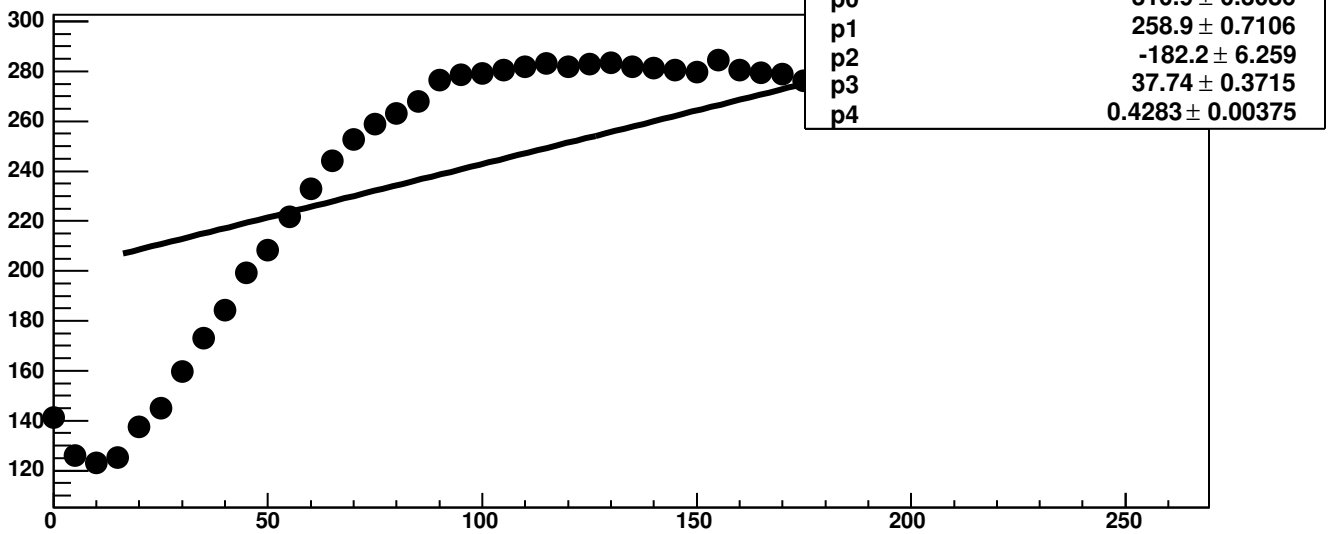

Chip 8, Channel 15, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
0
50
100
150
200
250


Chip 8, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 8.153e+05 / 41
p0 -280.7 ± 0.802
p1 94.99 ± 6.922e-05
p2 3698 ± 1.901
p3 -67.58 ± 0.002374
p4 3.64 ± 0.01359
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

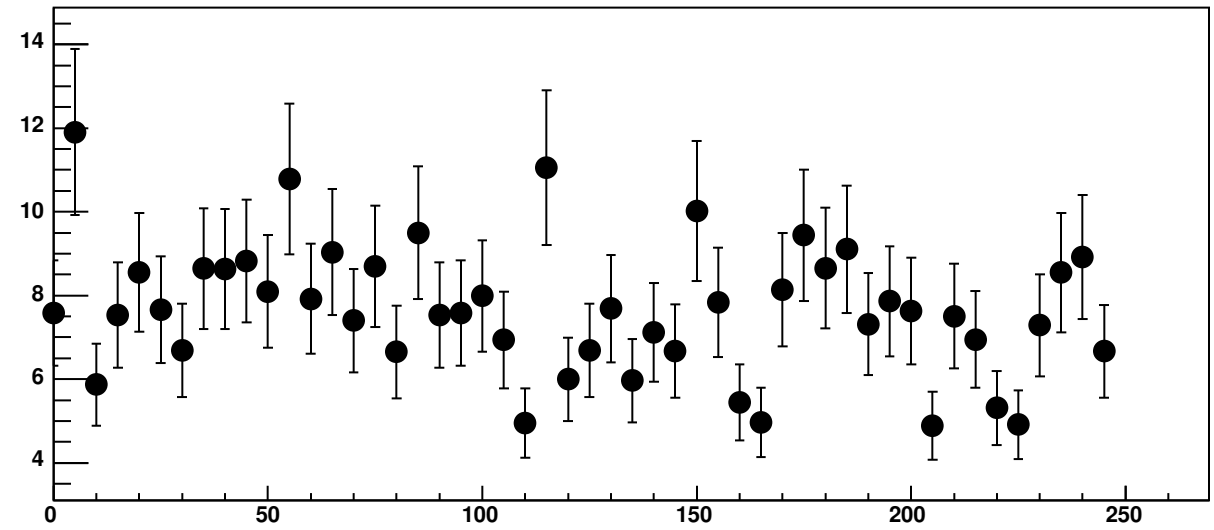

Chip 8, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250


Chip 8, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold
2500
2000
1500
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

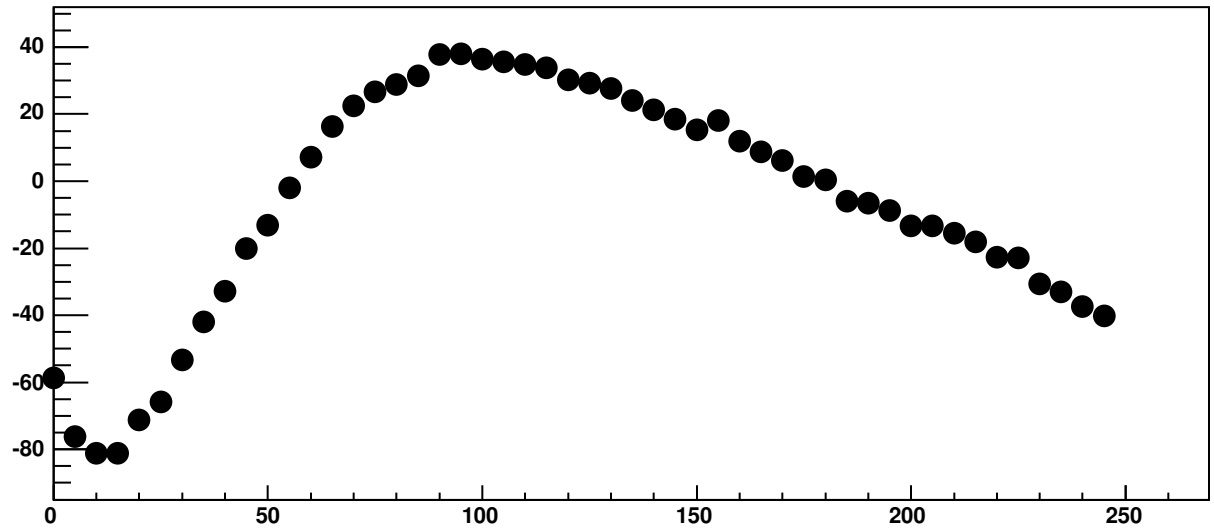
Chip 8, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold



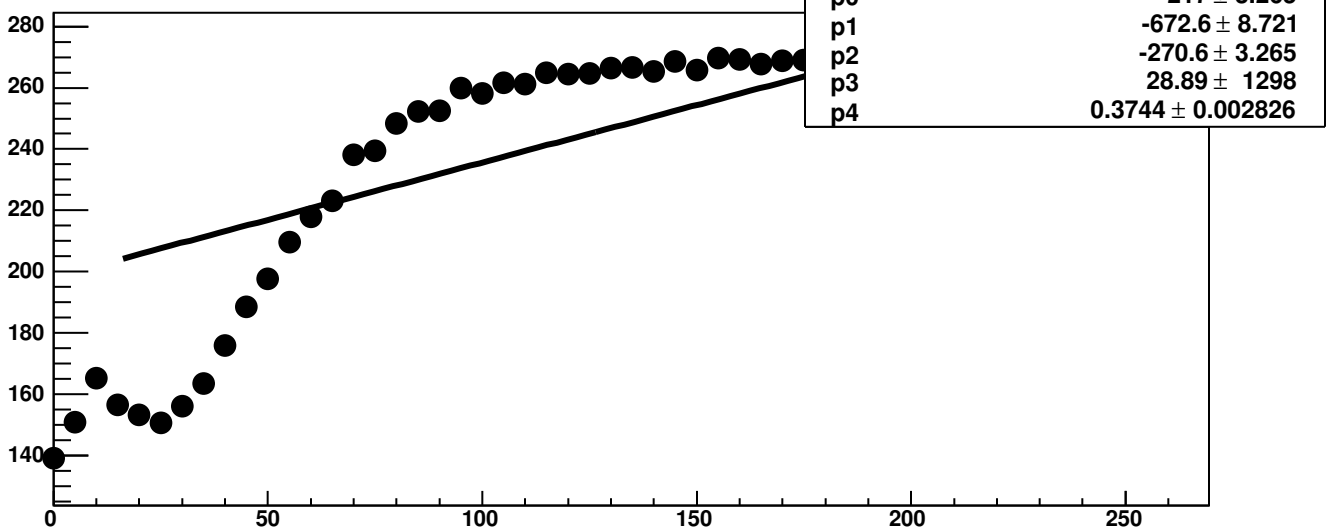
Chip 8, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



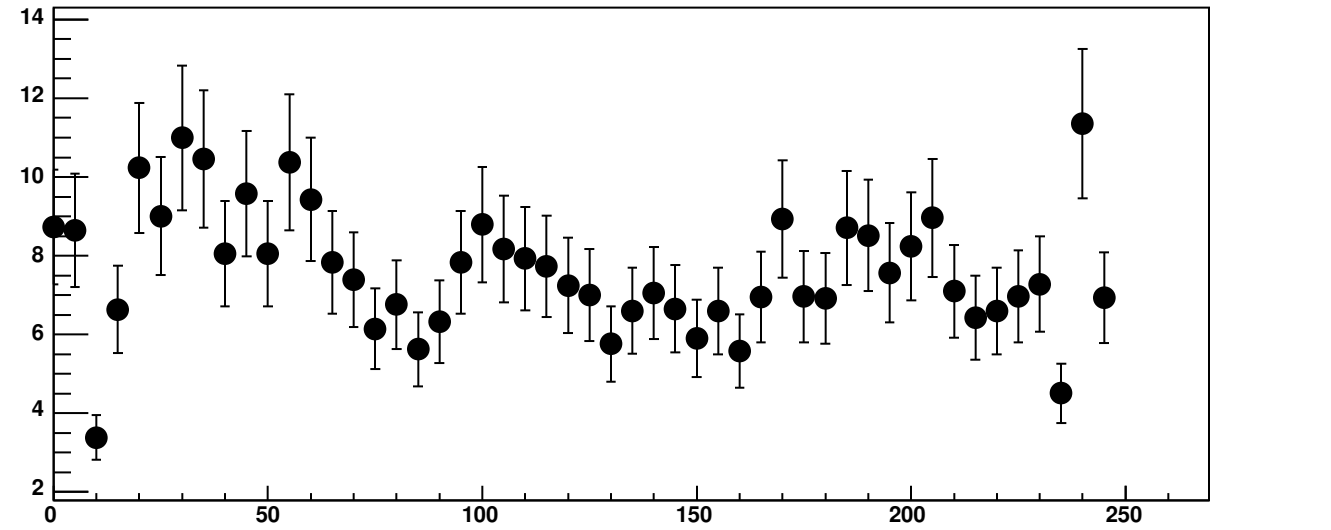
Chip 8, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold



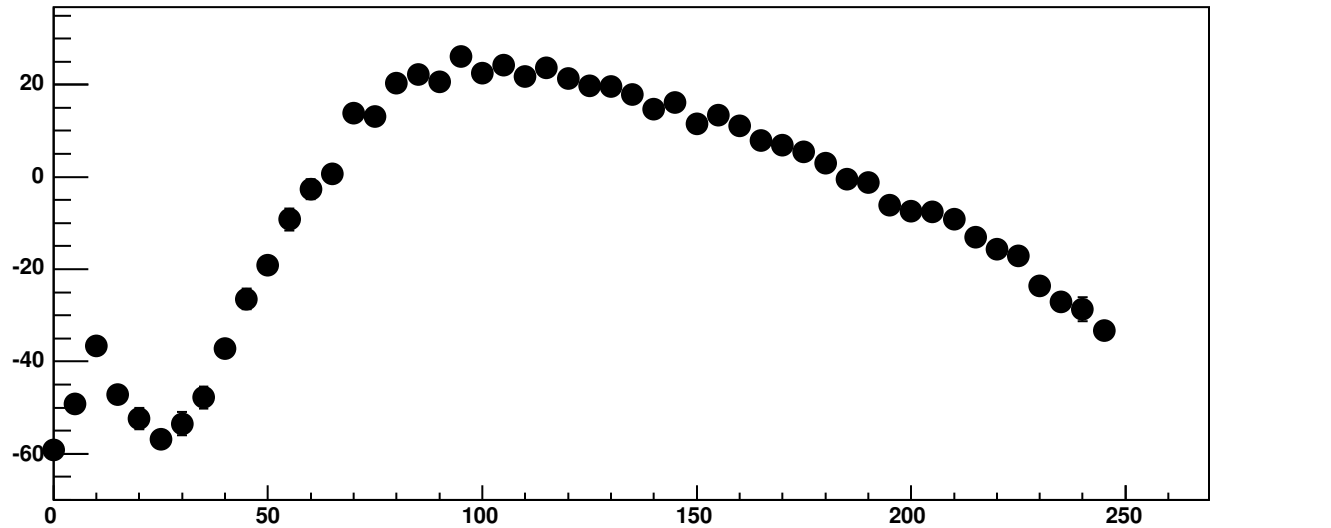
Chip 8, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold

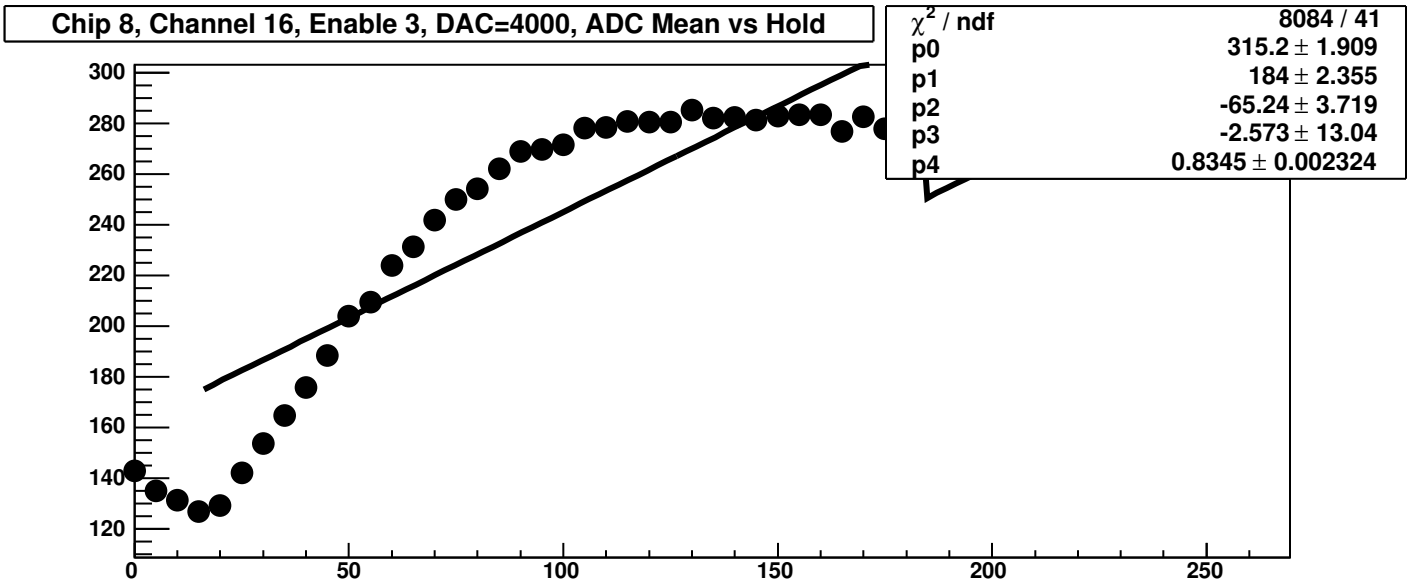


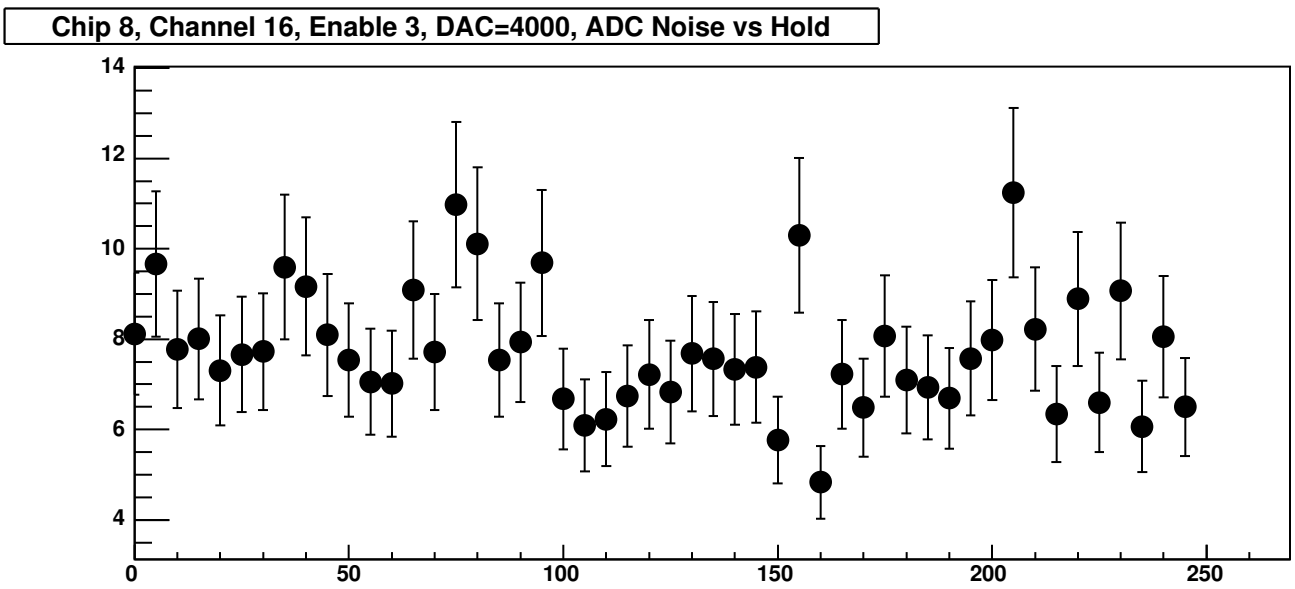
Chip 8, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold

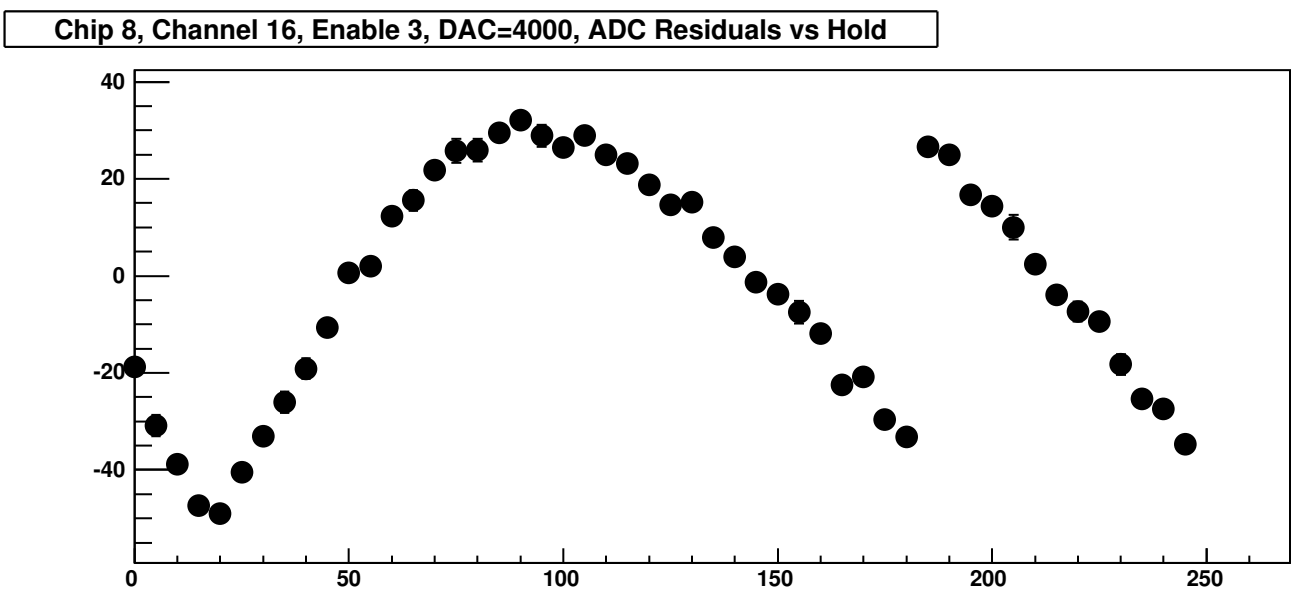


Chip 8, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold

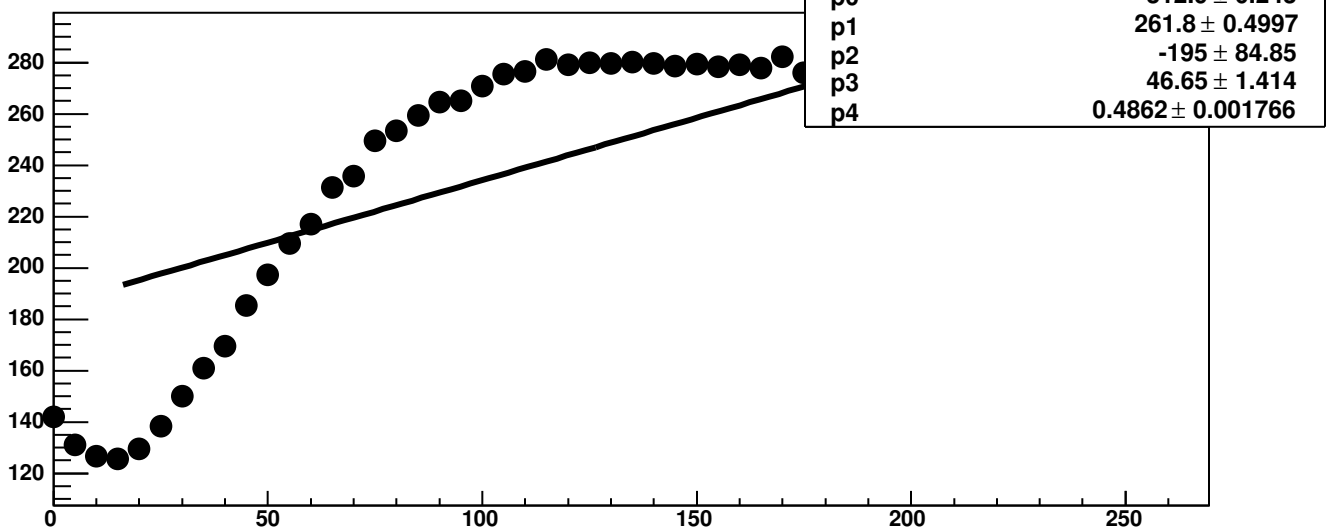



Chip 8, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 8084 / 41
p0 315.2 ± 1.909
p1 184 ± 2.355
p2 -65.24 ± 3.719
p3 -2.573 ± 13.04
p4 0.8345 ± 0.002324
300
280
260
240
220
200
180
160
140
120
0
50
100
150
200
250

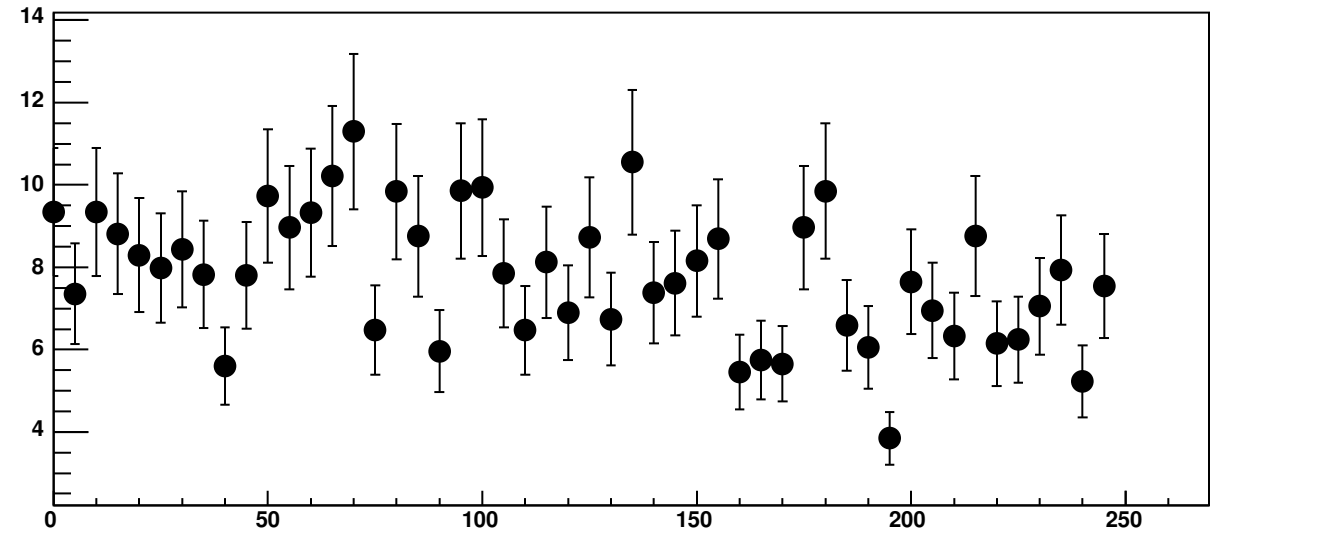

Chip 8, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 8, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
0
50
100
150
200
250

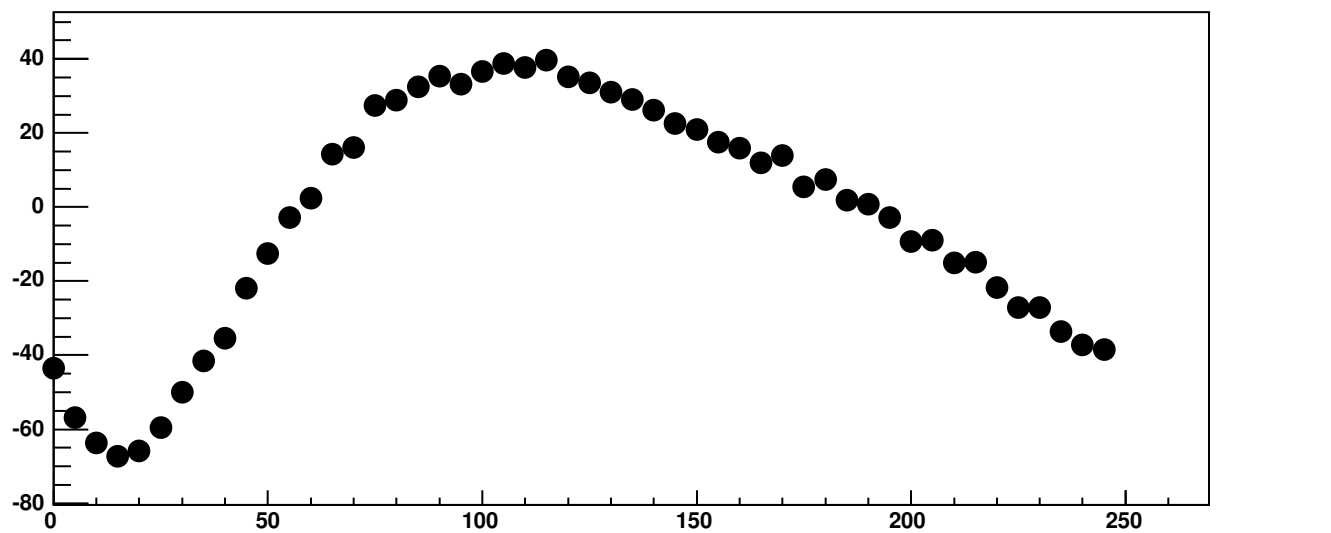
Chip 8, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

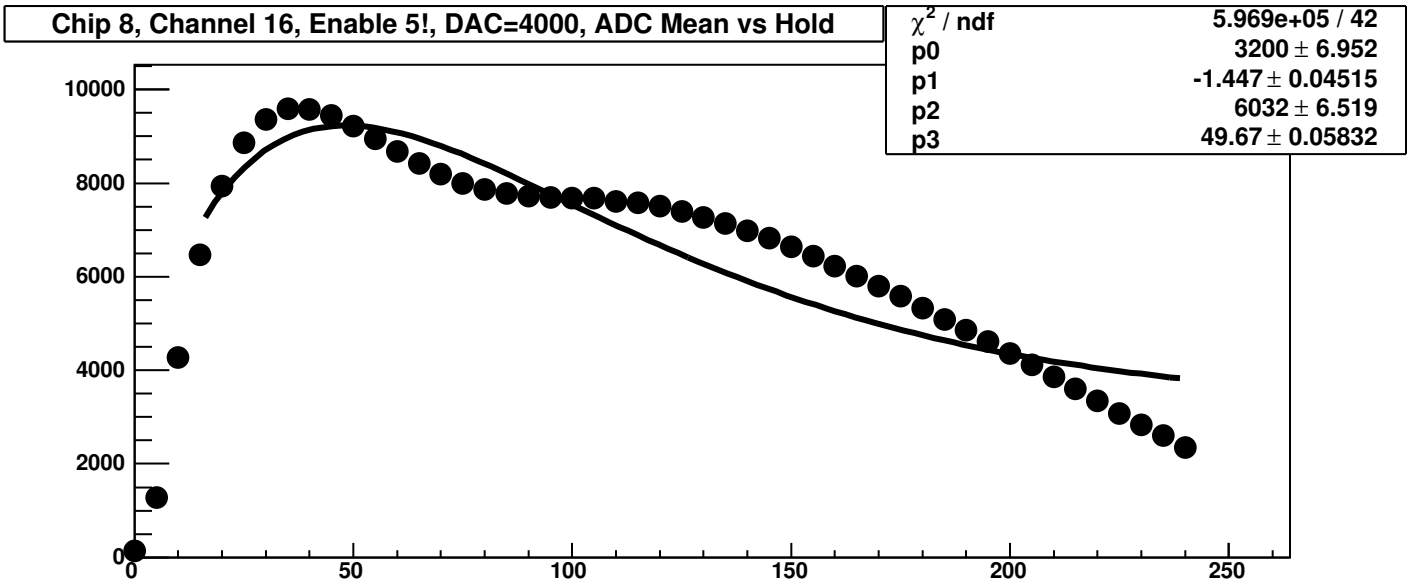


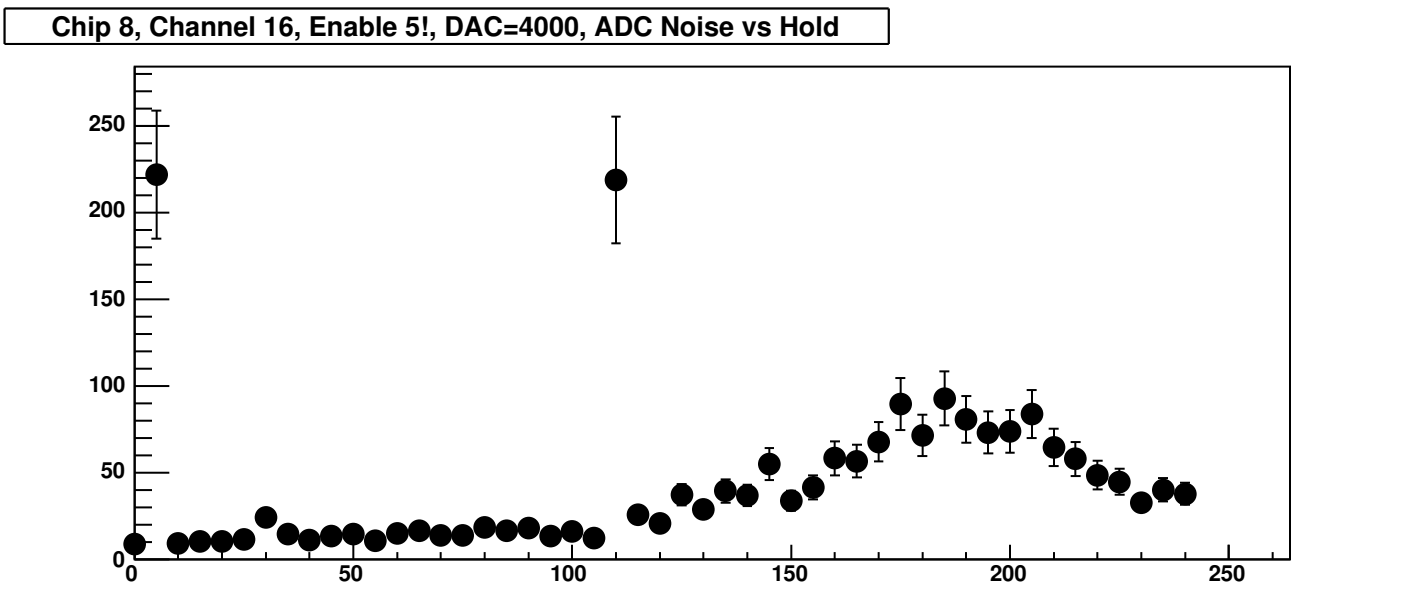
Chip 8, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold

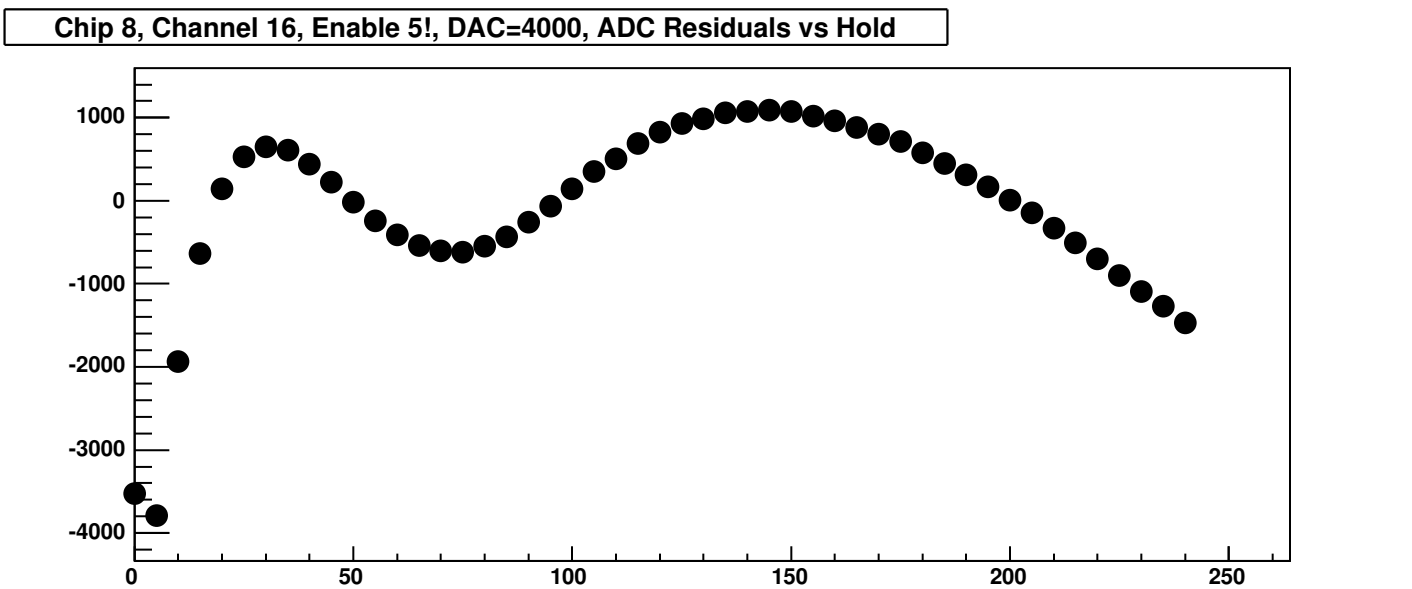


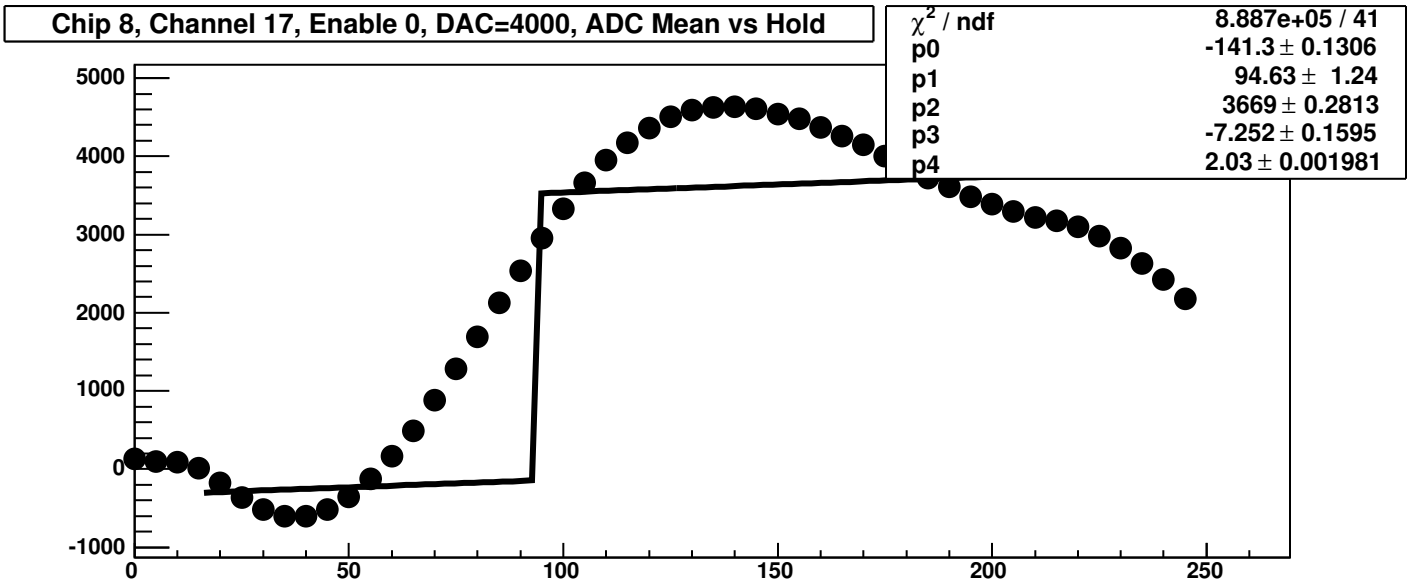
Chip 8, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

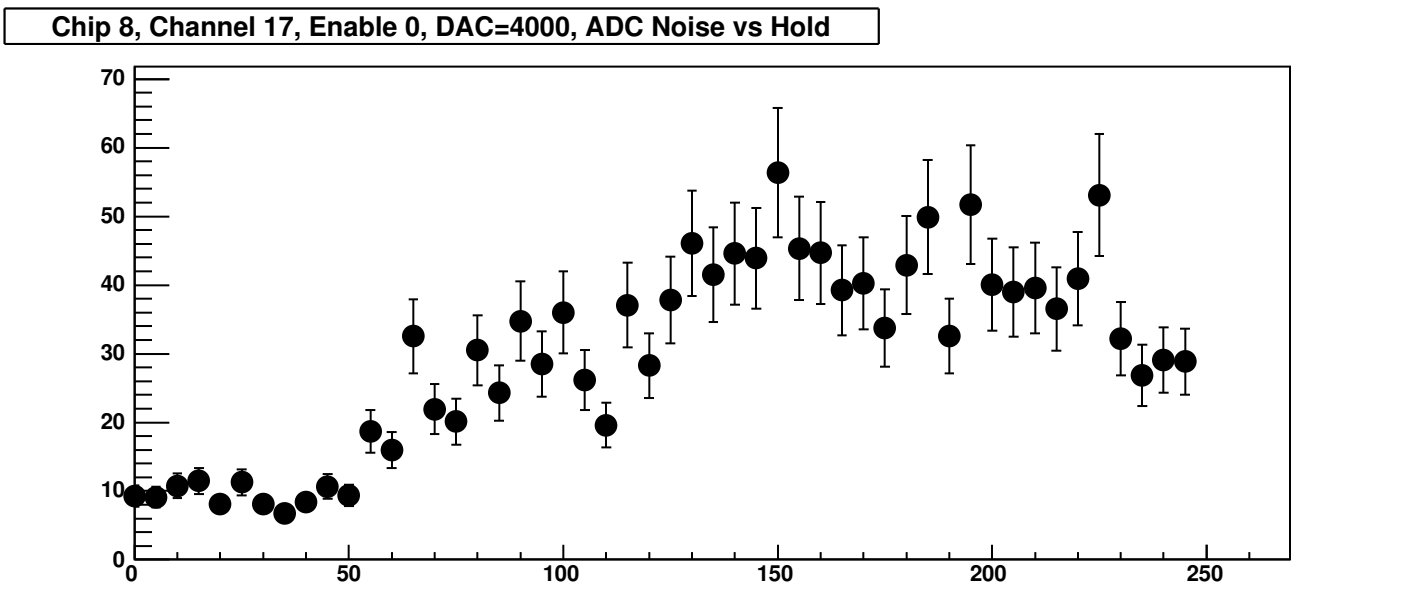


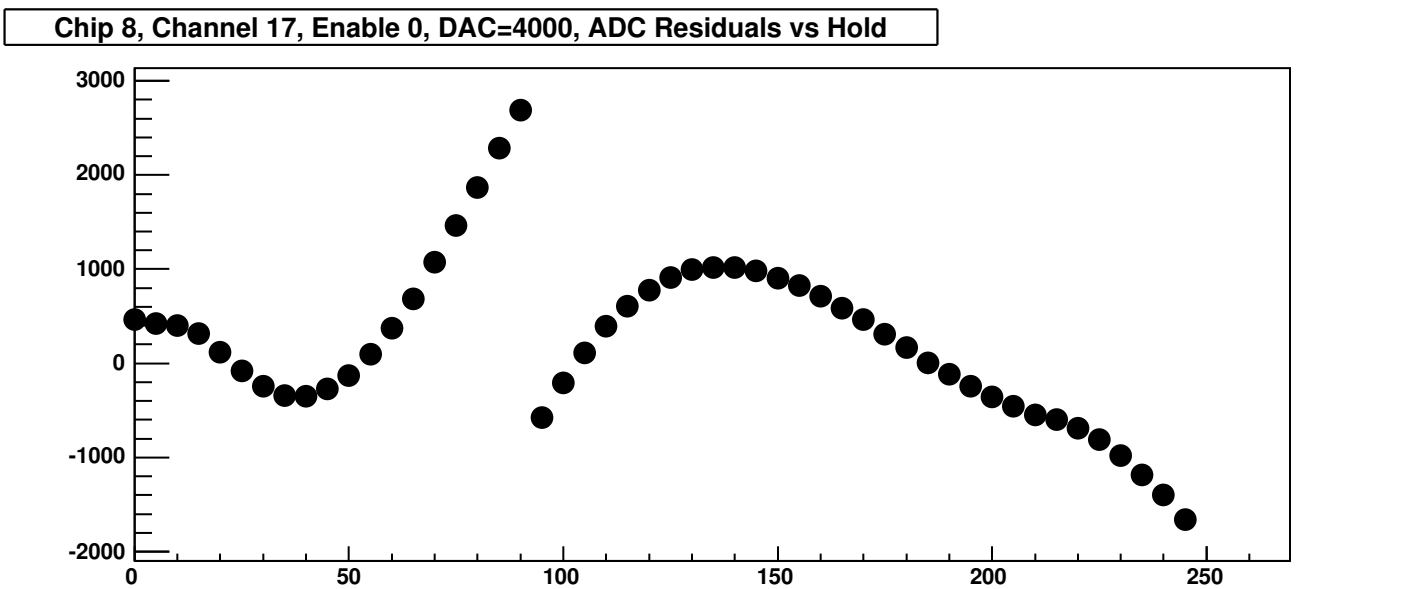

Chip 8, Channel 16, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.969e+05 / 42
p0 3200 ± 6.952
p1 -1.447 ± 0.04515
p2 6032 ± 6.519
p3 49.67 ± 0.05832
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 8, Channel 16, Enable 5!, DAC=4000, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250

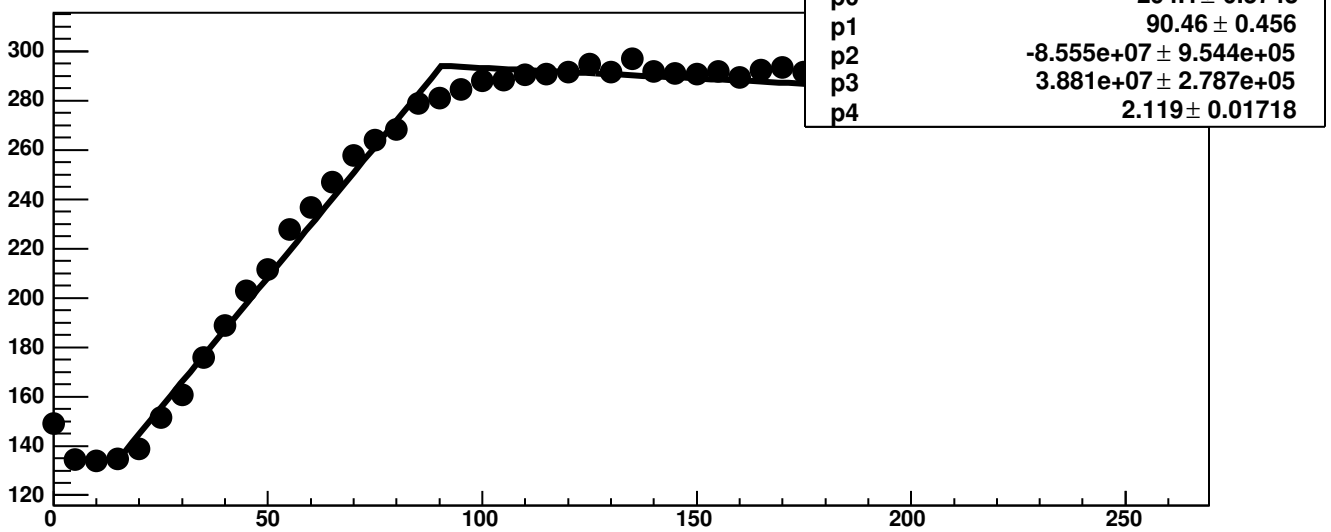

Chip 8, Channel 16, Enable 5!, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250


Chip 8, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 8.887e+05 / 41
p0 -141.3 ± 0.1306
p1 94.63 ± 1.24
p2 3669 ± 0.2813
p3 -7.252 ± 0.1595
p4 2.03 ± 0.001981
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

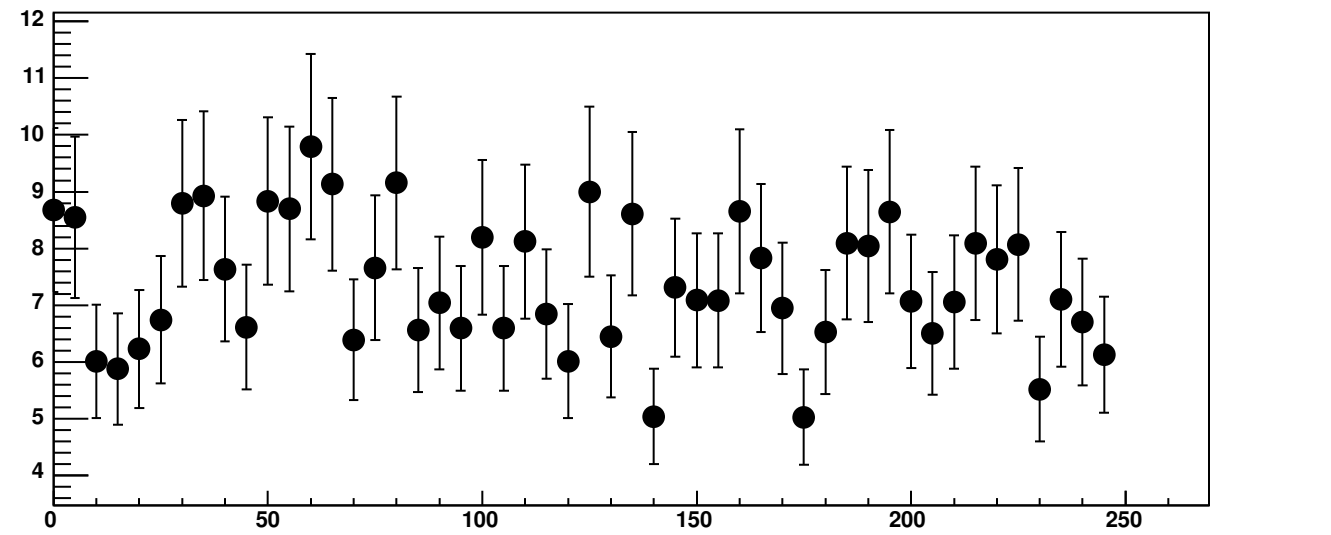

Chip 8, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold
70
60
50
40
30
20
10
0
0
50
100
150
200
250


Chip 8, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
0
50
100
150
200
250

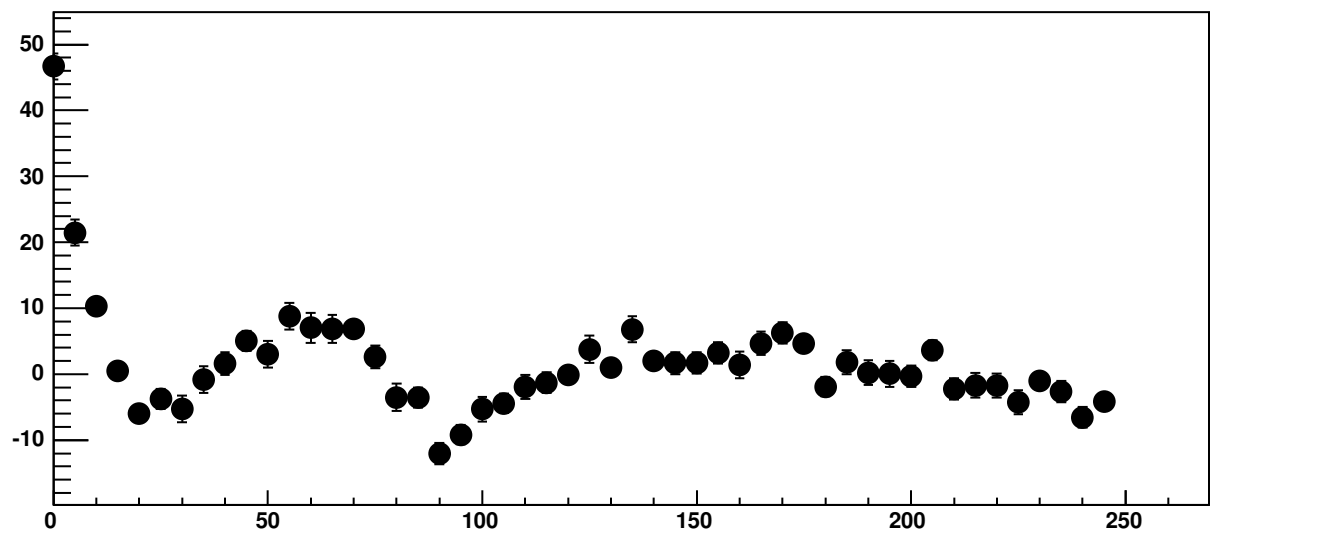
Chip 8, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

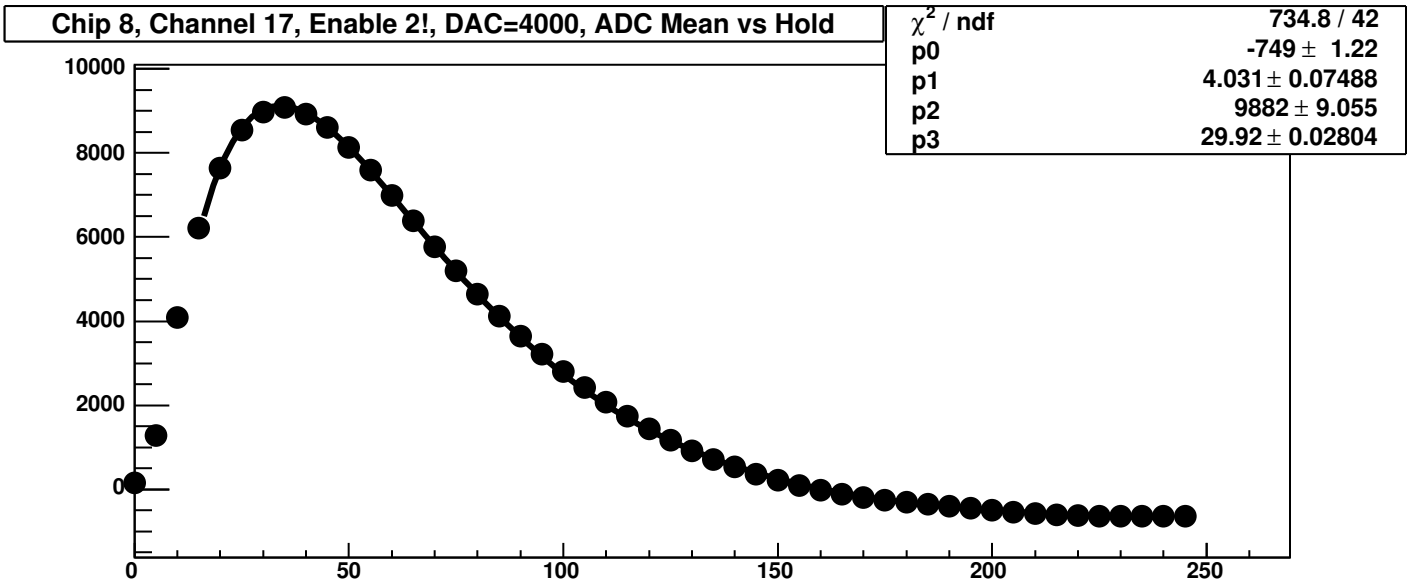


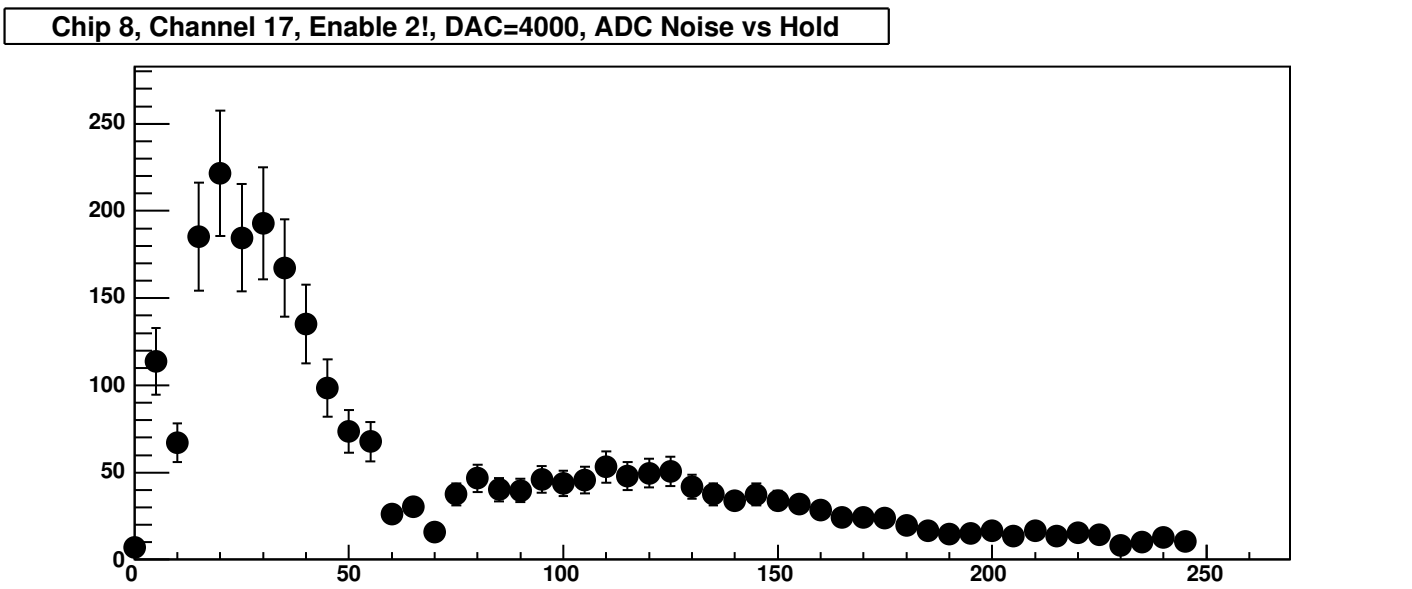
Chip 8, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold

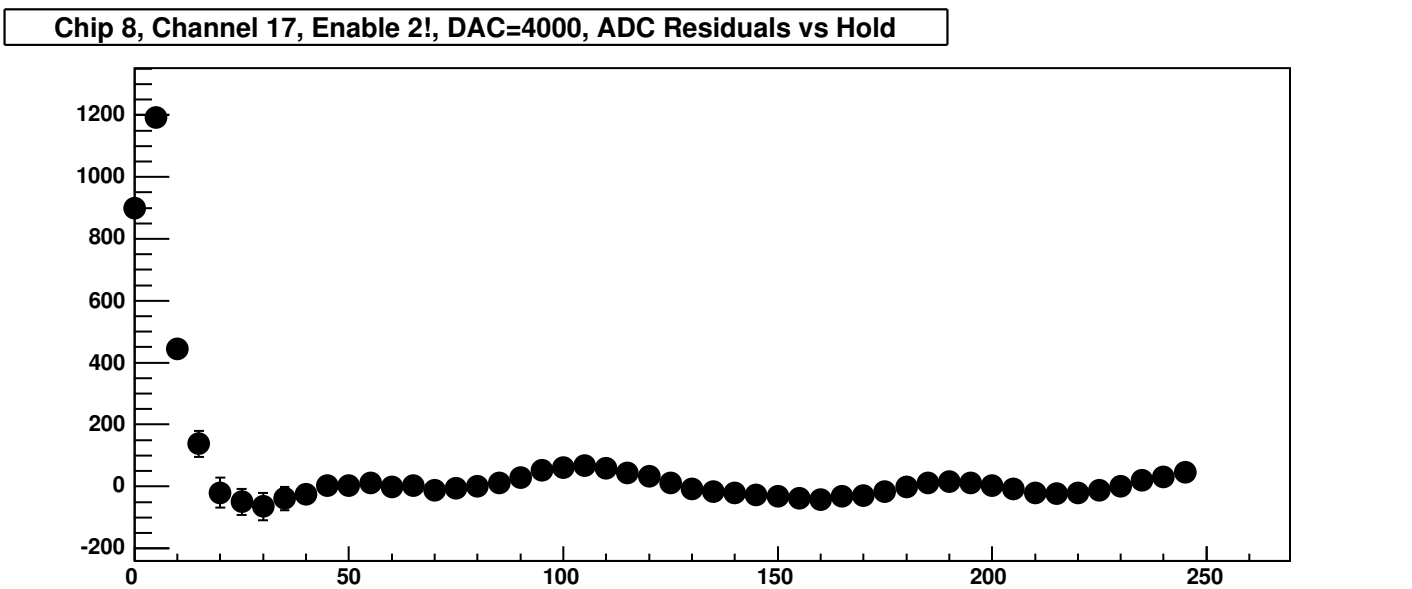


Chip 8, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

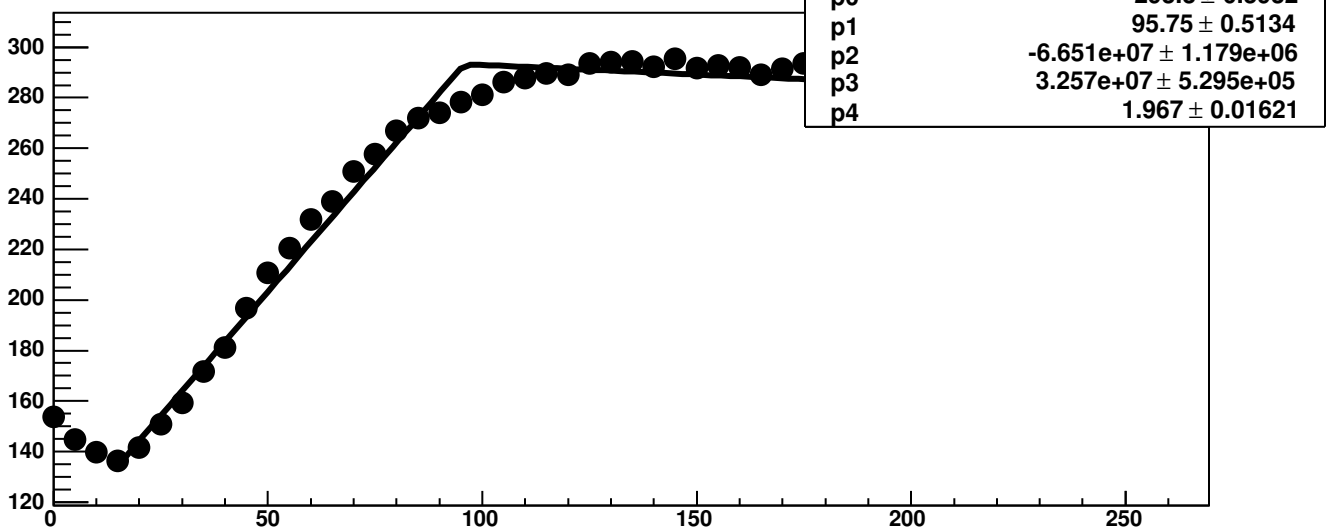



Chip 8, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 734.8 / 42
p0 -749 ± 1.22
p1 4.031 ± 0.07488
p2 9882 ± 9.055
p3 29.92 ± 0.02804
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

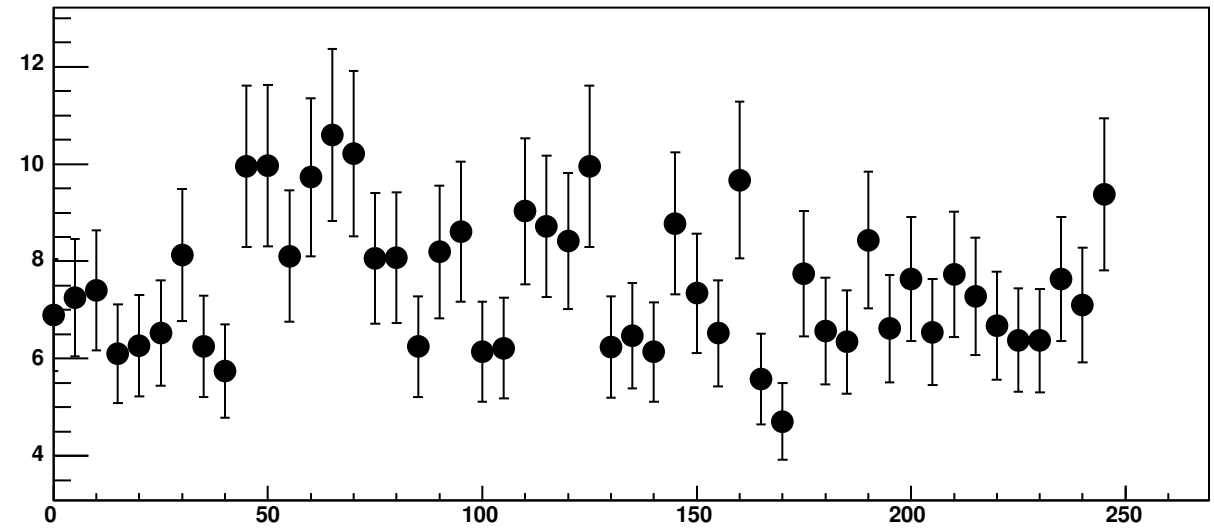

Chip 8, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250


Chip 8, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

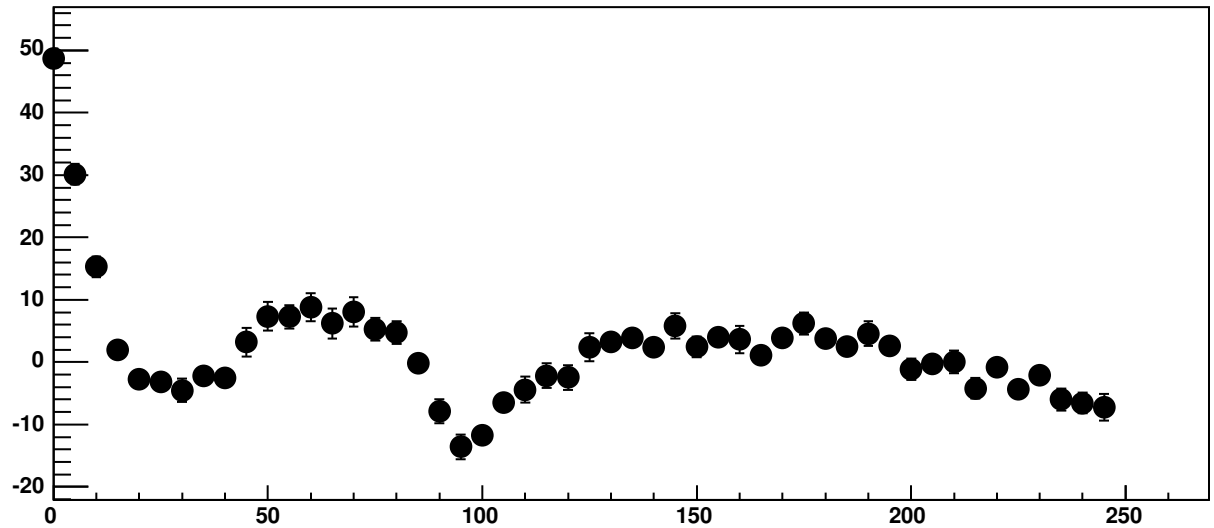
Chip 8, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



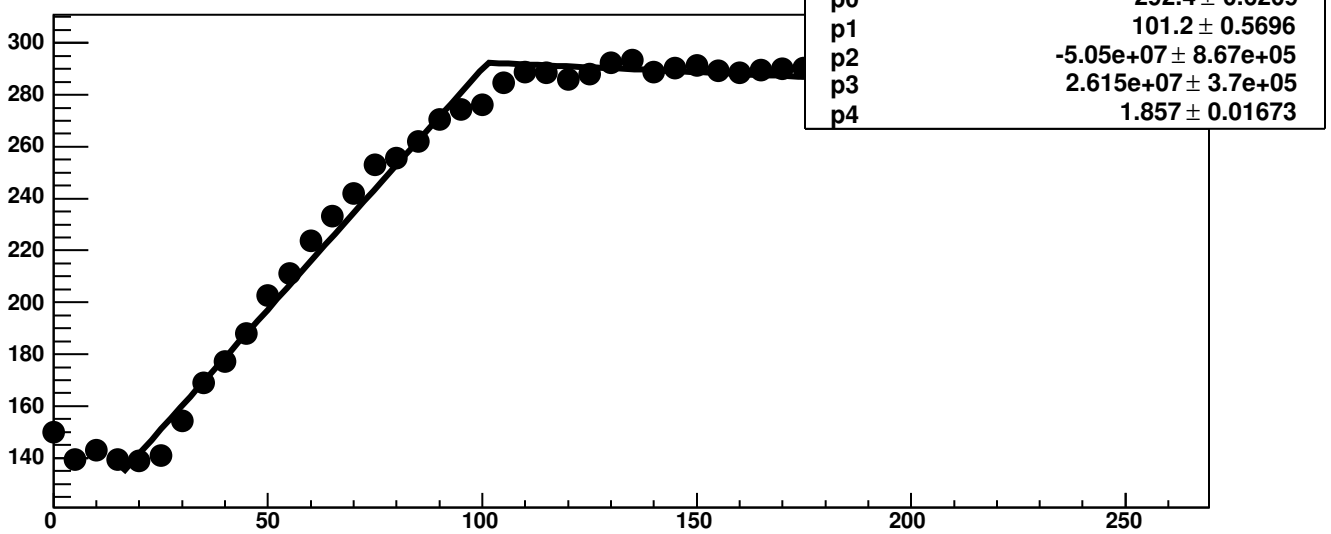
Chip 8, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



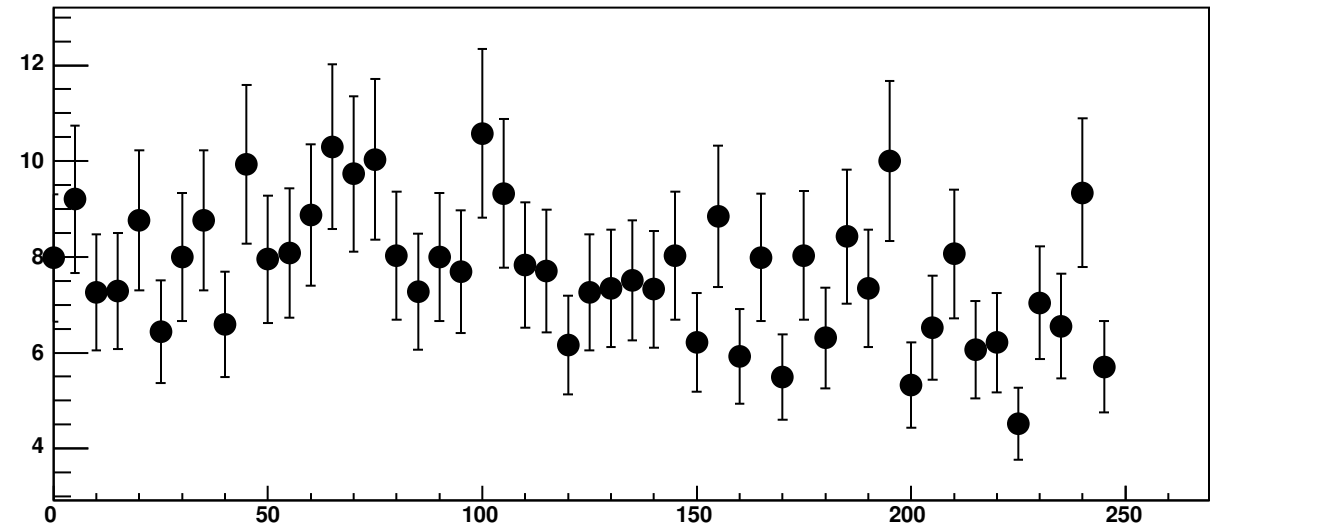
Chip 8, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold



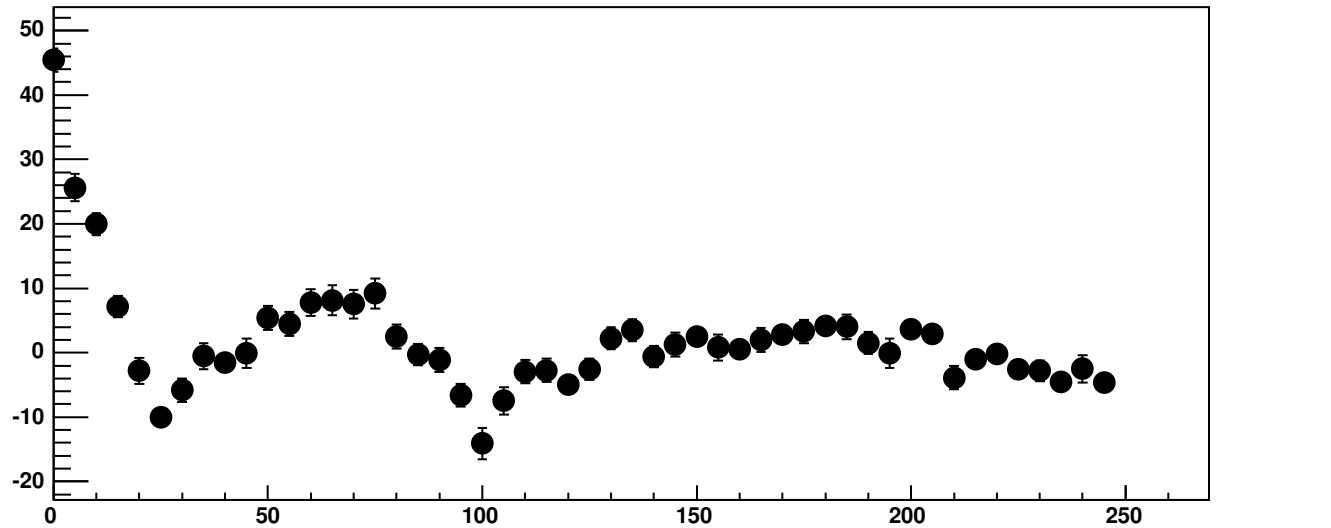
Chip 8, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



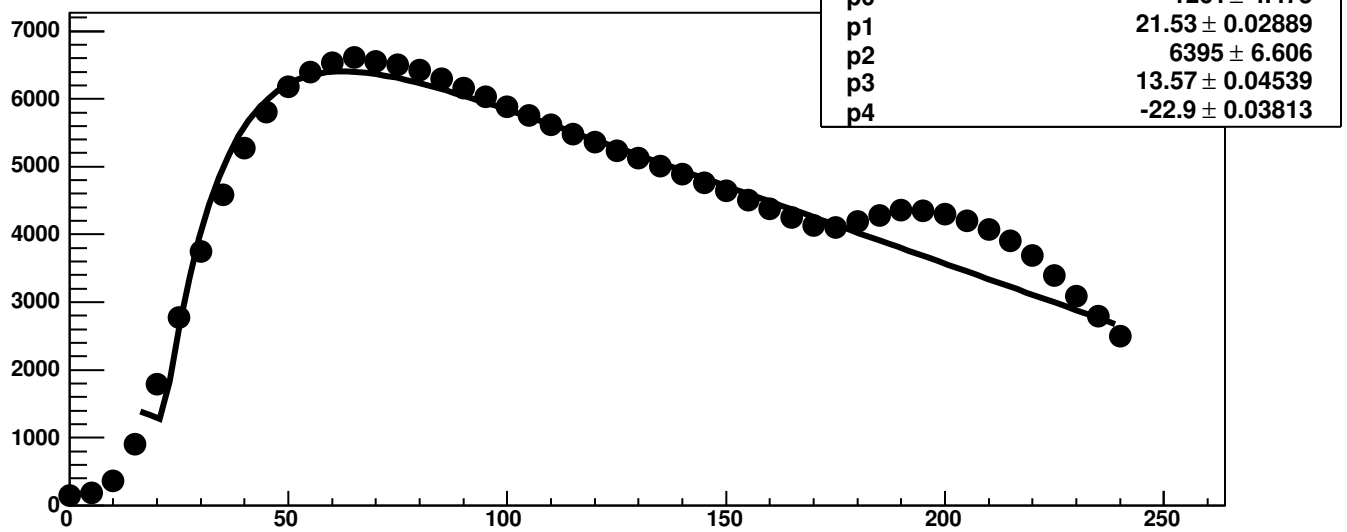
Chip 8, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



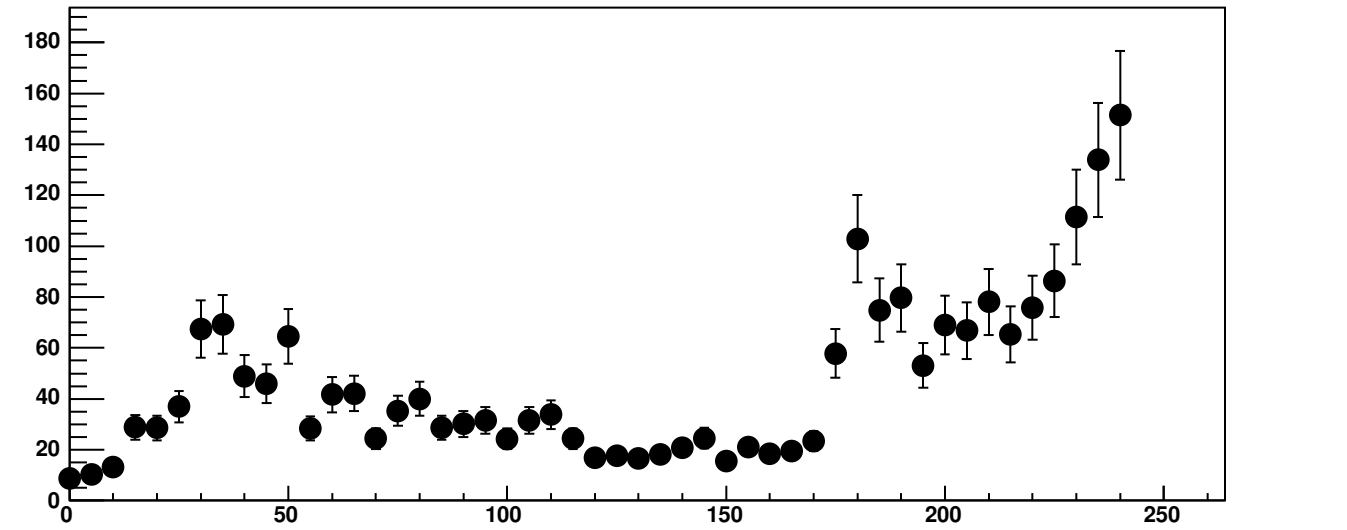
Chip 8, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold



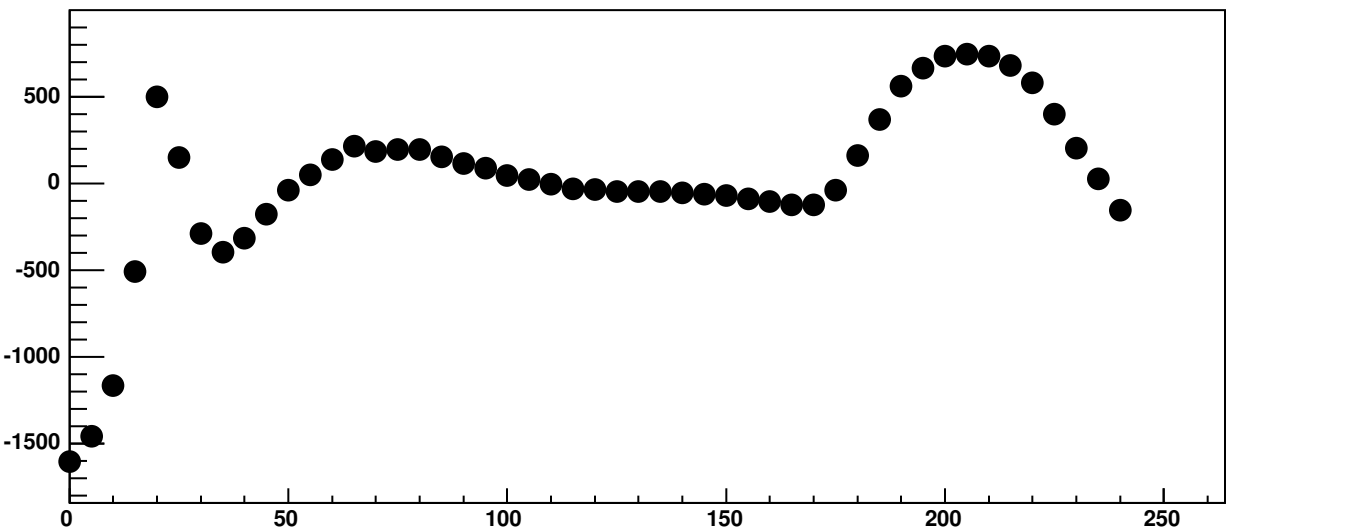
Chip 8, Channel 17, Enable 5, DAC=4000, ADC Mean vs Hold

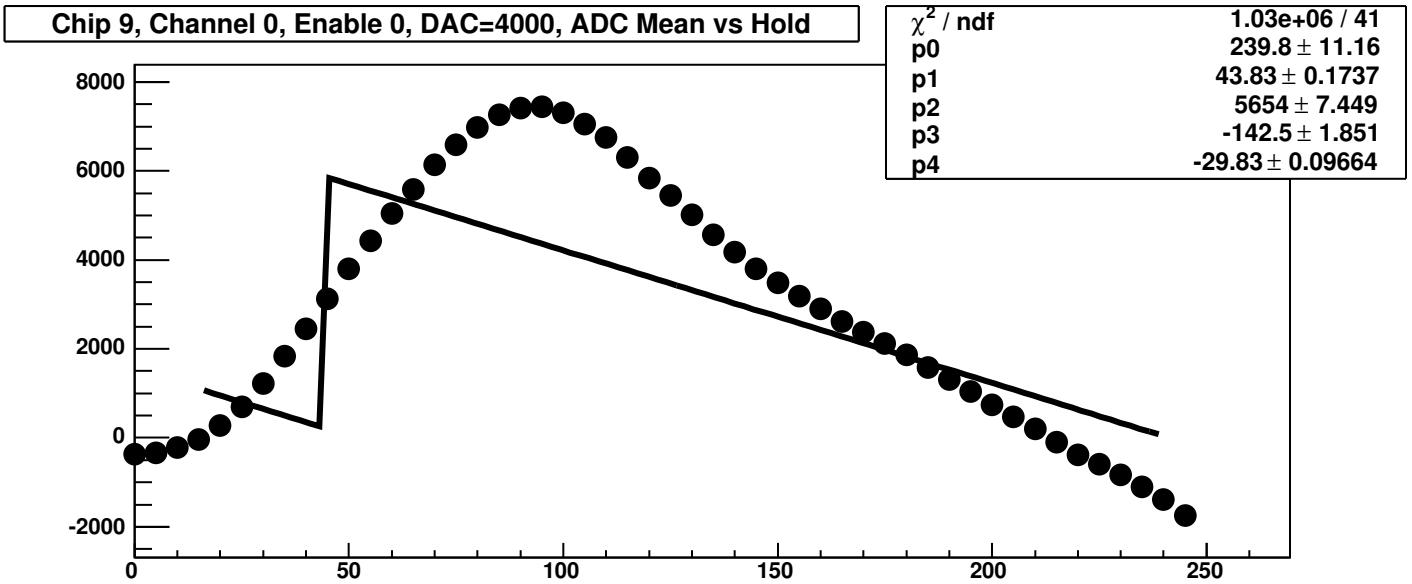


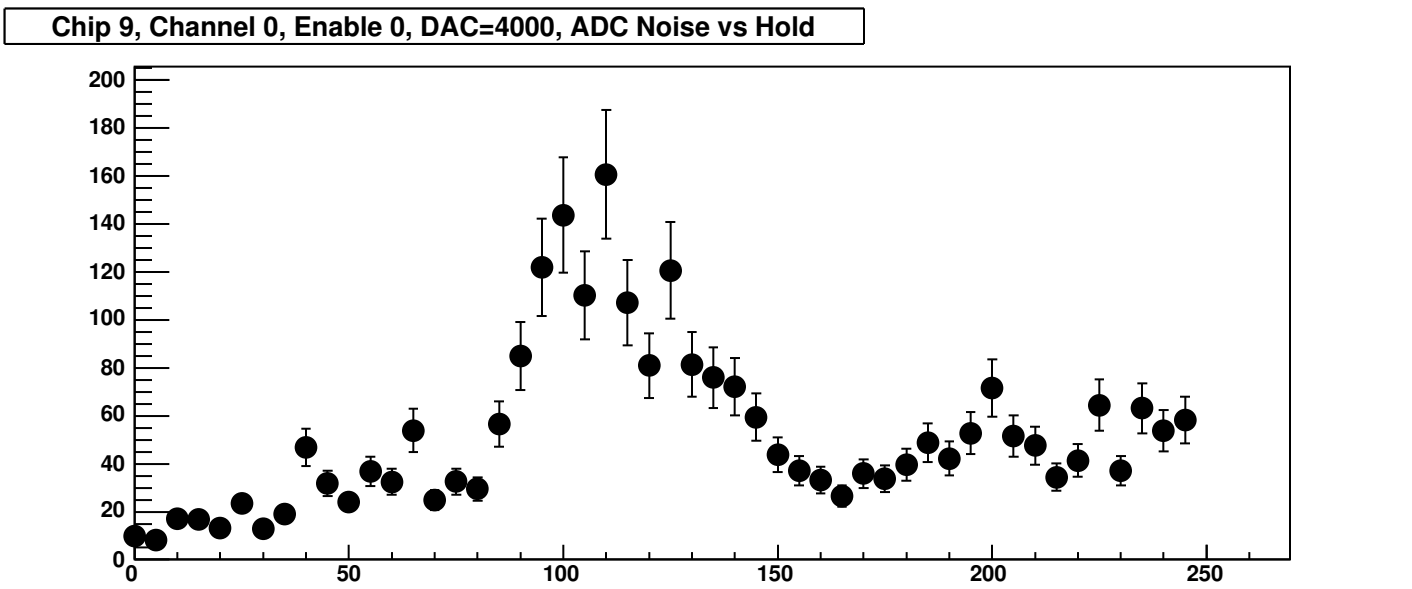
Chip 8, Channel 17, Enable 5, DAC=4000, ADC Noise vs Hold

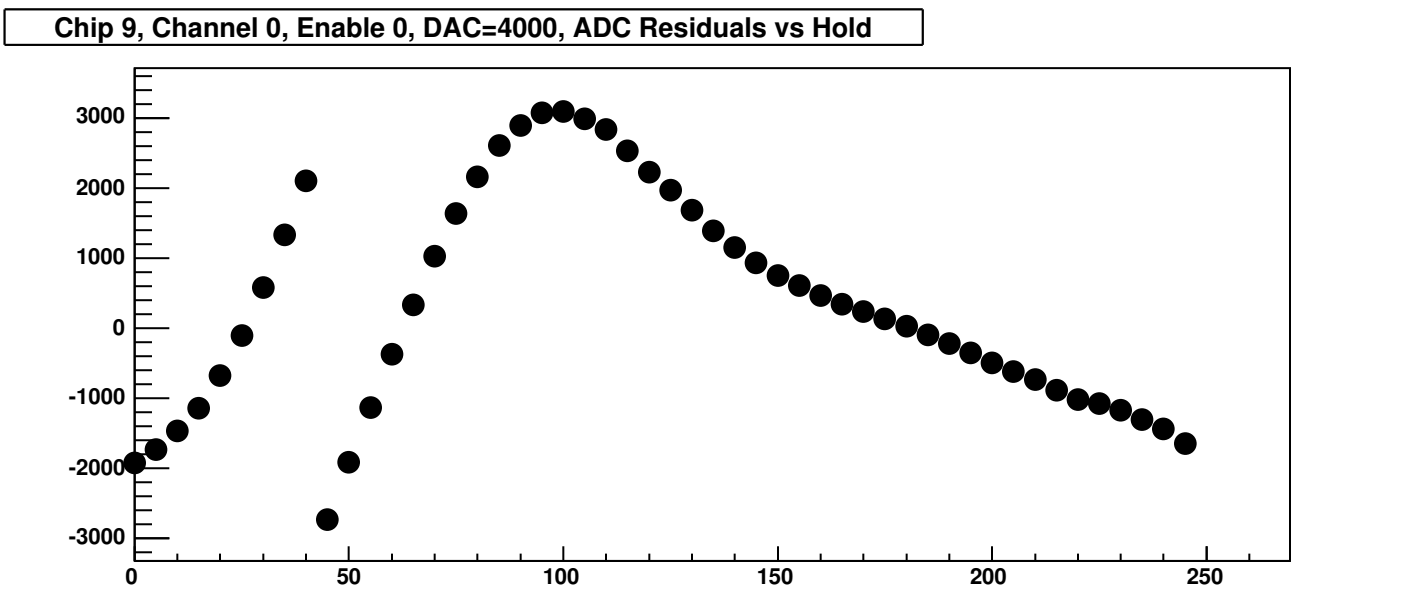


Chip 8, Channel 17, Enable 5, DAC=4000, ADC Residuals vs Hold

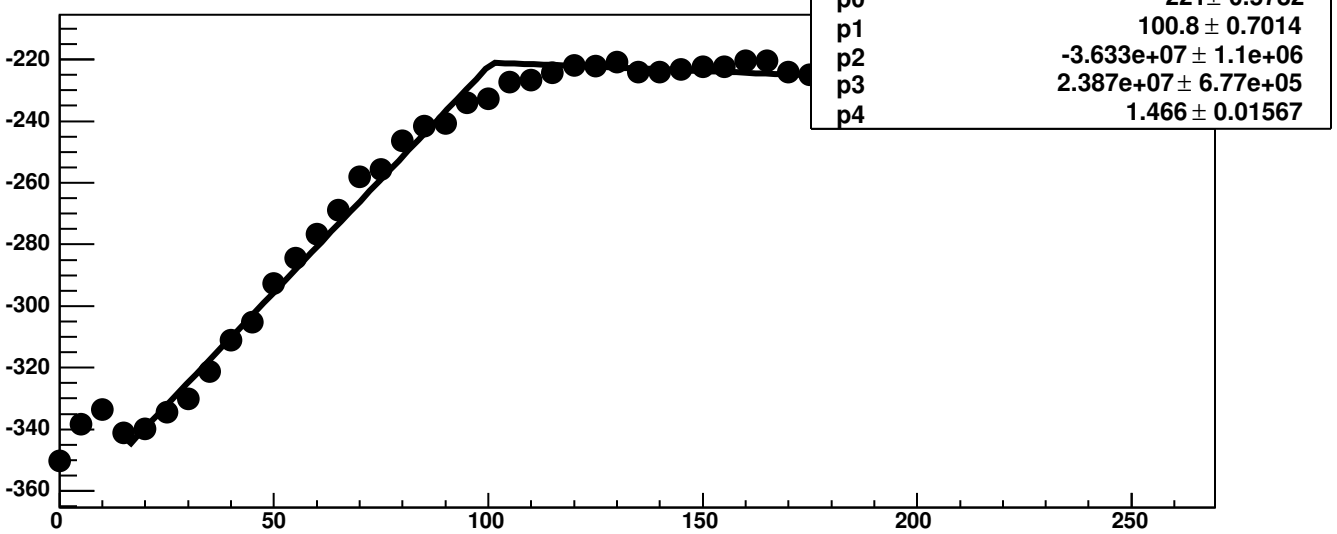



Chip 9, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.03e+06 / 41
p0 239.8 ± 11.16
p1 43.83 ± 0.1737
p2 5654 ± 7.449
p3 -142.5 ± 1.851
p4 -29.83 ± 0.09664
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

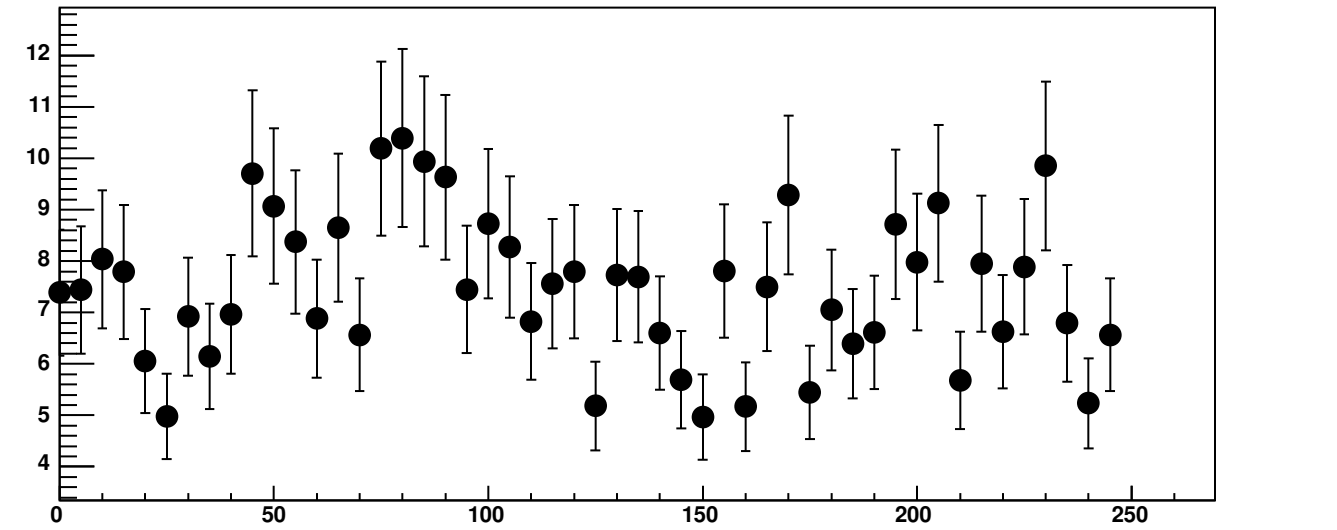

Chip 9, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 9, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

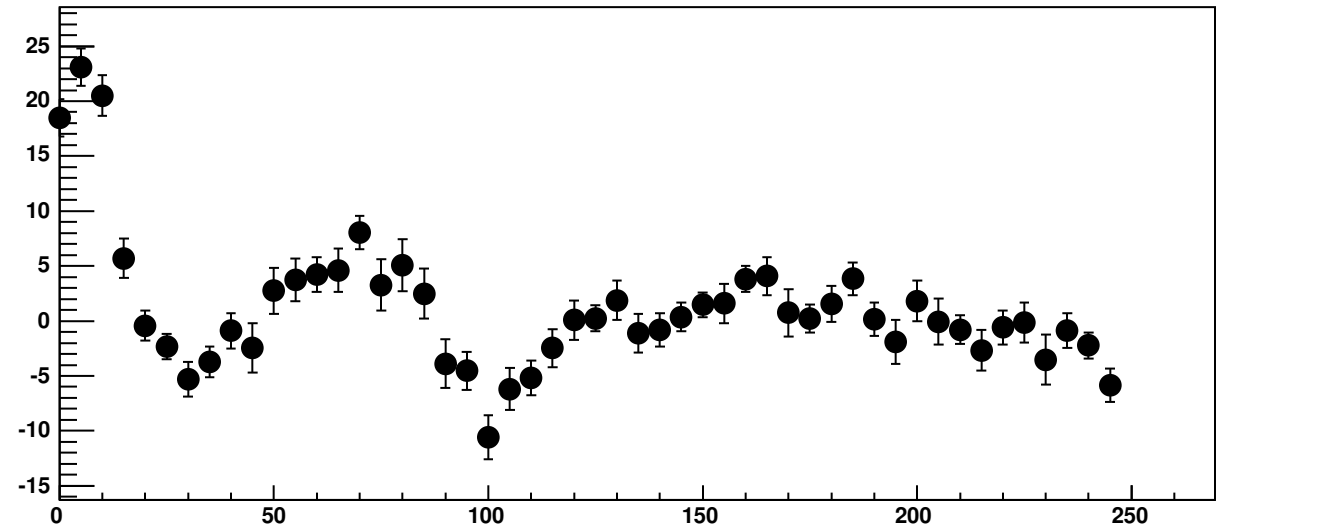
Chip 9, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold



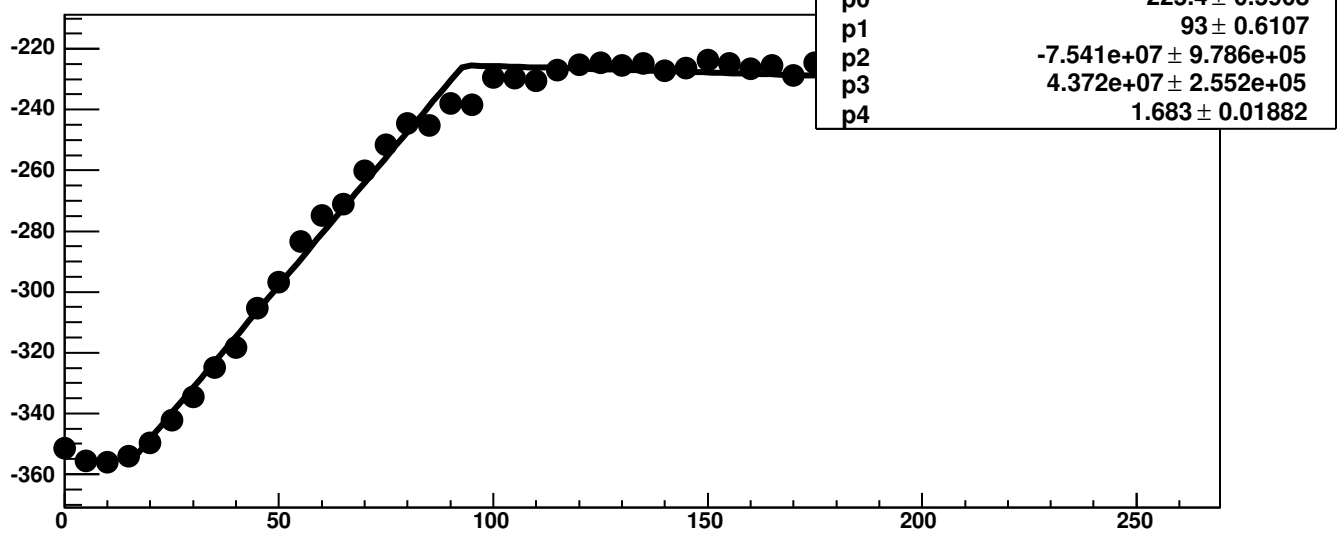
Chip 9, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



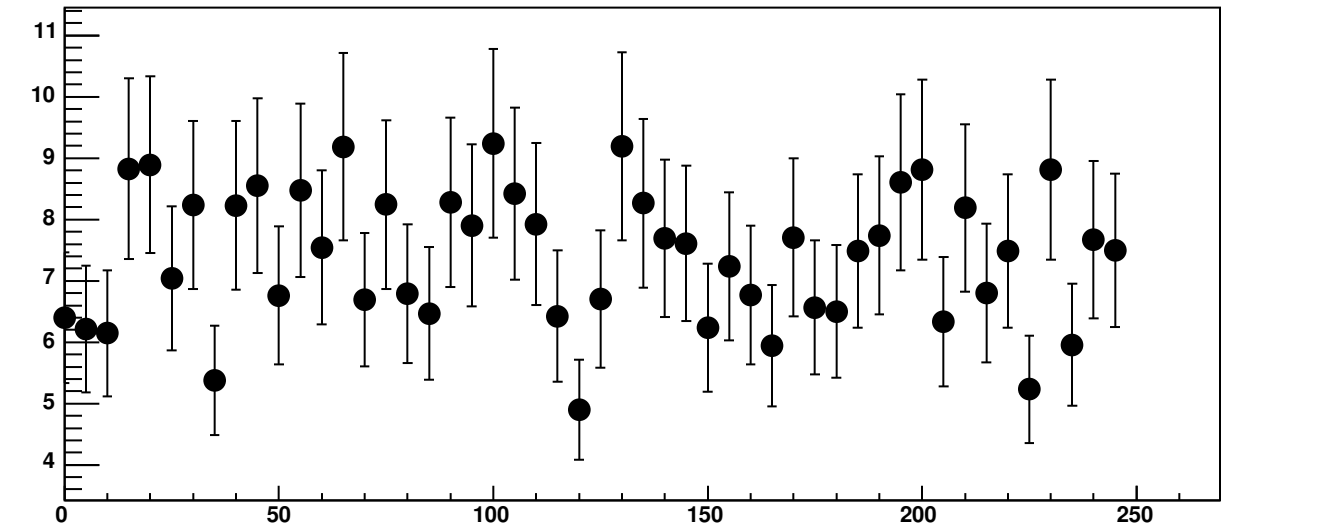
Chip 9, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold



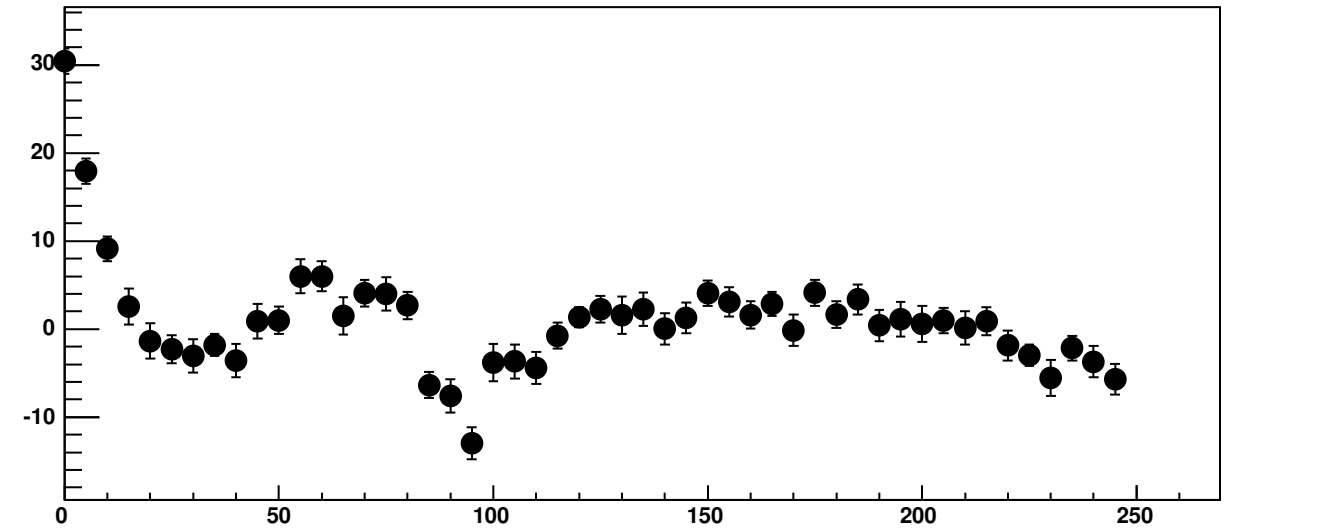
Chip 9, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold



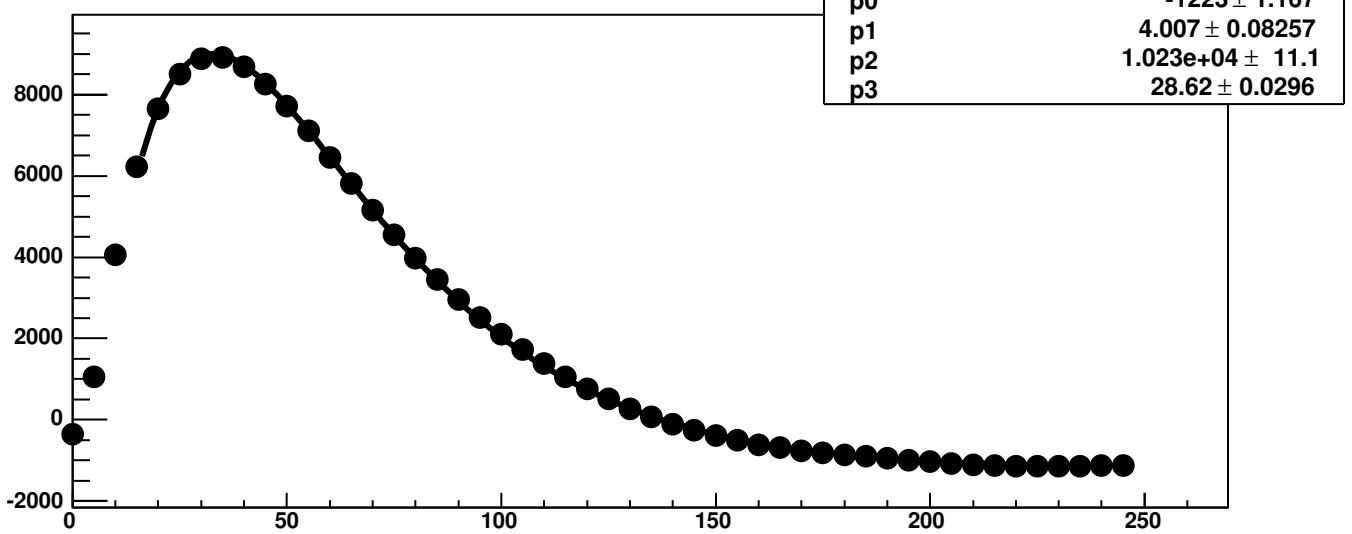
Chip 9, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



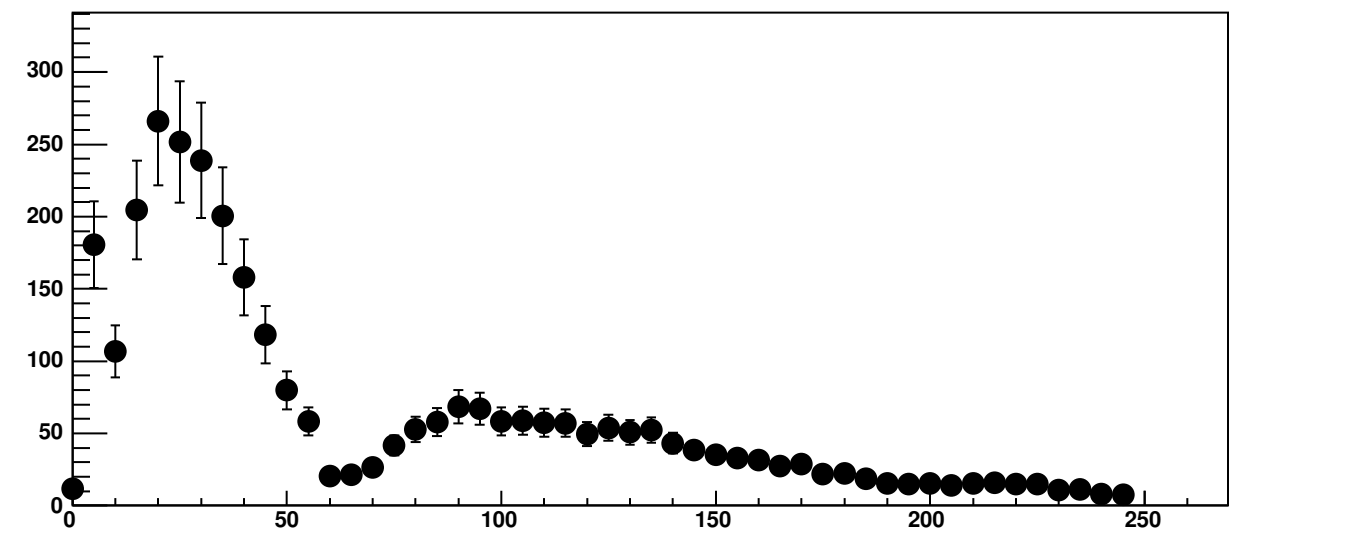
Chip 9, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold



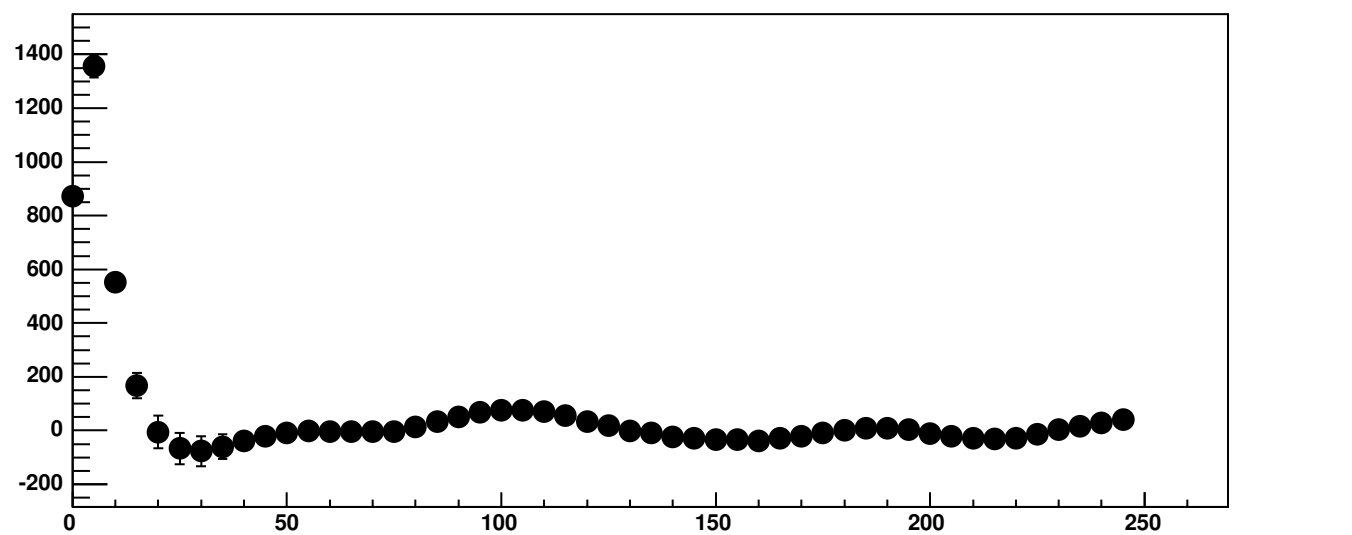
Chip 9, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold



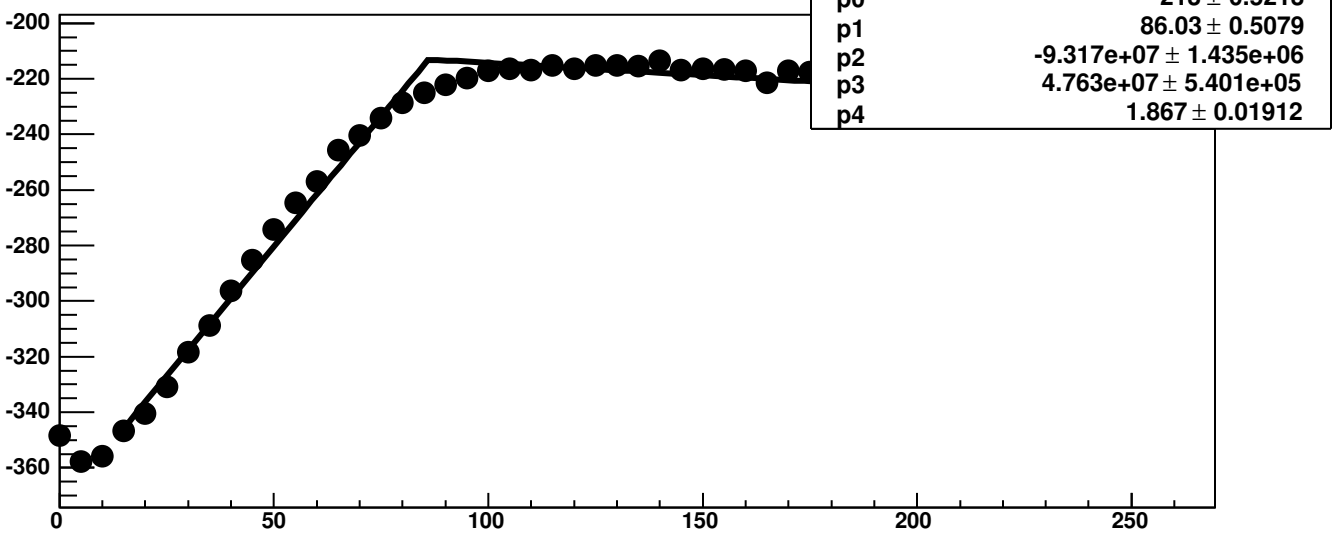
Chip 9, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold



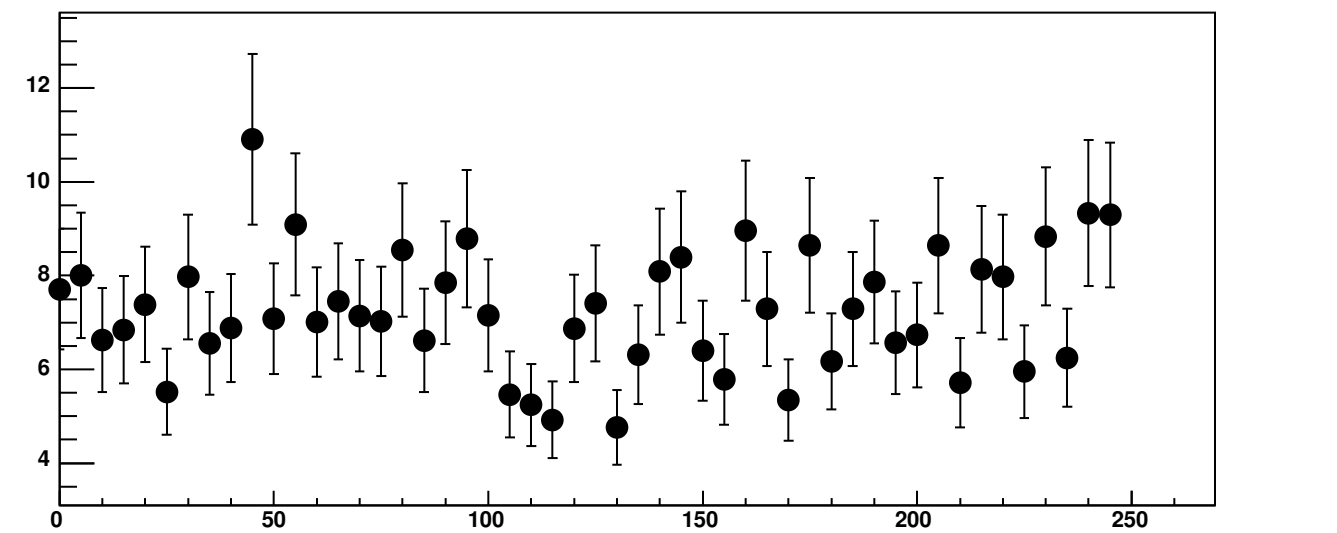
Chip 9, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold



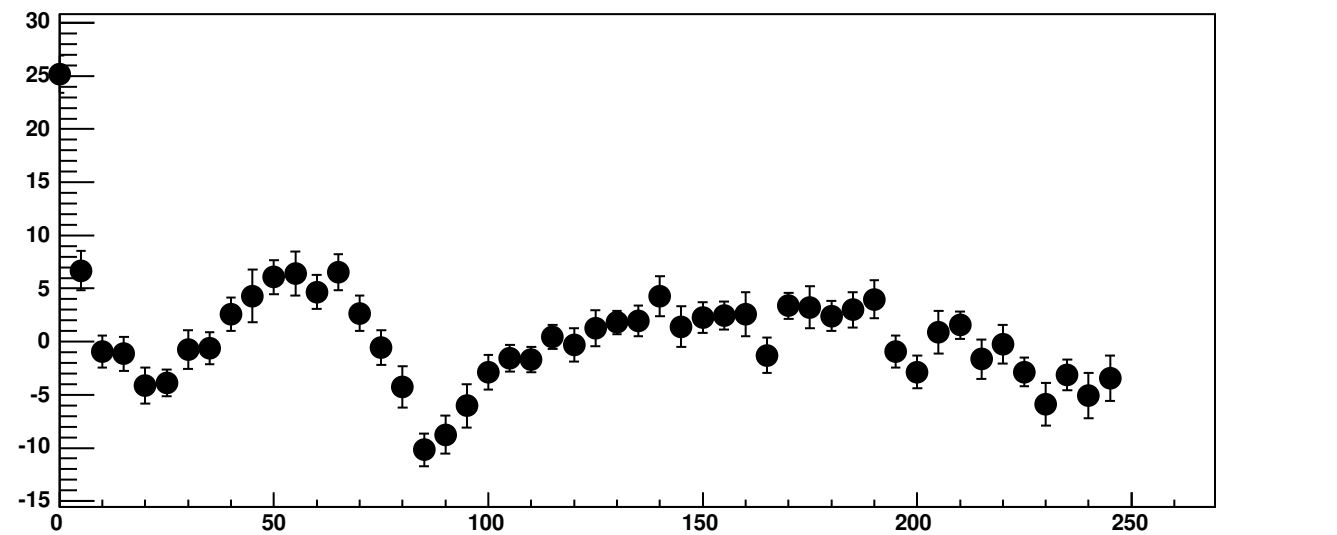
Chip 9, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

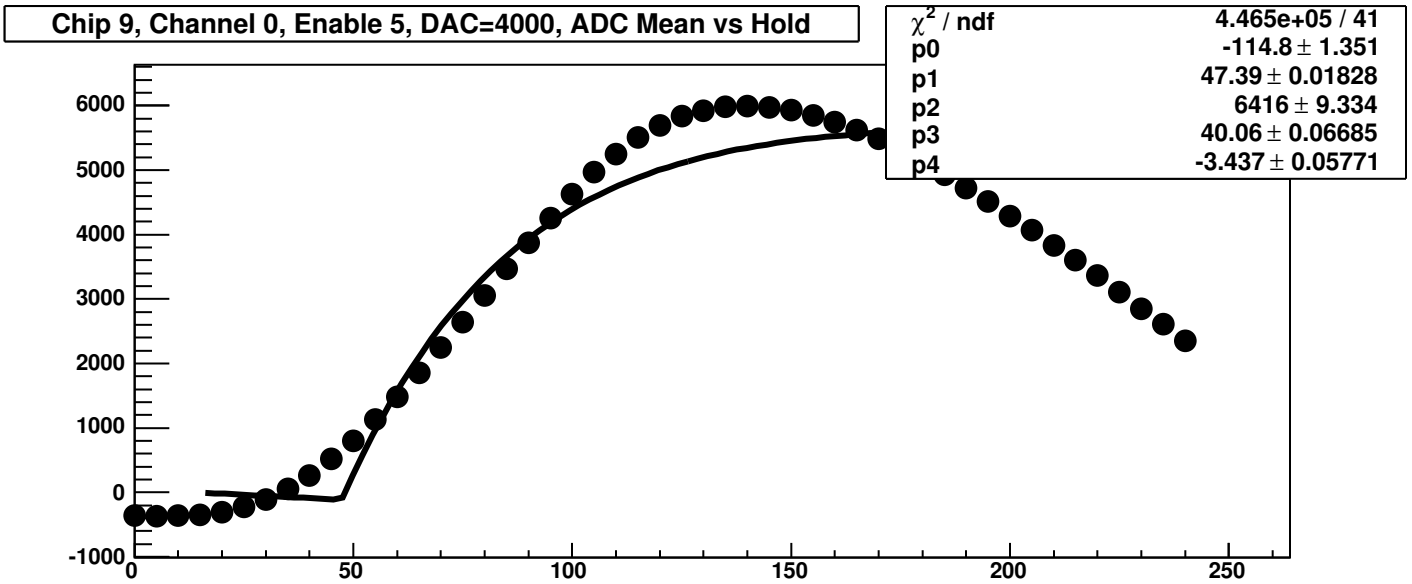


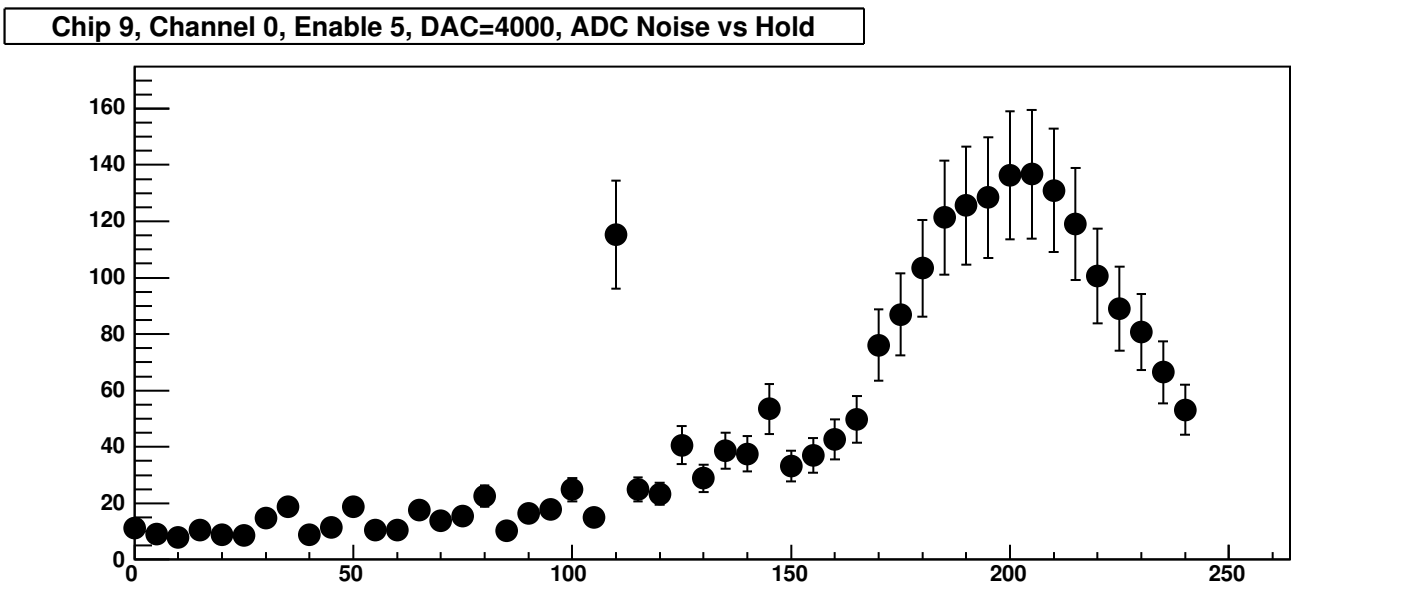
Chip 9, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold

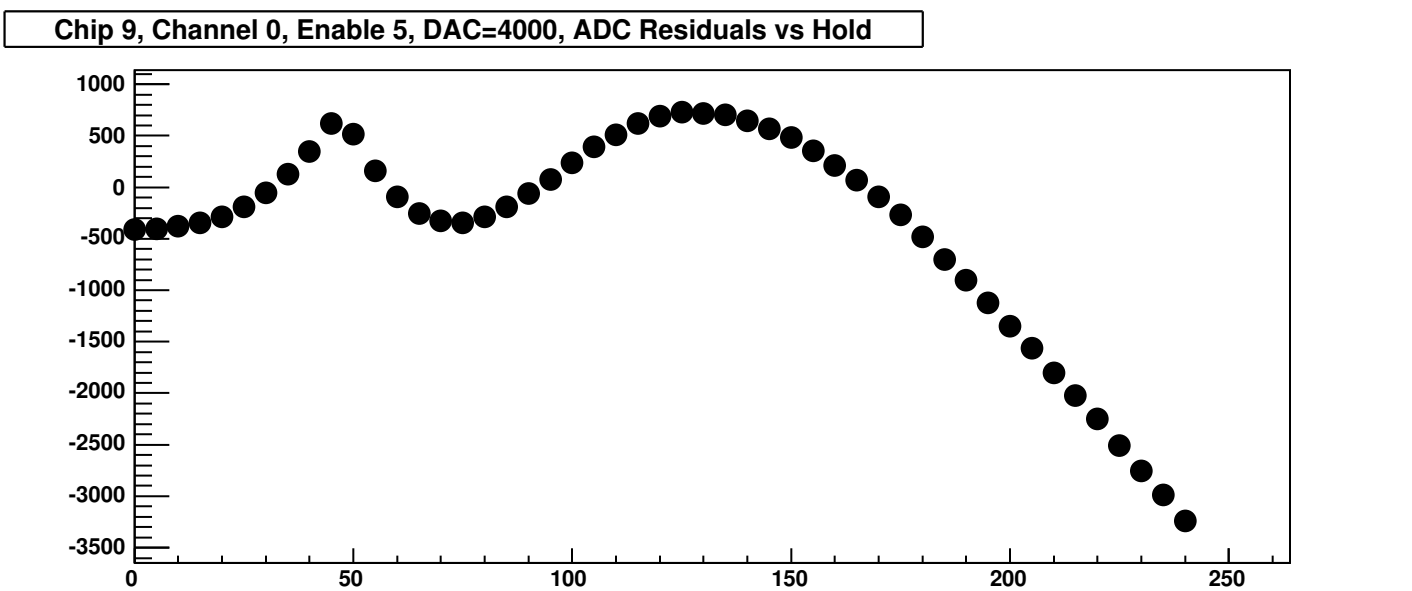


Chip 9, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

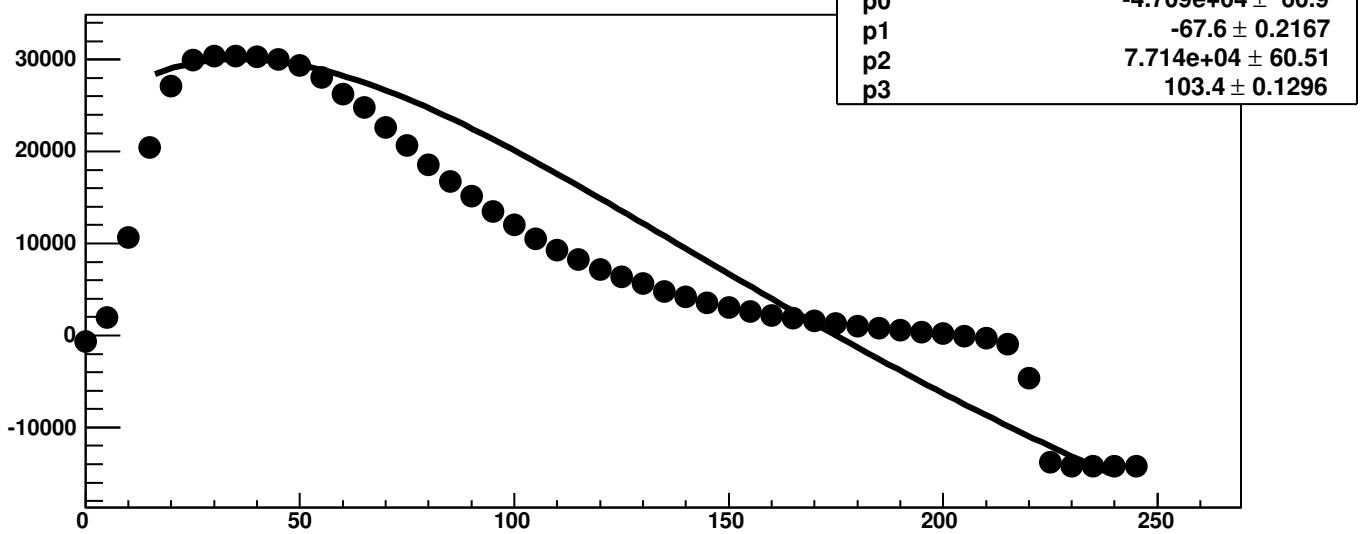



Chip 9, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.465e+05 / 41
p0 -114.8 ± 1.351
p1 47.39 ± 0.01828
p2 6416 ± 9.334
p3 40.06 ± 0.06685
p4 -3.437 ± 0.05771
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

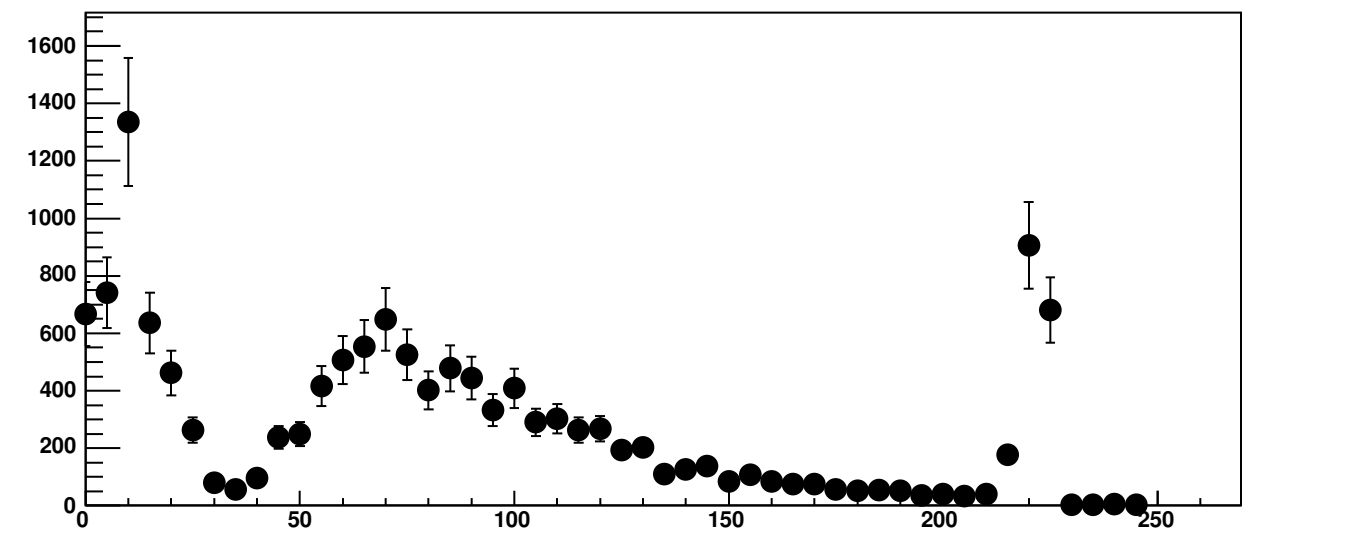

Chip 9, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 9, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
0
50
100
150
200
250

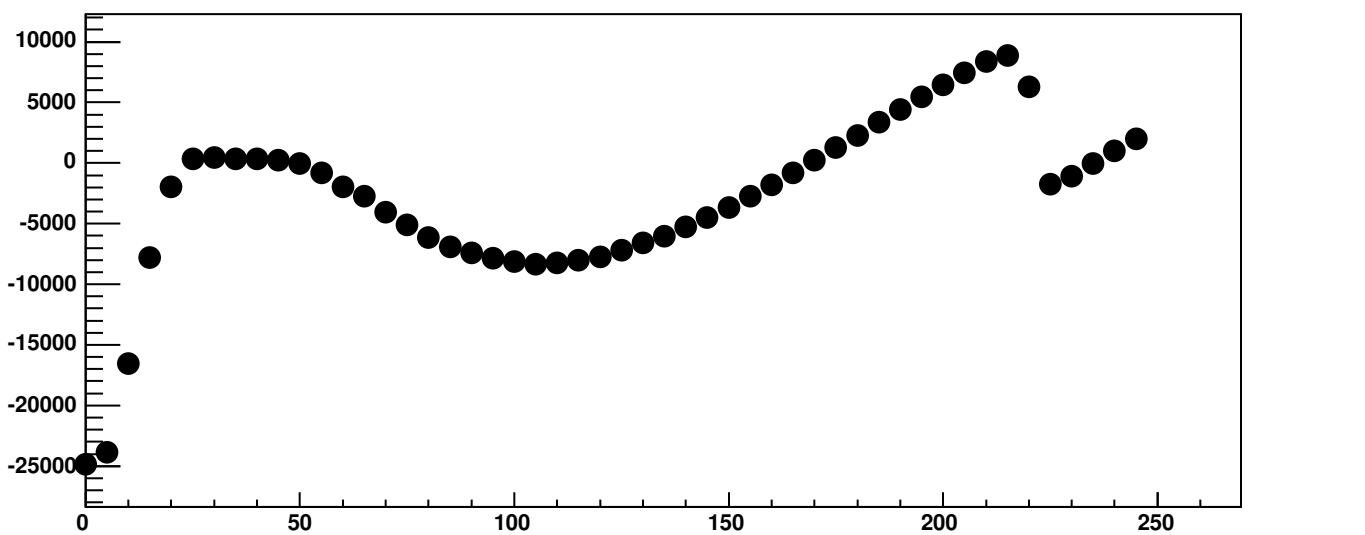
Chip 9, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold

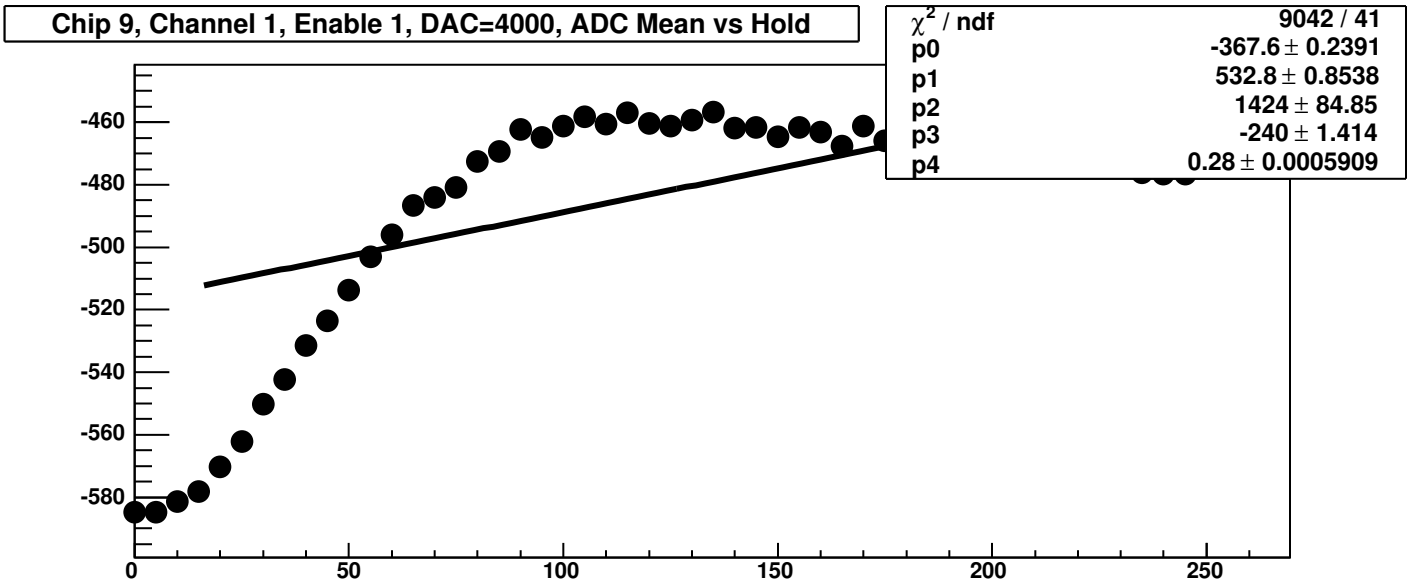


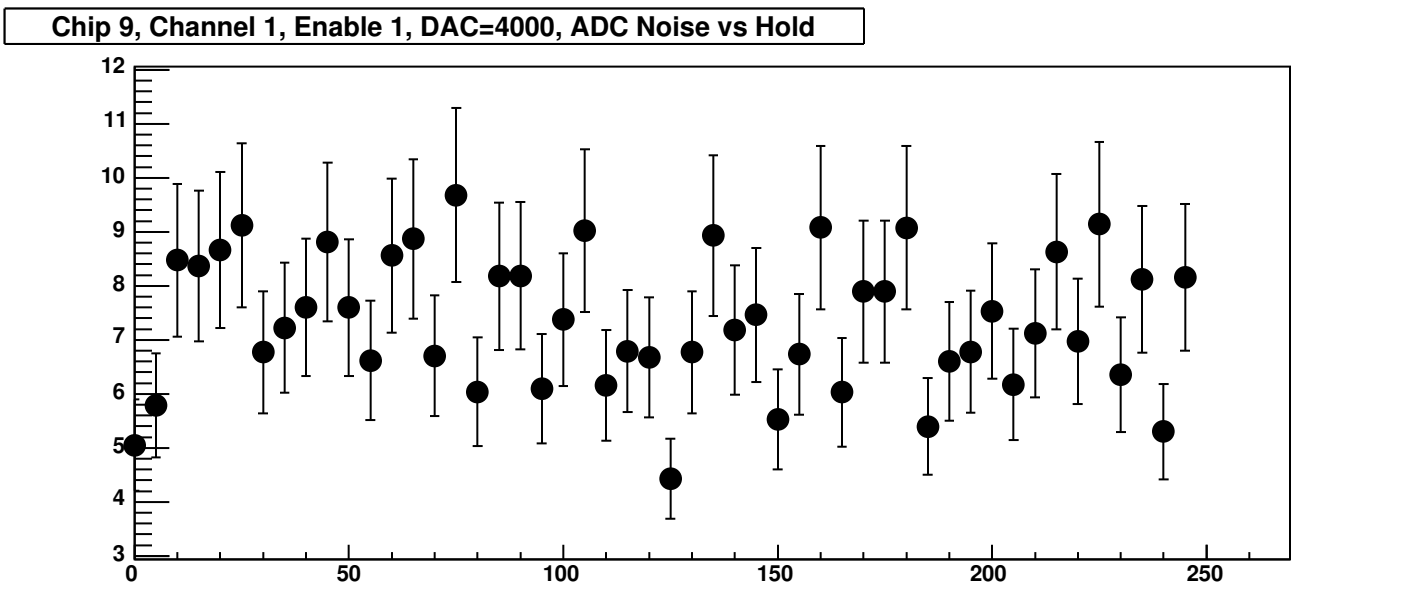
Chip 9, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold

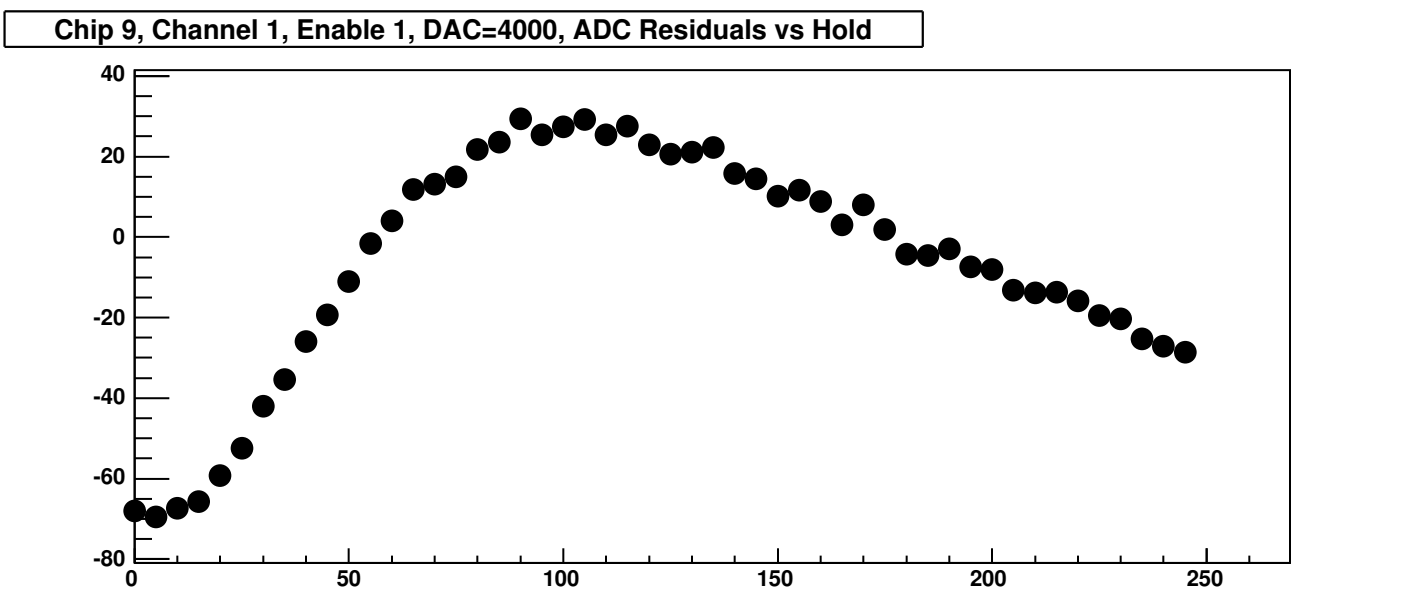


Chip 9, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold

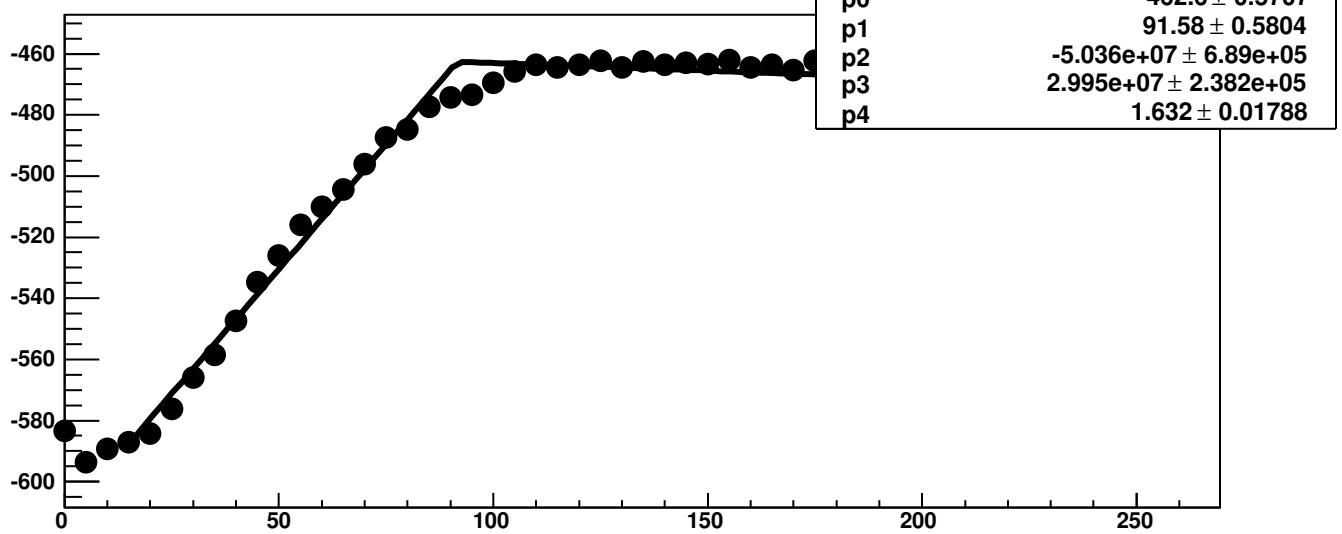



Chip 9, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 9042 / 41
p0 -367.6 ± 0.2391
p1 532.8 ± 0.8538
p2 1424 ± 84.85
p3 -240 ± 1.414
p4 0.28 ± 0.0005909
-460
-480
-500
-520
-540
-560
-580
0
50
100
150
200
250

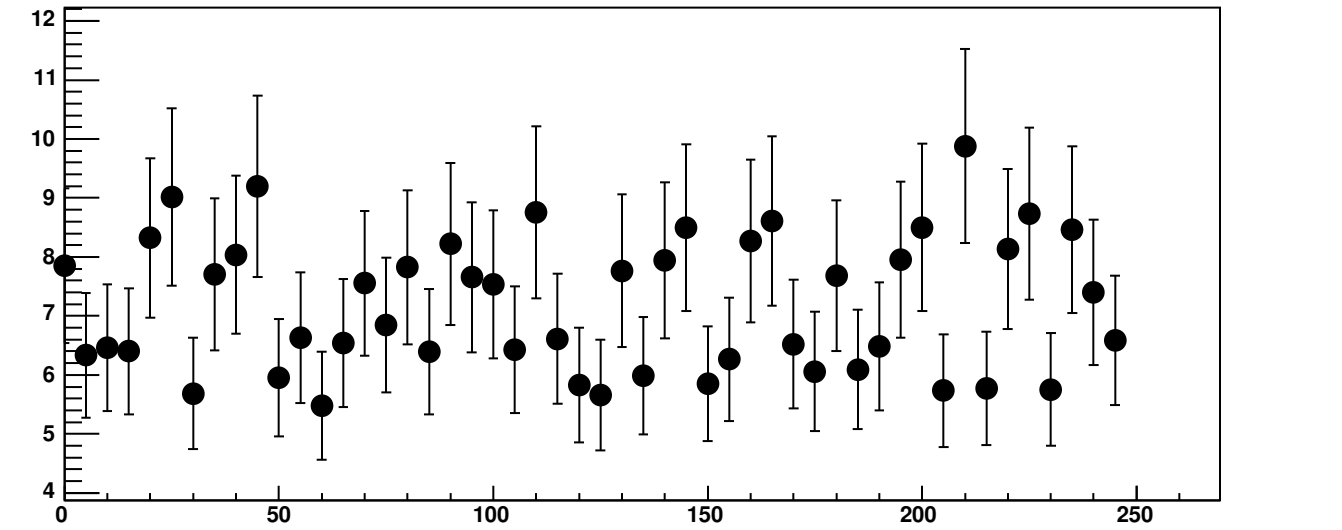

Chip 9, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 9, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
0
50
100
150
200
250

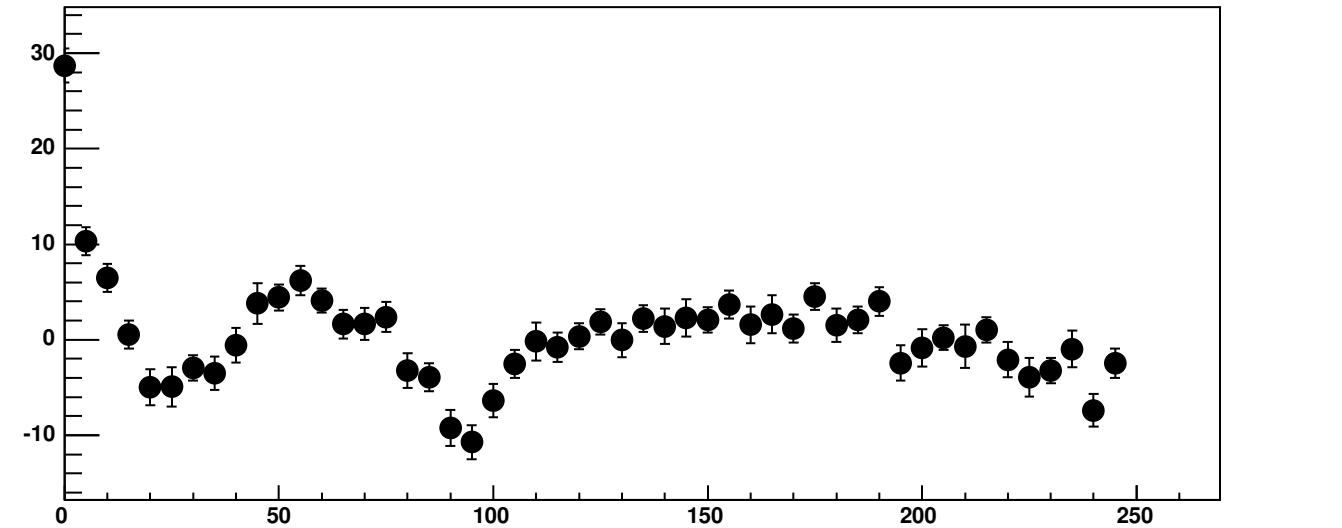
Chip 9, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold



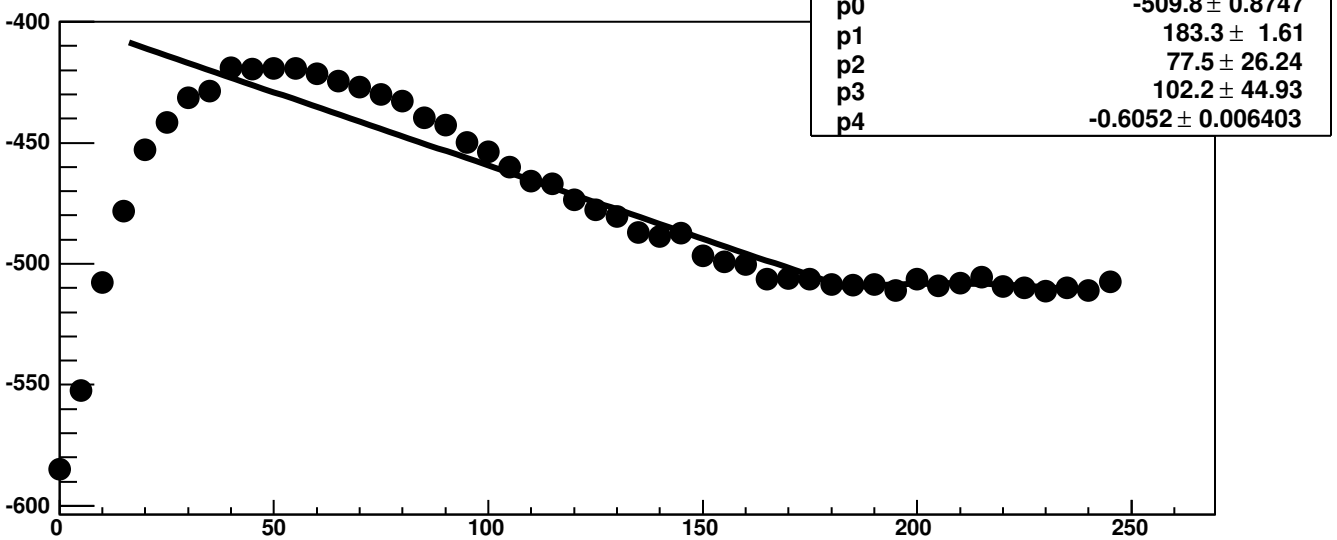
Chip 9, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



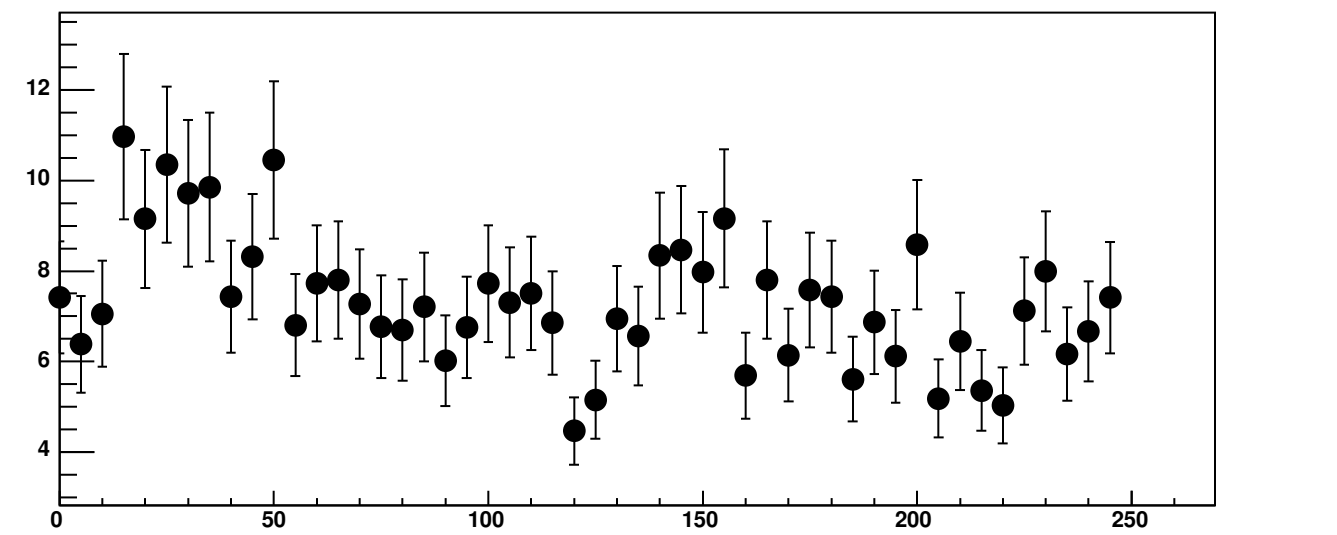
Chip 9, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold



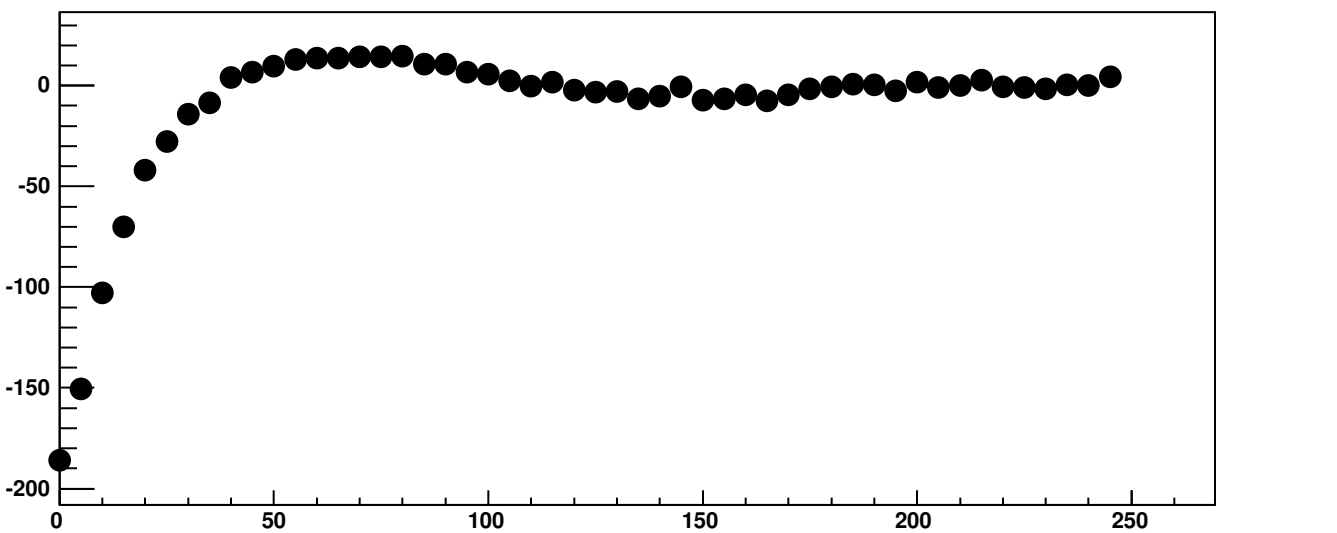
Chip 9, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold



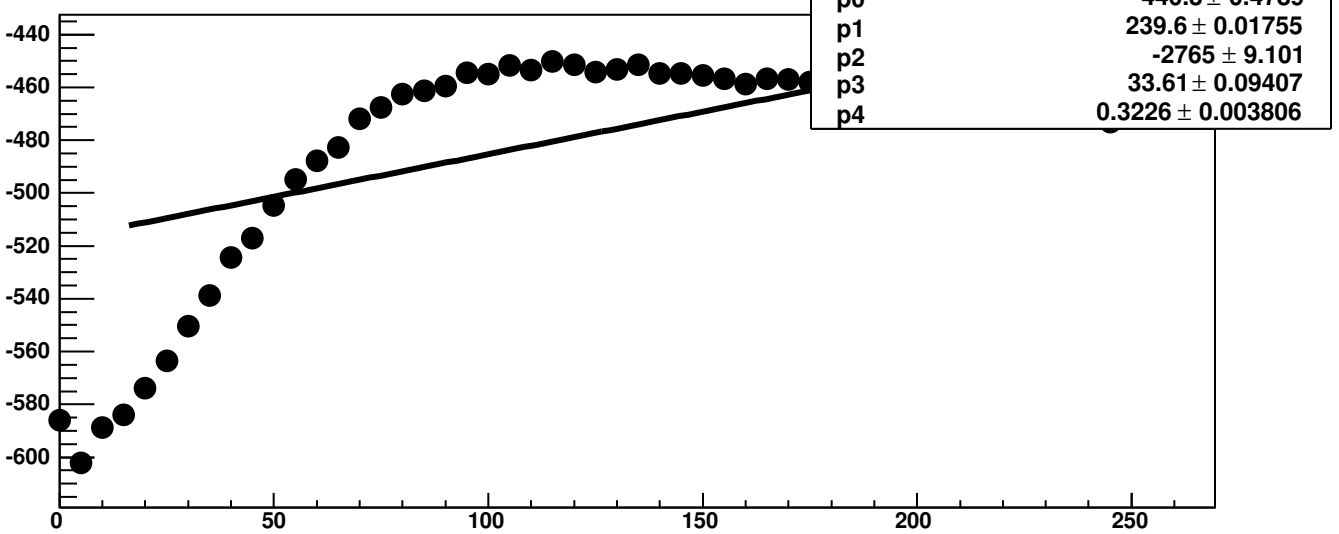
Chip 9, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold



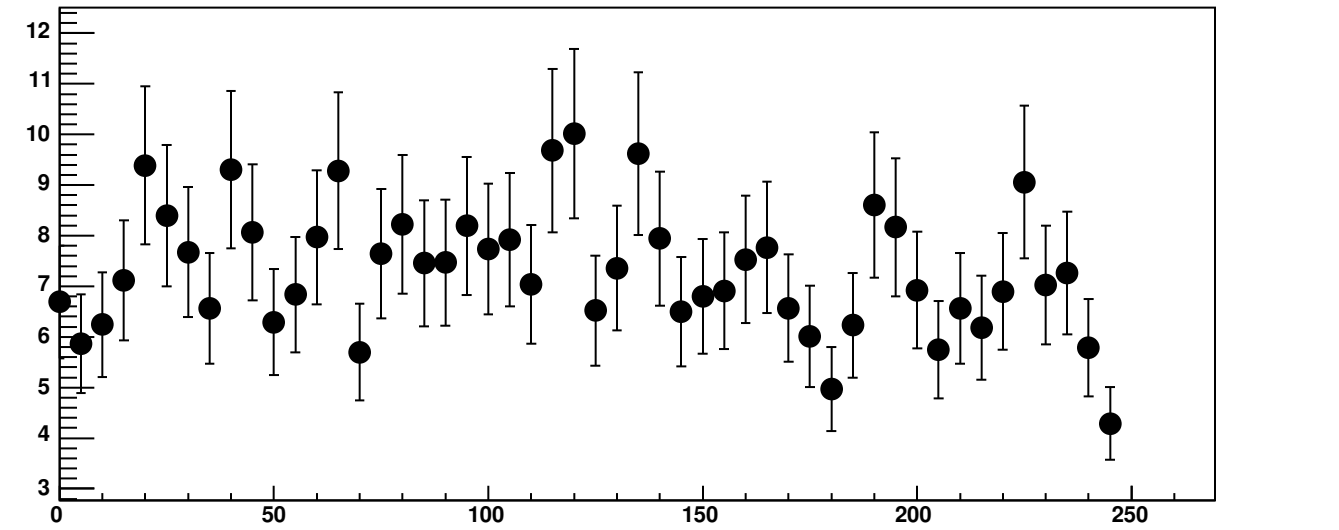
Chip 9, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold



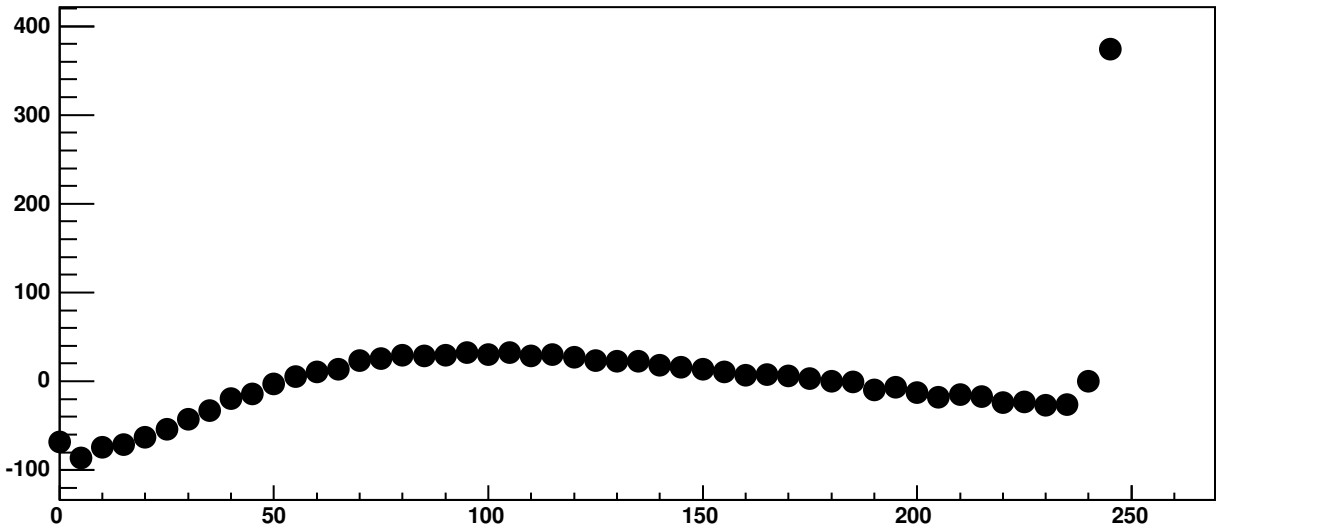
Chip 9, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

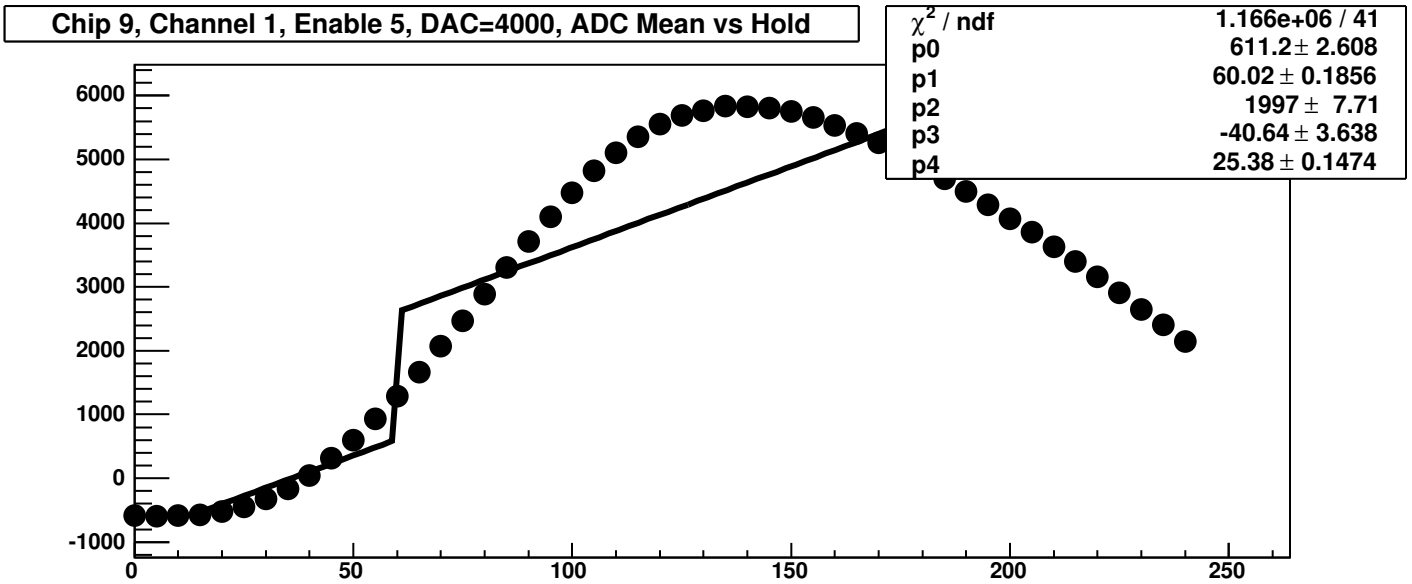


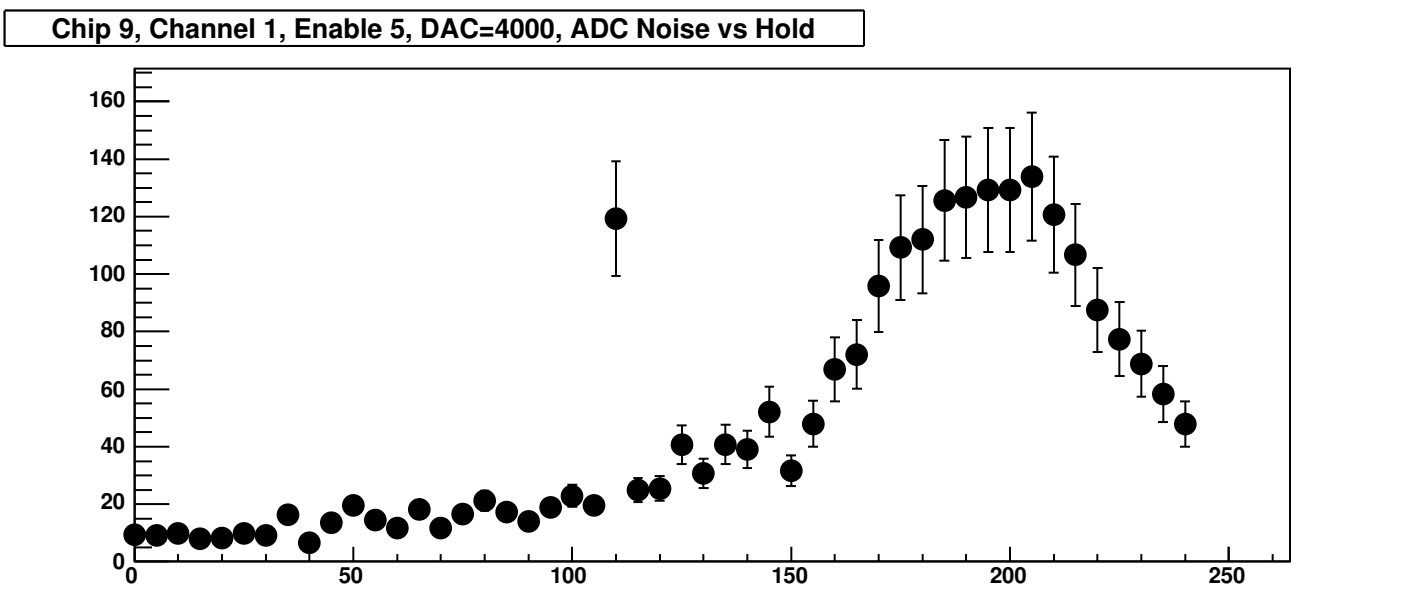
Chip 9, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold

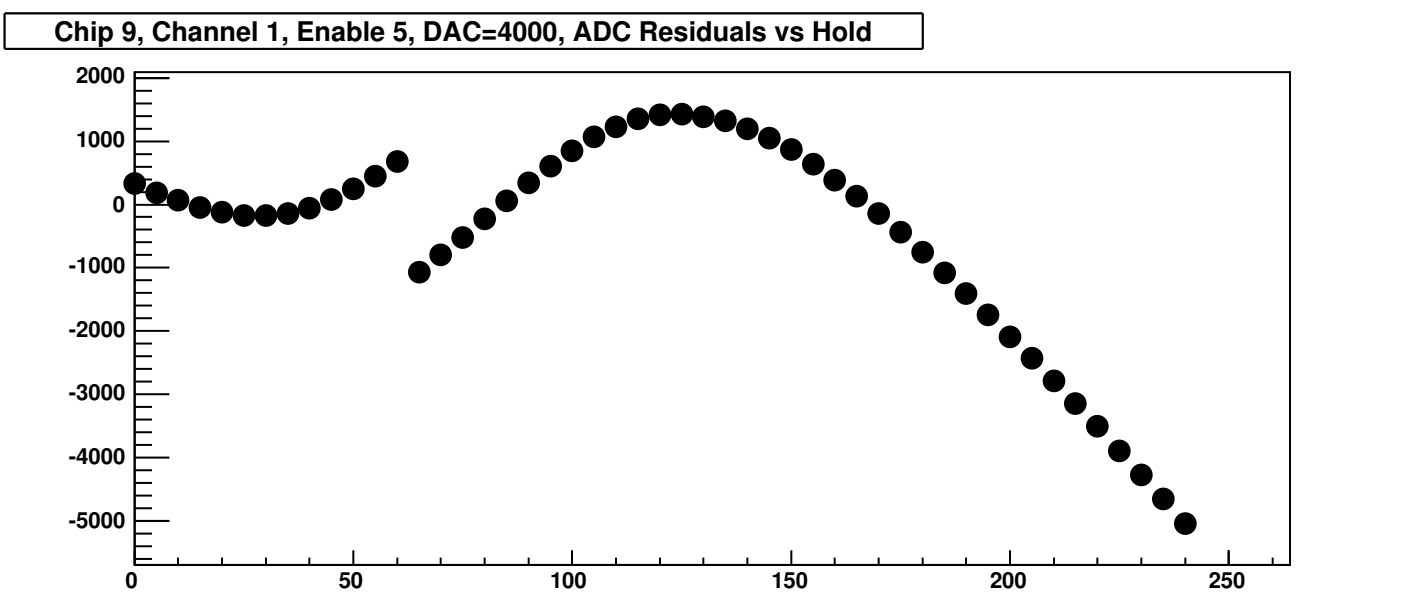


Chip 9, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

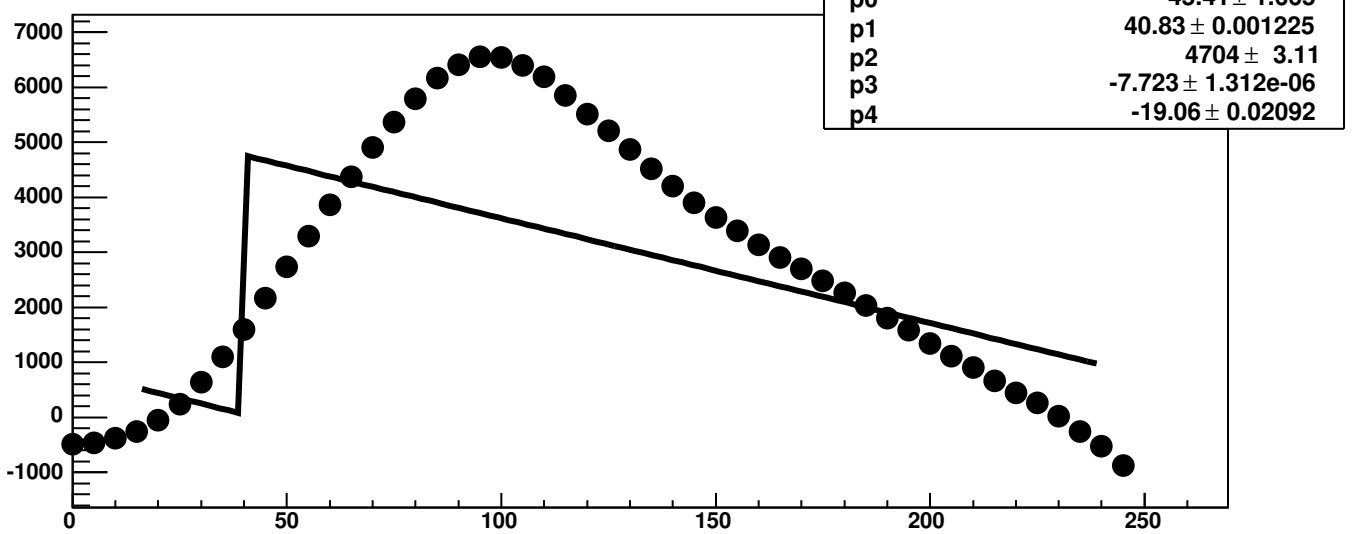



Chip 9, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.166e+06 / 41
p0 611.2 ± 2.608
p1 60.02 ± 0.1856
p2 1997 ± 7.71
p3 -40.64 ± 3.638
p4 25.38 ± 0.1474
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

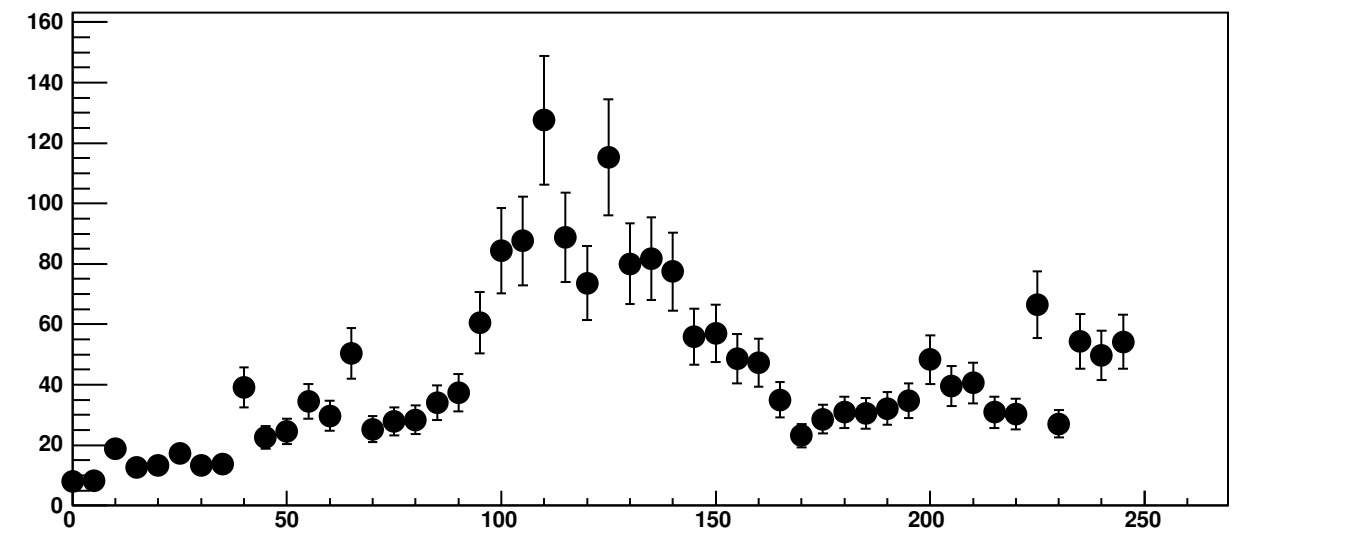

Chip 9, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 9, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

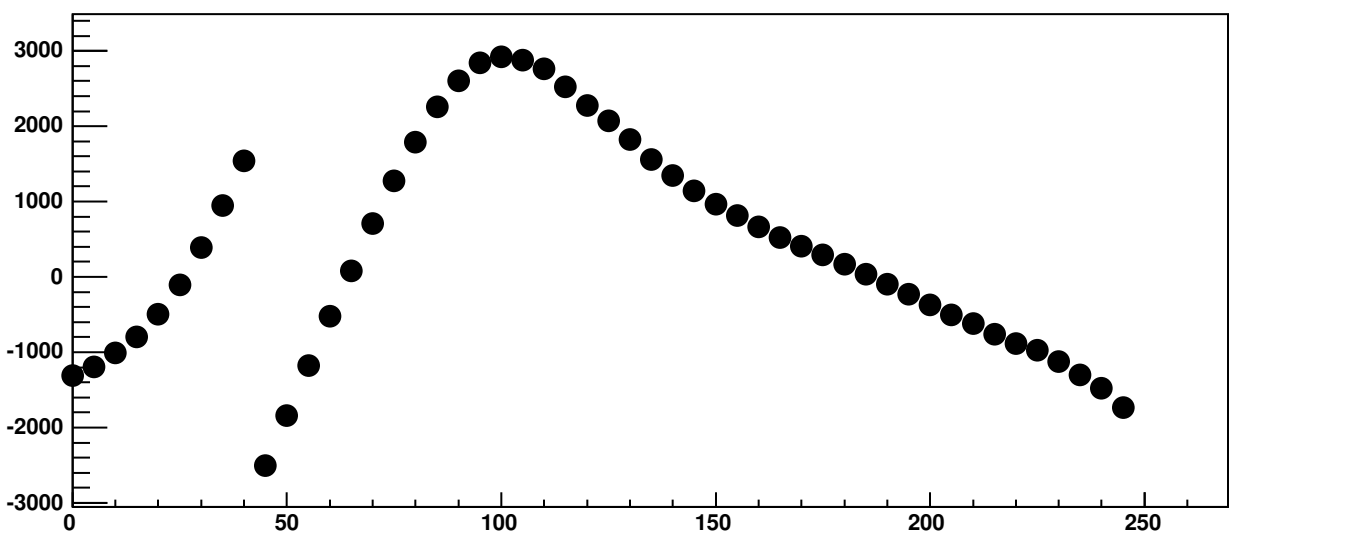
Chip 9, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold



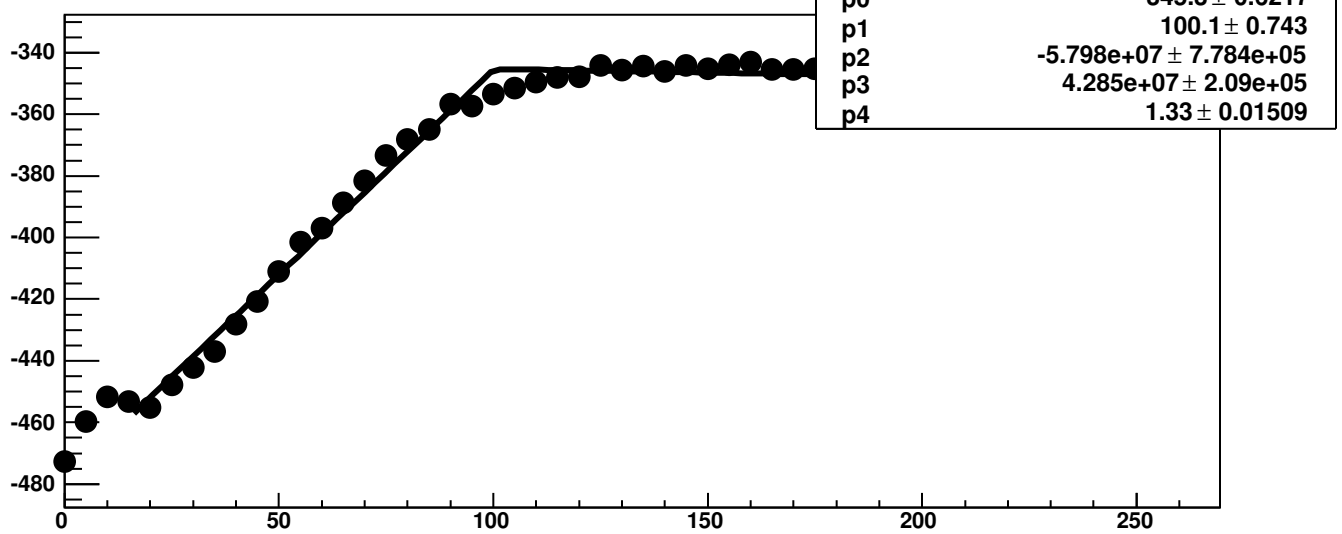
Chip 9, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold



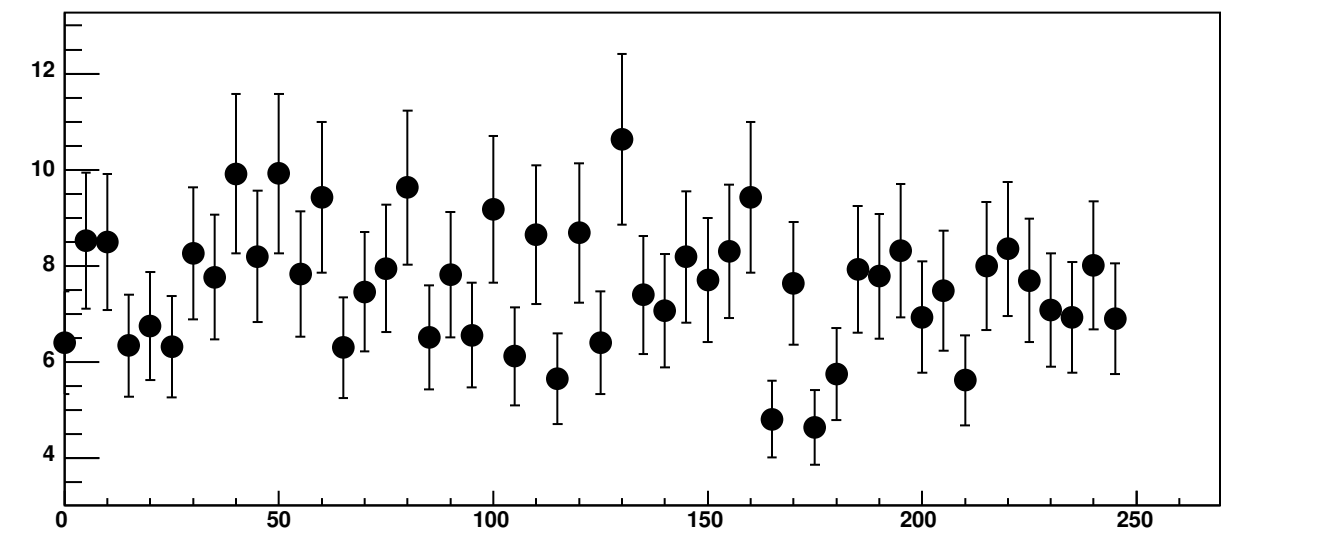
Chip 9, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold



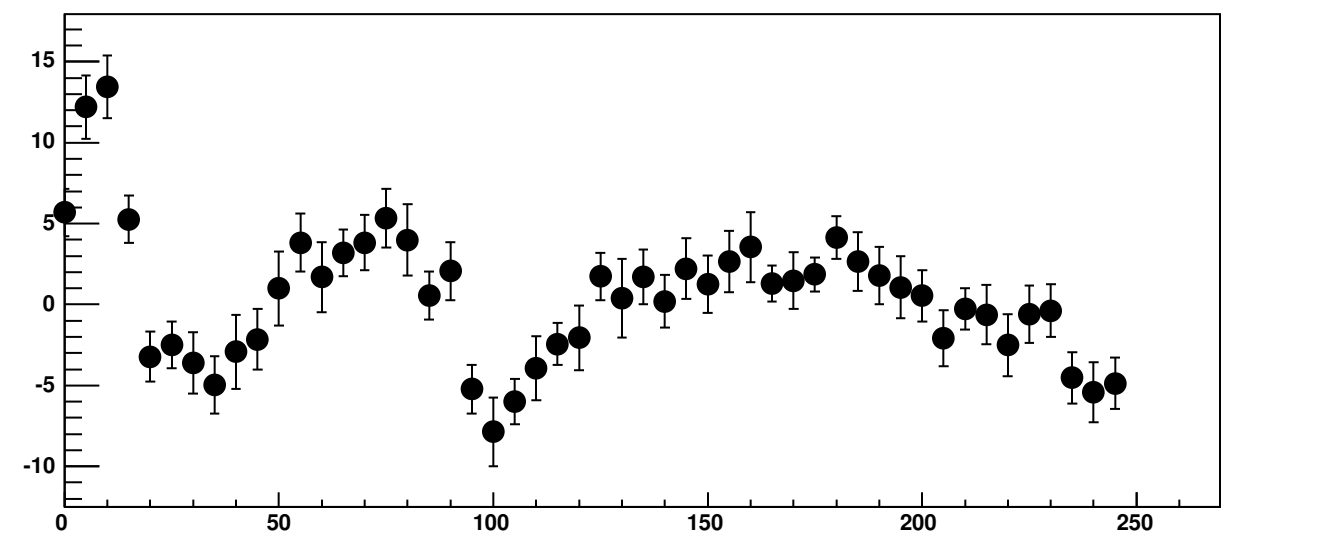
Chip 9, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

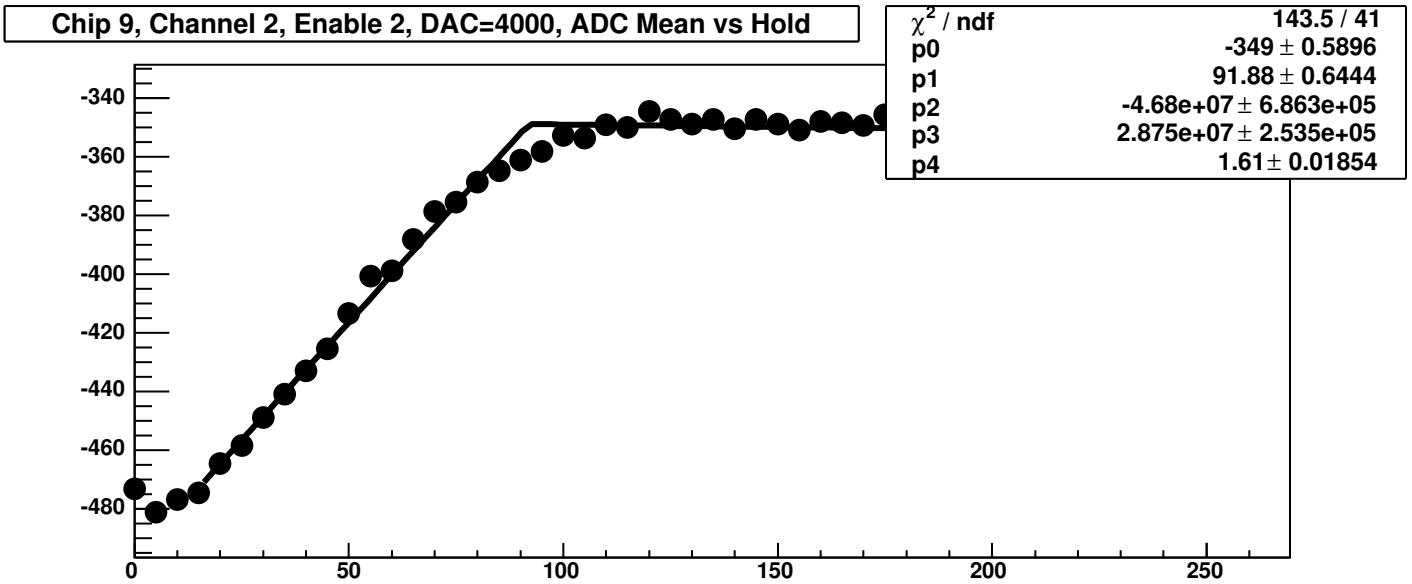


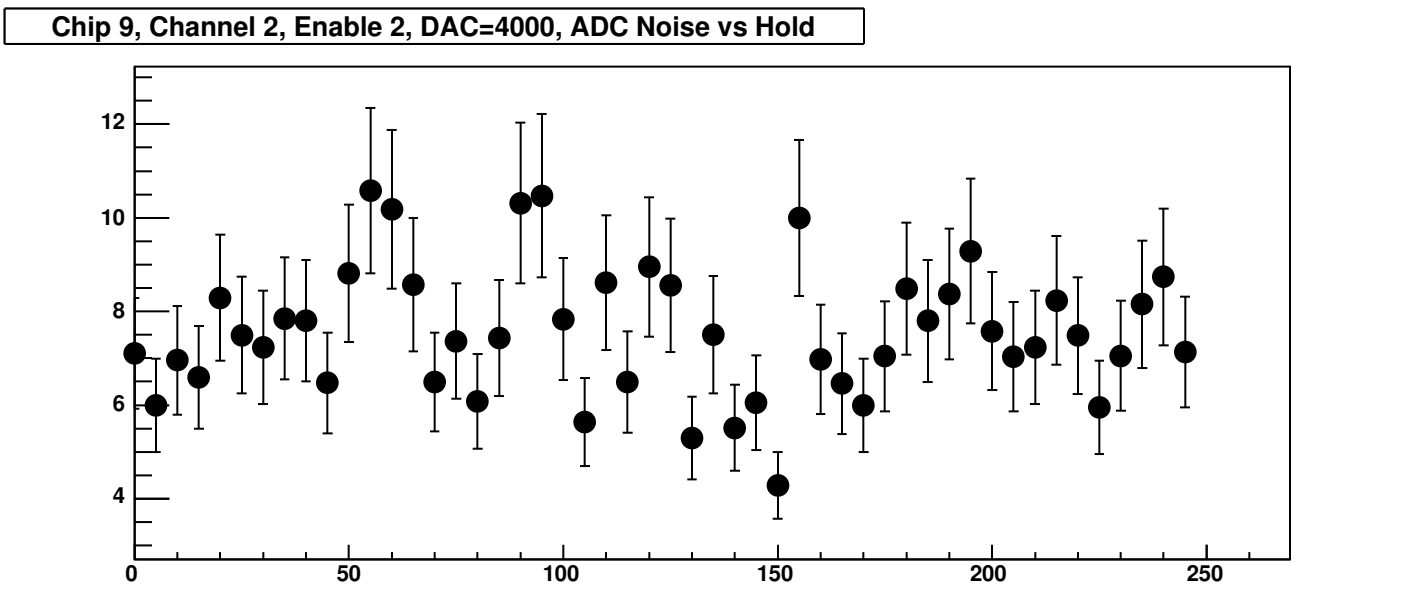
Chip 9, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold

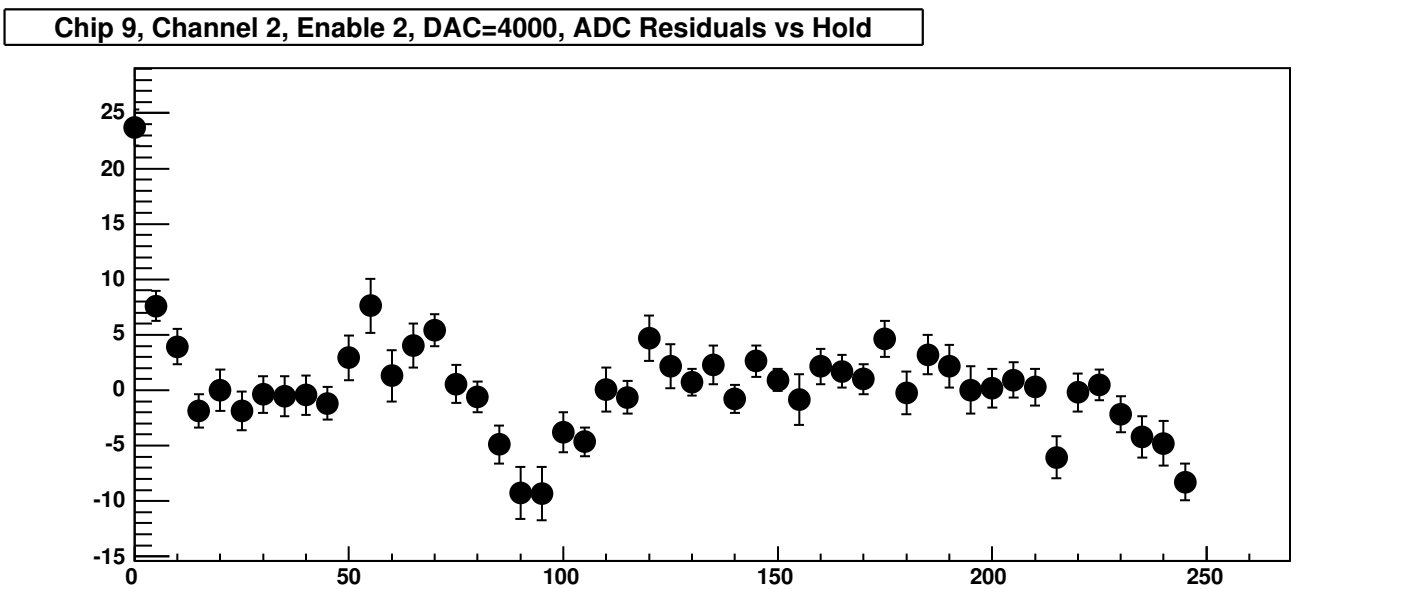


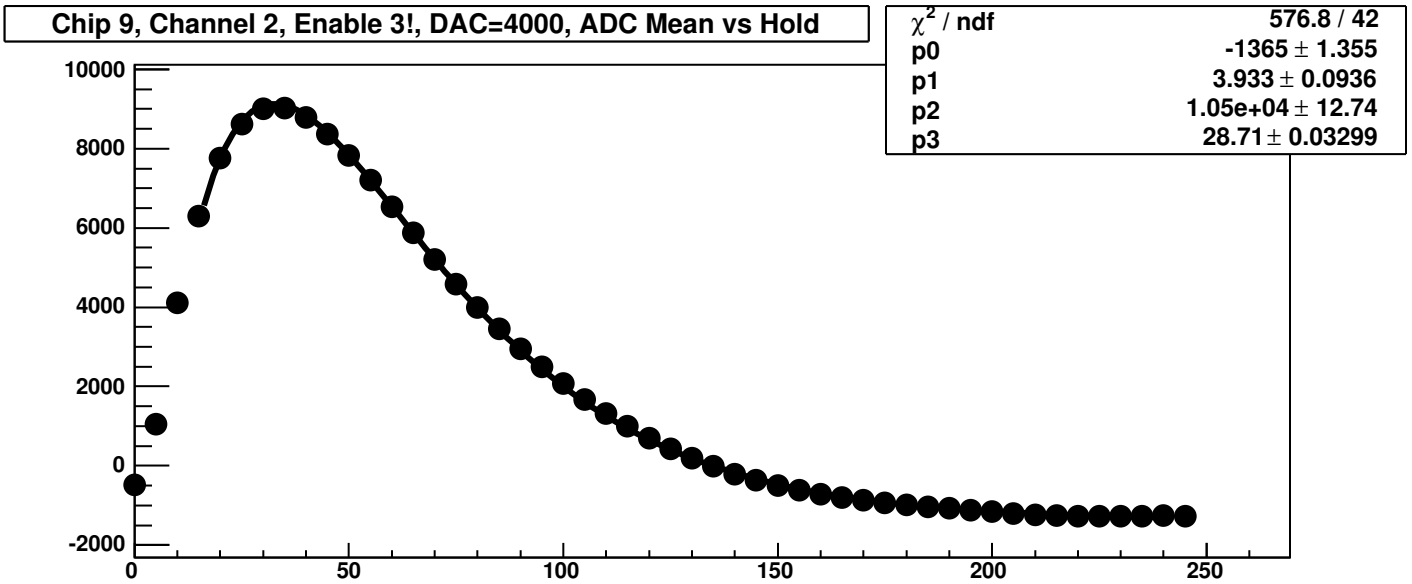
Chip 9, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold

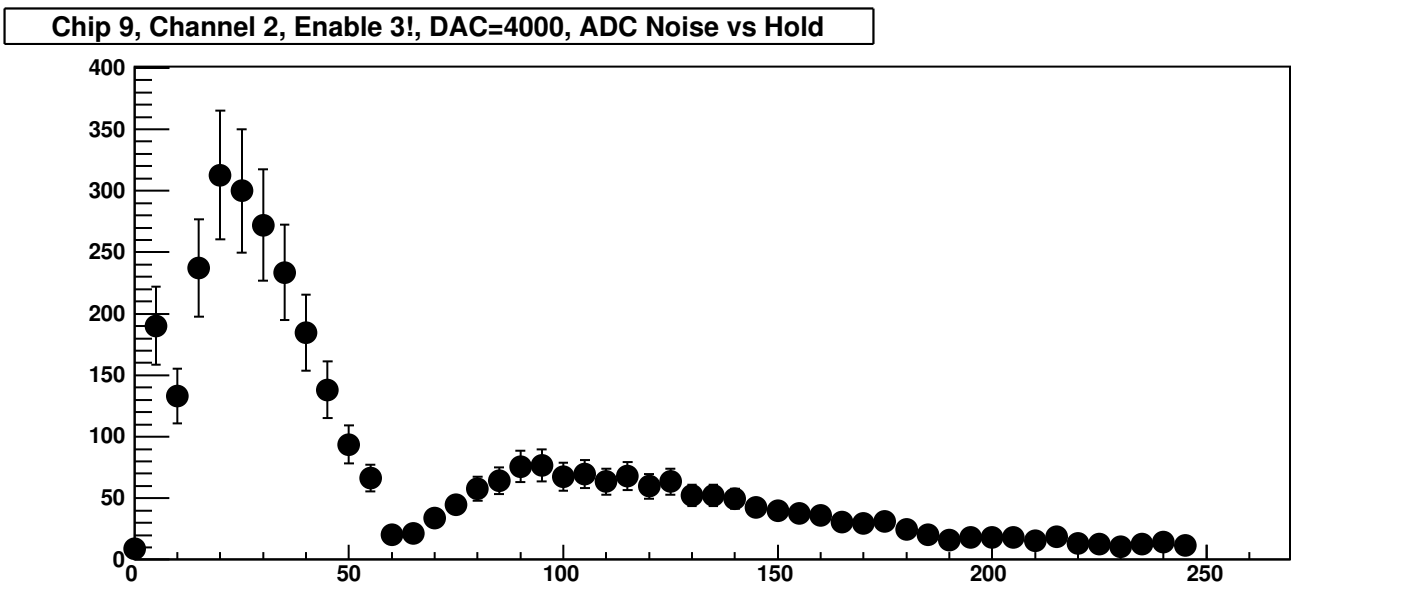


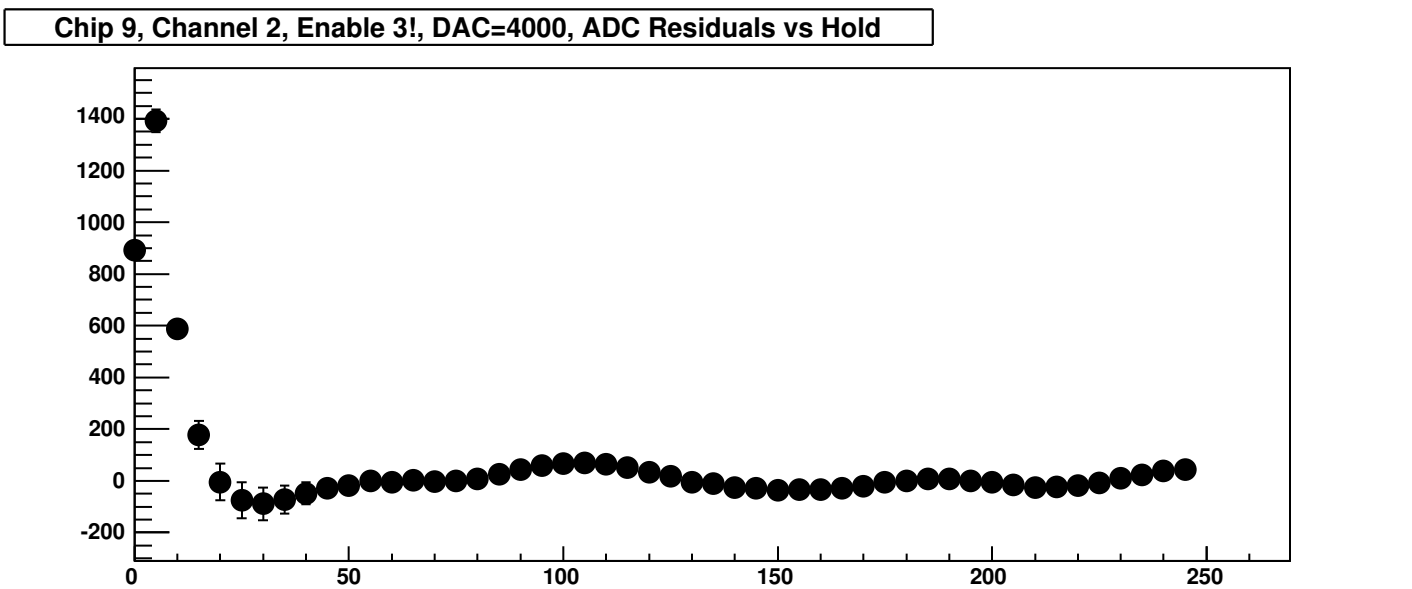

Chip 9, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 143.5 / 41
p0 -349 ± 0.5896
p1 91.88 ± 0.6444
p2 -4.68e+07 ± 6.863e+05
p3 2.875e+07 ± 2.535e+05
p4 1.61 ± 0.01854
-340
-360
-380
-400
-420
-440
-460
-480
0
50
100
150
200
250

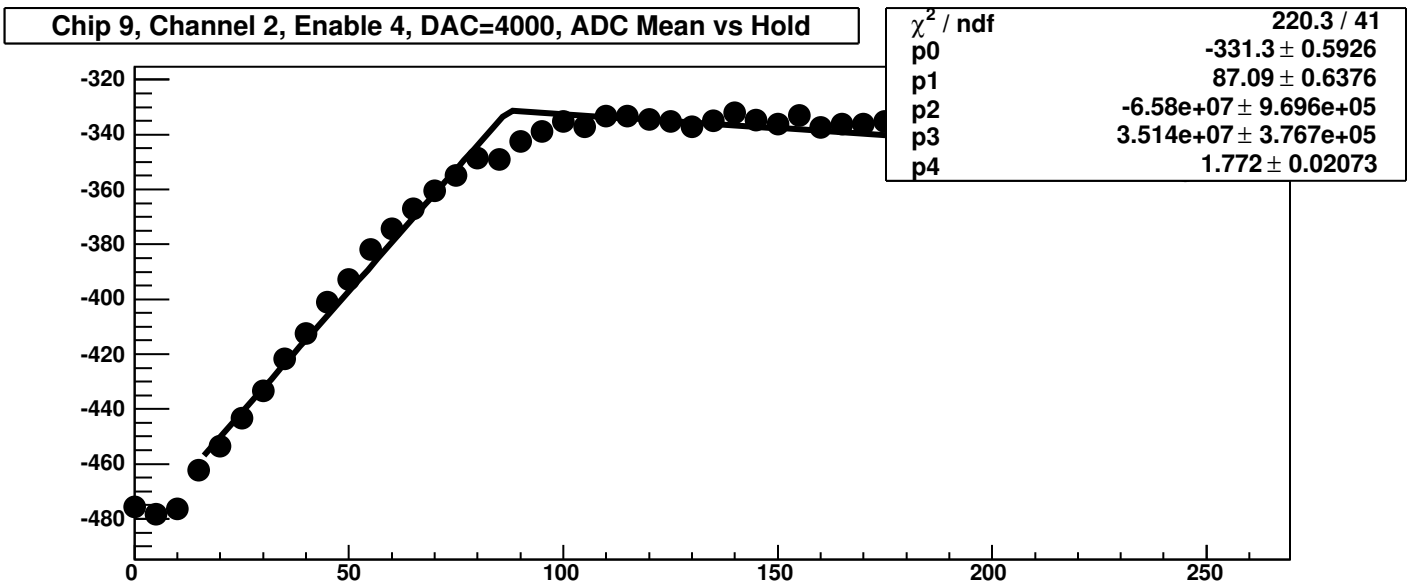

Chip 9, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

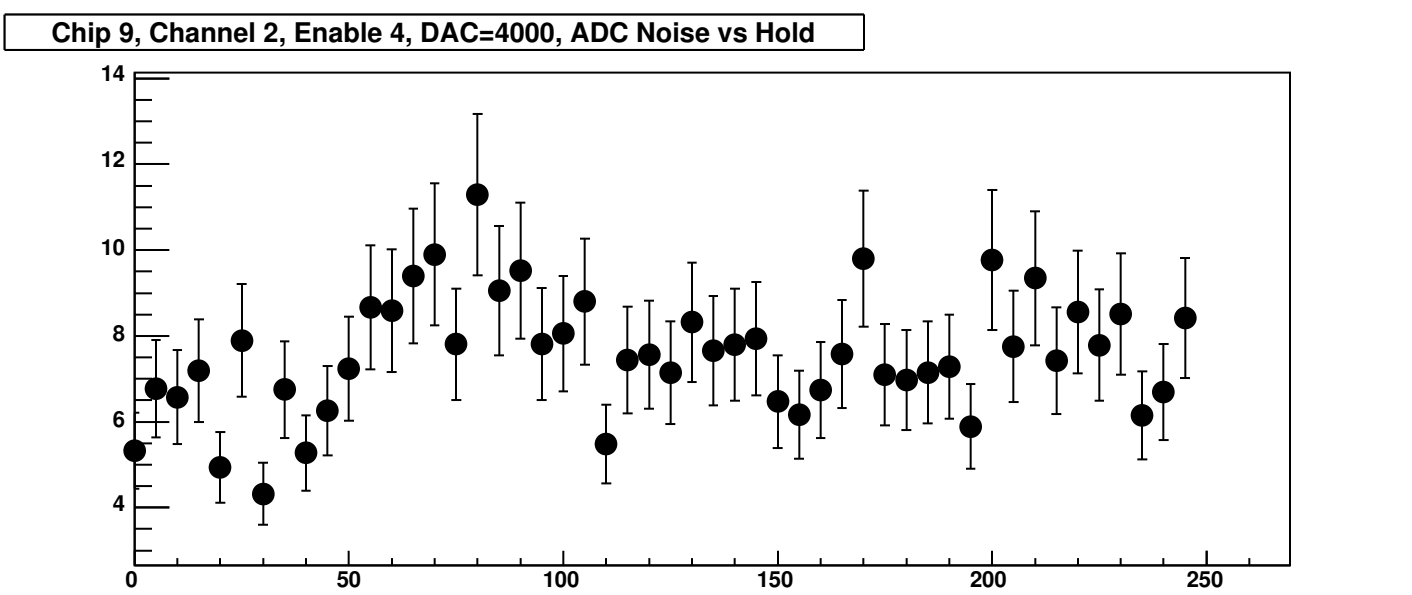

Chip 9, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

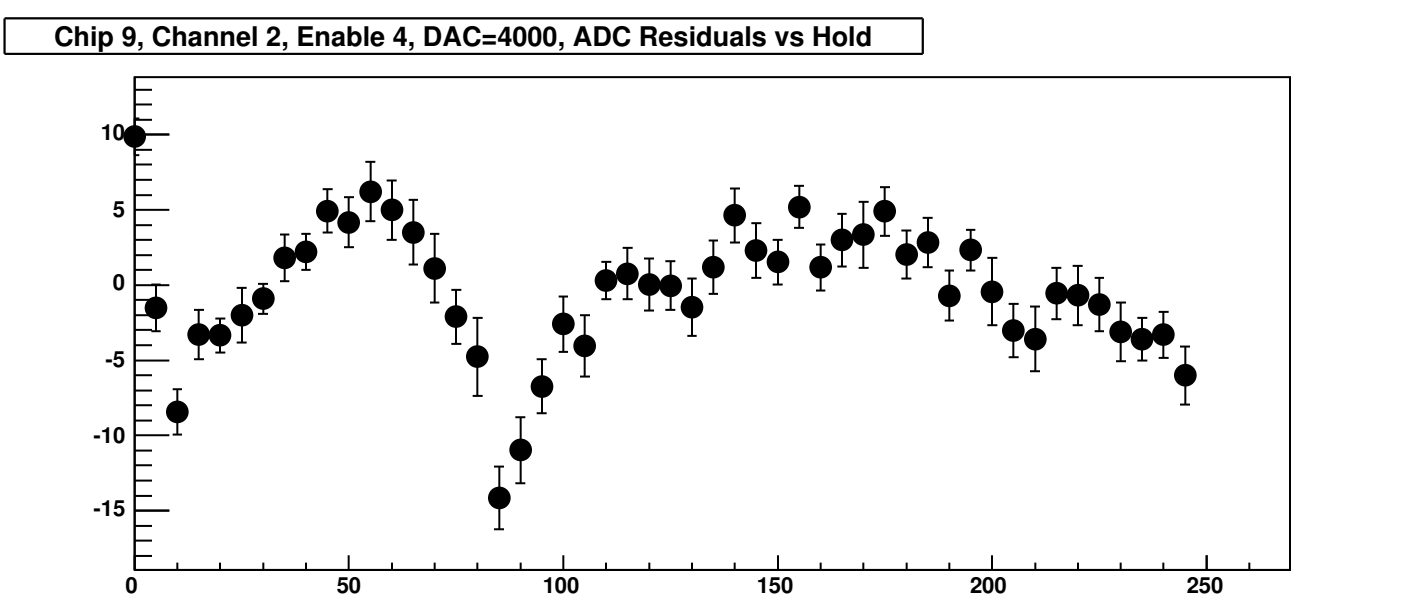

Chip 9, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 576.8 / 42
p0 -1365 ± 1.355
p1 3.933 ± 0.0936
p2 1.05e+04 ± 12.74
p3 28.71 ± 0.03299
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

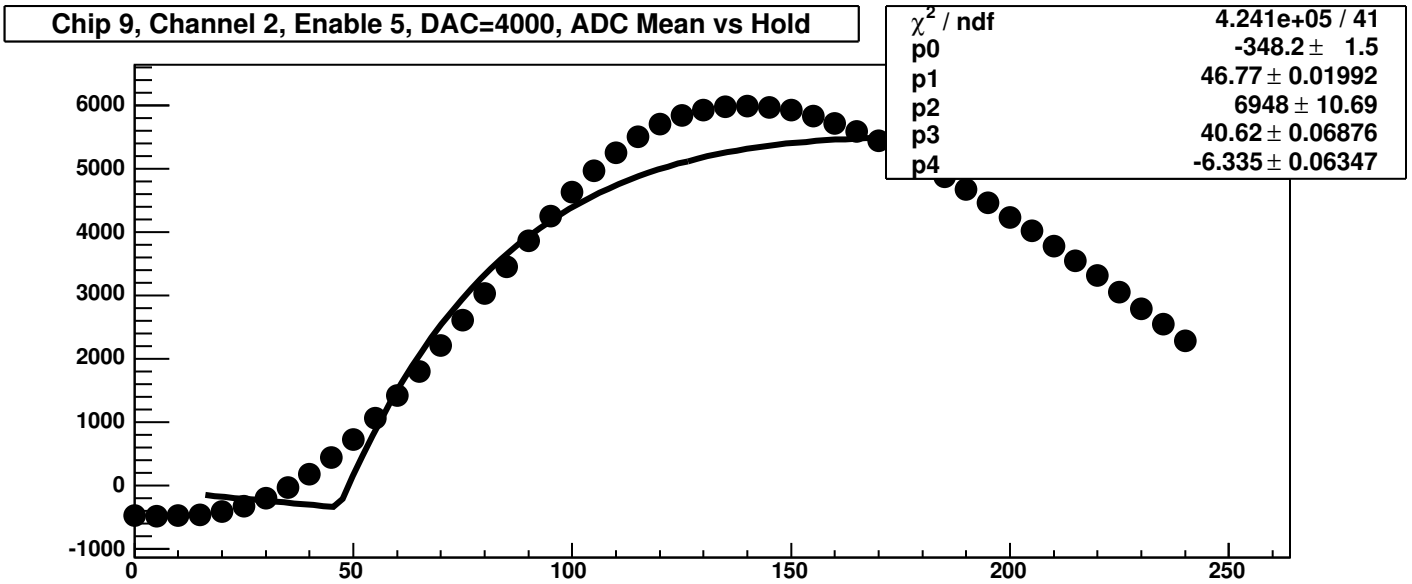

Chip 9, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold
400
350
300
250
200
150
100
50
0
0
50
100
150
200
250

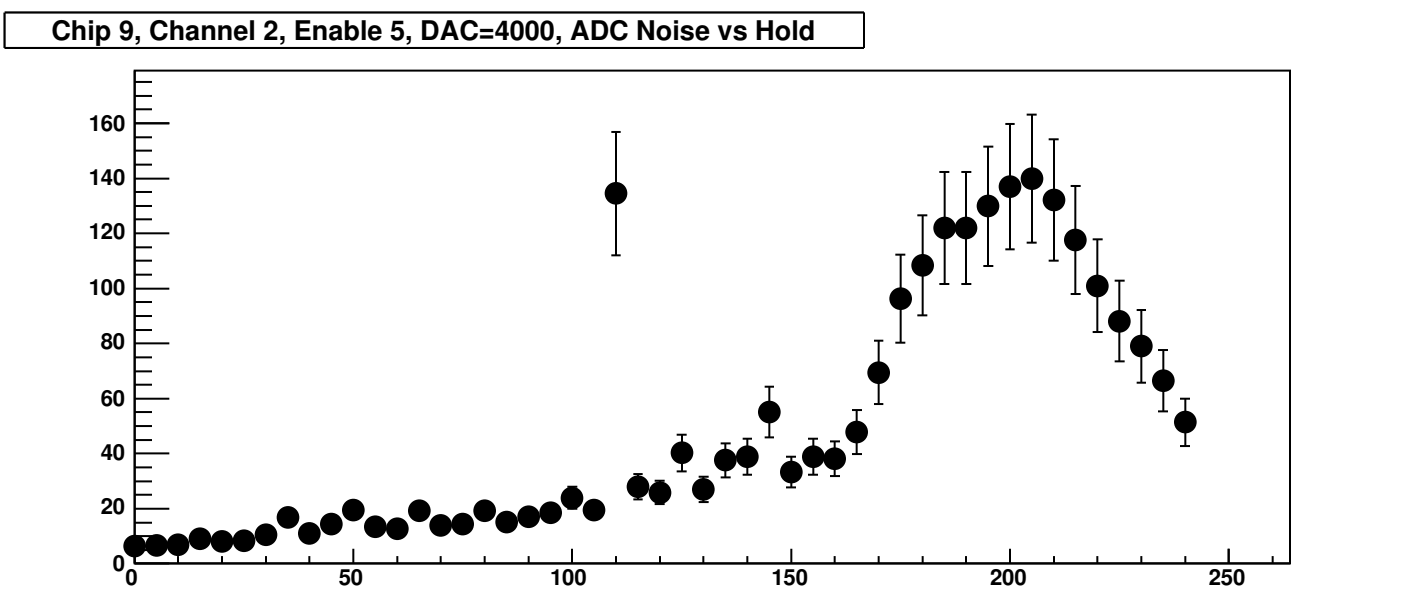

Chip 9, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

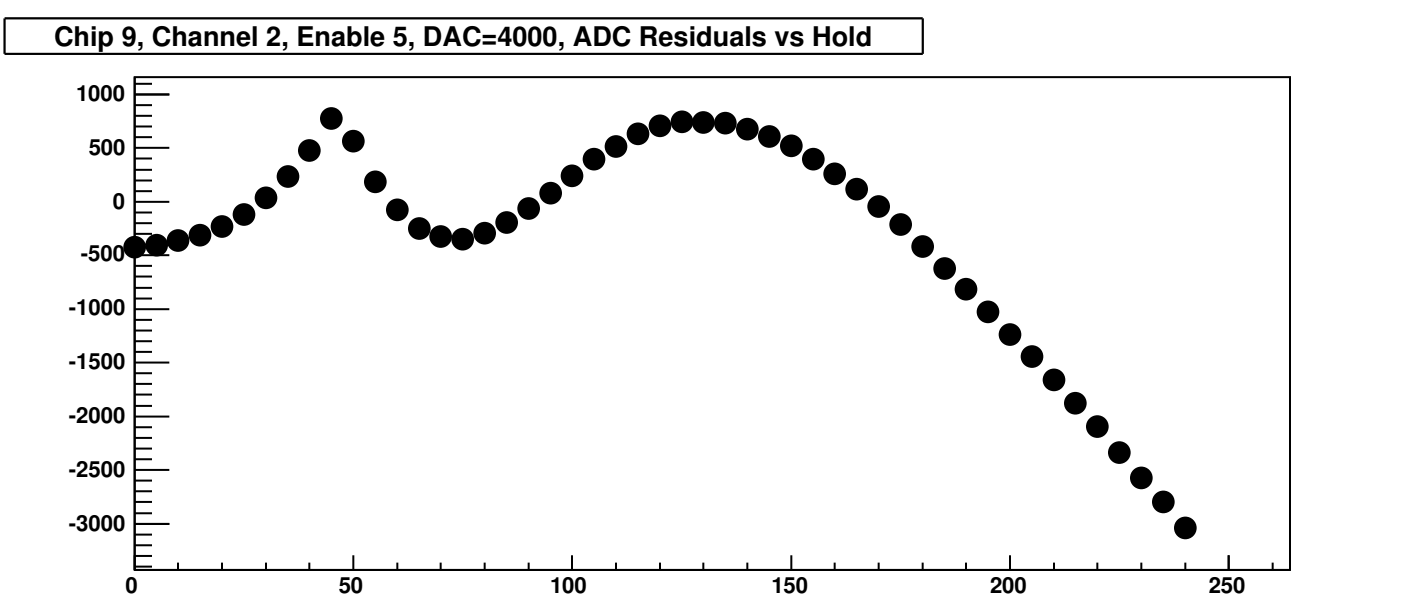

Chip 9, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 220.3 / 41
p0 -331.3 ± 0.5926
p1 87.09 ± 0.6376
p2 -6.58e+07 ± 9.696e+05
p3 3.514e+07 ± 3.767e+05
p4 1.772 ± 0.02073
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
50
100
150
200
250


Chip 9, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

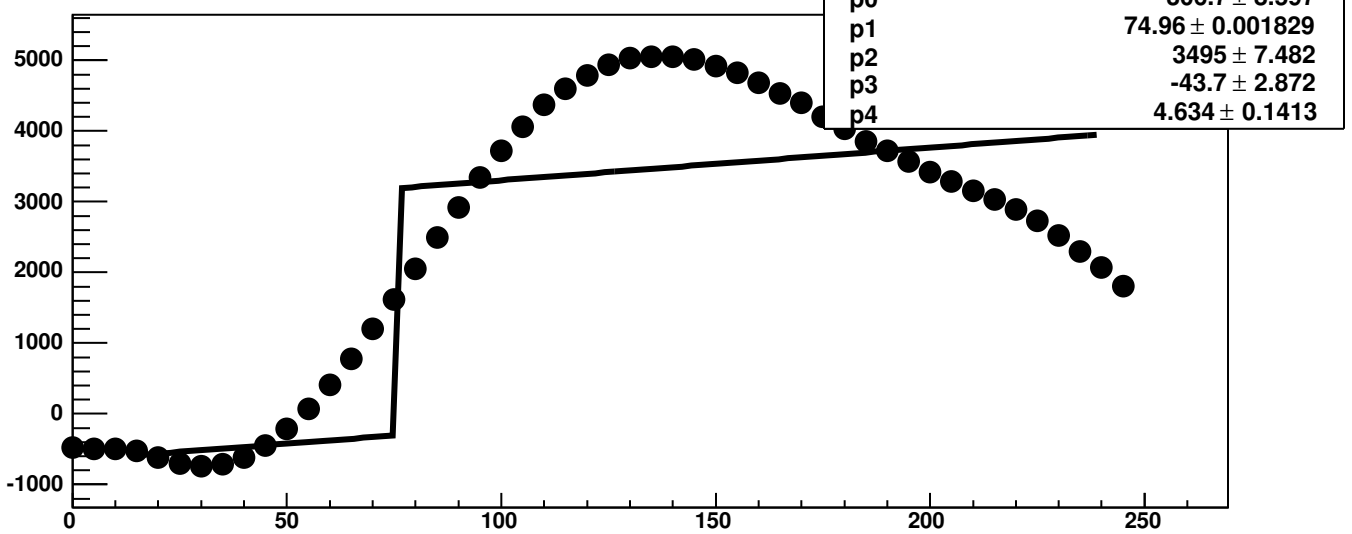

Chip 9, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
0
50
100
150
200
250


Chip 9, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.241e+05 / 41
p0 -348.2 ± 1.5
p1 46.77 ± 0.01992
p2 6948 ± 10.69
p3 40.62 ± 0.06876
p4 -6.335 ± 0.06347
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

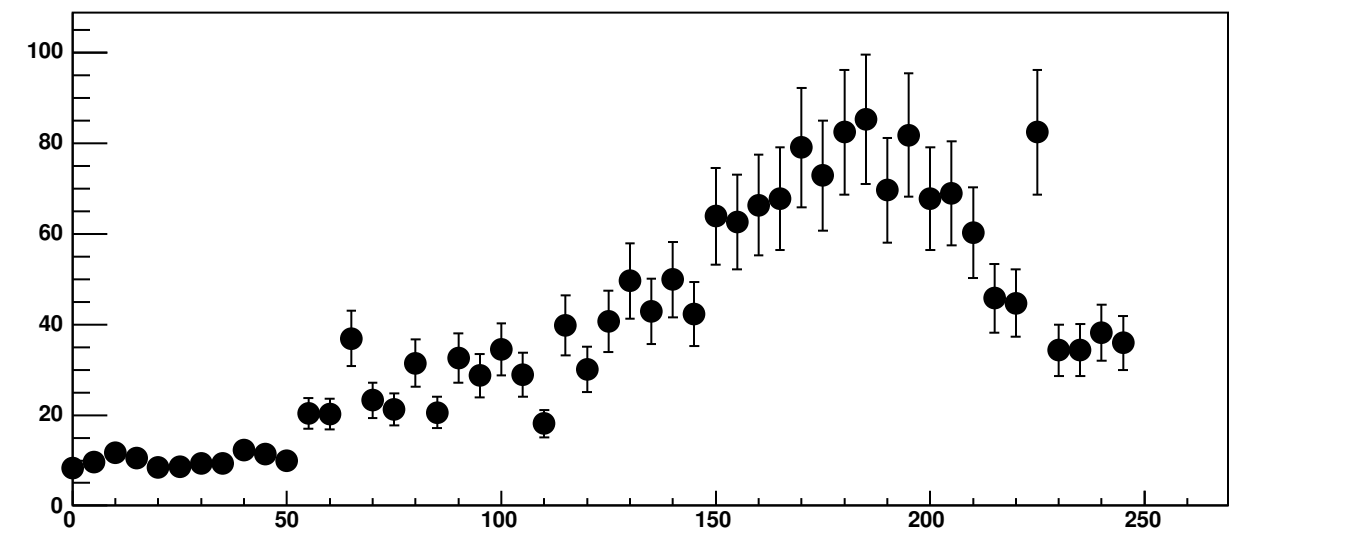

Chip 9, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 9, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250

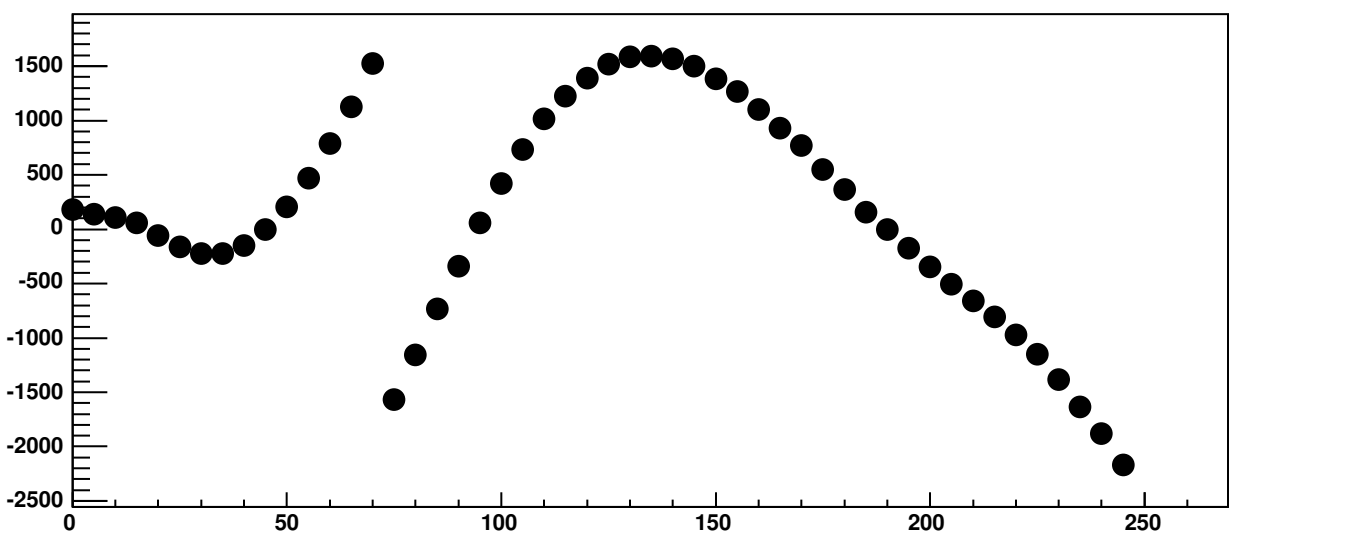
Chip 9, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold

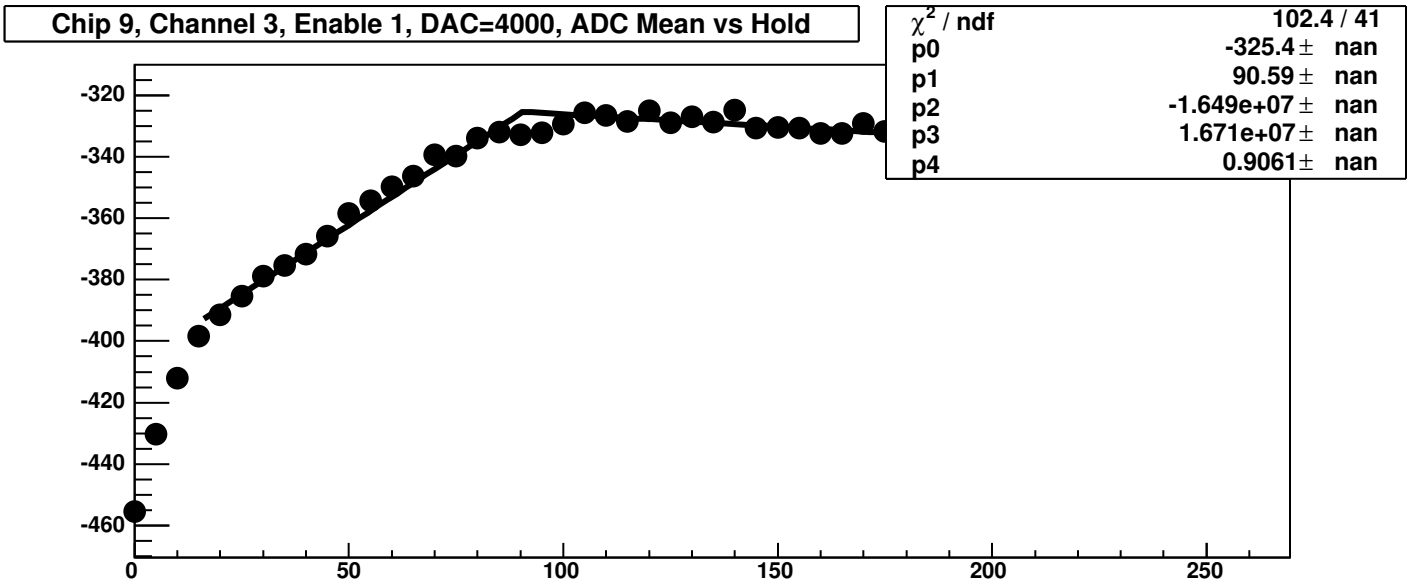


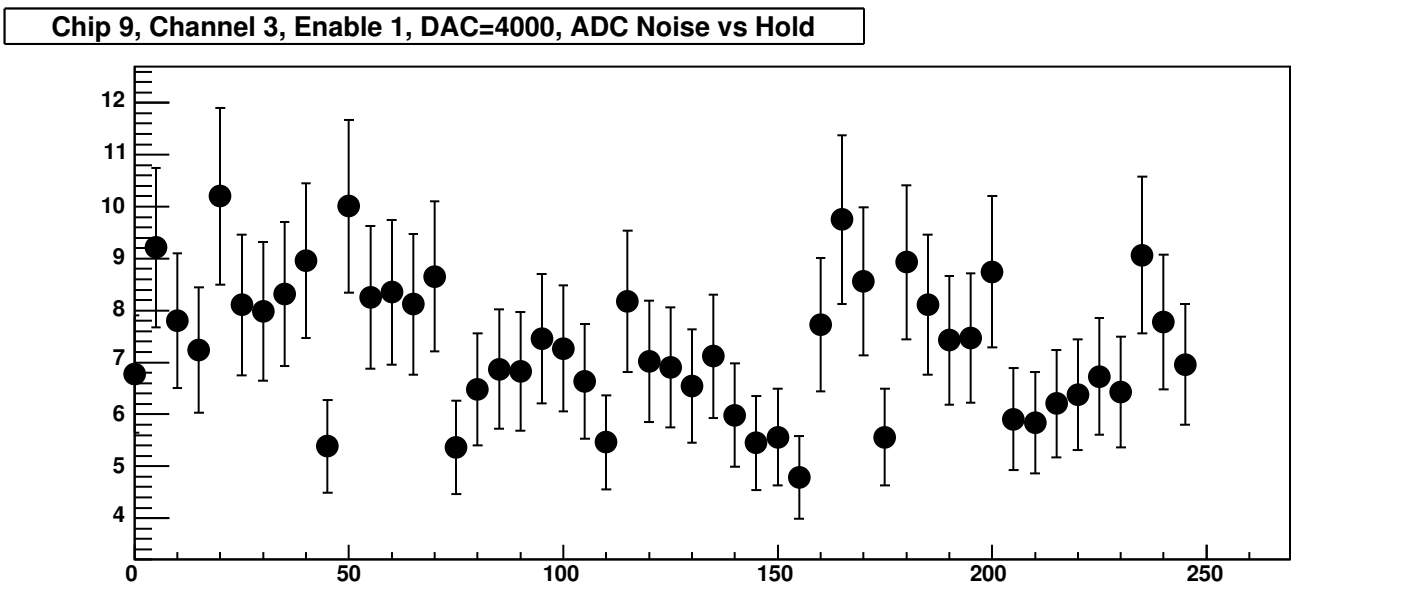
Chip 9, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold

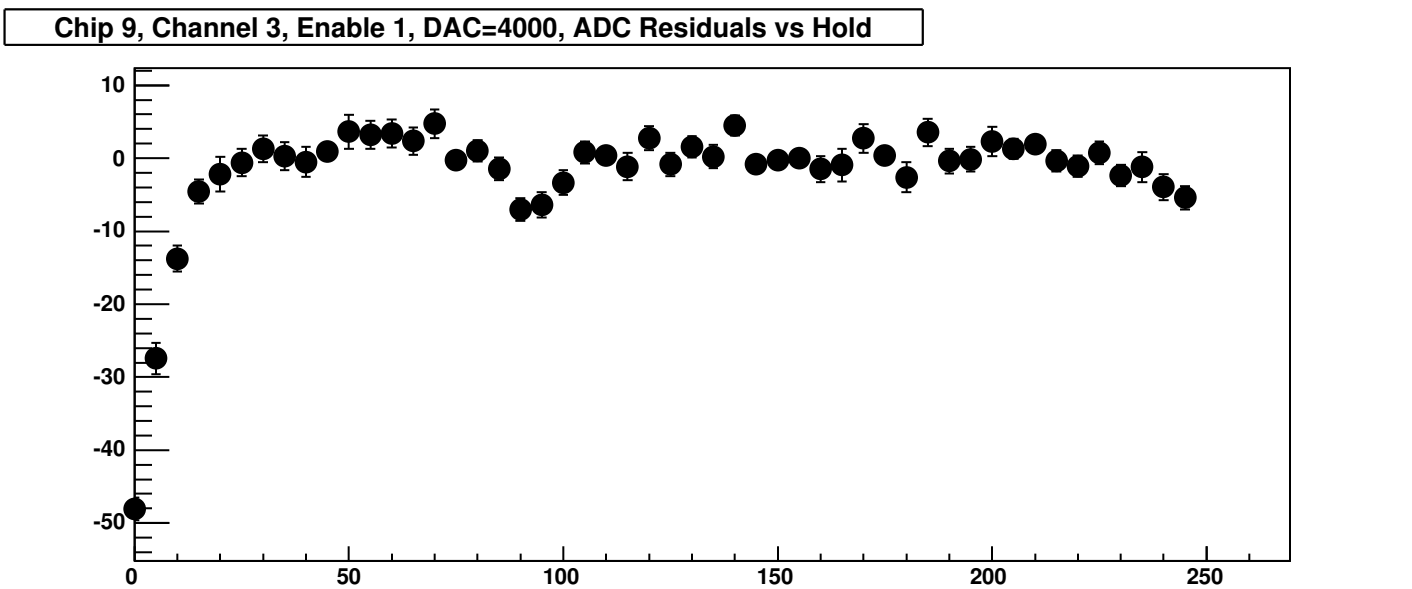


Chip 9, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

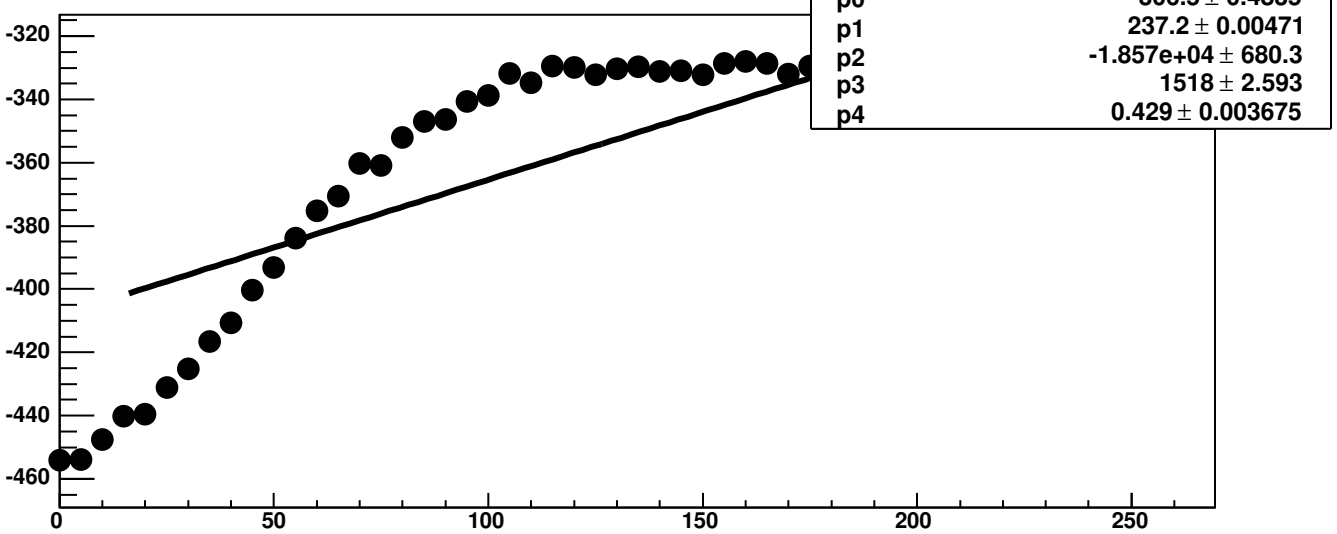



Chip 9, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 102.4 / 41
p0 -325.4 ± nan
p1 90.59 ± nan
p2 -1.649e+07 ± nan
p3 1.671e+07 ± nan
p4 0.9061 ± nan
-320
-340
-360
-380
-400
-420
-440
-460
0
50
100
150
200
250

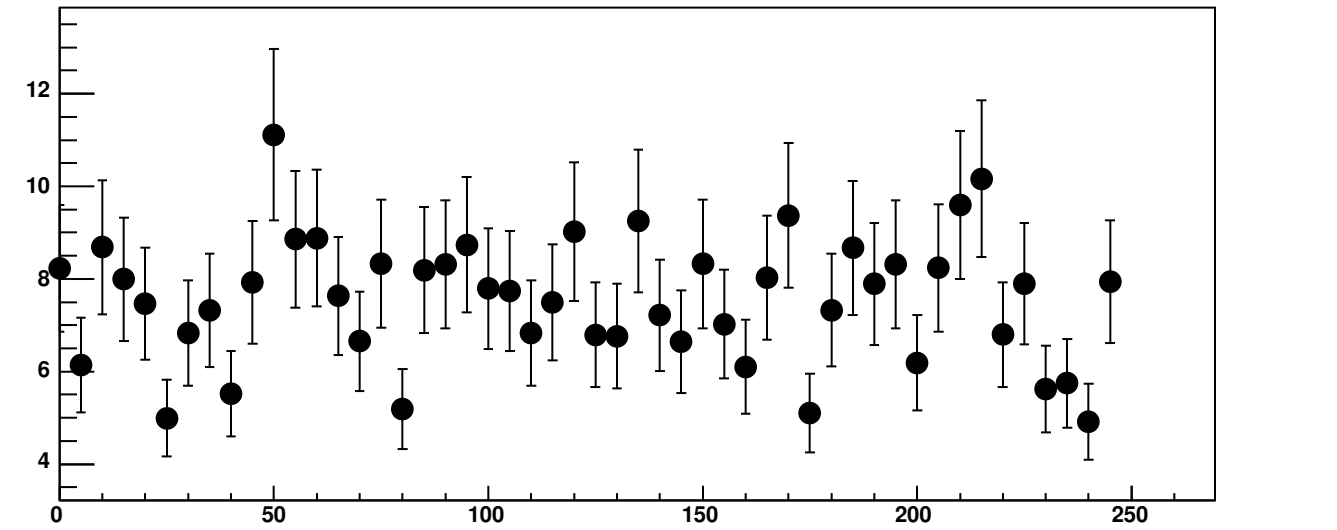

Chip 9, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 9, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold
10
0
-10
-20
-30
-40
-50
0
50
100
150
200
250

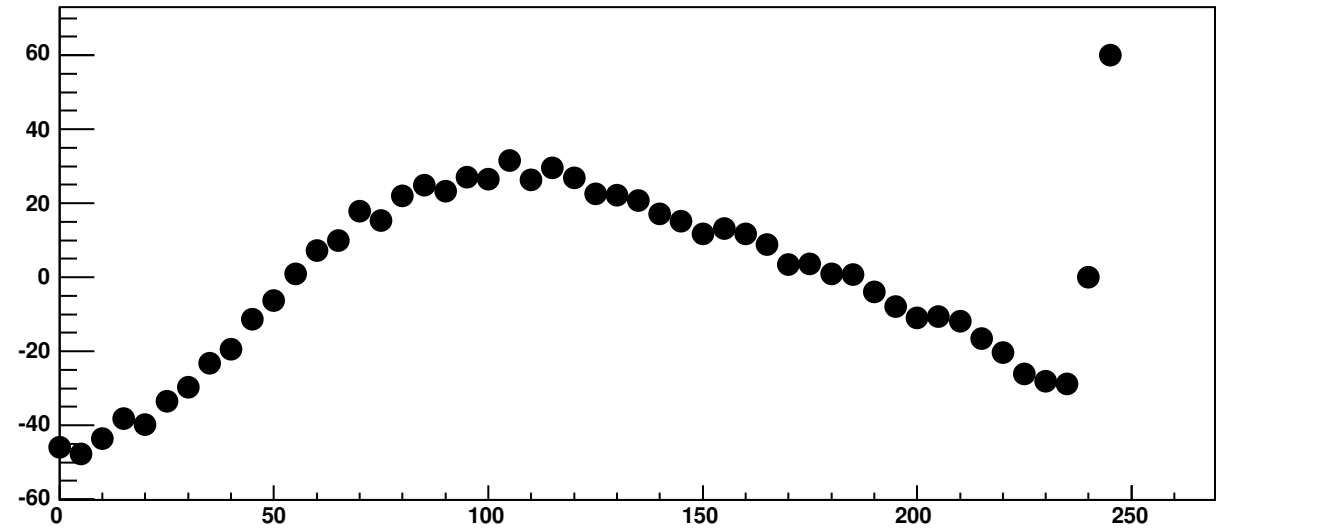
Chip 9, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

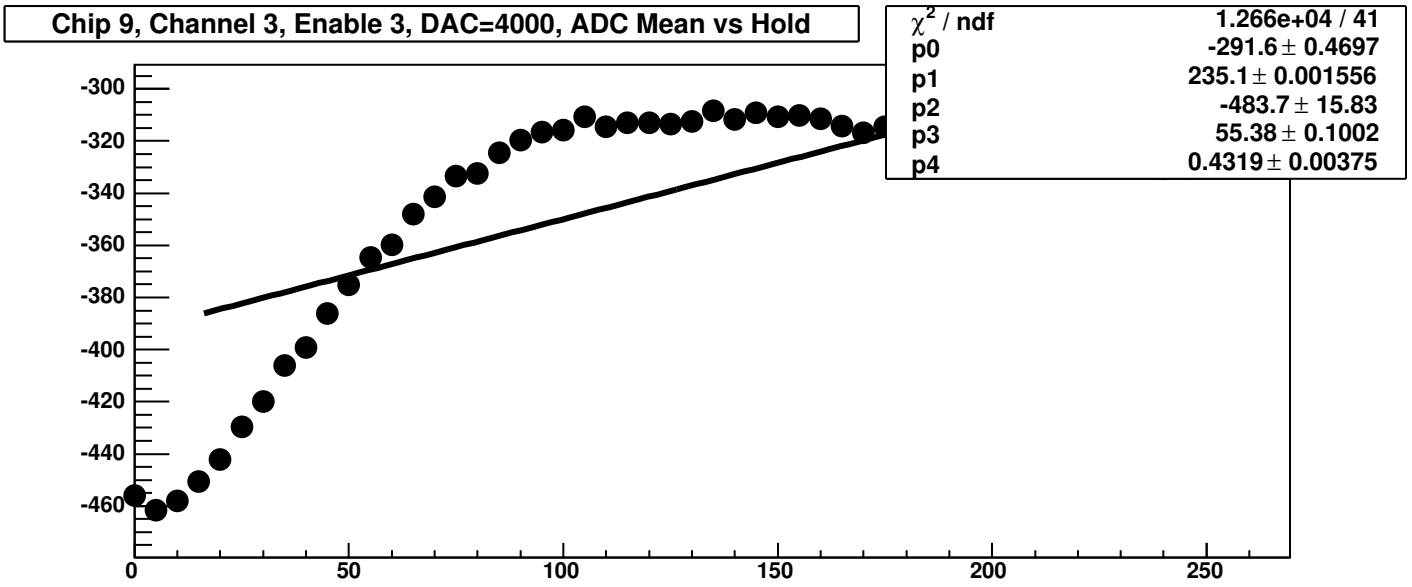


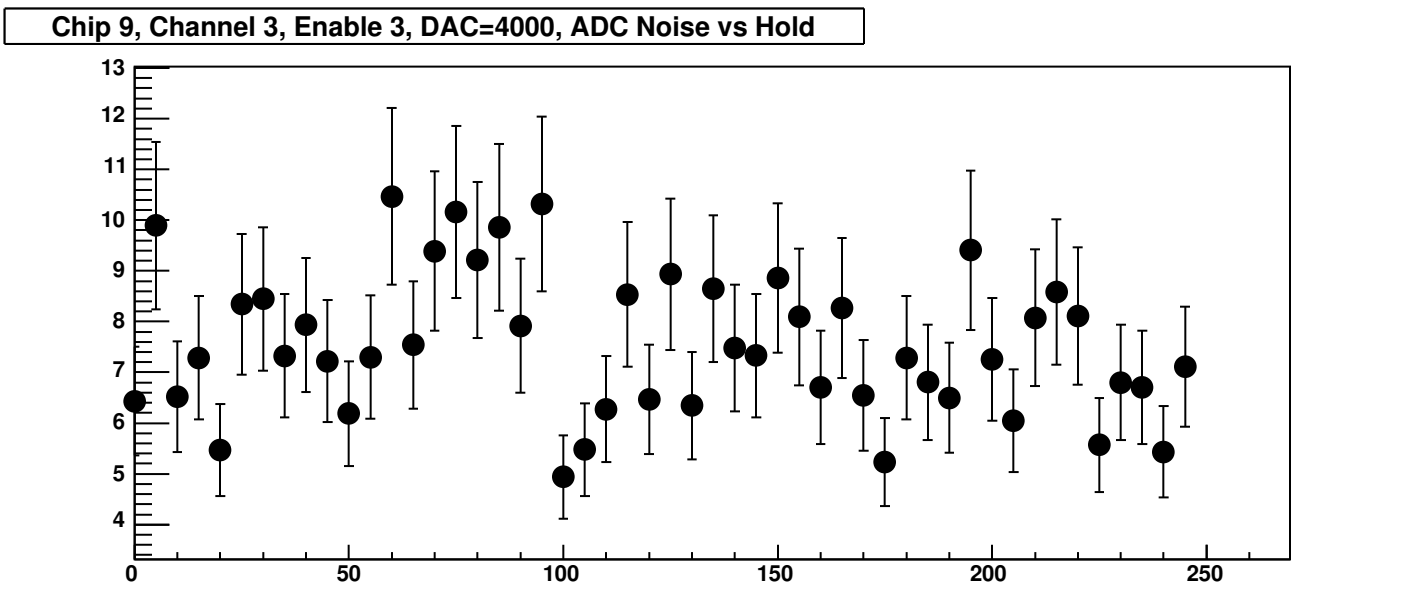
Chip 9, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold

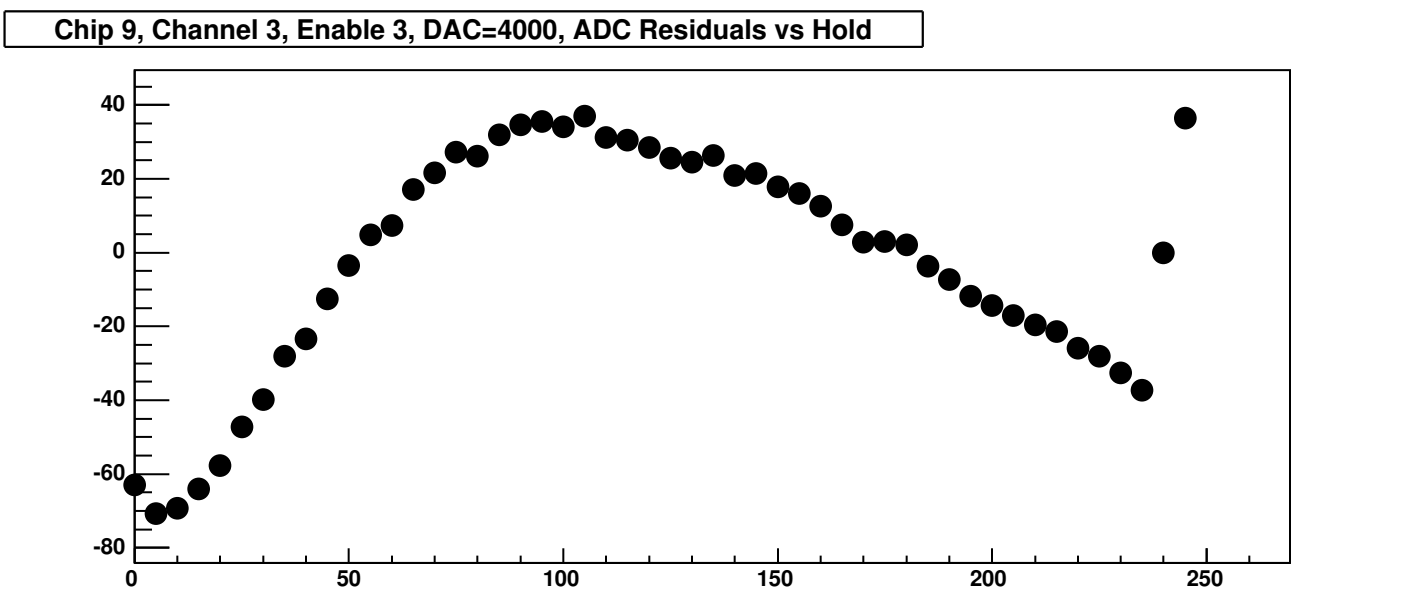


Chip 9, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold



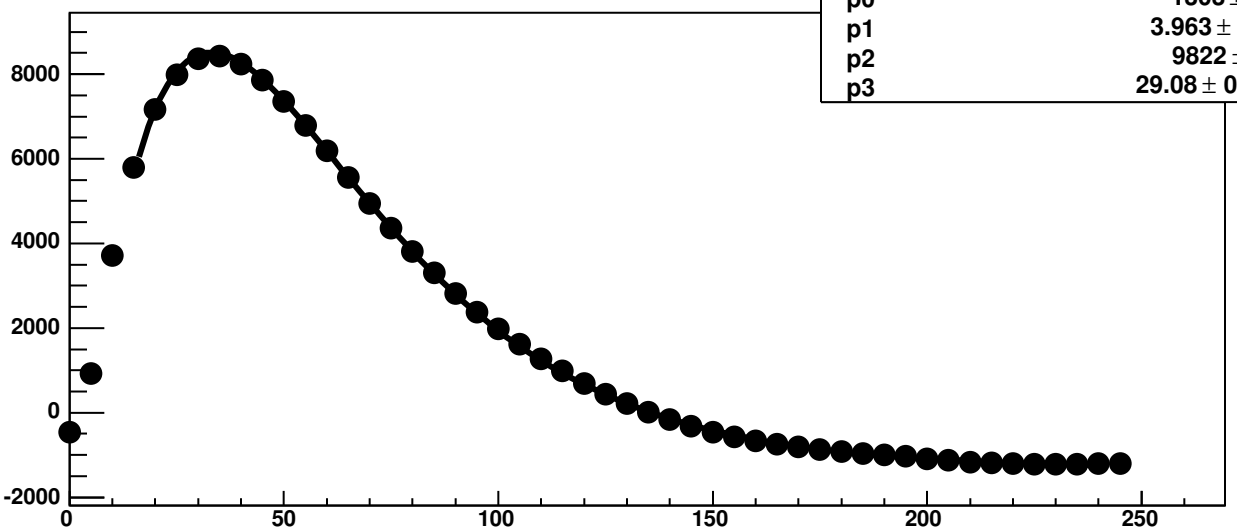

Chip 9, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.266e+04 / 41
p0 -291.6 ± 0.4697
p1 235.1 ± 0.001556
p2 -483.7 ± 15.83
p3 55.38 ± 0.1002
p4 0.4319 ± 0.00375
-300
-320
-340
-360
-380
-400
-420
-440
-460
0
50
100
150
200
250


Chip 9, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

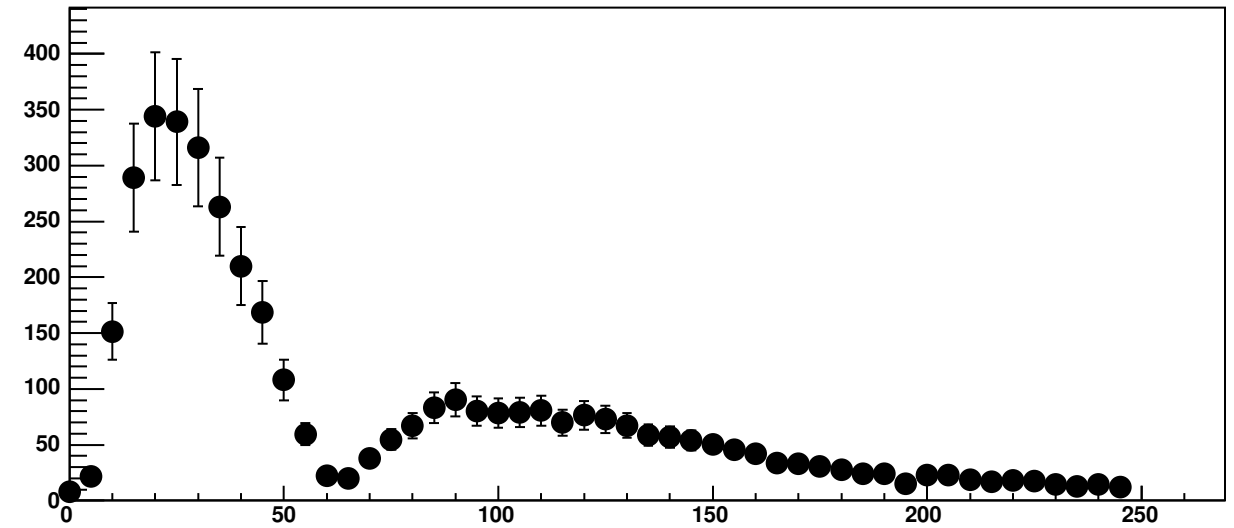

Chip 9, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
0
50
100
150
200
250

Chip 9, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold

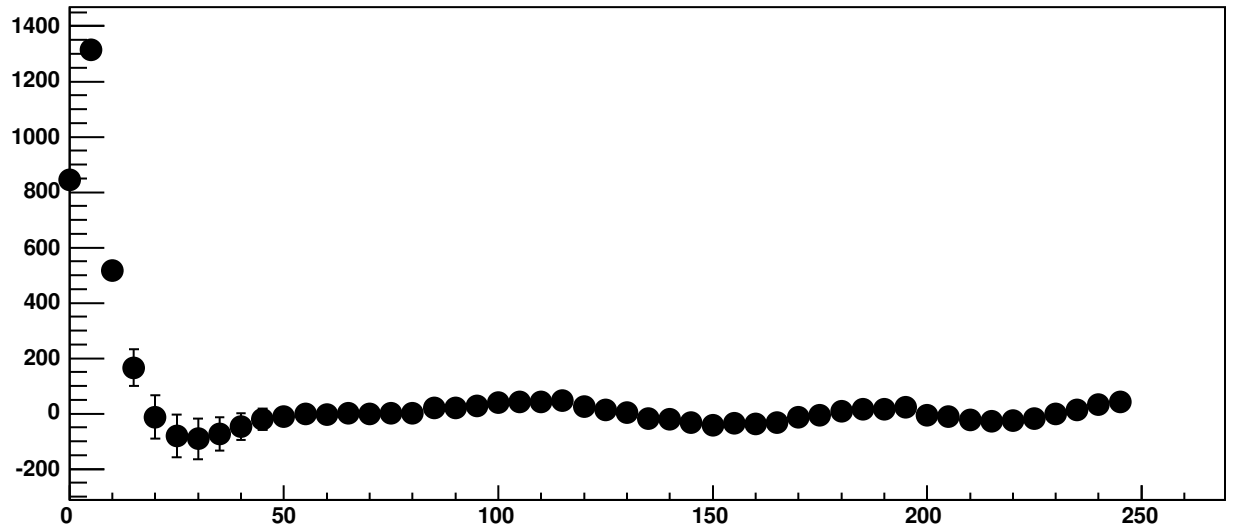
χ^2 / ndf	427.6 / 42
p0	-1303 ± 1.626
p1	3.963 ± 0.1176
p2	9822 ± 14.41
p3	29.08 ± 0.04115

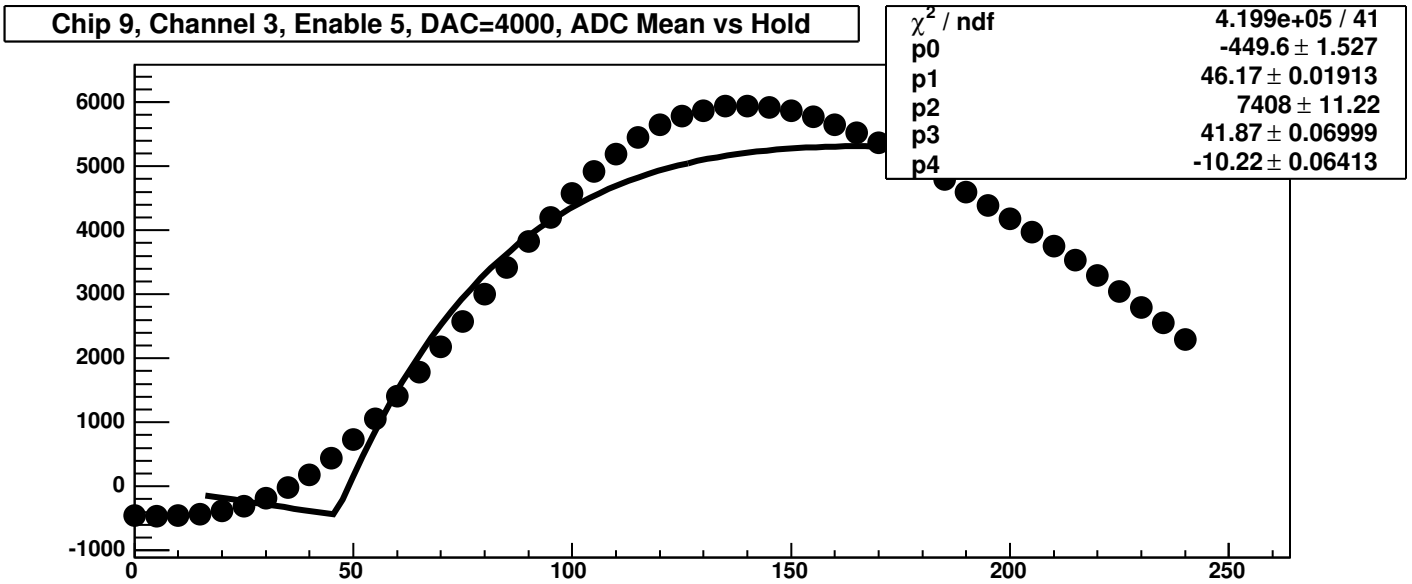


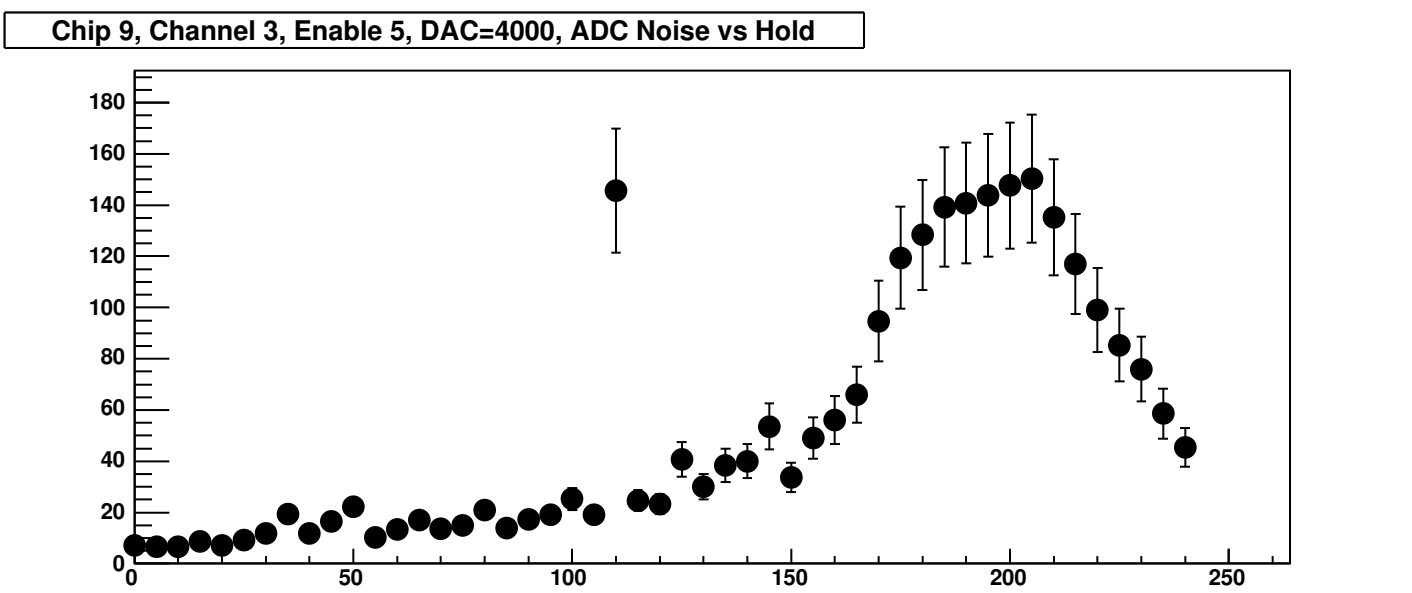
Chip 9, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold

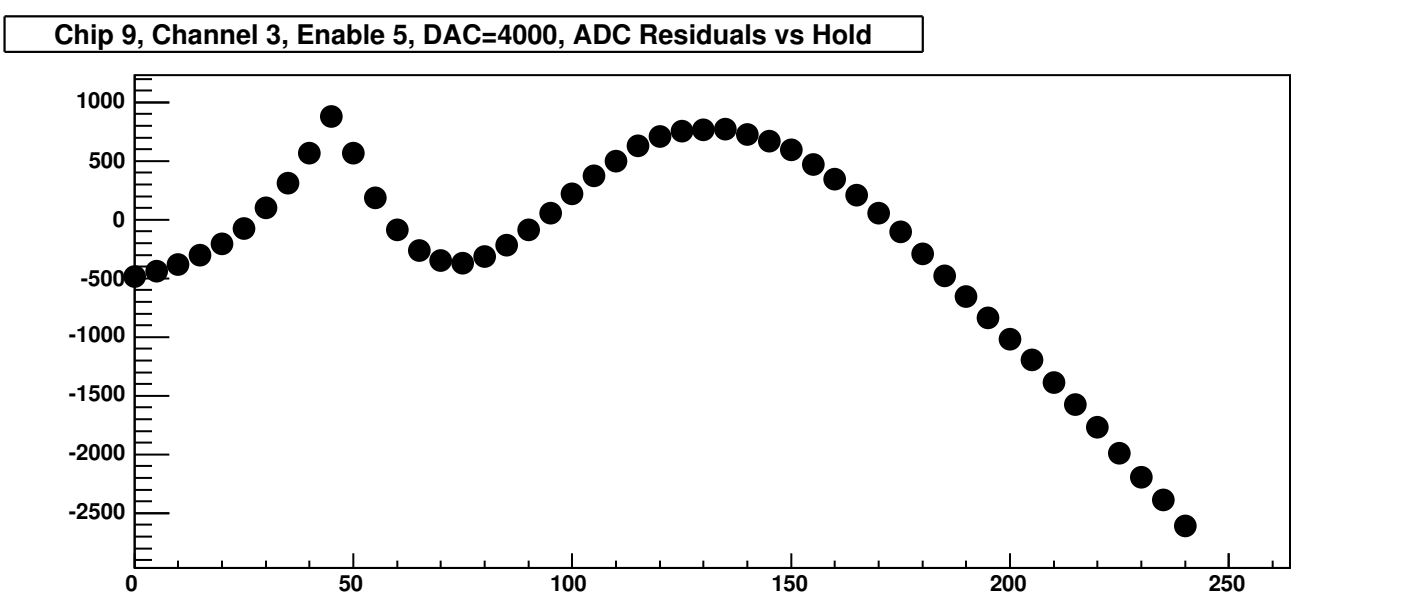


Chip 9, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold

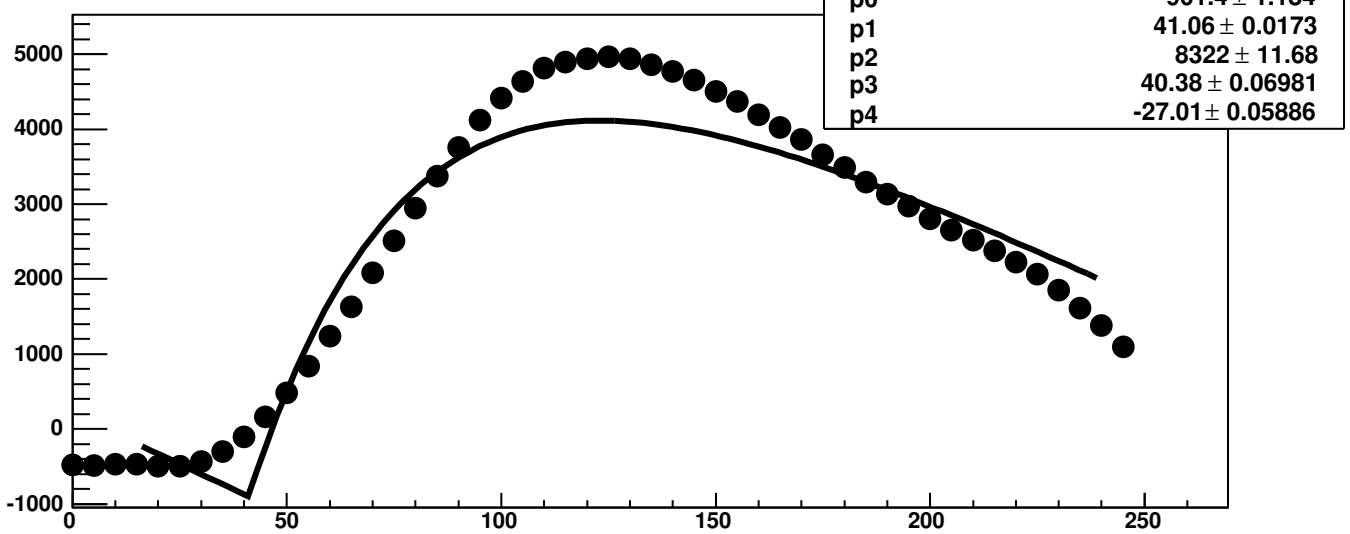



Chip 9, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.199e+05 / 41
p0 -449.6 ± 1.527
p1 46.17 ± 0.01913
p2 7408 ± 11.22
p3 41.87 ± 0.06999
p4 -10.22 ± 0.06413
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

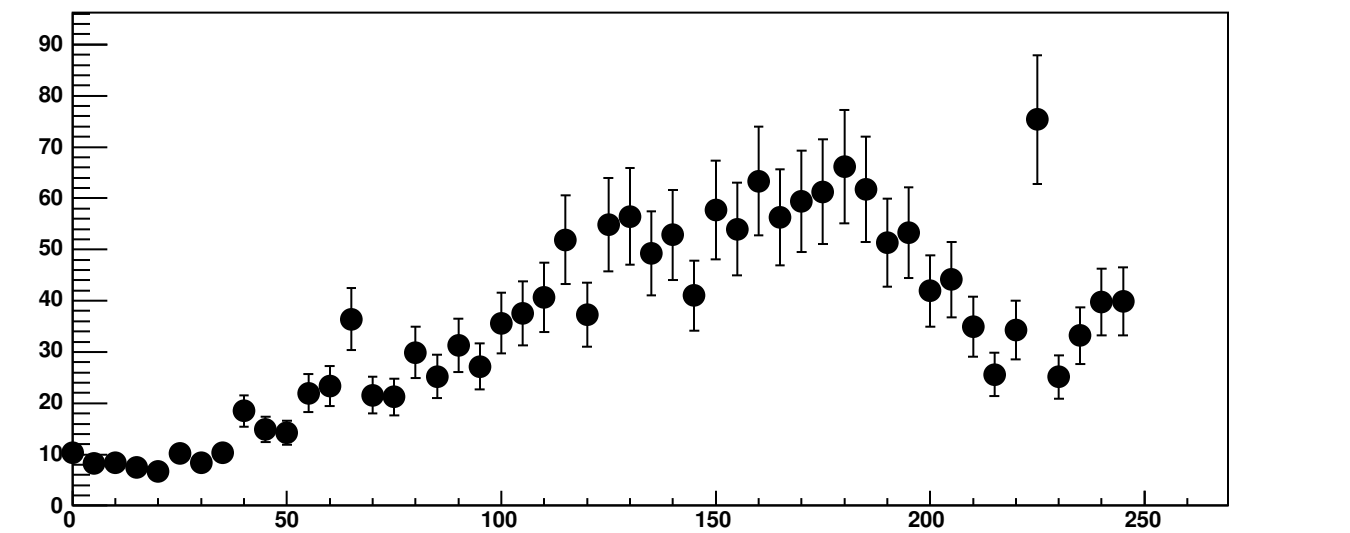

Chip 9, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 9, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
0
50
100
150
200
250

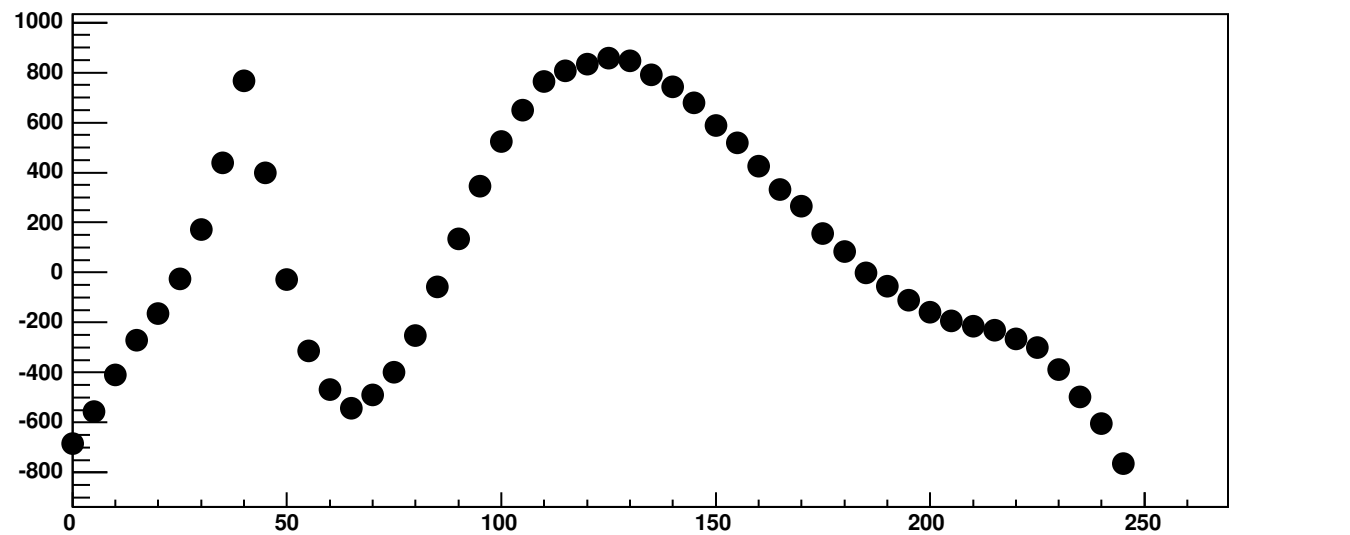
Chip 9, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold



Chip 9, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold

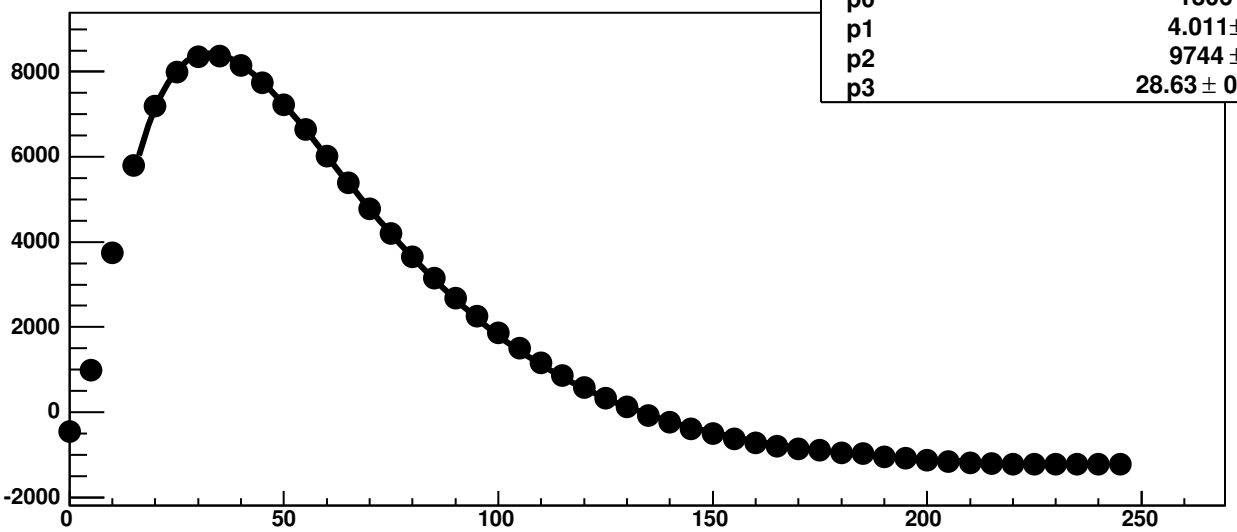


Chip 9, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

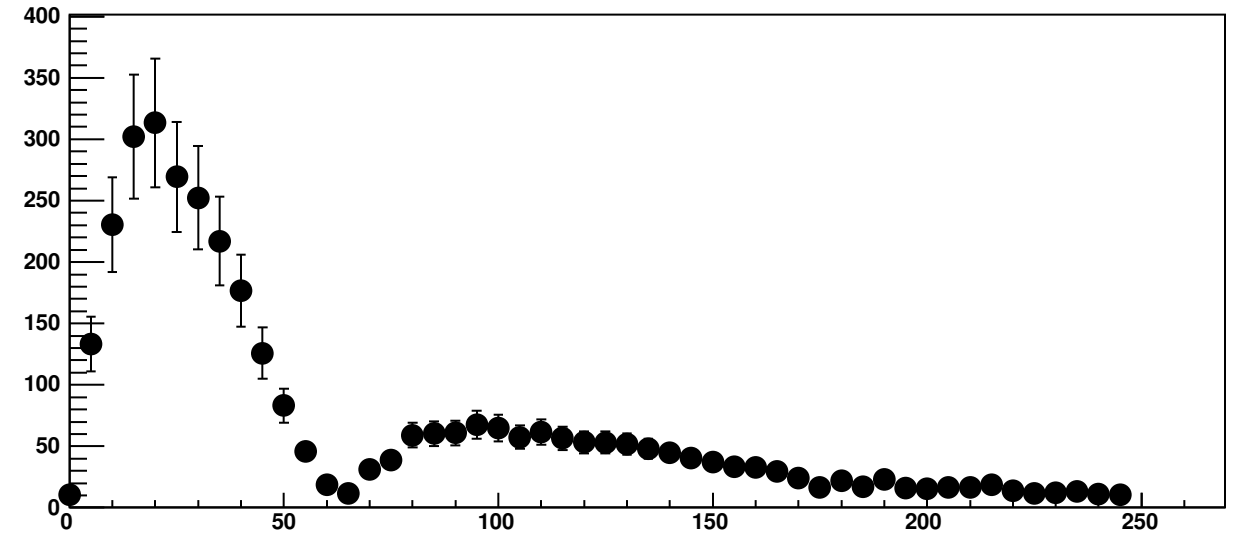


Chip 9, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold

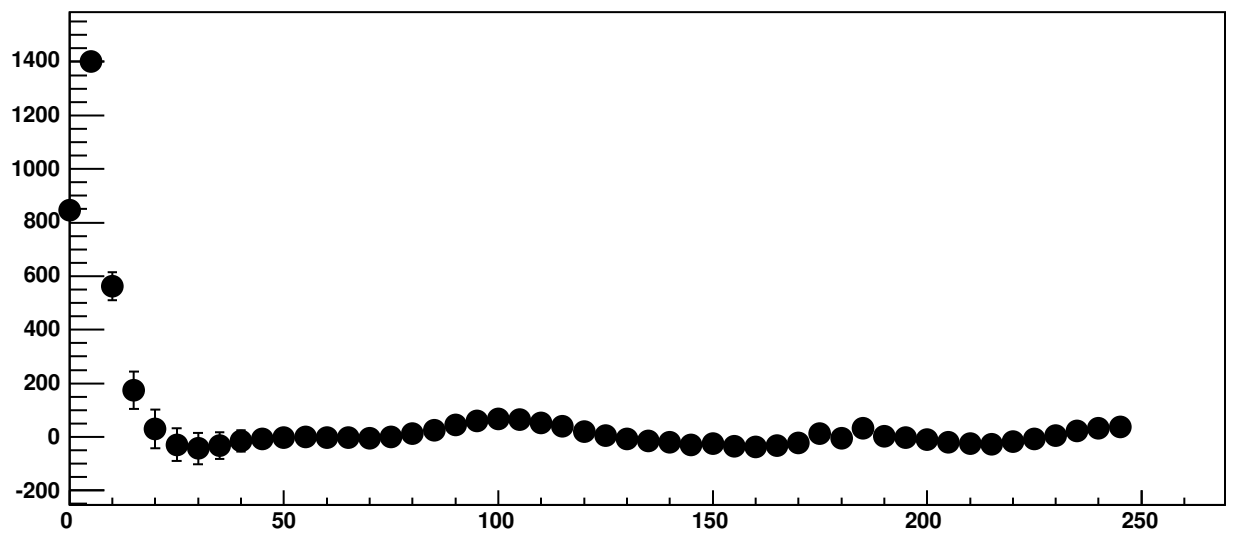
χ^2 / ndf	671.3 / 42
p0	-1306 ± 1.31
p1	4.011 ± 0.109
p2	9744 ± 13.37
p3	28.63 ± 0.03378

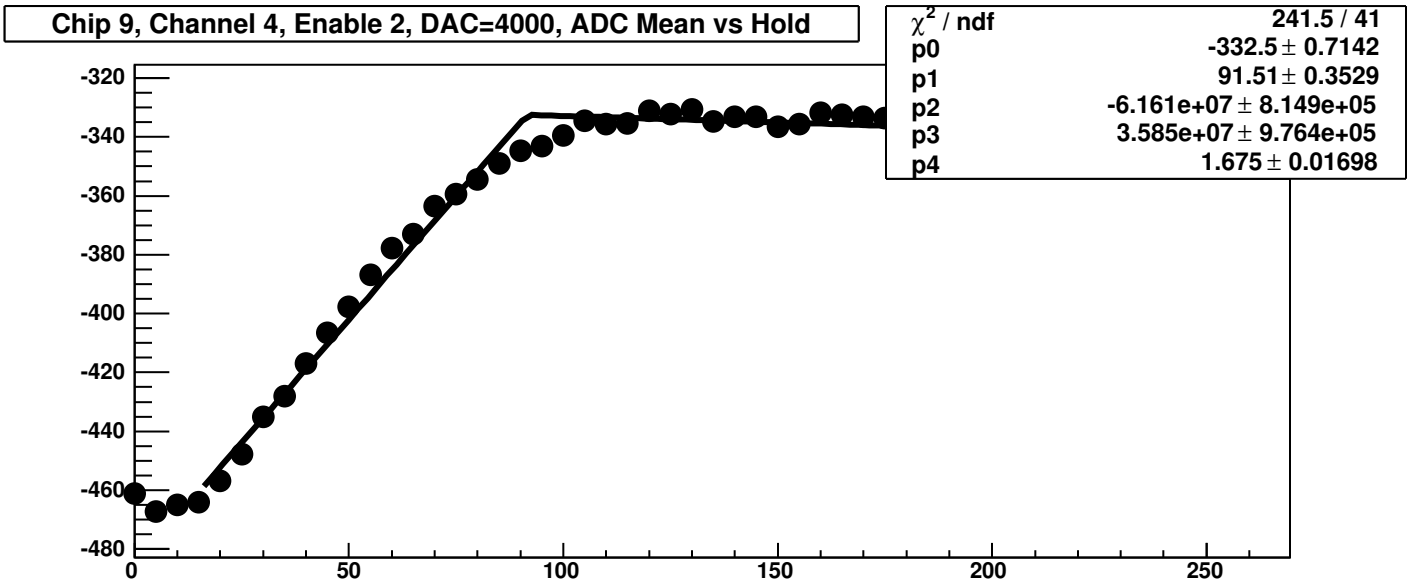


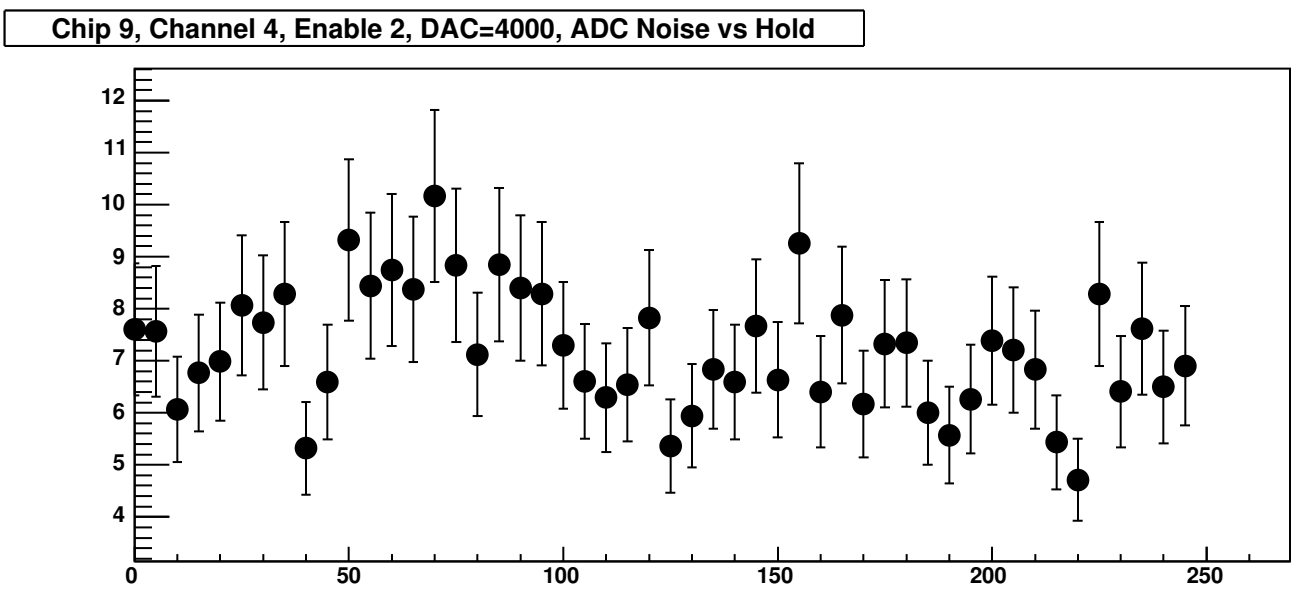
Chip 9, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold

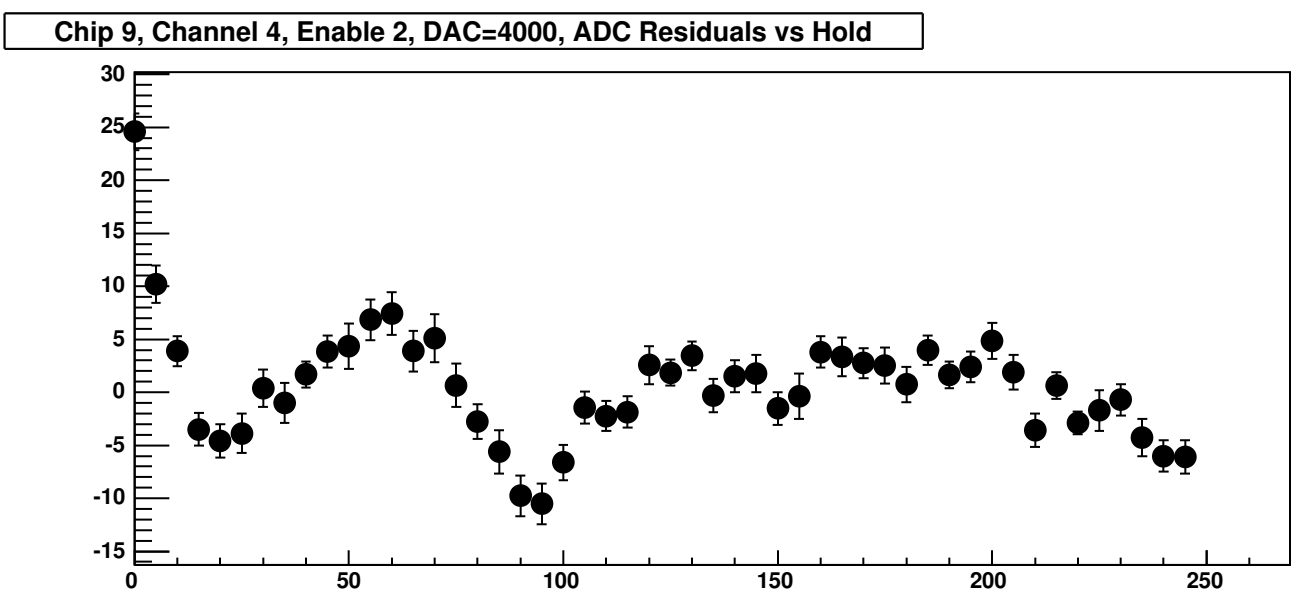


Chip 9, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold

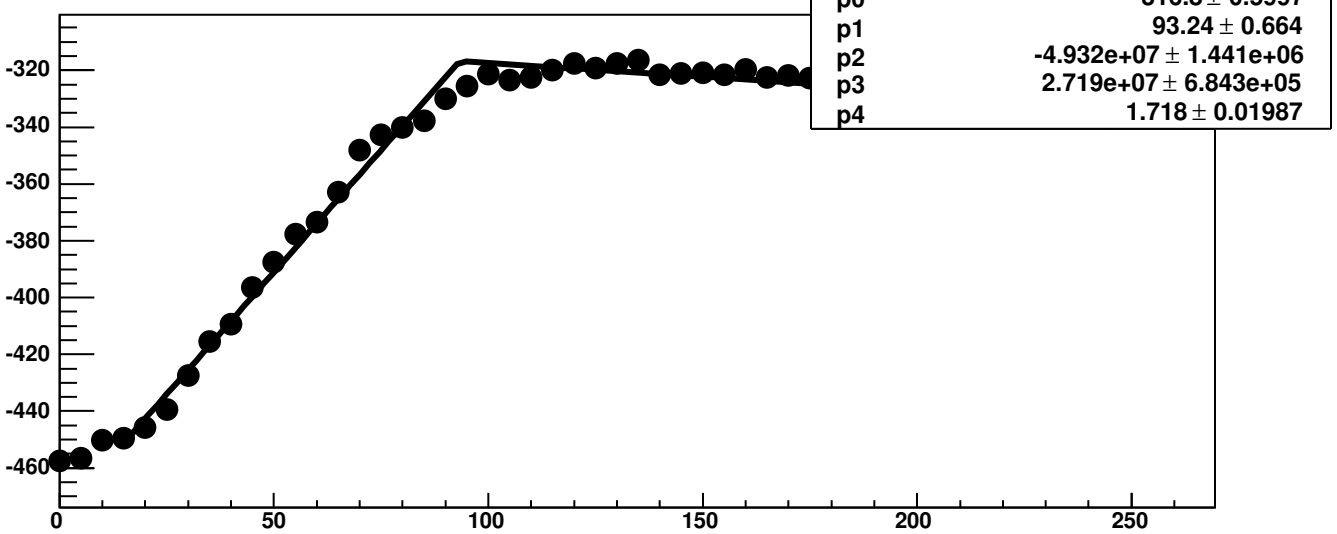



Chip 9, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 241.5 / 41
p0 -332.5 ± 0.7142
p1 91.51 ± 0.3529
p2 -6.161e+07 ± 8.149e+05
p3 3.585e+07 ± 9.764e+05
p4 1.675 ± 0.01698
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
50
100
150
200
250

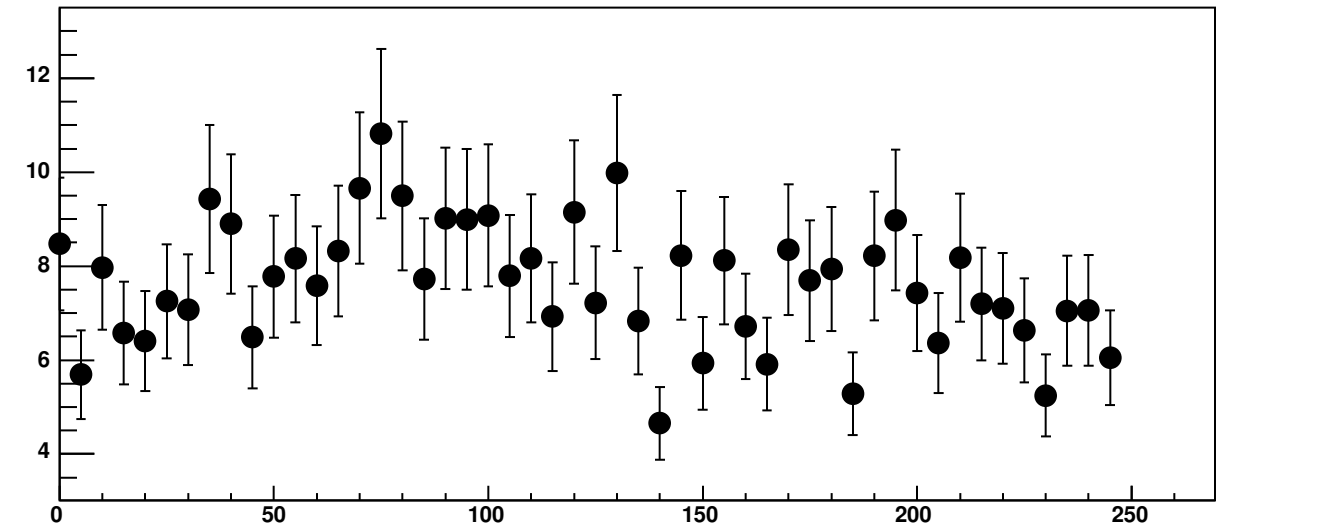

Chip 9, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 9, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

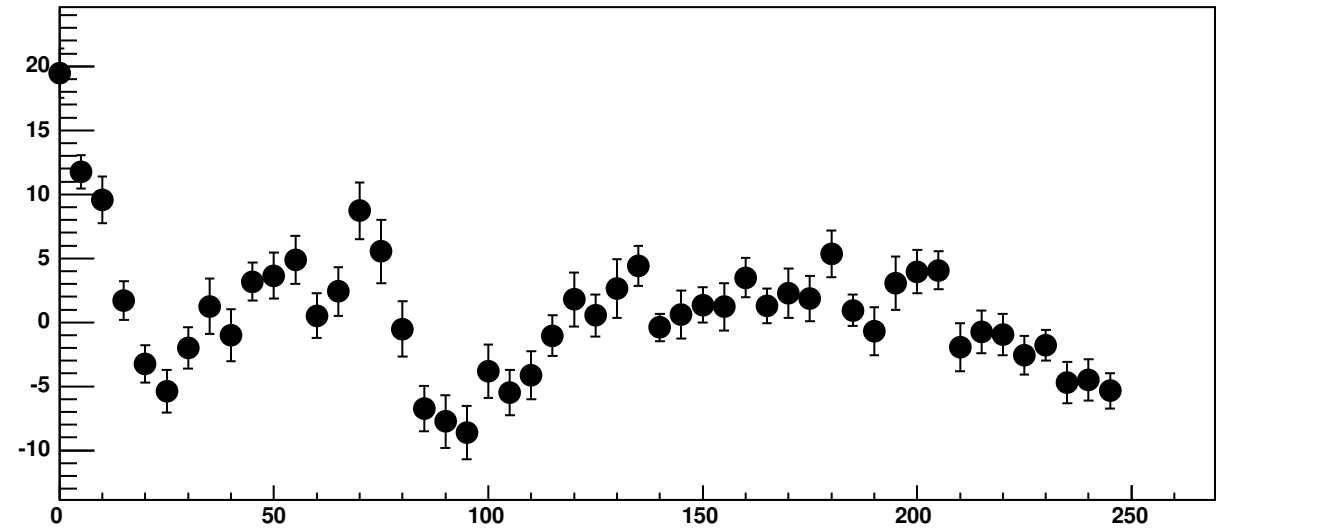
Chip 9, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold



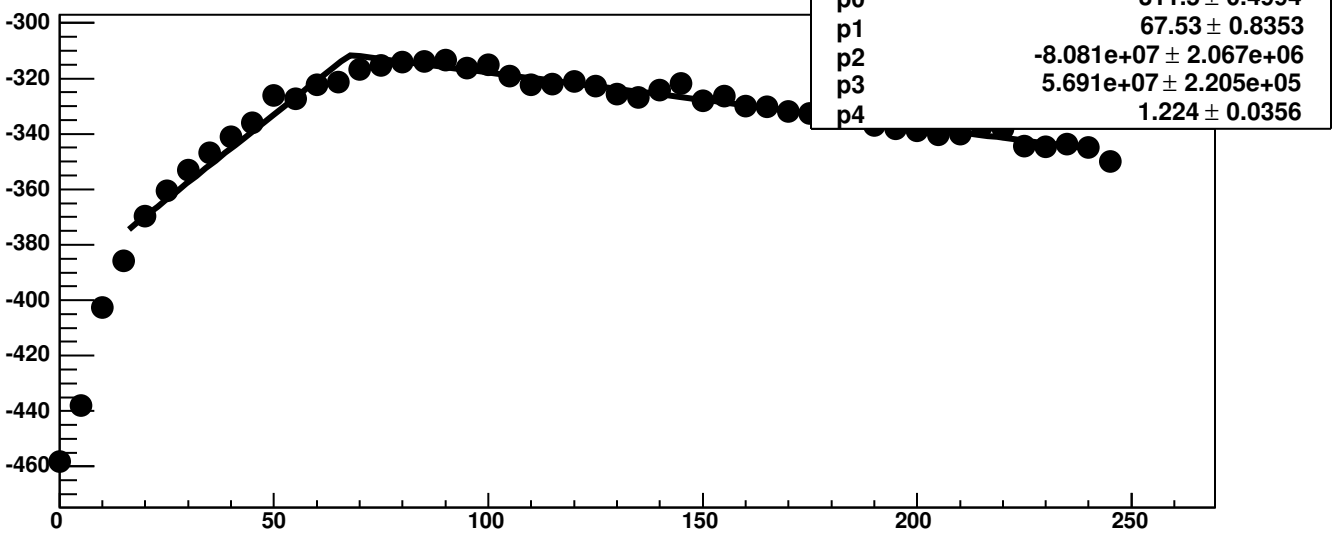
Chip 9, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



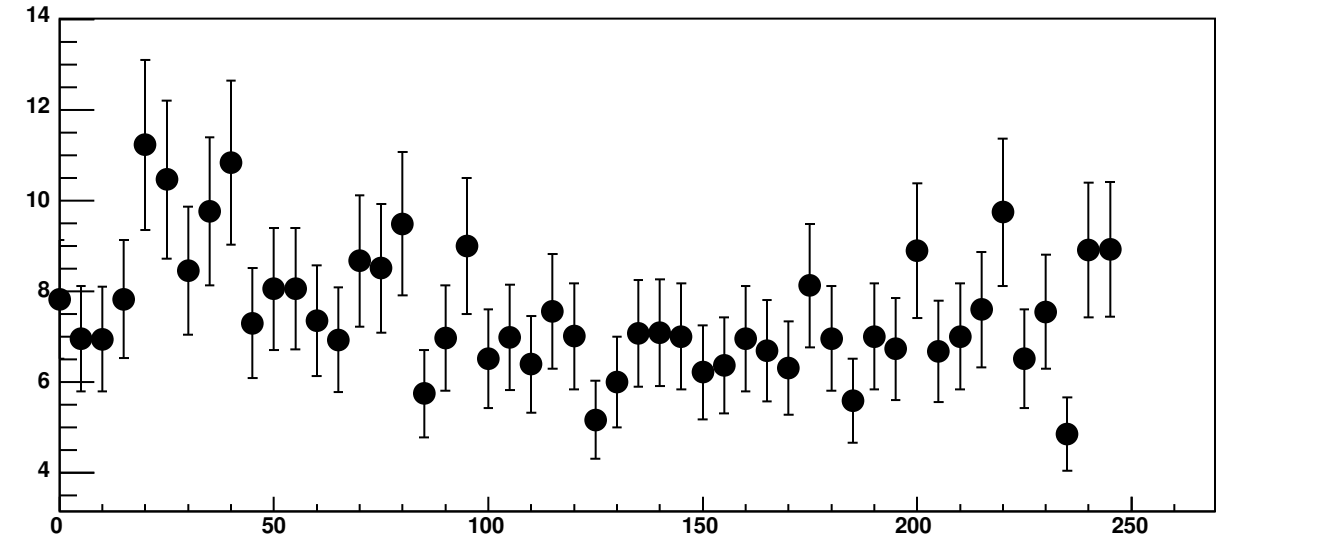
Chip 9, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold



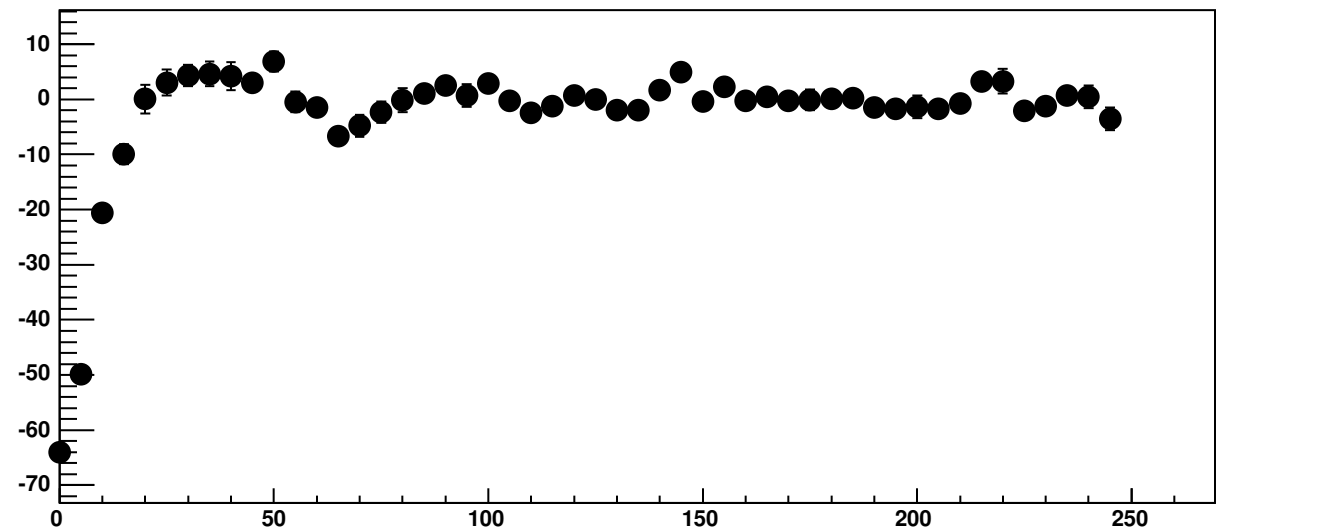
Chip 9, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold

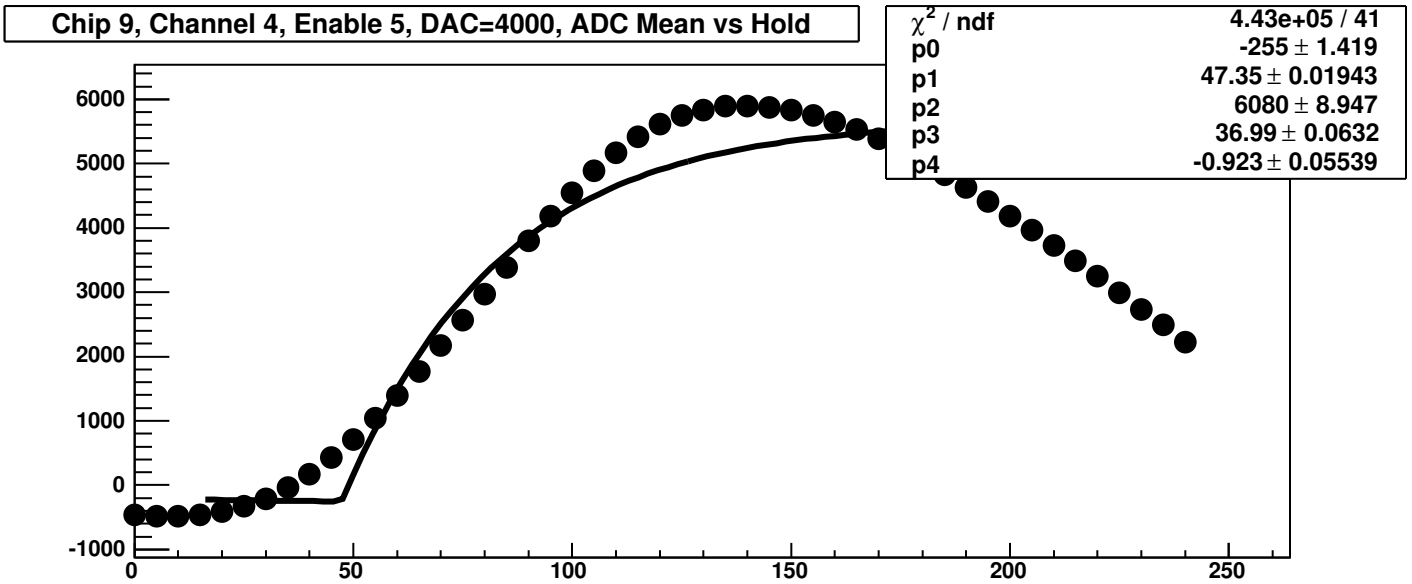


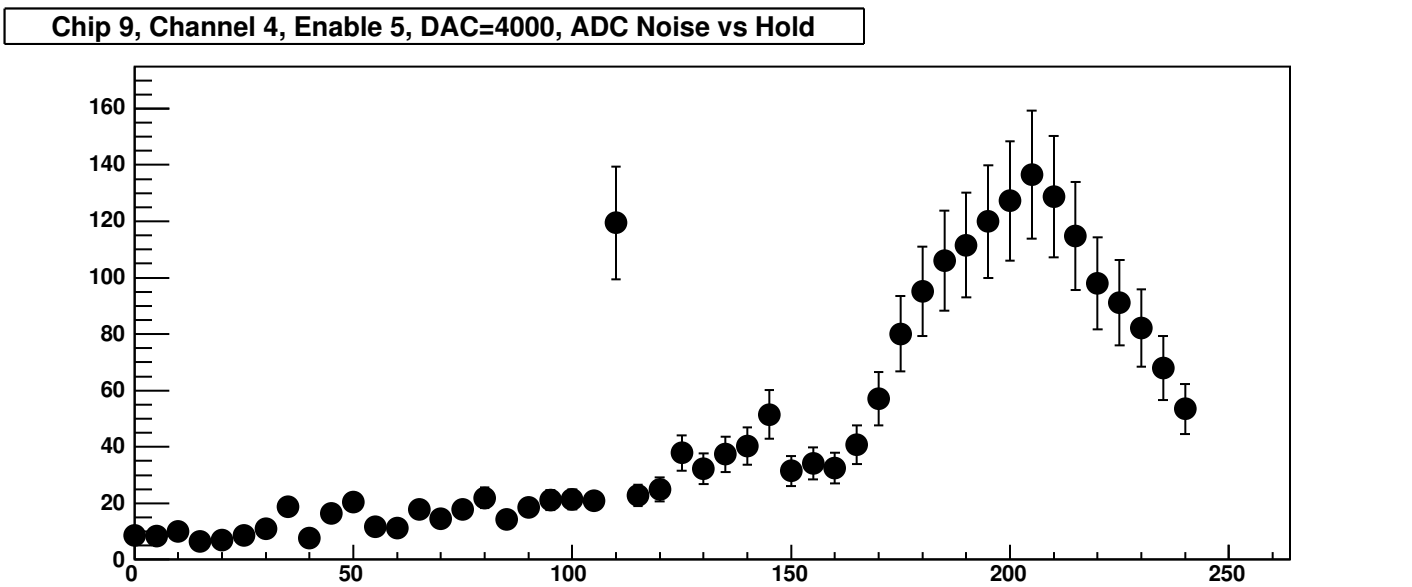
Chip 9, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold

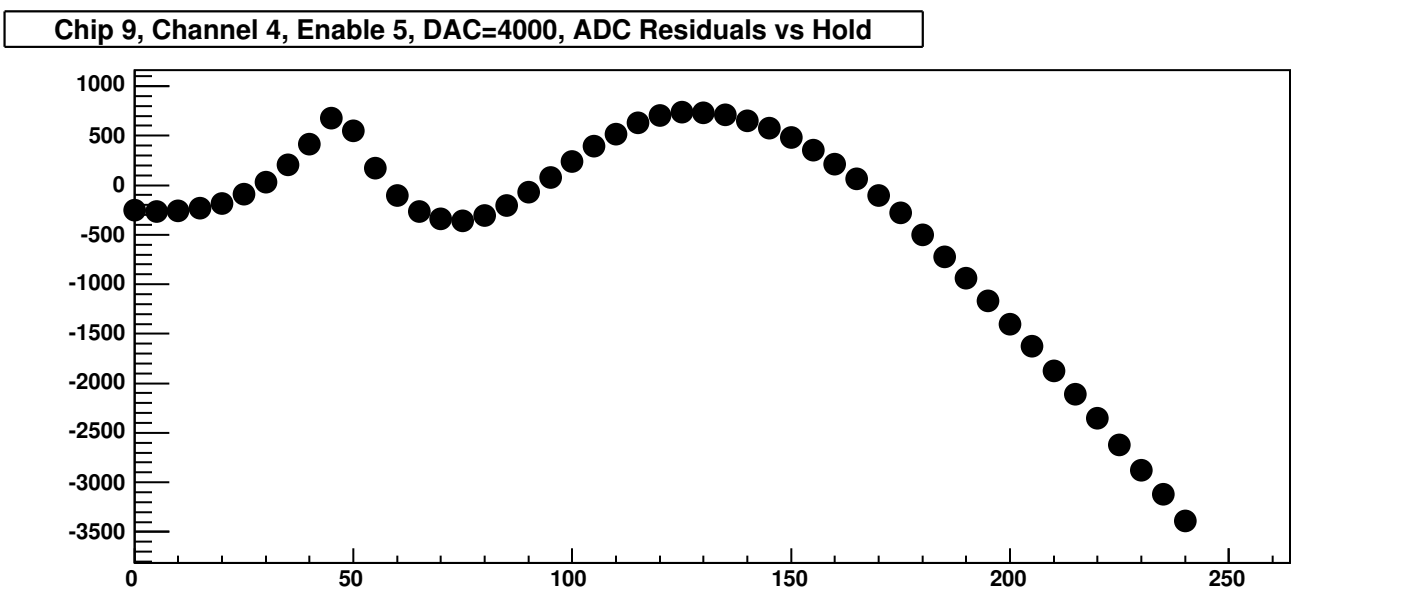


Chip 9, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold

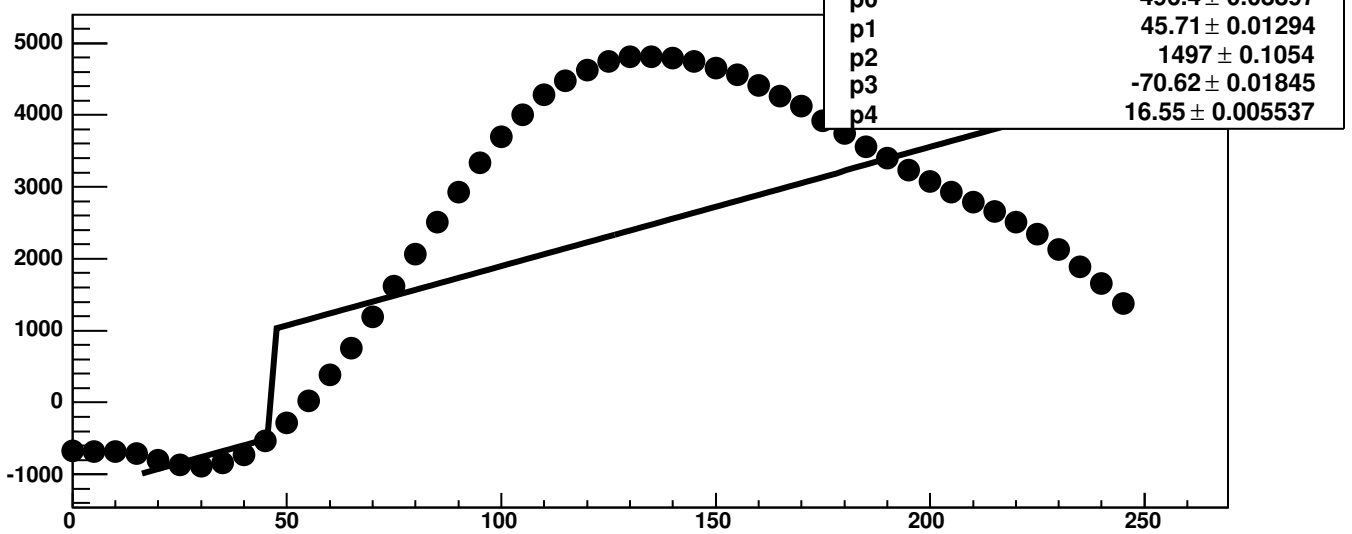



Chip 9, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.43e+05 / 41
p0 -255 ± 1.419
p1 47.35 ± 0.01943
p2 6080 ± 8.947
p3 36.99 ± 0.0632
p4 -0.923 ± 0.05539
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

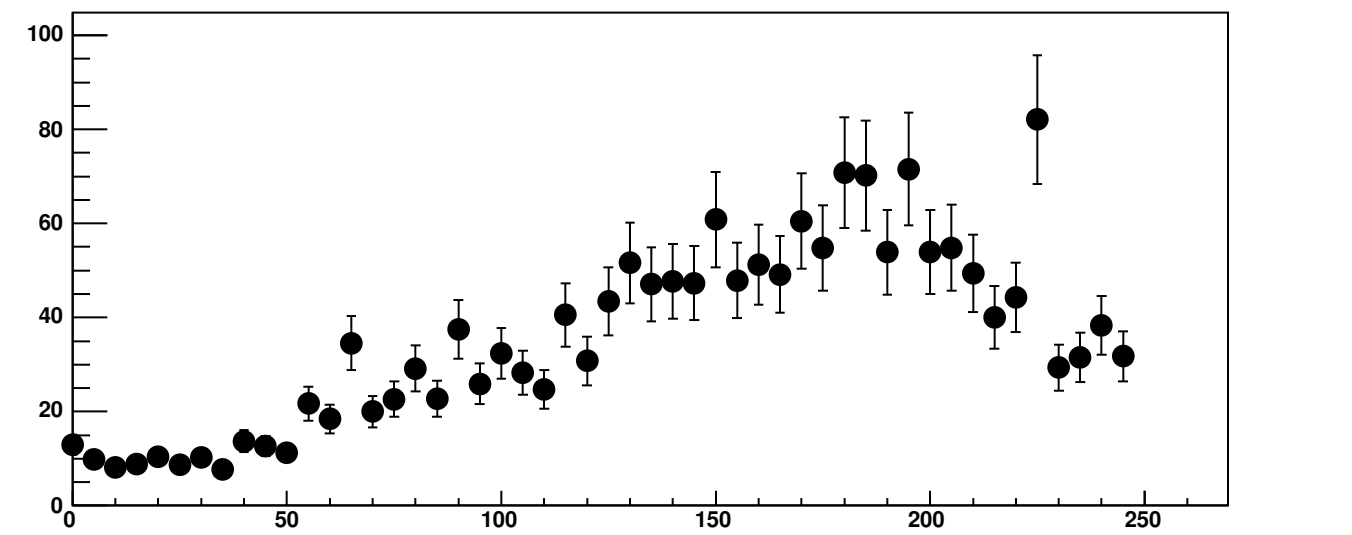

Chip 9, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 9, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
0
50
100
150
200
250

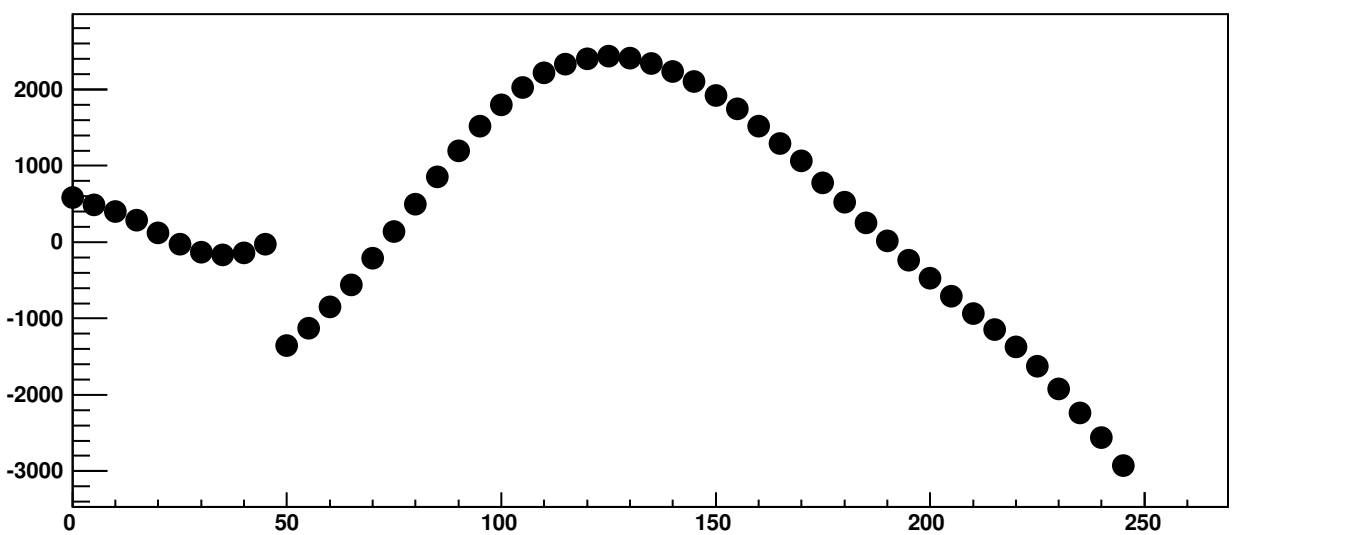
Chip 9, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold



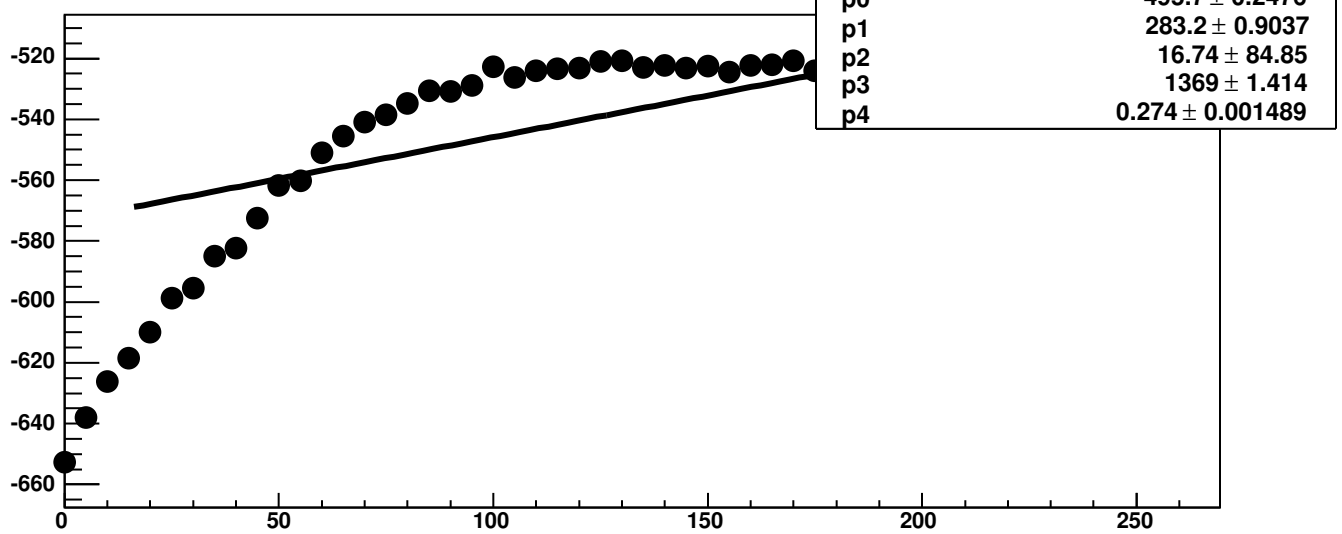
Chip 9, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



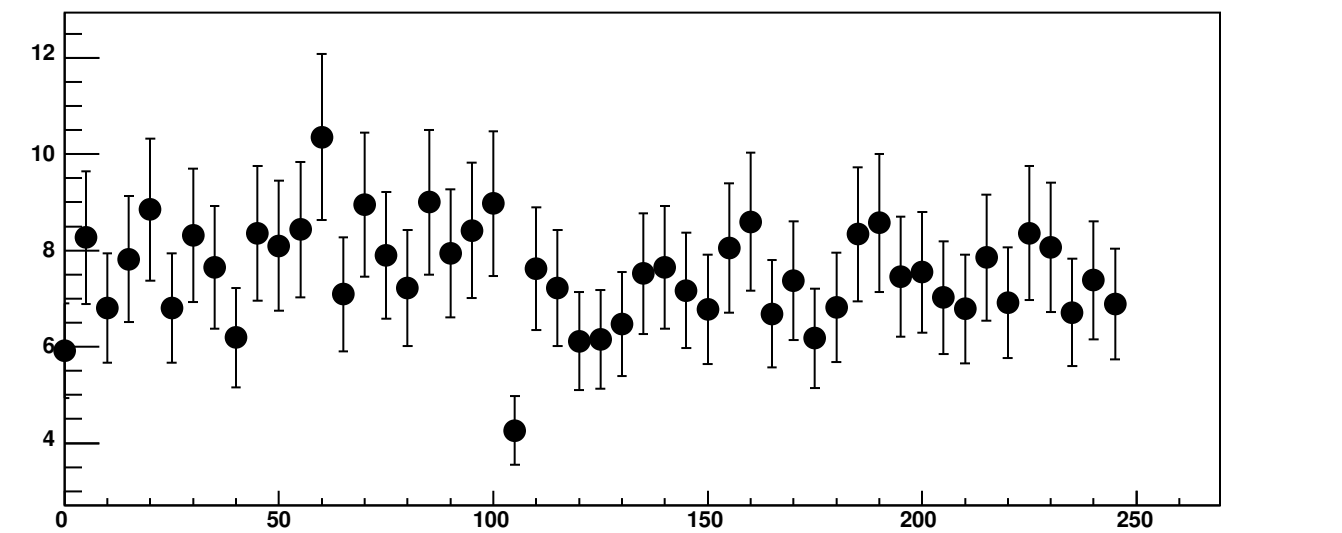
Chip 9, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold



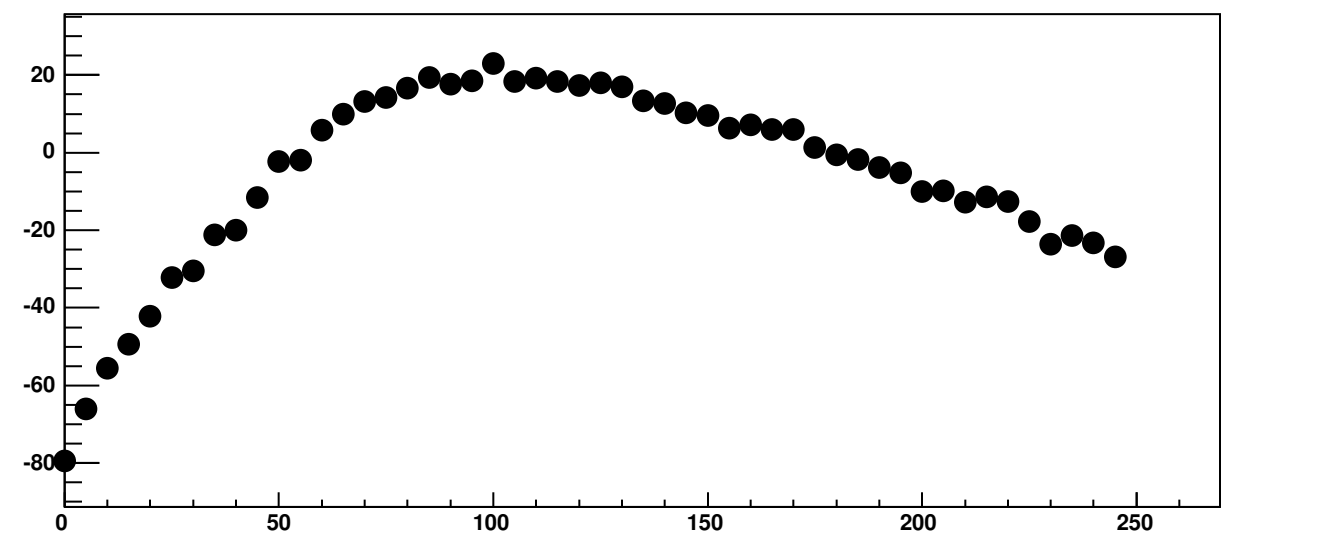
Chip 9, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold



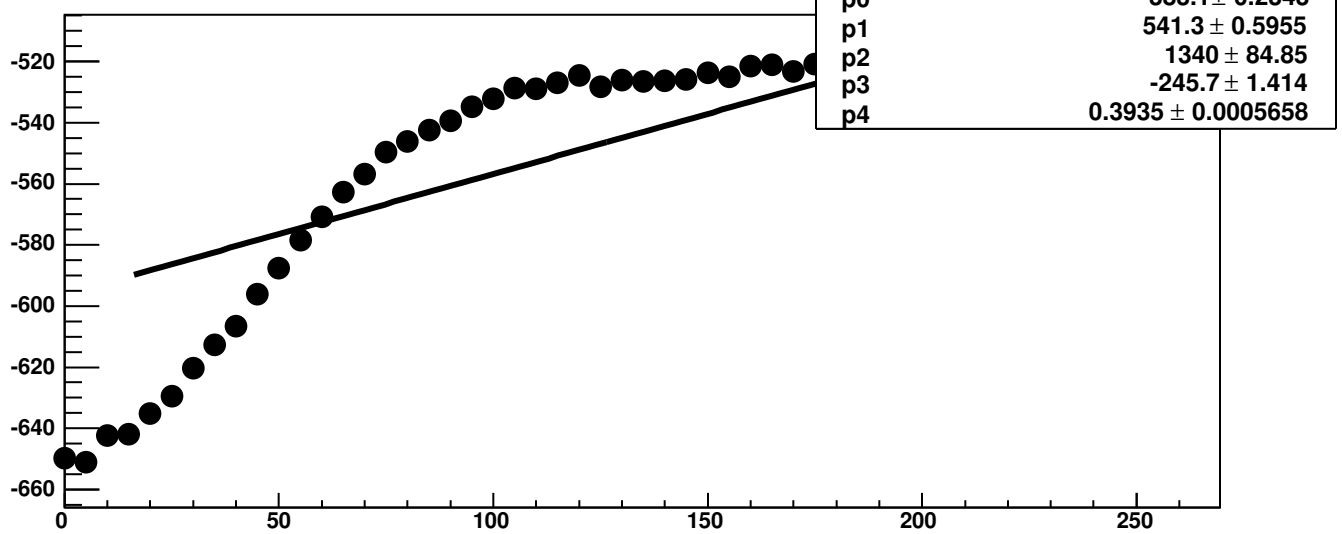
Chip 9, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold



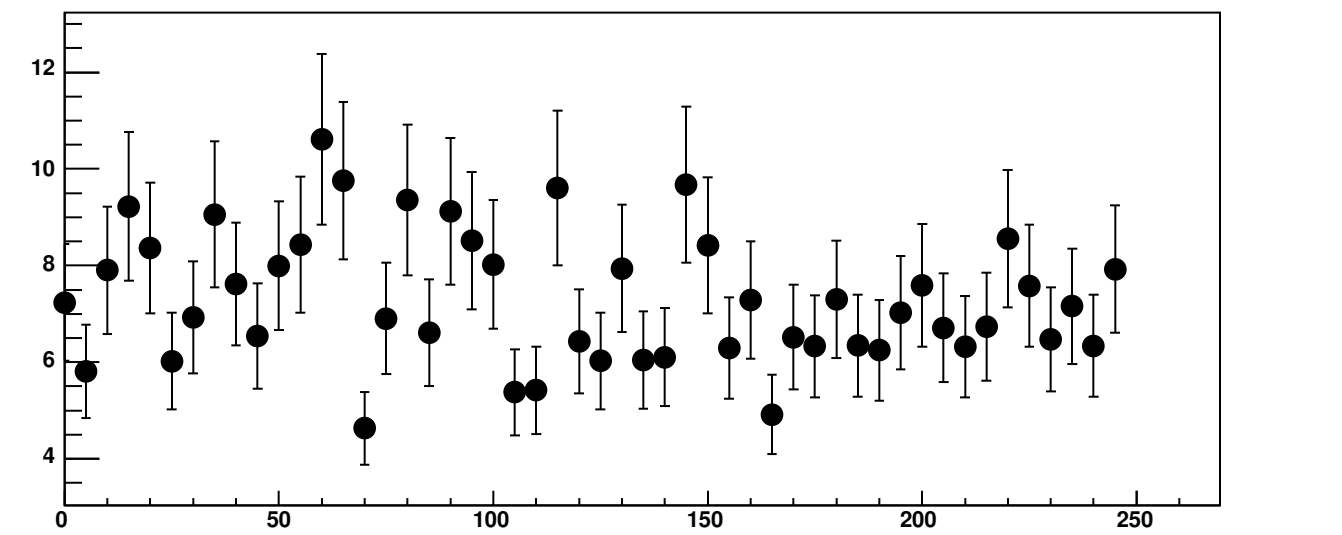
Chip 9, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold



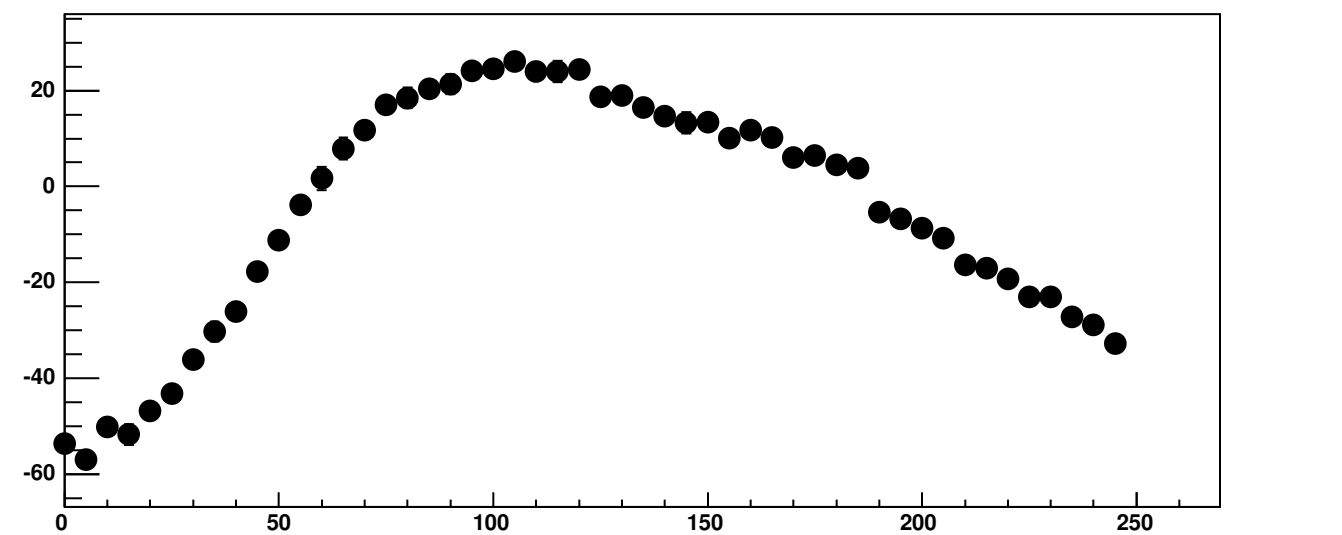
Chip 9, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

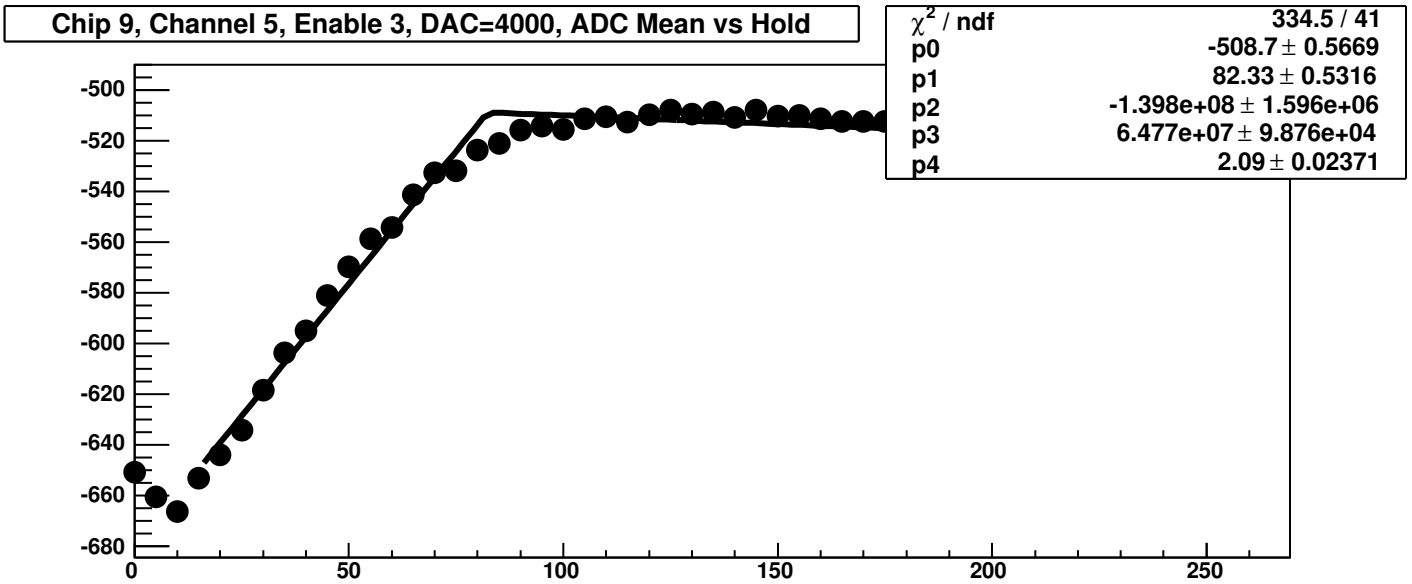


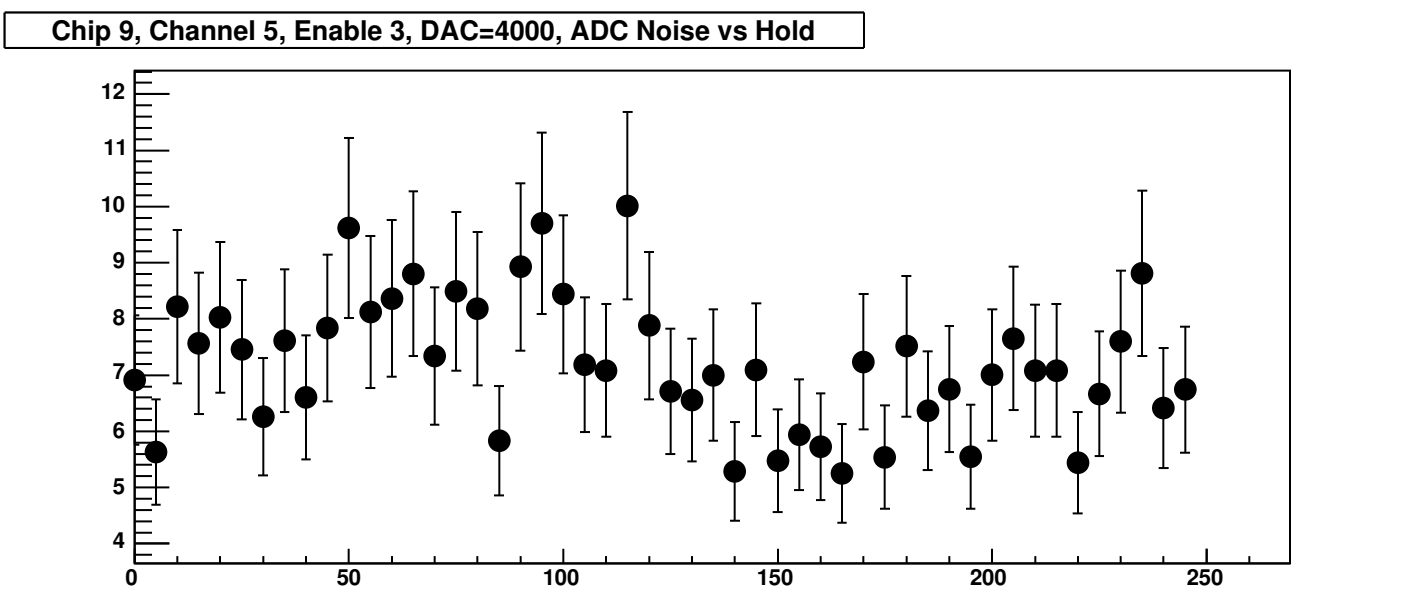
Chip 9, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold

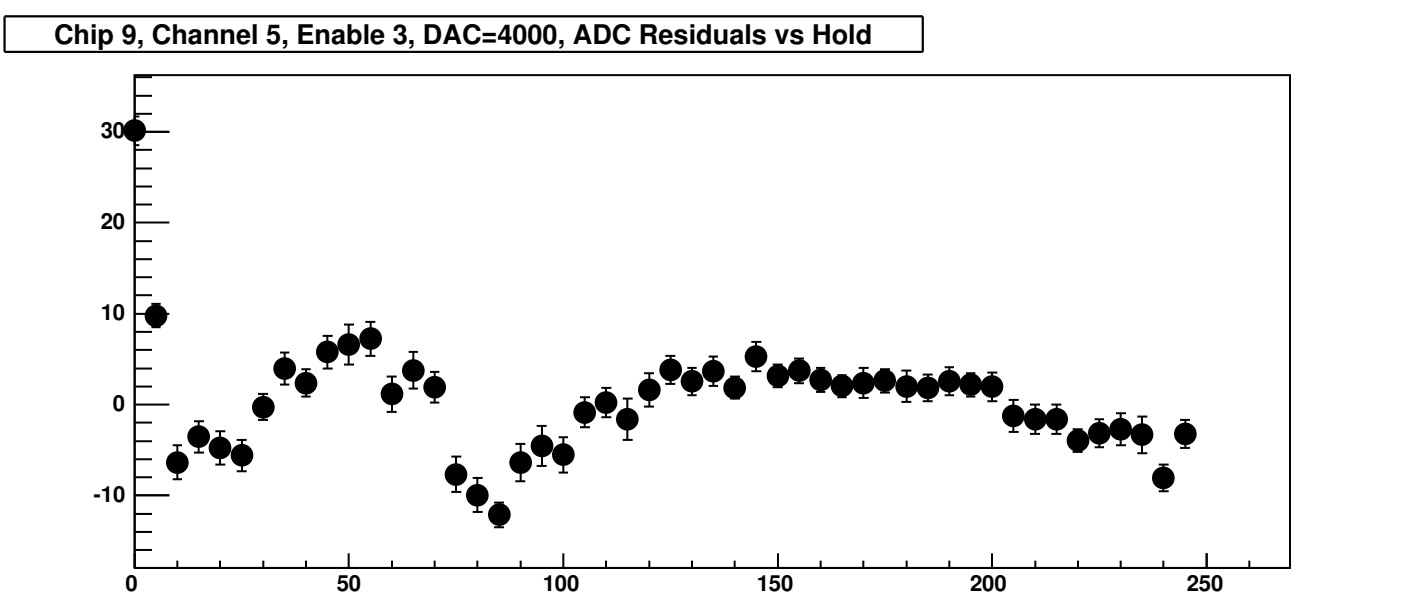


Chip 9, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold



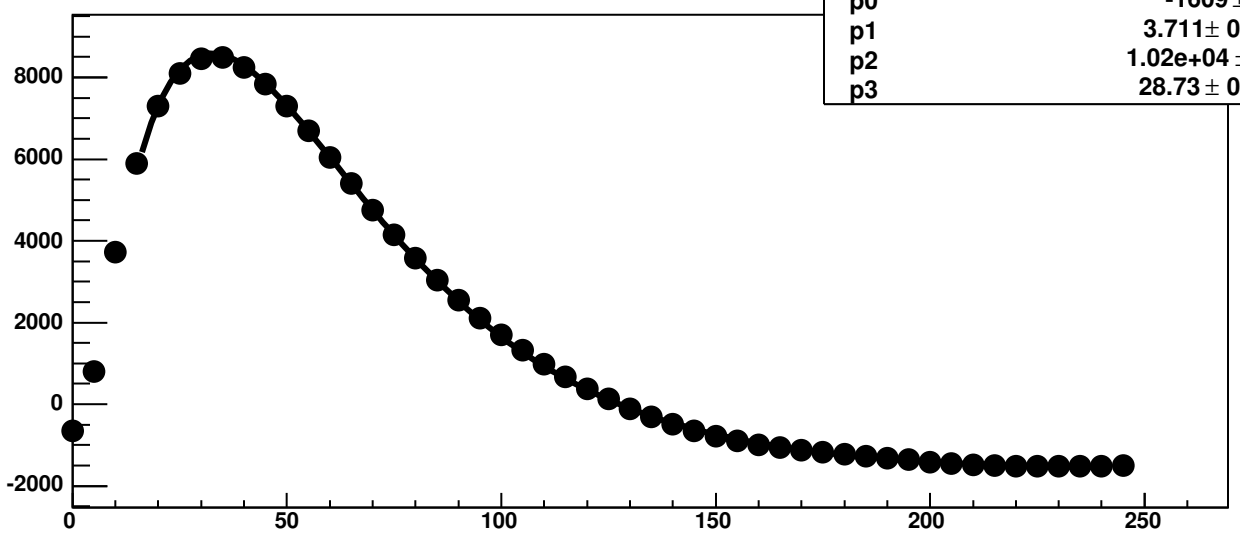

Chip 9, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 334.5 / 41
p0 -508.7 ± 0.5669
p1 82.33 ± 0.5316
p2 -1.398e+08 ± 1.596e+06
p3 6.477e+07 ± 9.876e+04
p4 2.09 ± 0.02371
-500
-520
-540
-560
-580
-600
-620
-640
-660
-680
0
50
100
150
200
250


Chip 9, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

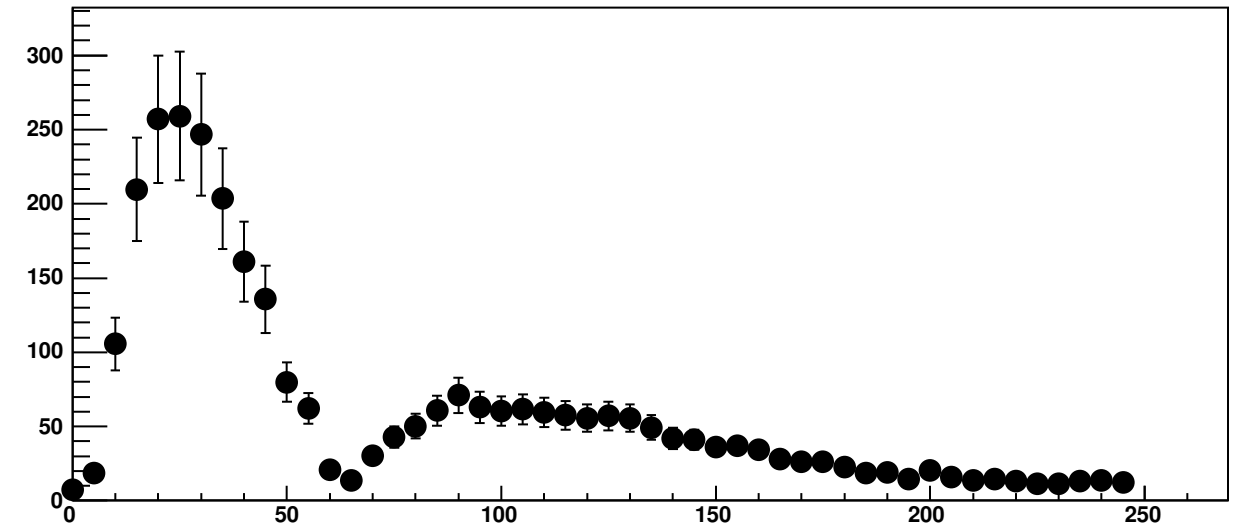

Chip 9, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
0
50
100
150
200
250

Chip 9, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold

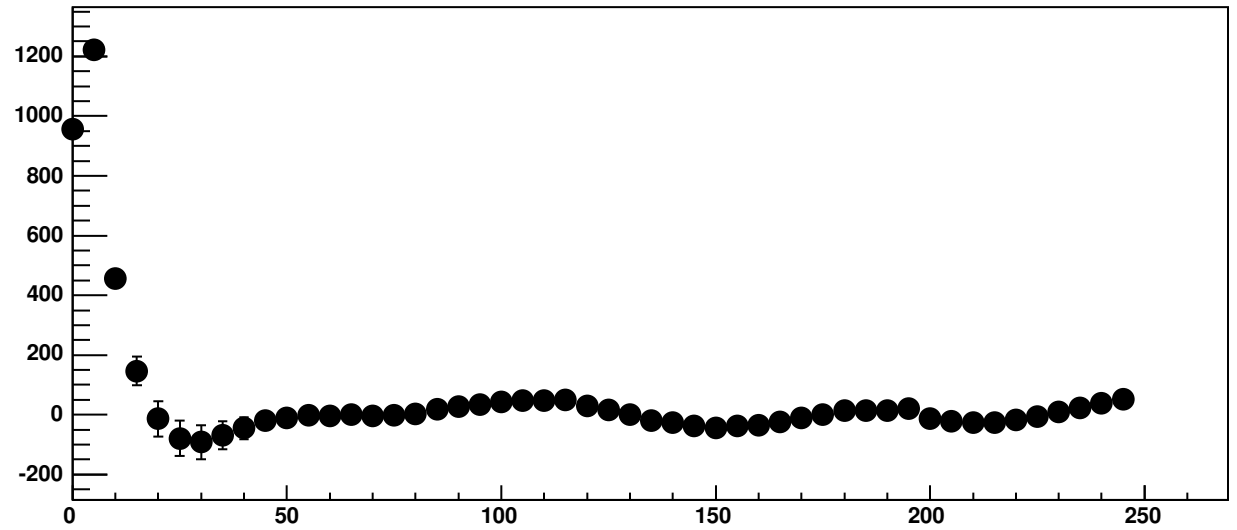
χ^2 / ndf	691.8 / 42
p0	-1609 ± 1.304
p1	3.711 ± 0.08615
p2	1.02e+04 ± 11.39
p3	28.73 ± 0.03167

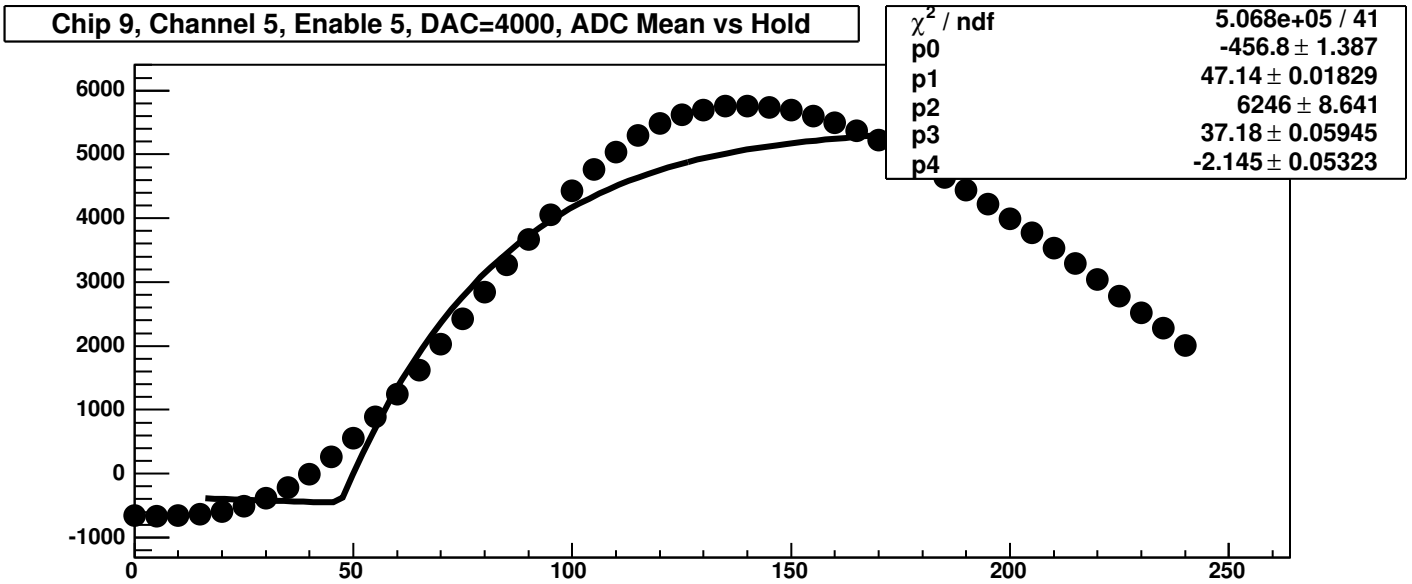


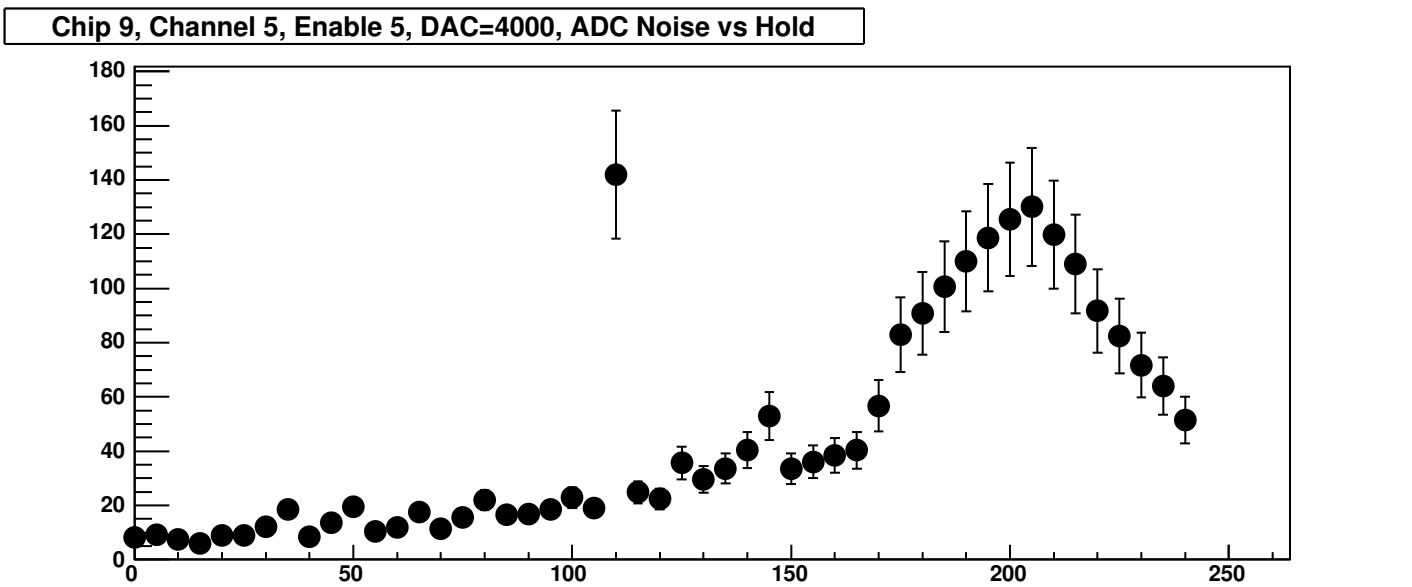
Chip 9, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold

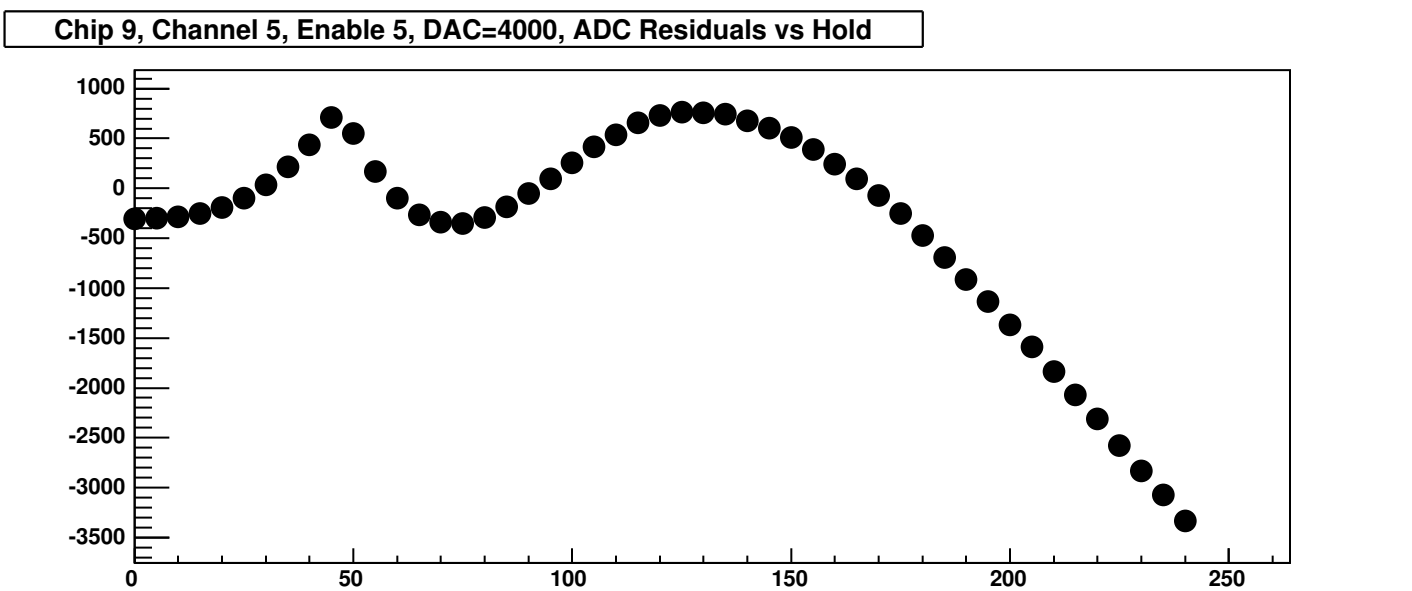


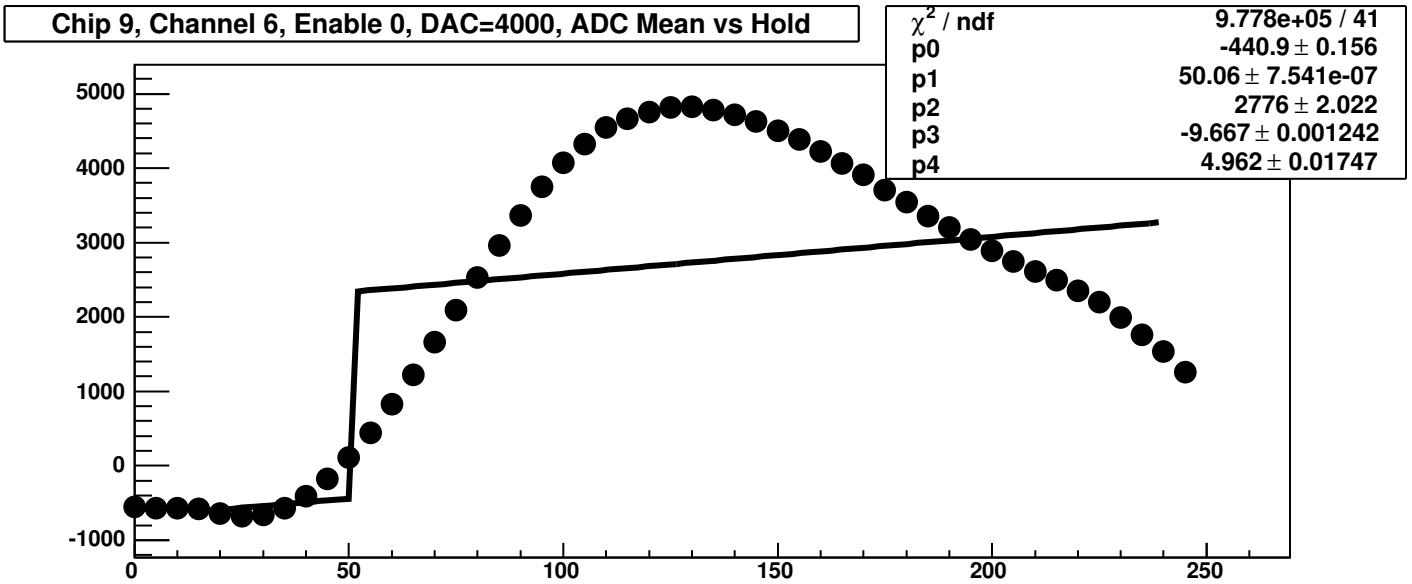
Chip 9, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold

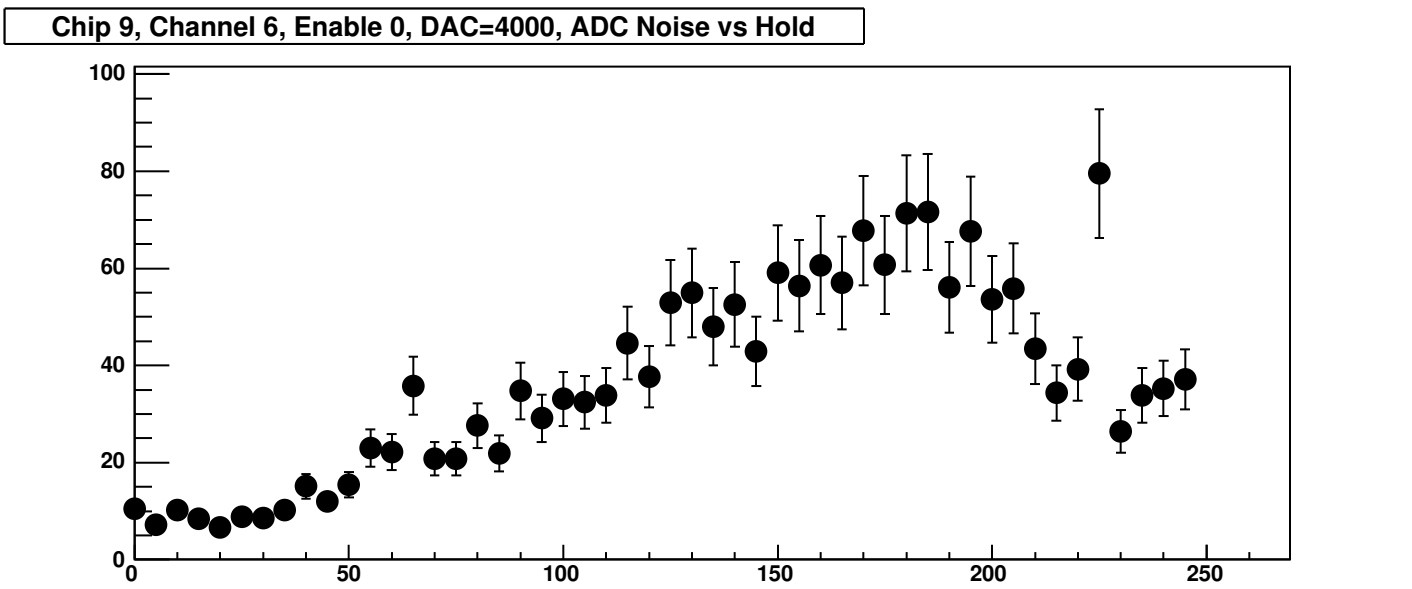


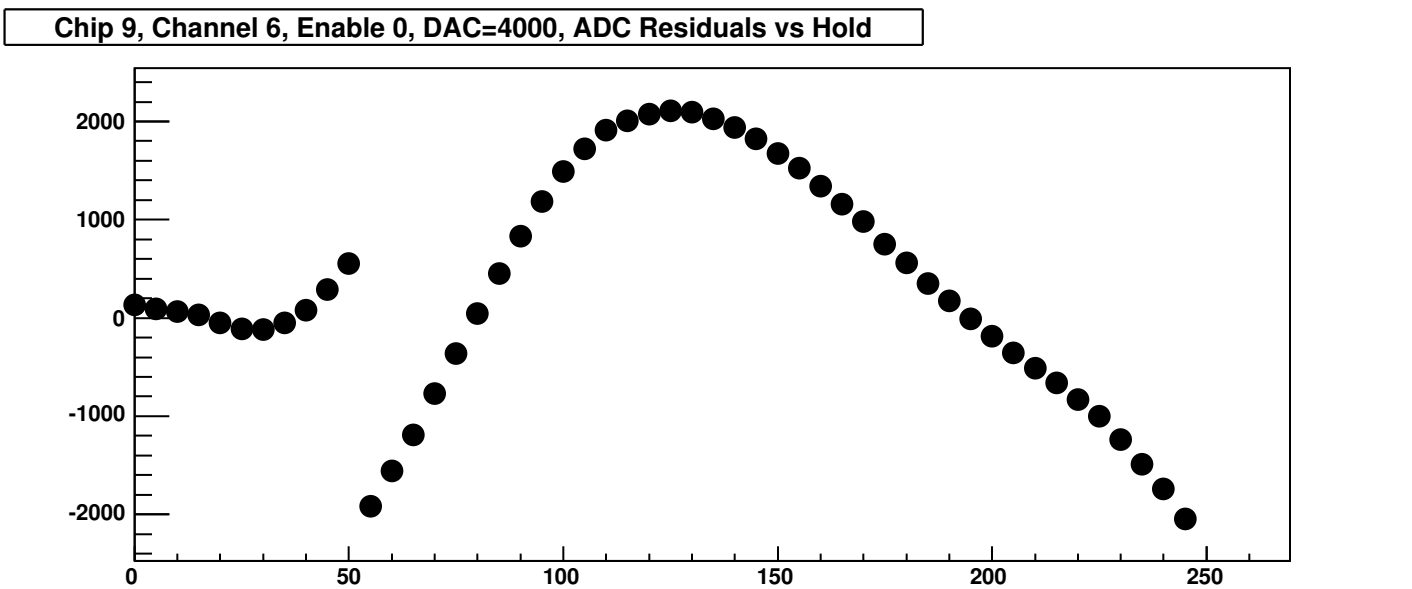

Chip 9, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.068e+05 / 41
p0 -456.8 ± 1.387
p1 47.14 ± 0.01829
p2 6246 ± 8.641
p3 37.18 ± 0.05945
p4 -2.145 ± 0.05323
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 9, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

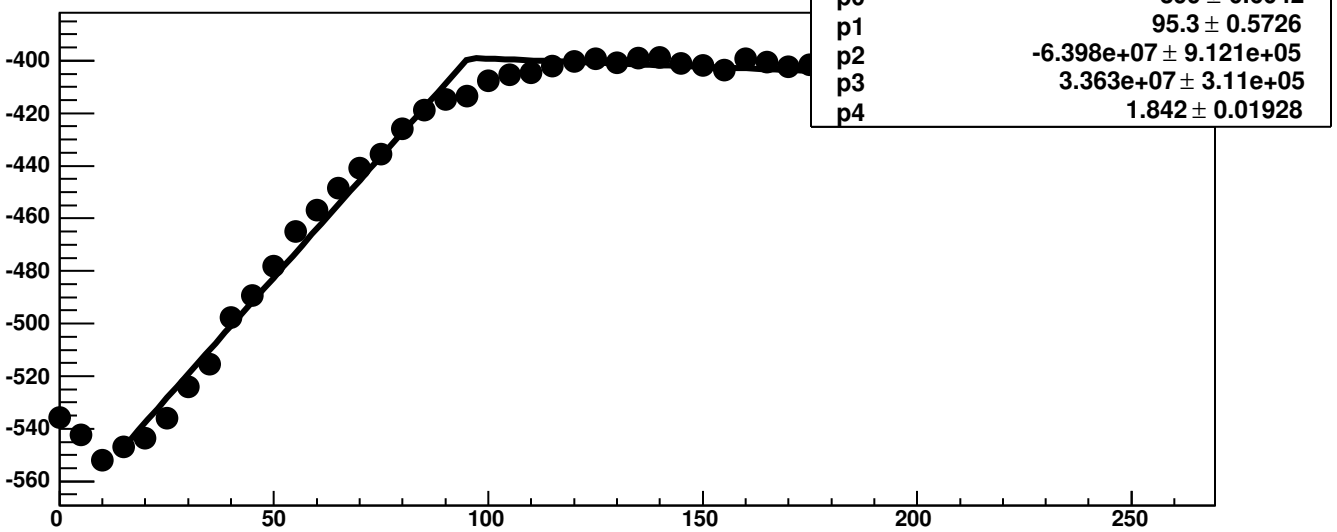

Chip 9, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
0
50
100
150
200
250


Chip 9, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 9.778e+05 / 41
p0 -440.9 ± 0.156
p1 50.06 ± 7.541e-07
p2 2776 ± 2.022
p3 -9.667 ± 0.001242
p4 4.962 ± 0.01747
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

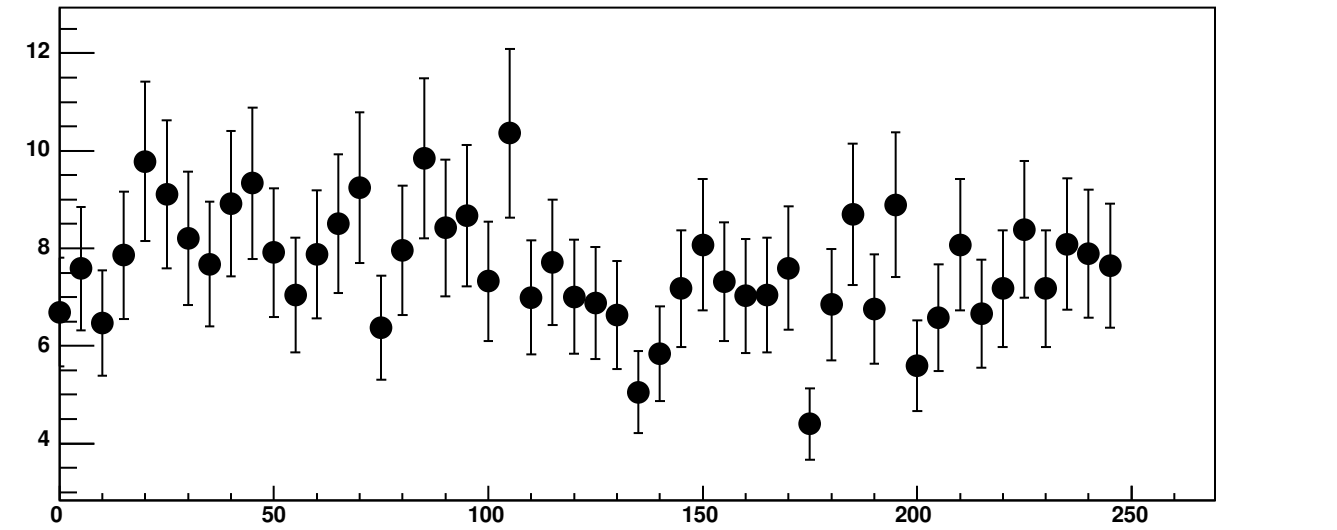

Chip 9, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250


Chip 9, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
0
50
100
150
200
250

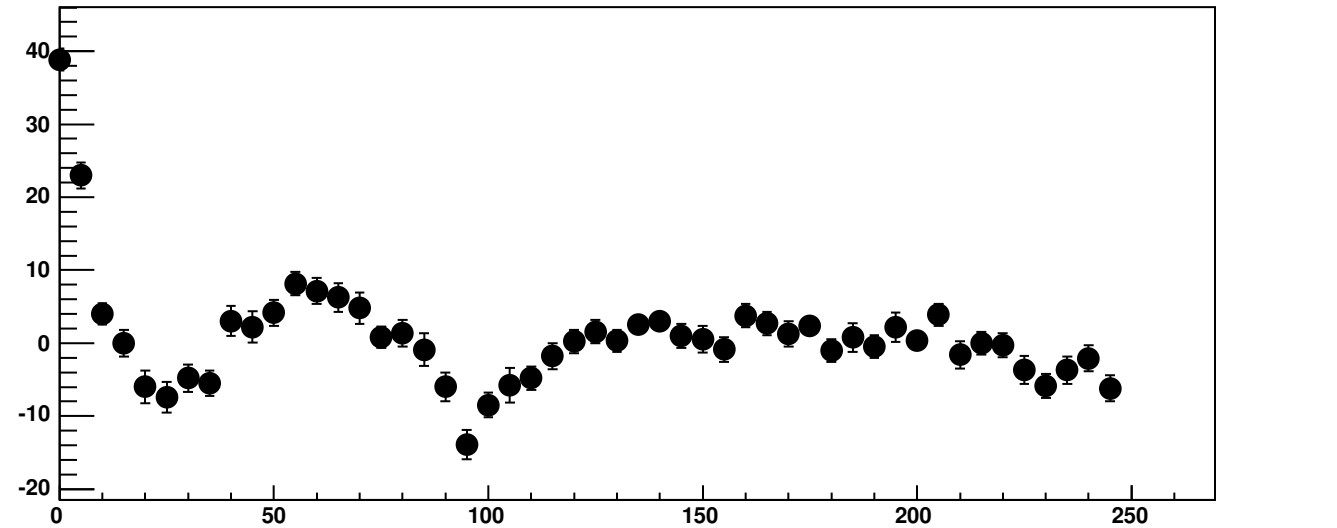
Chip 9, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold



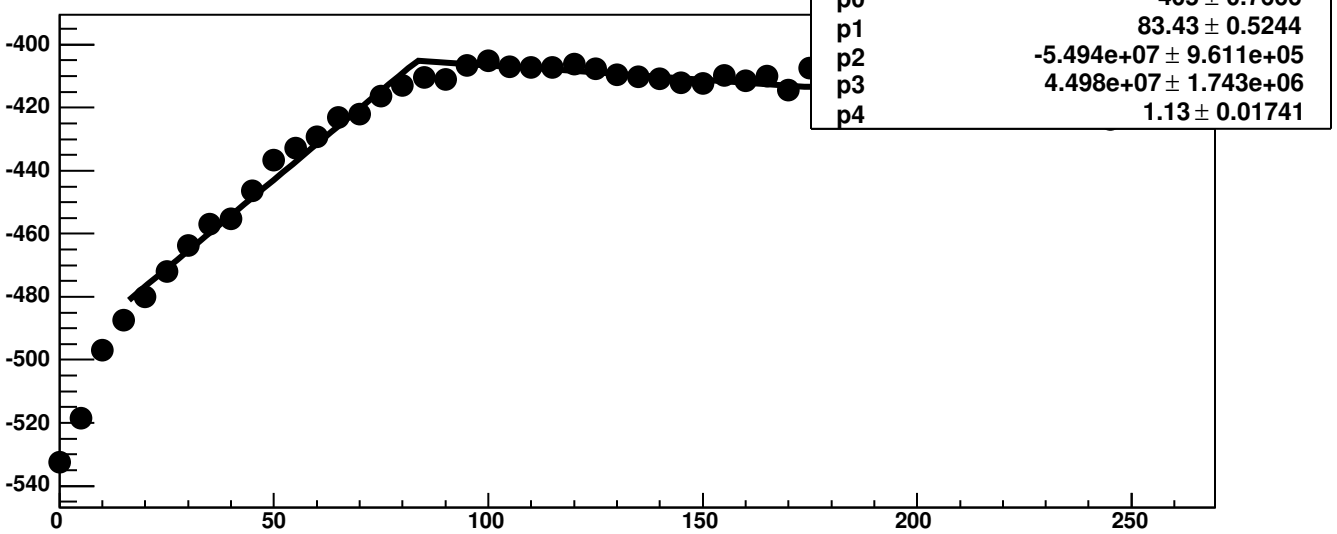
Chip 9, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



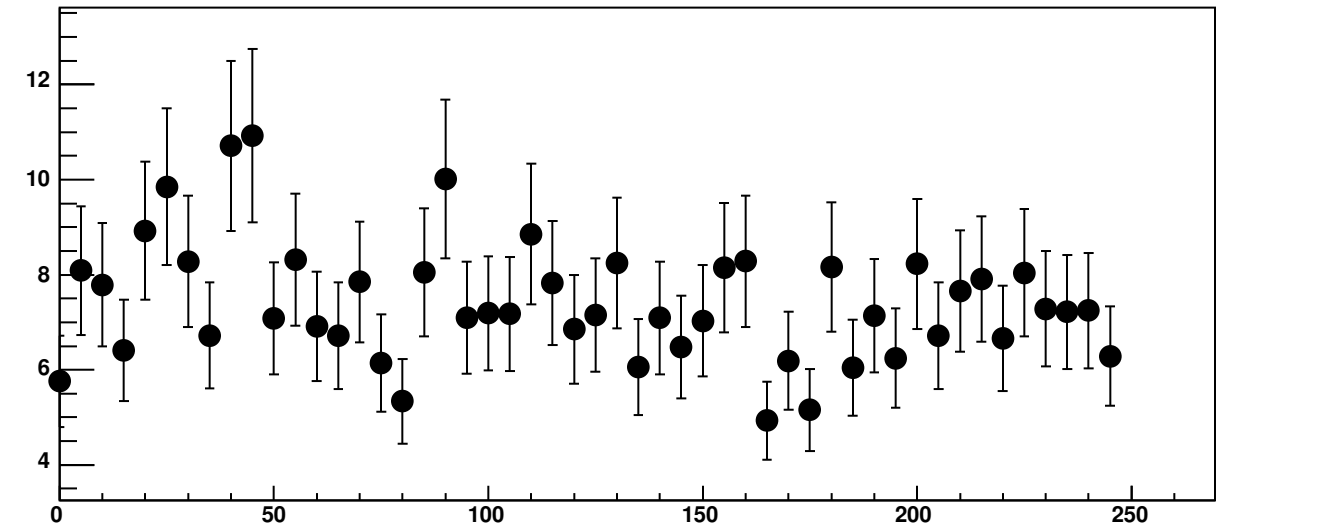
Chip 9, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold



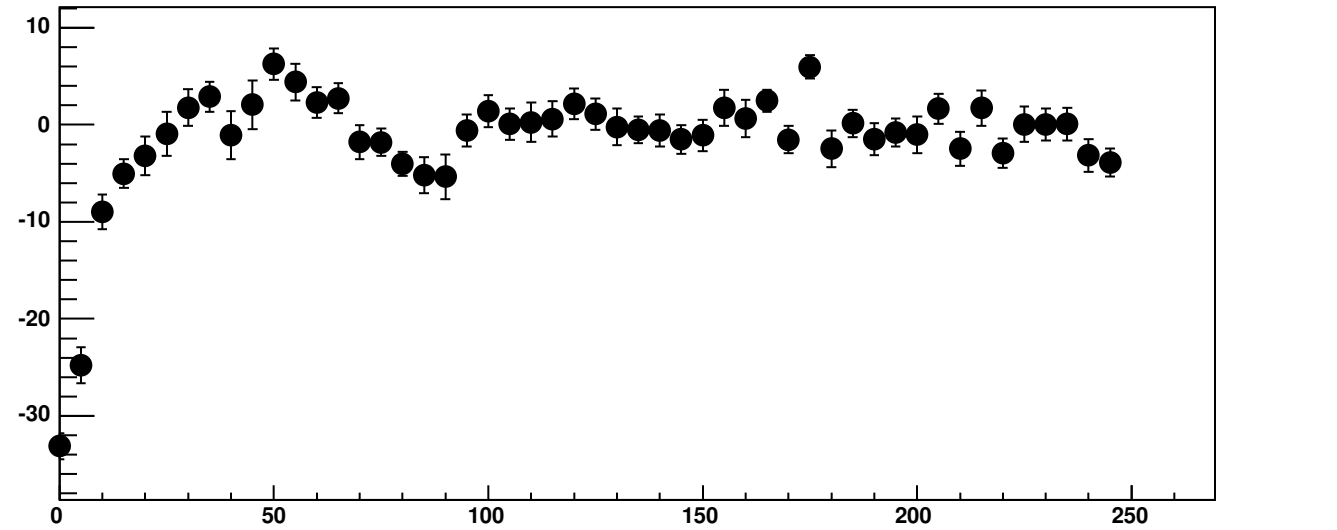
Chip 9, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold

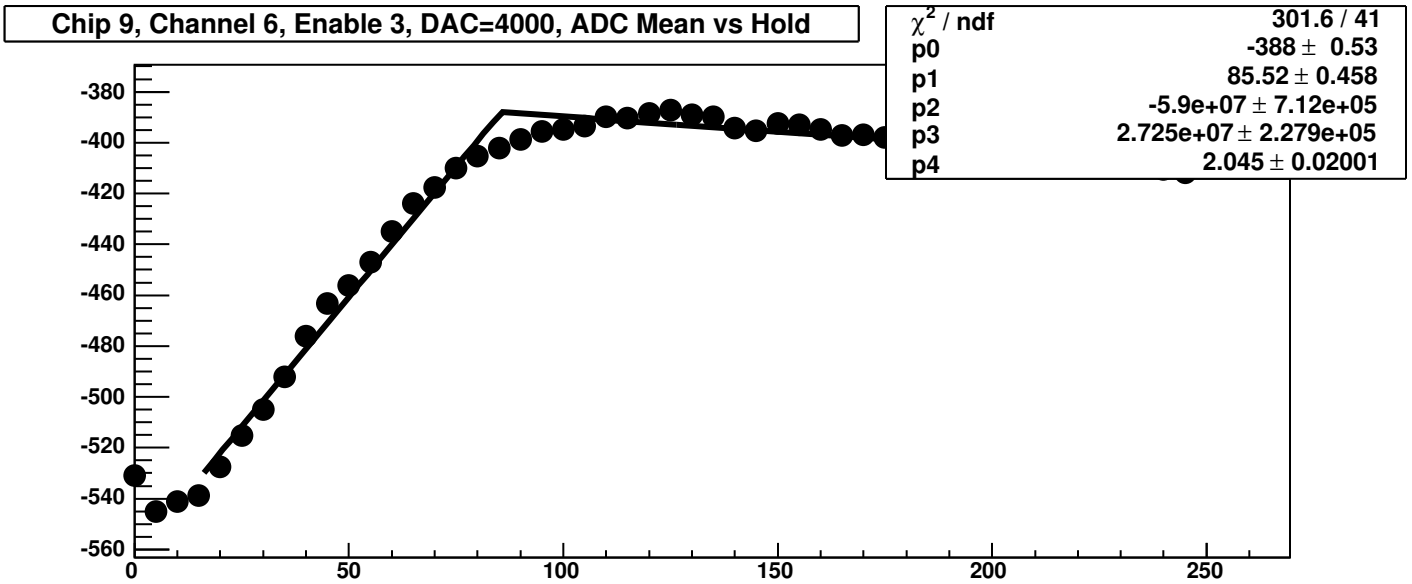


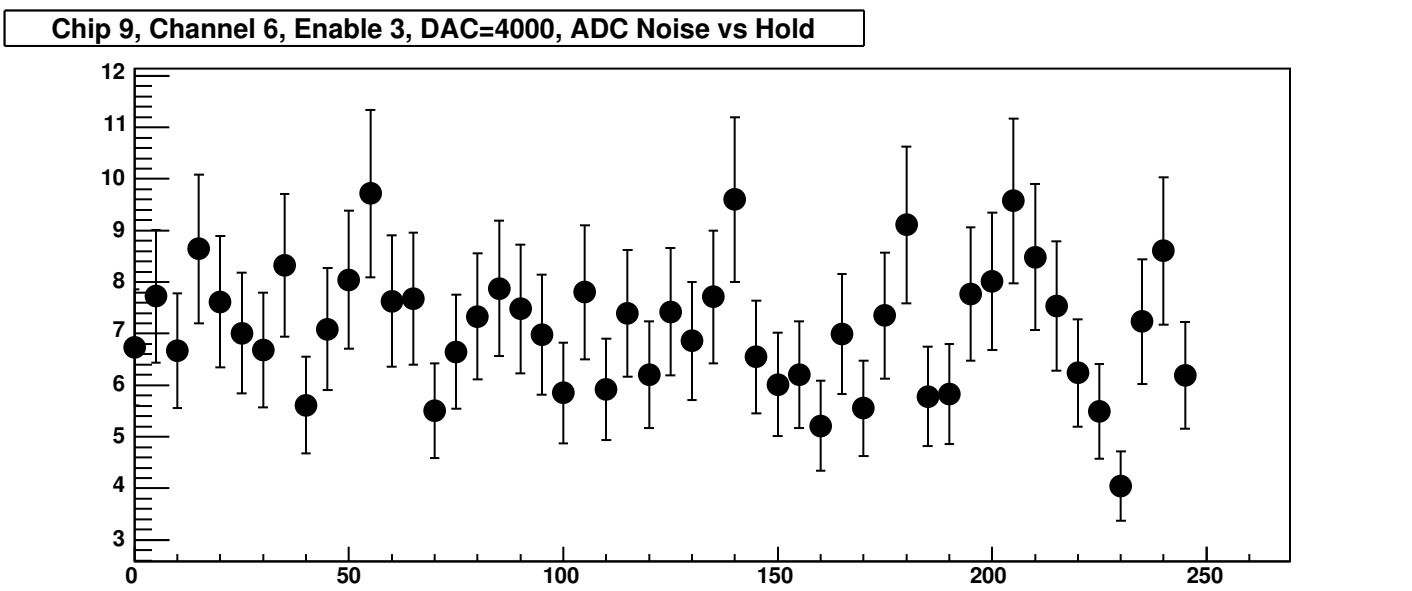
Chip 9, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold

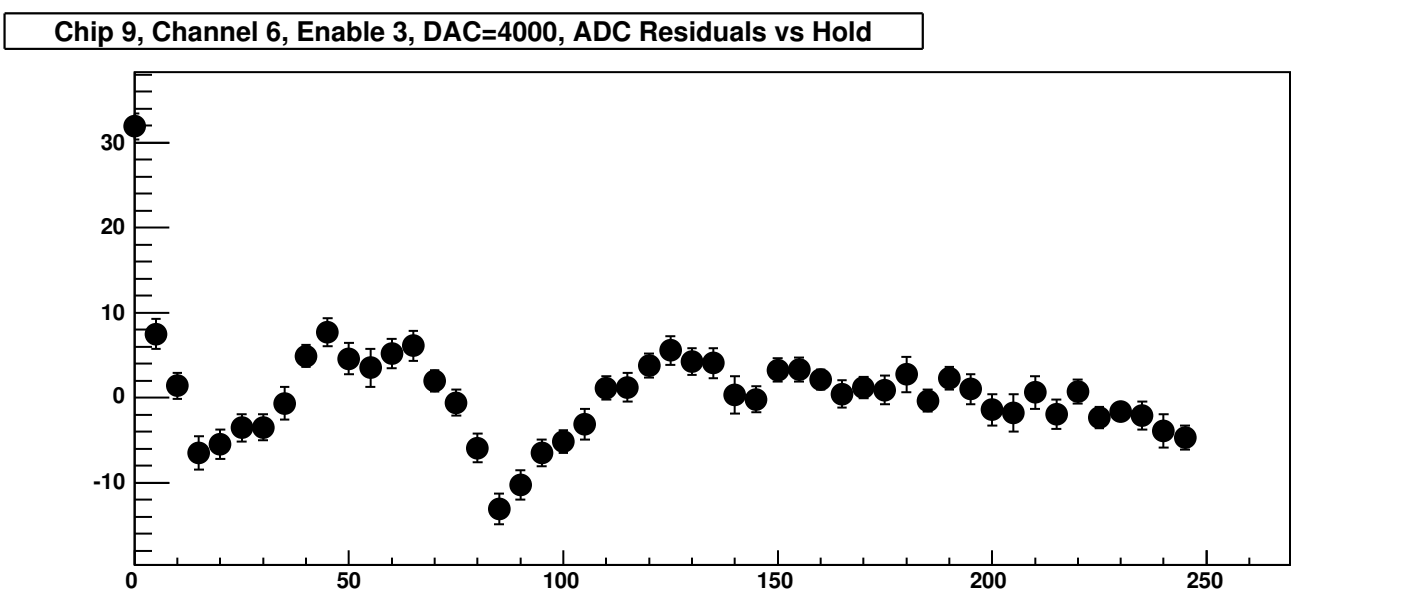


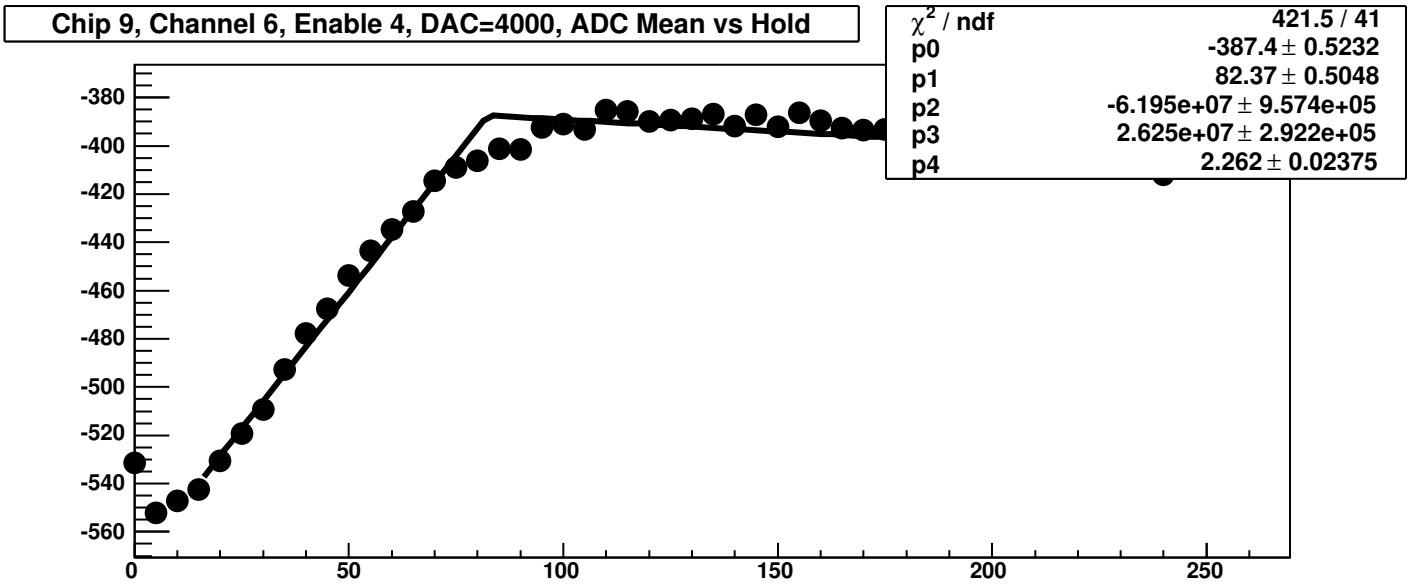
Chip 9, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold

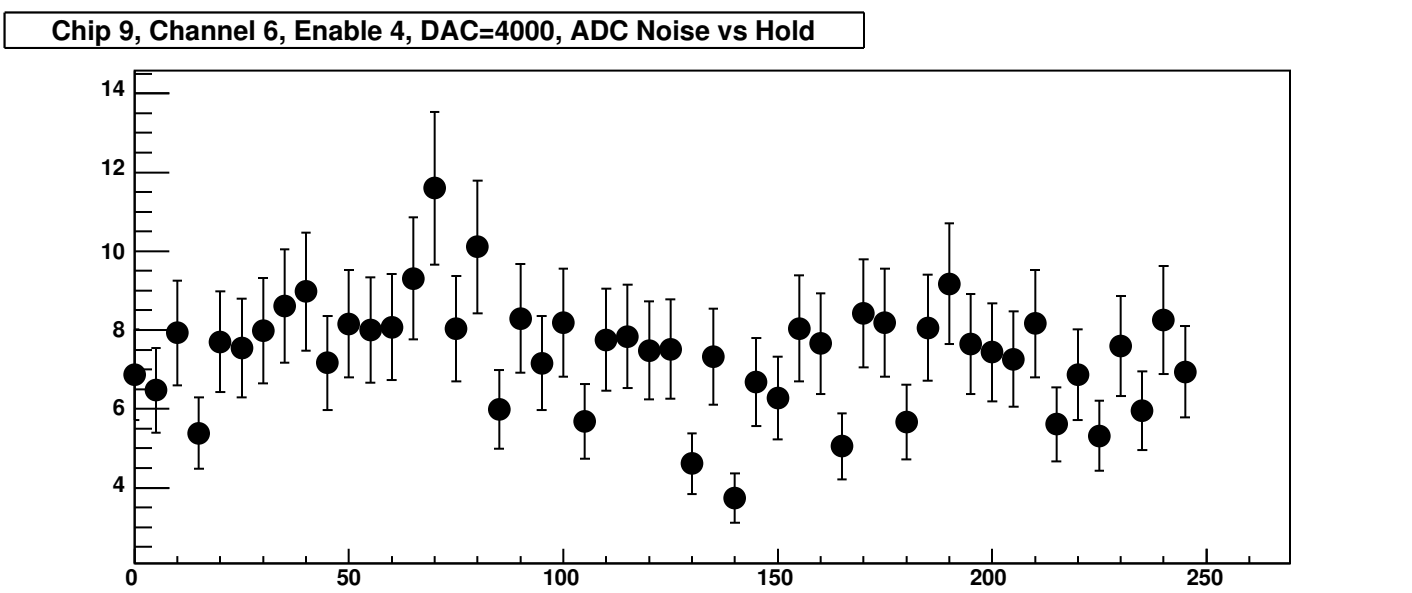


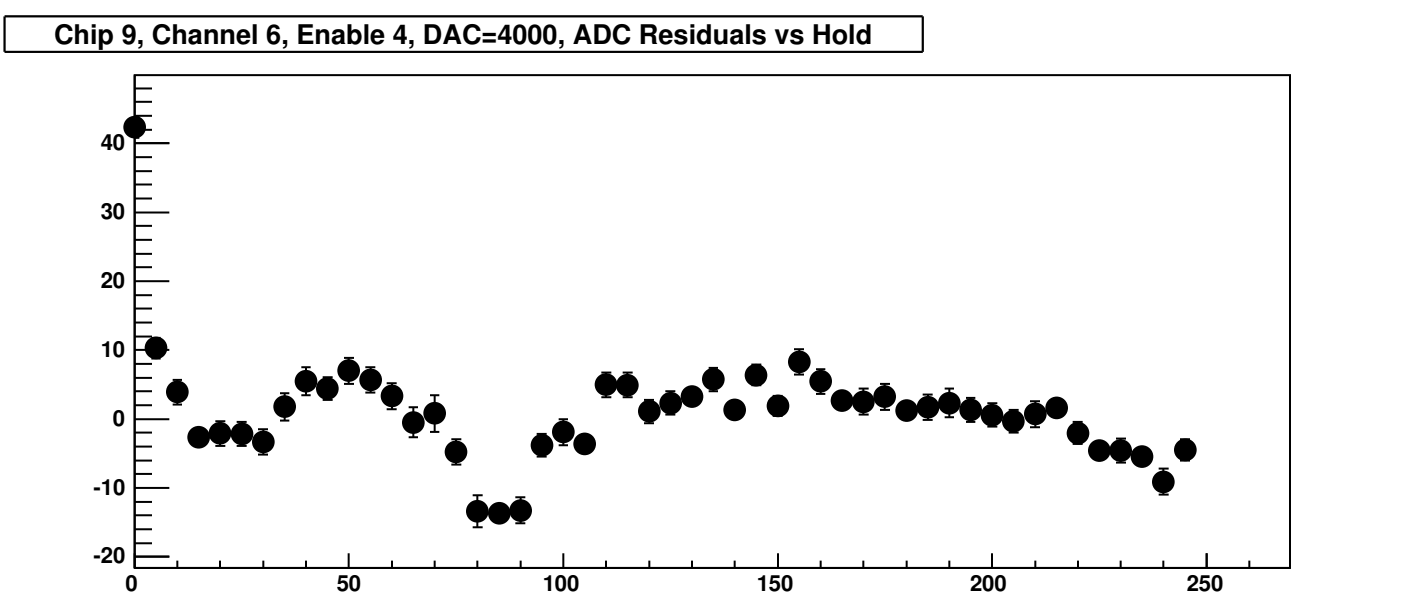

Chip 9, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 301.6 / 41
p0 -388 ± 0.53
p1 85.52 ± 0.458
p2 -5.9e+07 ± 7.12e+05
p3 2.725e+07 ± 2.279e+05
p4 2.045 ± 0.02001
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
0
50
100
150
200
250

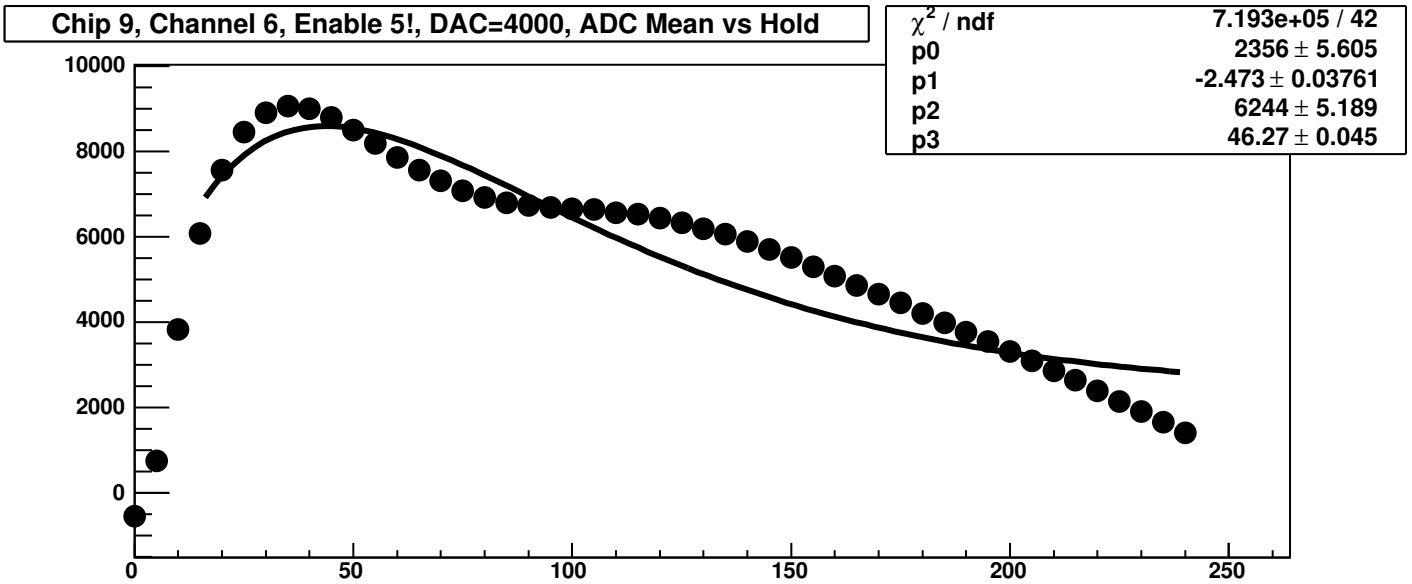

Chip 9, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

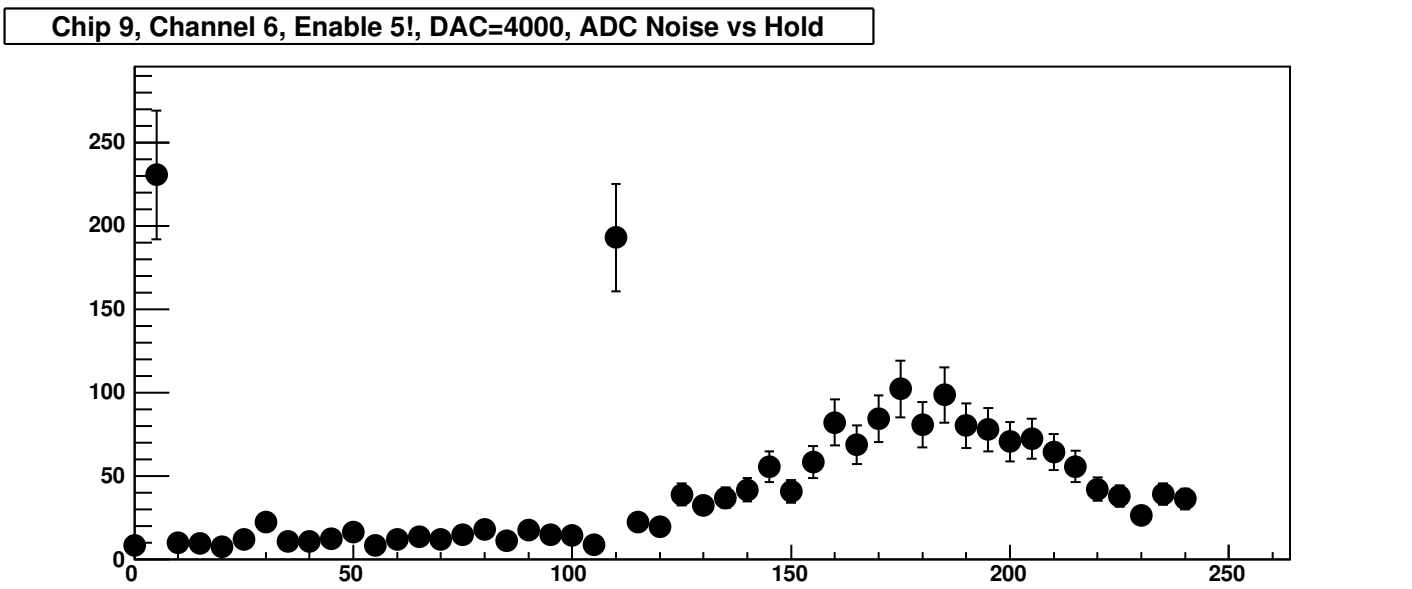

Chip 9, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
0
50
100
150
200
250

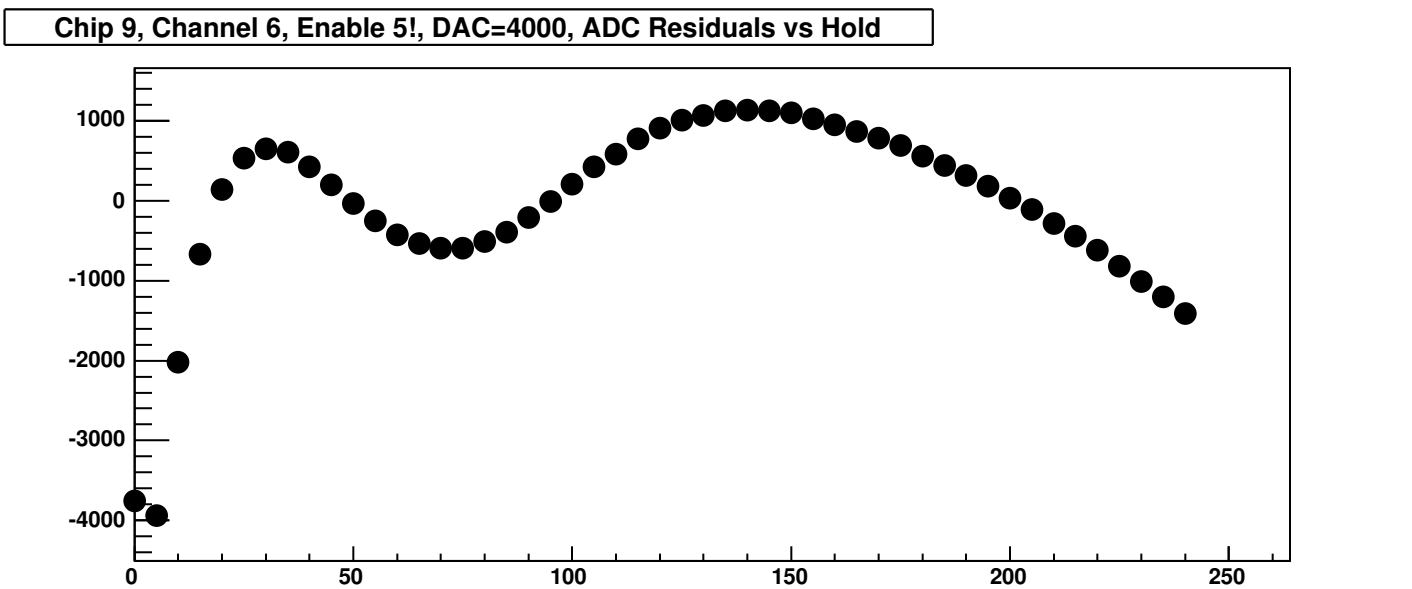

Chip 9, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 421.5 / 41
p0 -387.4 ± 0.5232
p1 82.37 ± 0.5048
p2 -6.195e+07 ± 9.574e+05
p3 2.625e+07 ± 2.922e+05
p4 2.262 ± 0.02375
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
0
50
100
150
200
250

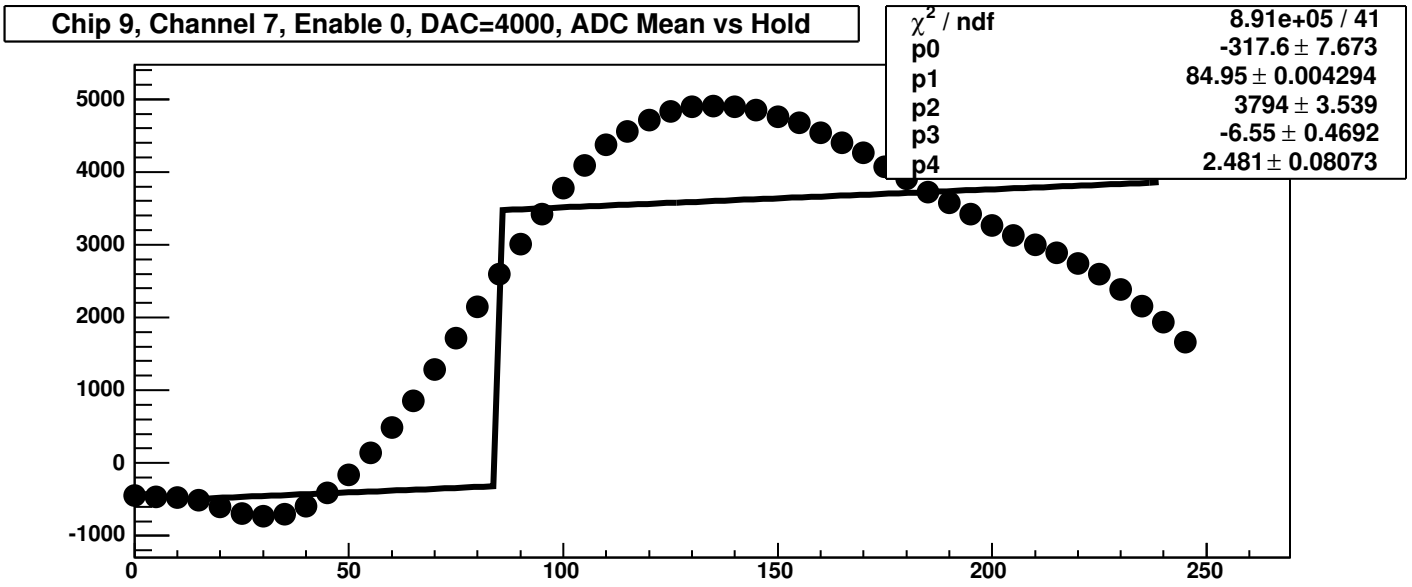

Chip 9, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

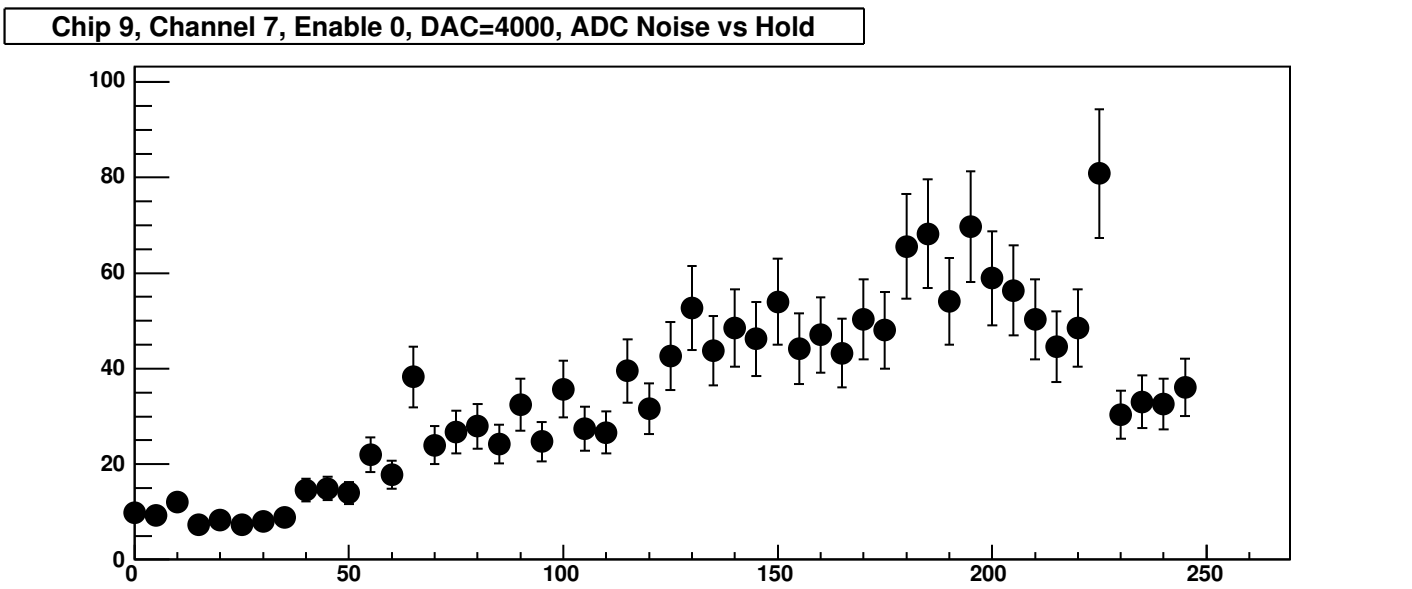

Chip 9, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
-20
0
50
100
150
200
250

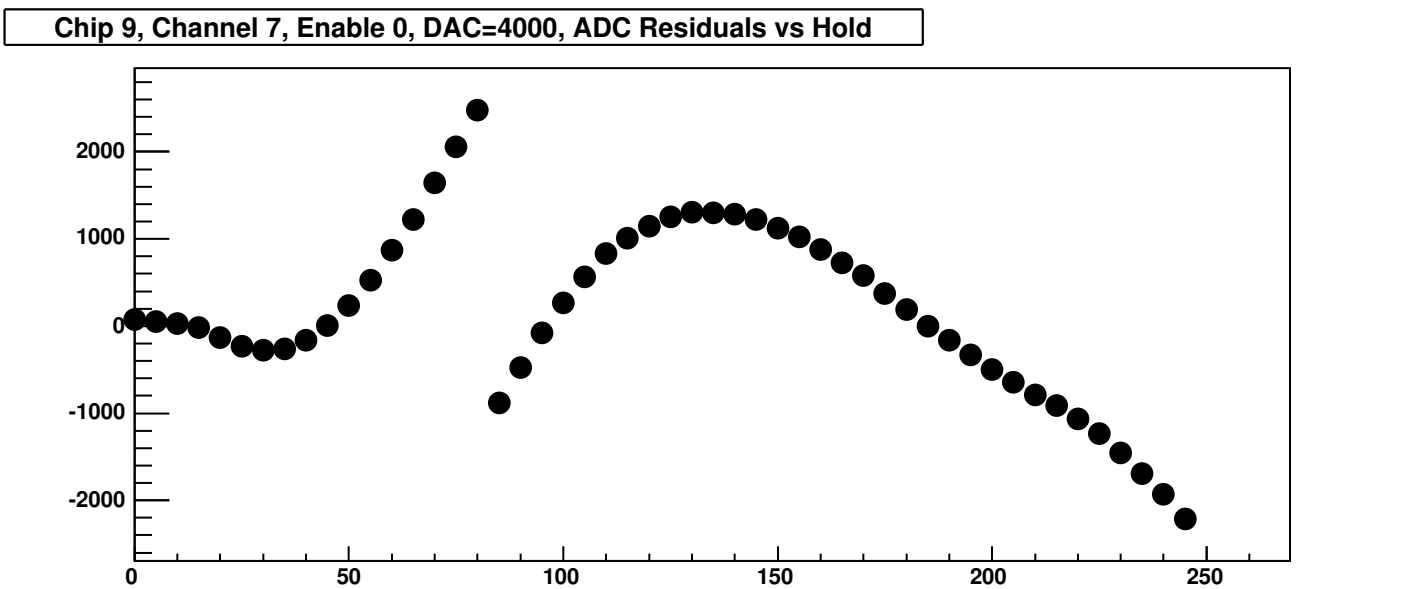

Chip 9, Channel 6, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 7.193e+05 / 42
p0 2356 ± 5.605
p1 -2.473 ± 0.03761
p2 6244 ± 5.189
p3 46.27 ± 0.045
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

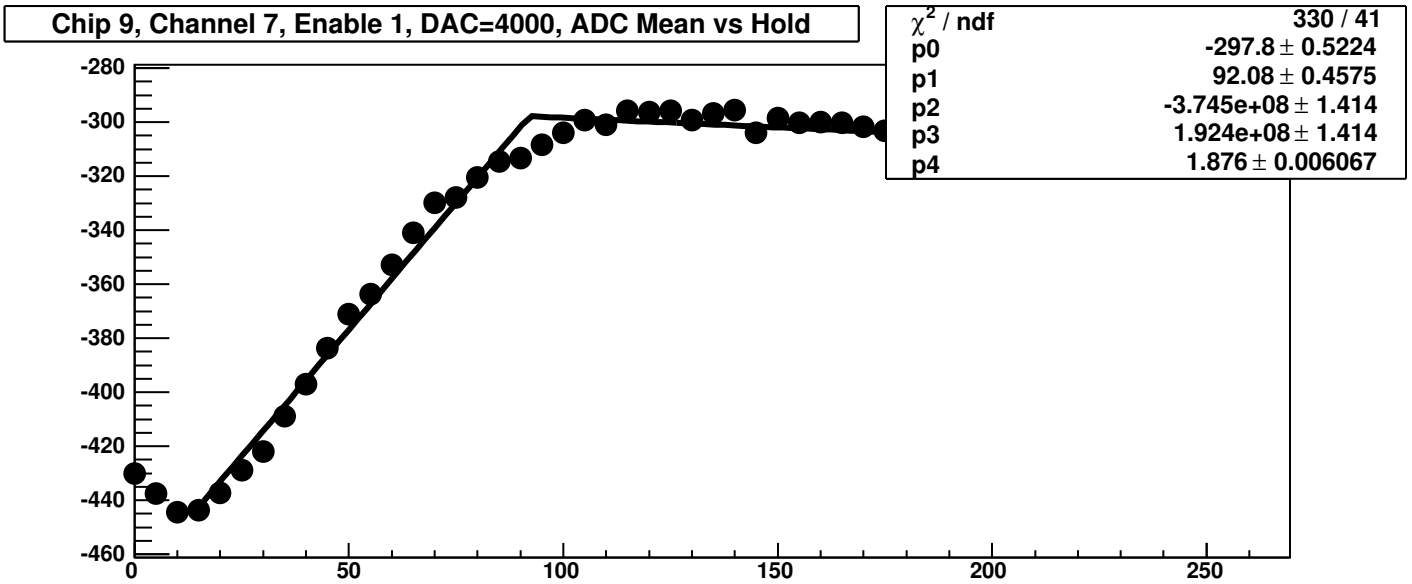

Chip 9, Channel 6, Enable 5!, DAC=4000, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250

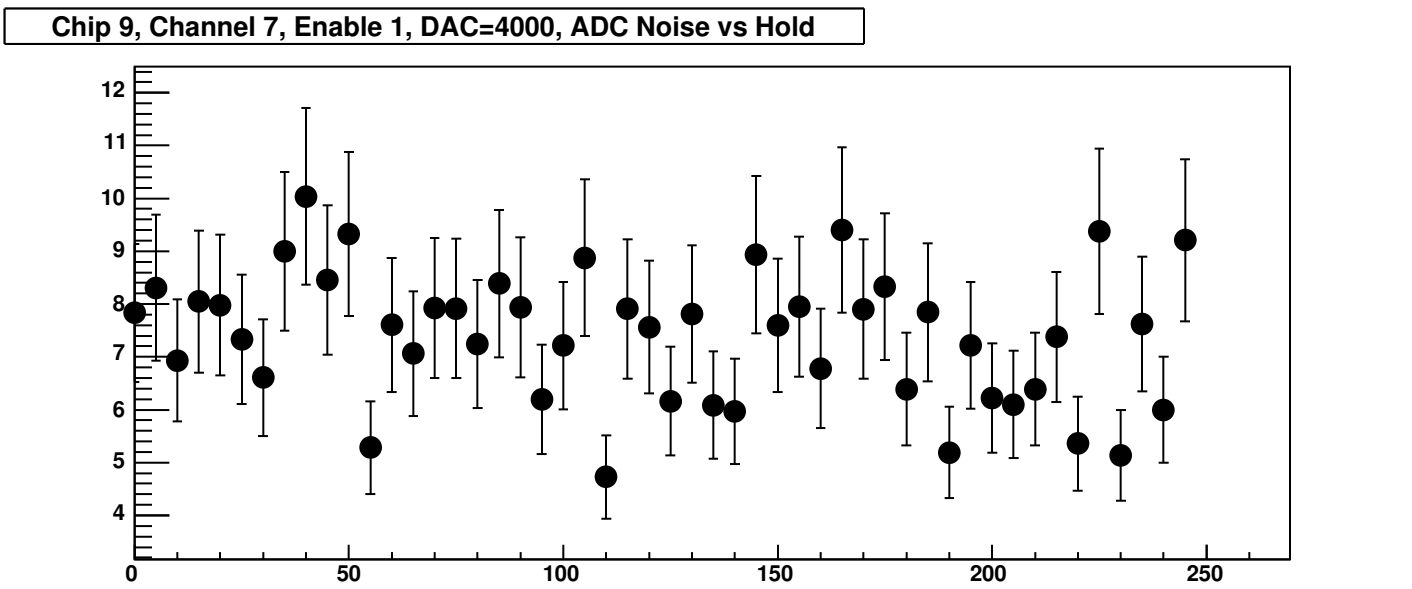

Chip 9, Channel 6, Enable 5!, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

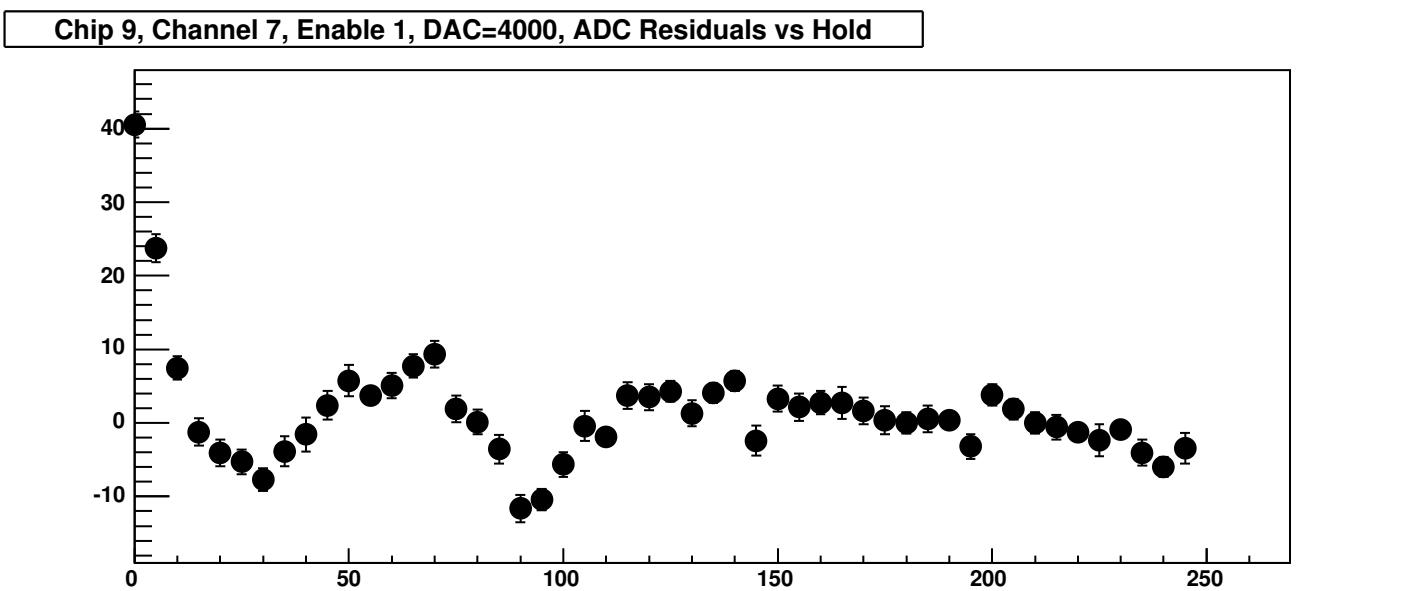

Chip 9, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 8.91e+05 / 41
p0 -317.6 ± 7.673
p1 84.95 ± 0.004294
p2 3794 ± 3.539
p3 -6.55 ± 0.4692
p4 2.481 ± 0.08073
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

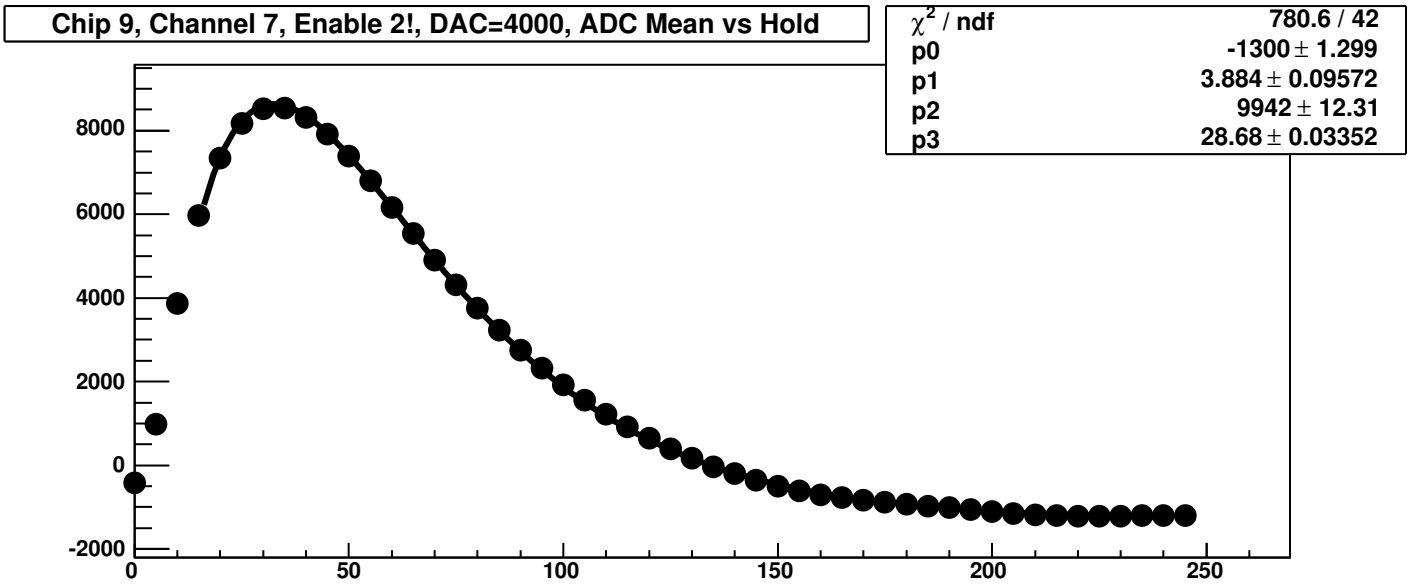

Chip 9, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250

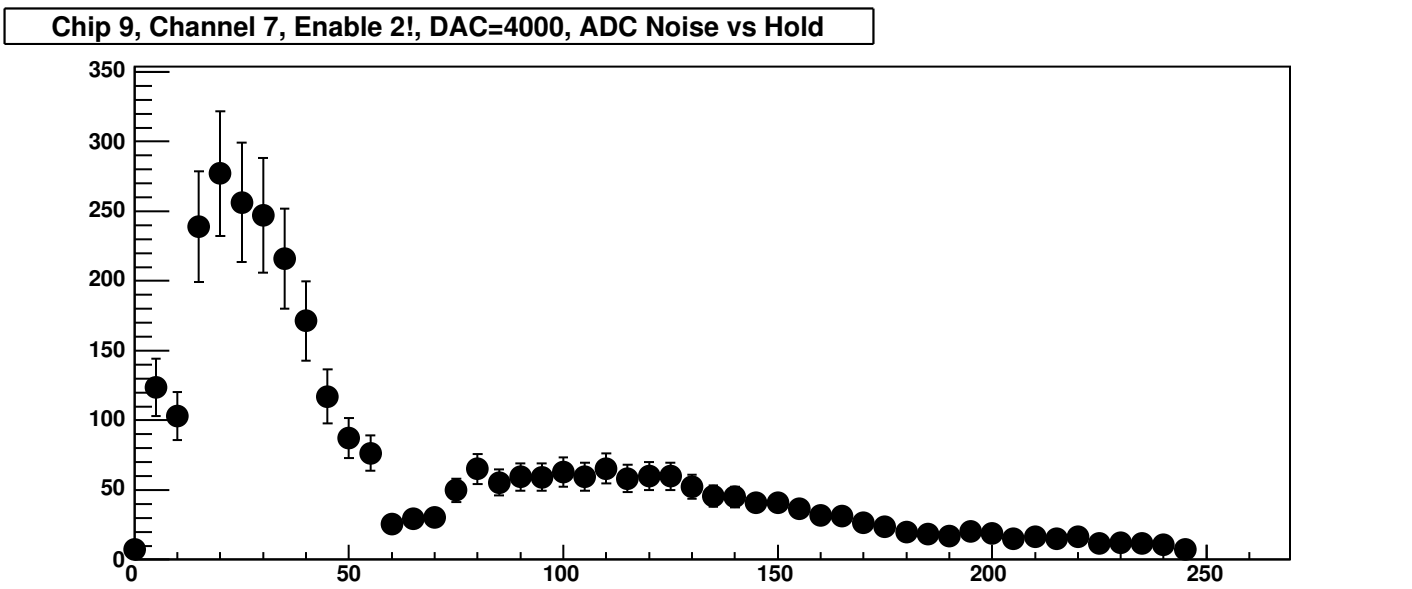

Chip 9, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
0
50
100
150
200
250

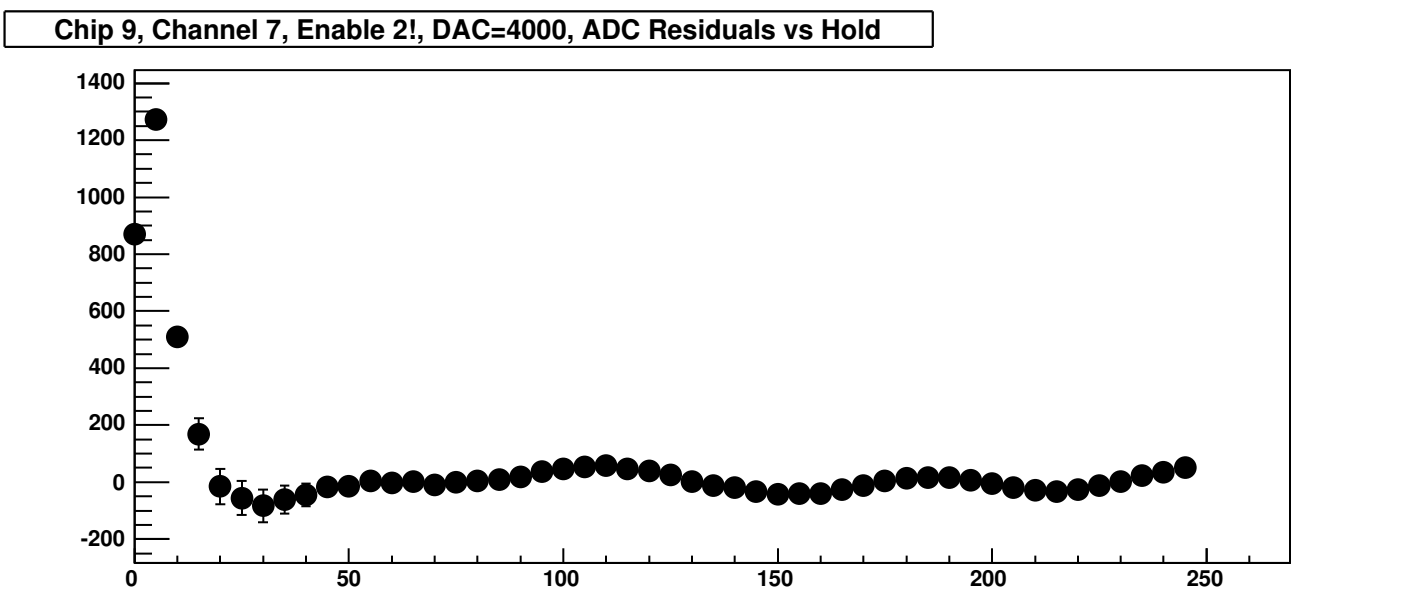

Chip 9, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 330 / 41
p0 -297.8 ± 0.5224
p1 92.08 ± 0.4575
p2 -3.745e+08 ± 1.414
p3 1.924e+08 ± 1.414
p4 1.876 ± 0.006067
-280
-300
-320
-340
-360
-380
-400
-420
-440
-460
0
50
100
150
200
250


Chip 9, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

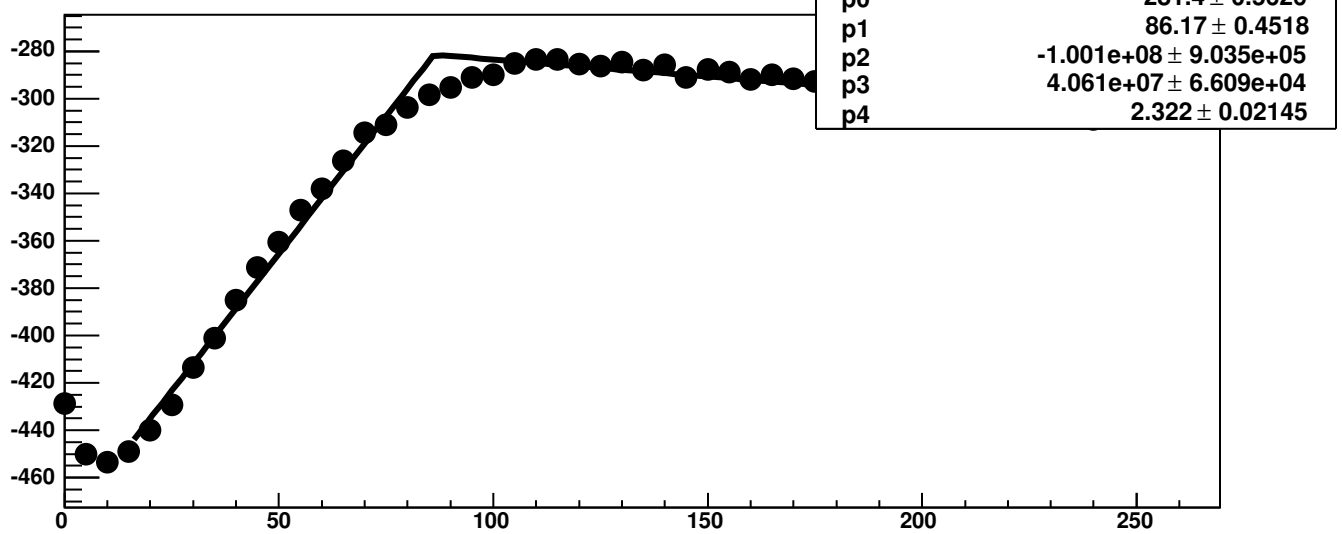

Chip 9, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250


Chip 9, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 780.6 / 42
p0 -1300 ± 1.299
p1 3.884 ± 0.09572
p2 9942 ± 12.31
p3 28.68 ± 0.03352
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

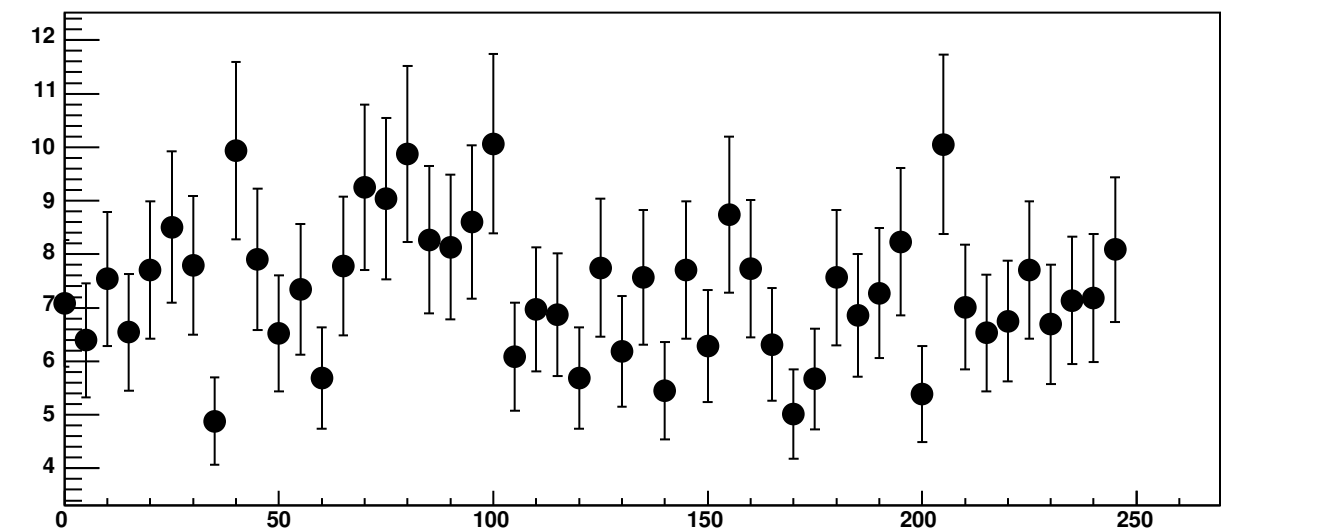

Chip 9, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold
350
300
250
200
150
100
50
0
0
50
100
150
200
250


Chip 9, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

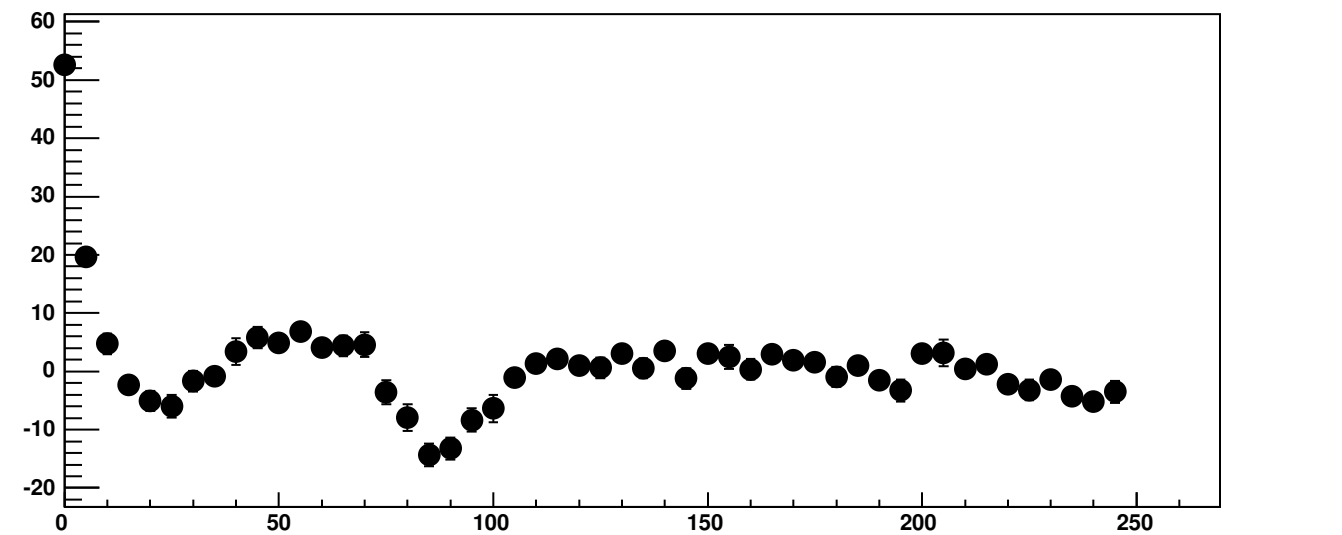
Chip 9, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold



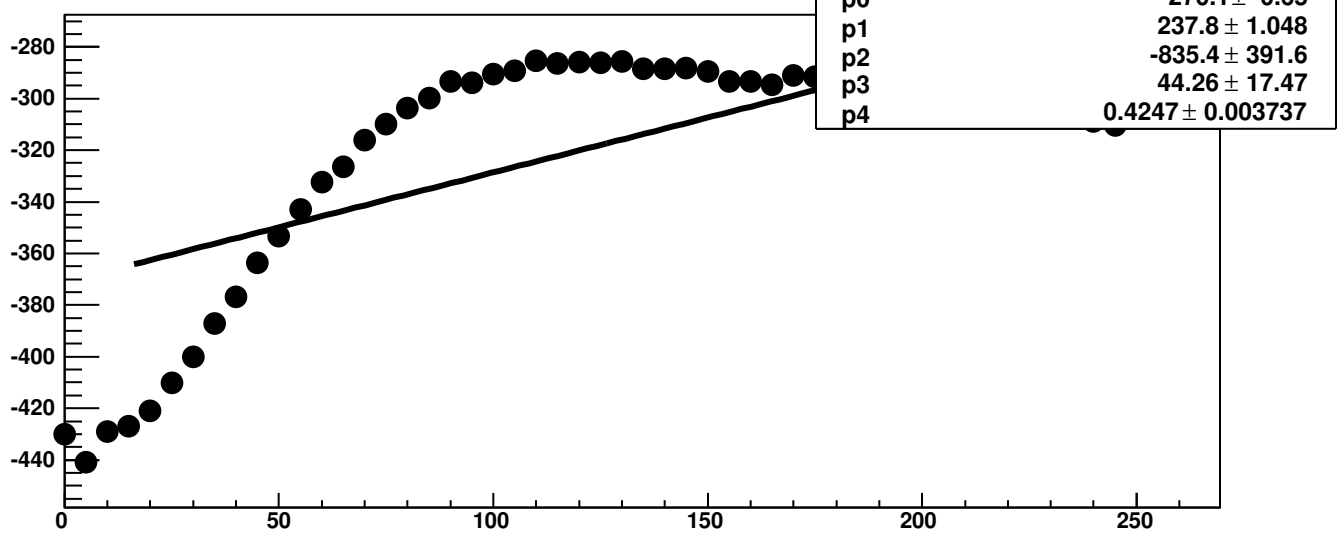
Chip 9, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



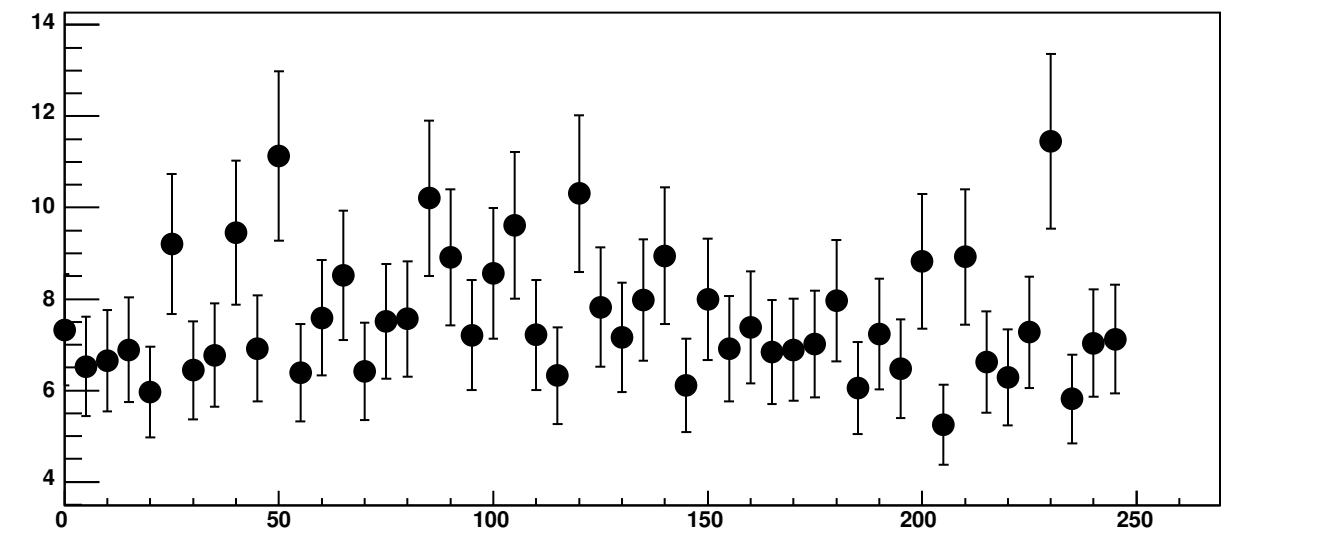
Chip 9, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold



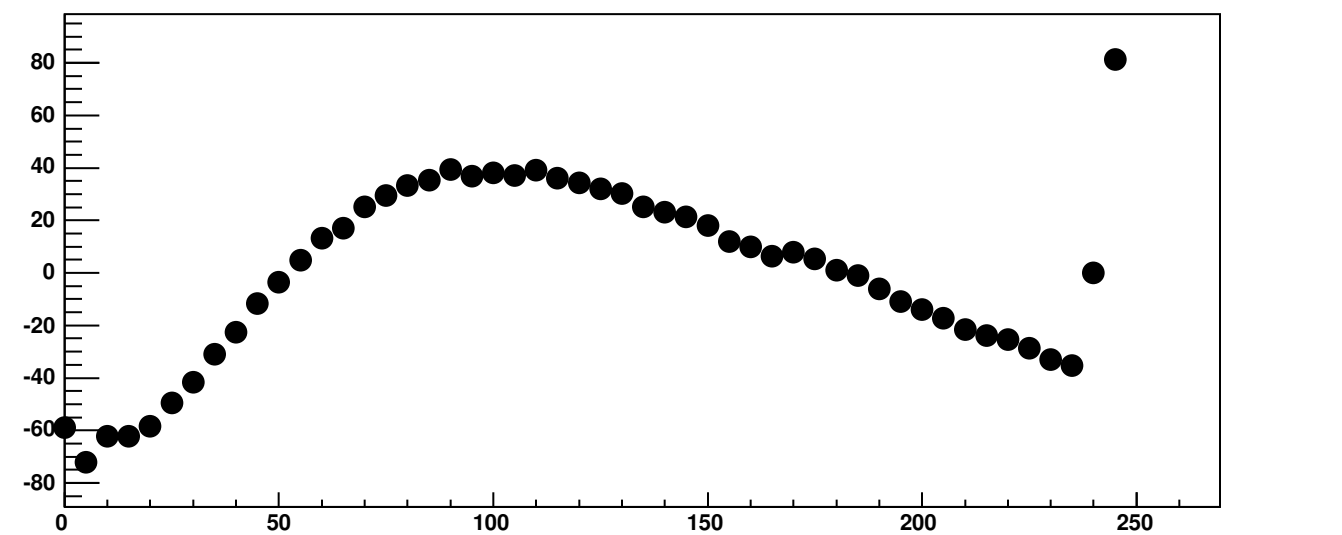
Chip 9, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

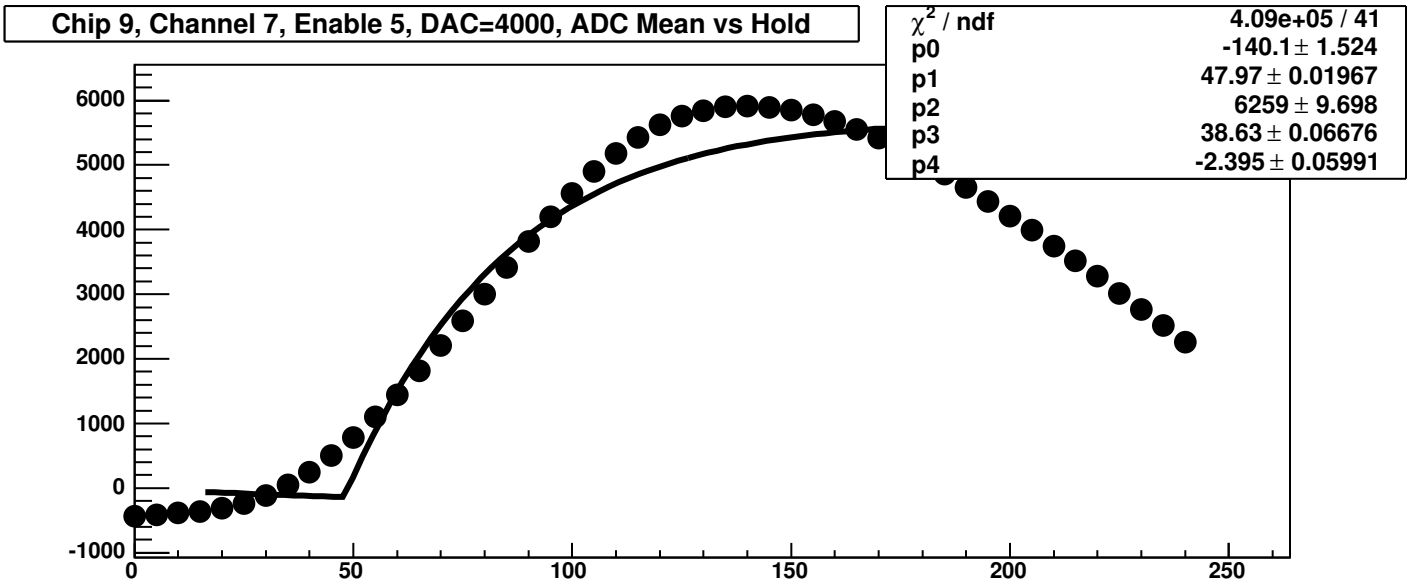


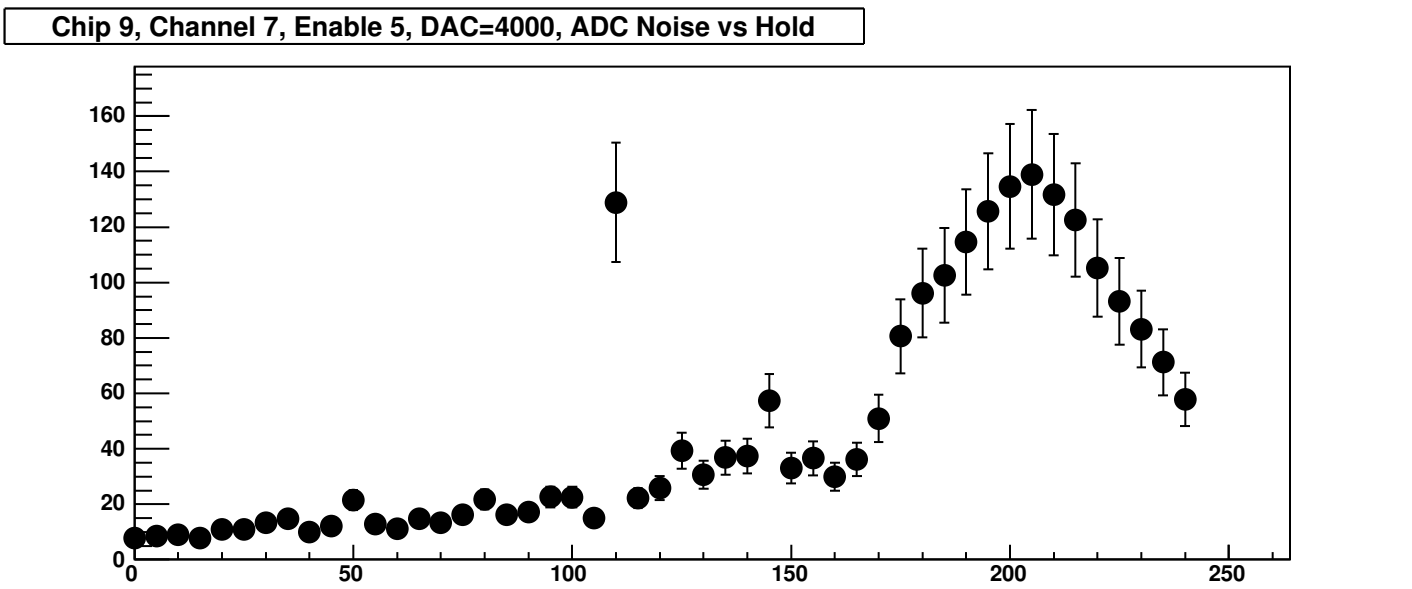
Chip 9, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold

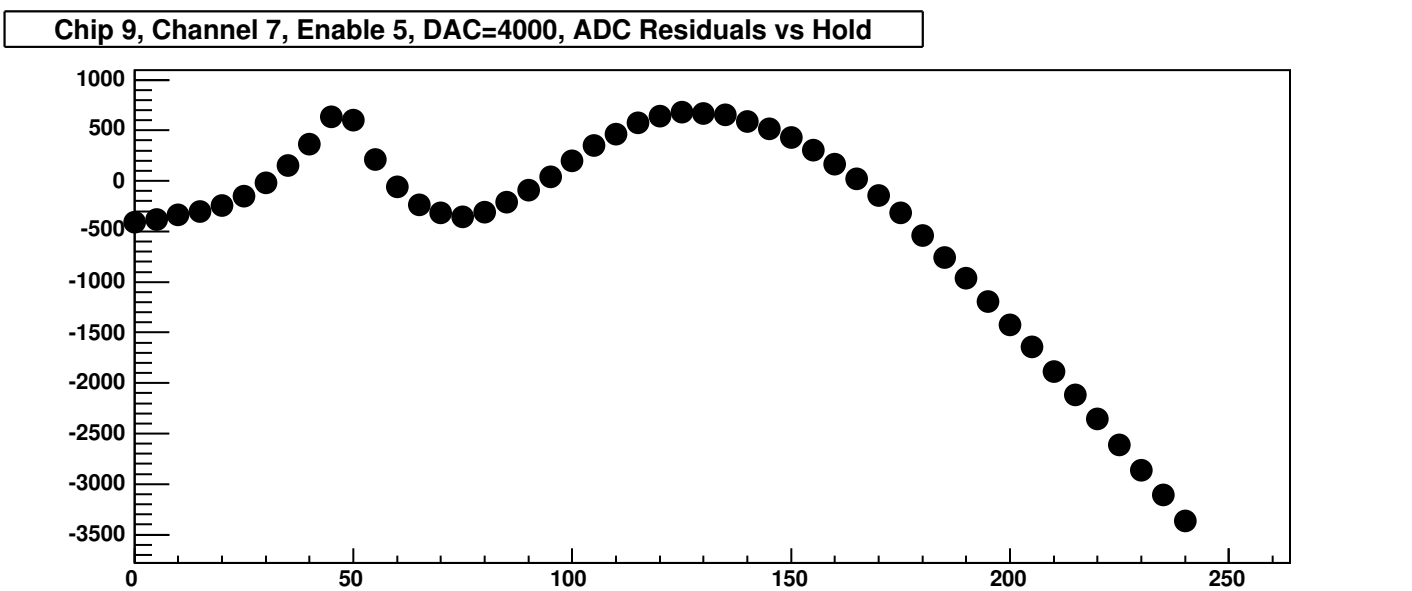


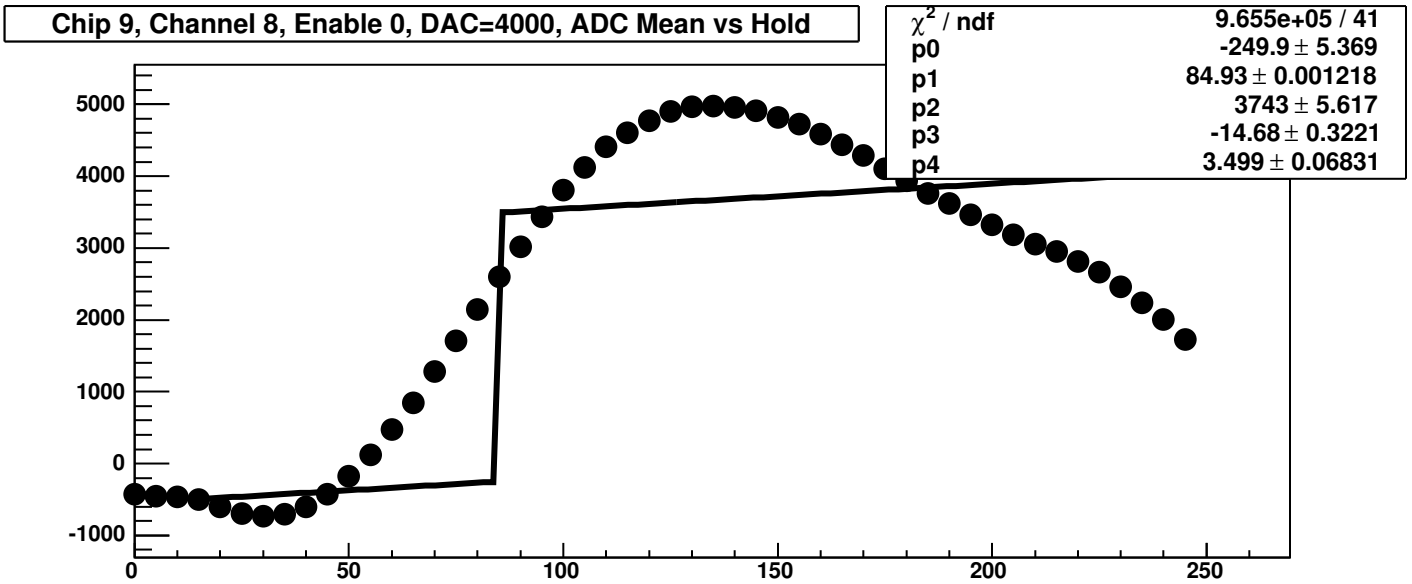
Chip 9, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

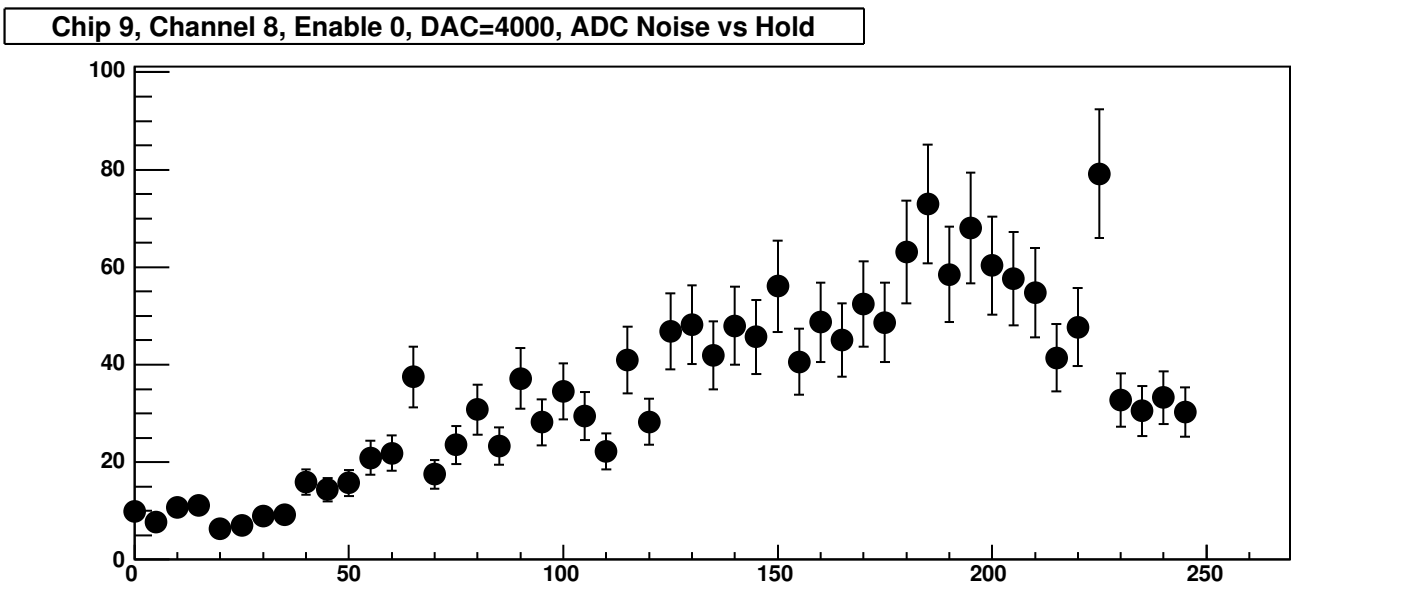


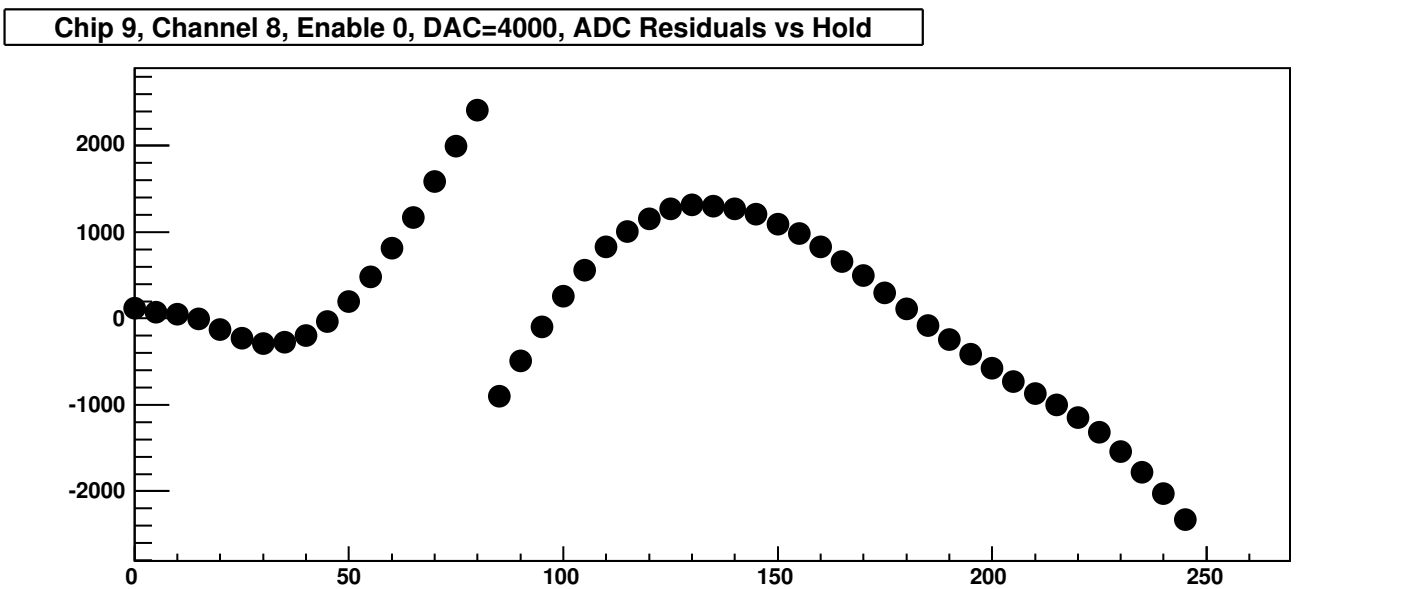

Chip 9, Channel 7, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.09e+05 / 41
p0 -140.1 ± 1.524
p1 47.97 ± 0.01967
p2 6259 ± 9.698
p3 38.63 ± 0.06676
p4 -2.395 ± 0.05991
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

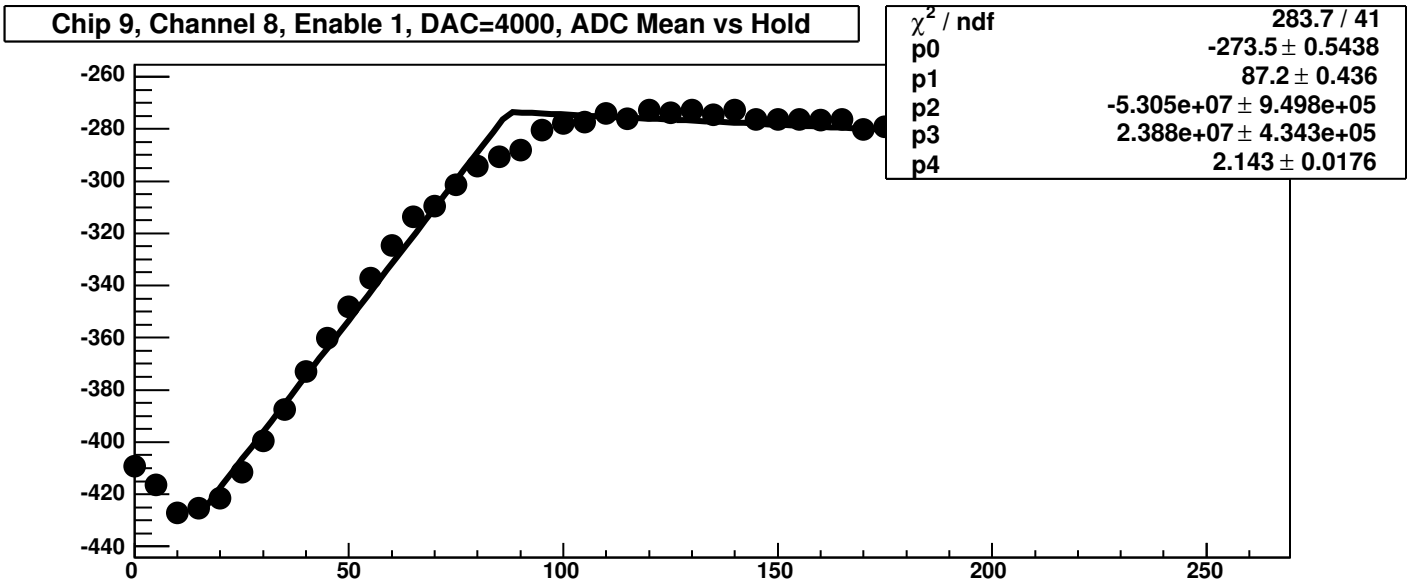

Chip 9, Channel 7, Enable 5, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

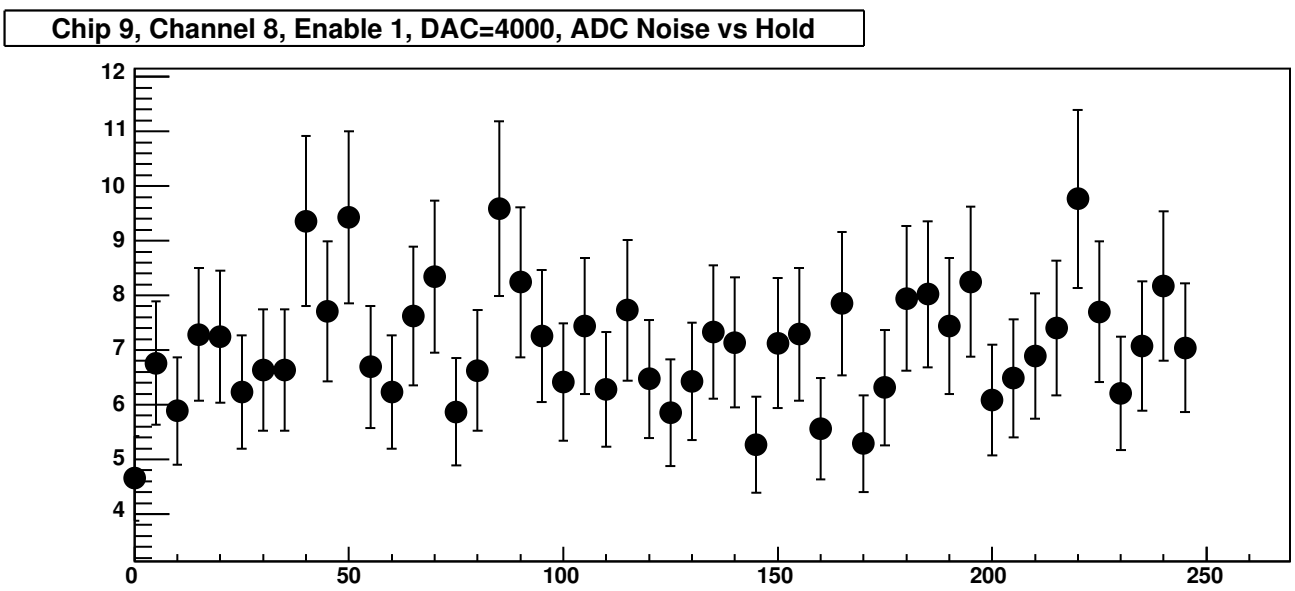

Chip 9, Channel 7, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
0
50
100
150
200
250

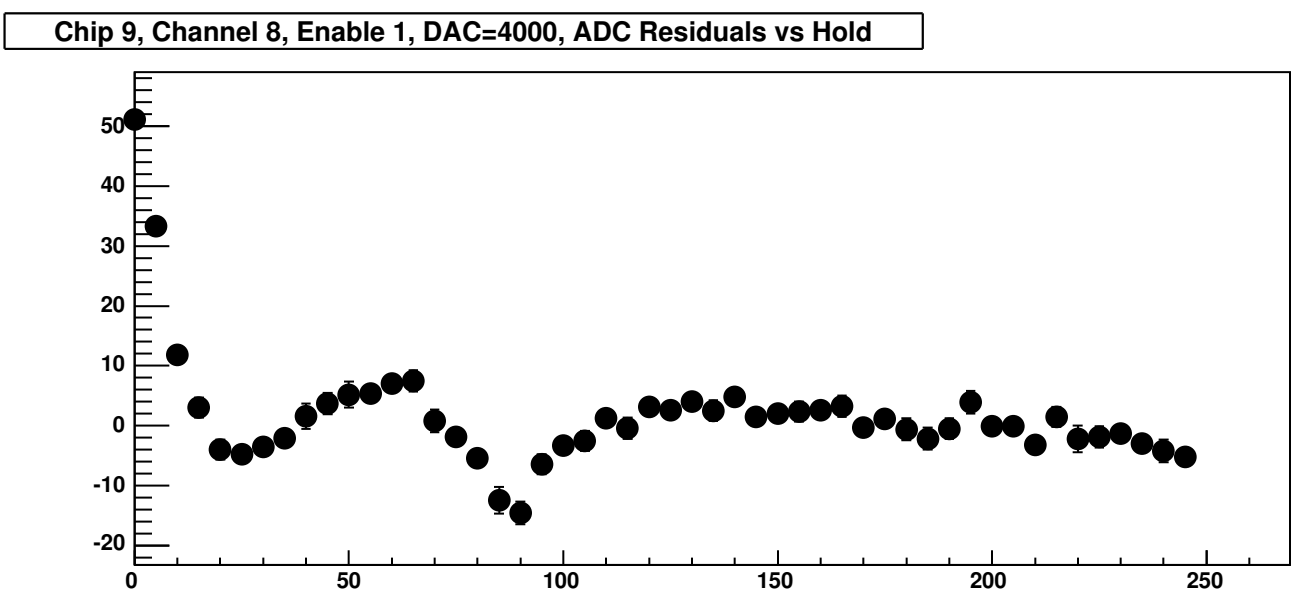

Chip 9, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 9.655e+05 / 41
p0 -249.9 ± 5.369
p1 84.93 ± 0.001218
p2 3743 ± 5.617
p3 -14.68 ± 0.3221
p4 3.499 ± 0.06831
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 9, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250

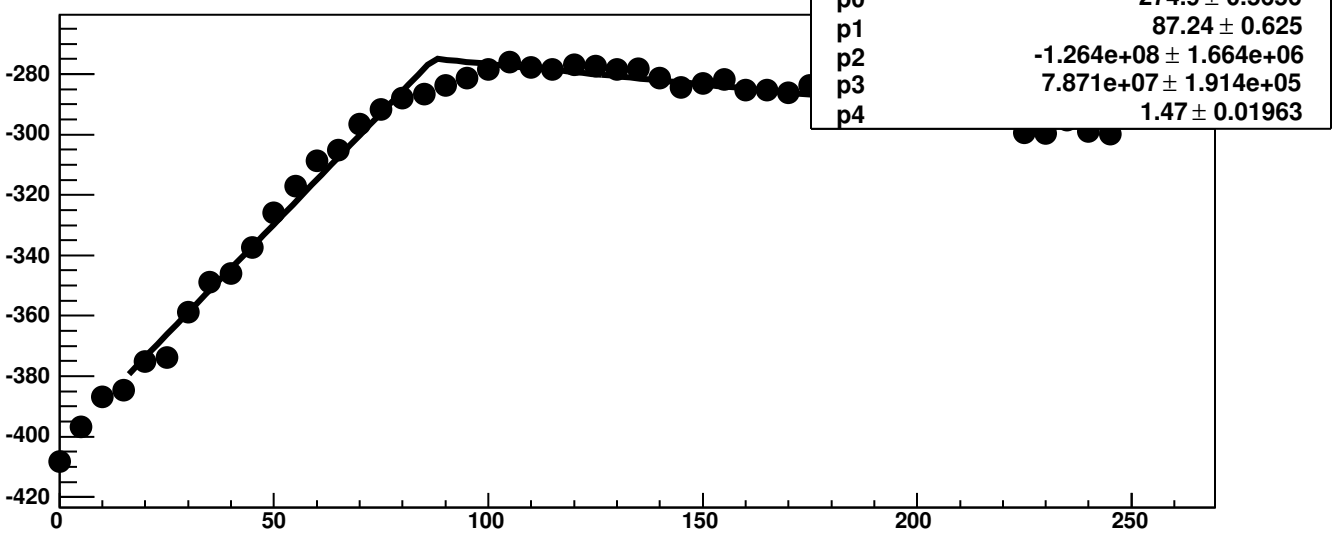

Chip 9, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
0
50
100
150
200
250


Chip 9, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 283.7 / 41
p0 -273.5 ± 0.5438
p1 87.2 ± 0.436
p2 -5.305e+07 ± 9.498e+05
p3 2.388e+07 ± 4.343e+05
p4 2.143 ± 0.0176
-260
-280
-300
-320
-340
-360
-380
-400
-420
-440
0
50
100
150
200
250

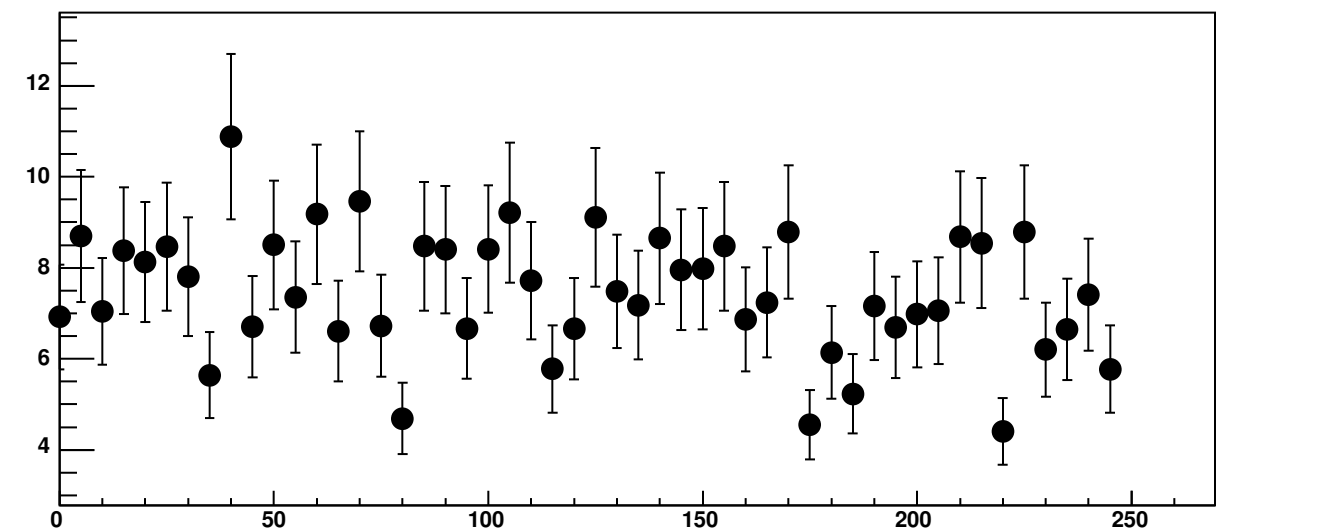

Chip 9, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 9, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
-20
0
50
100
150
200
250

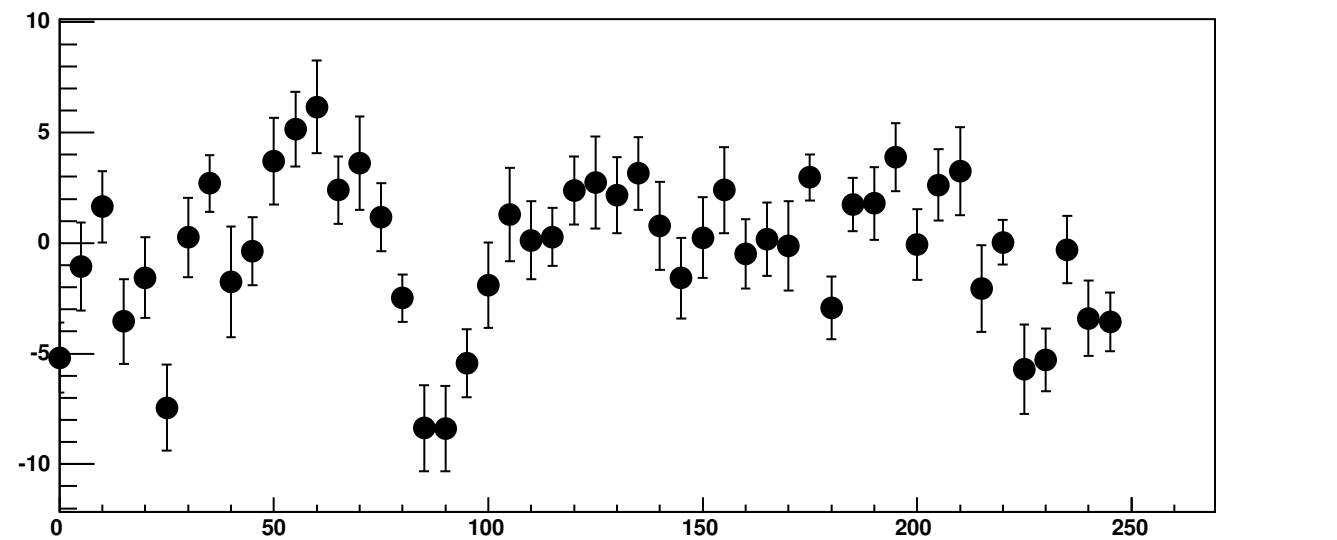
Chip 9, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold



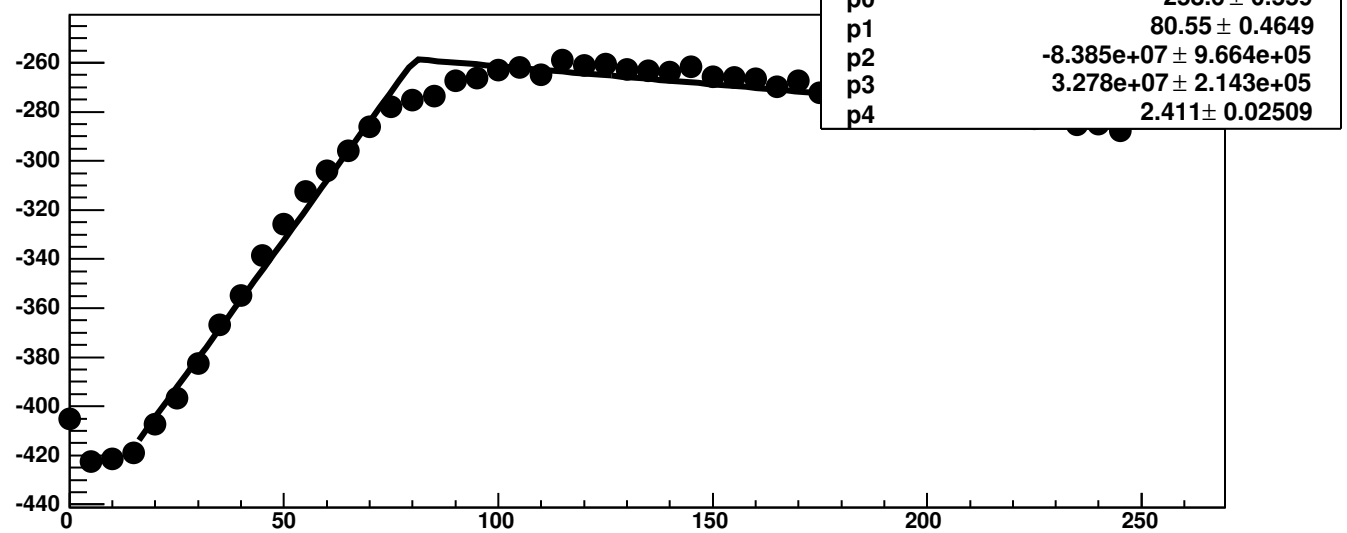
Chip 9, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold



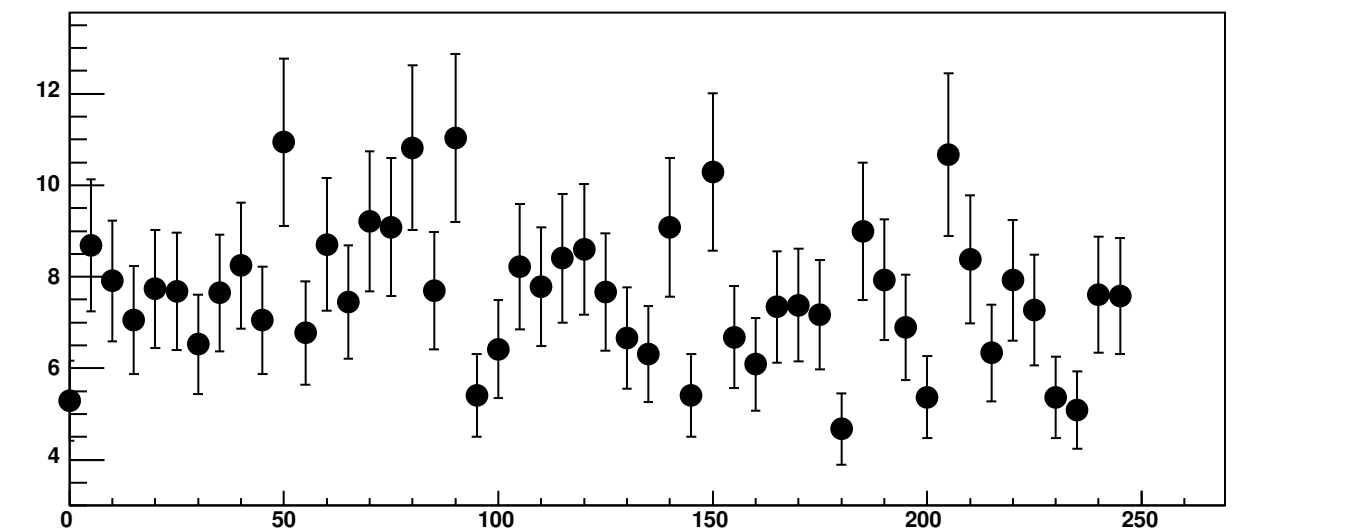
Chip 9, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold



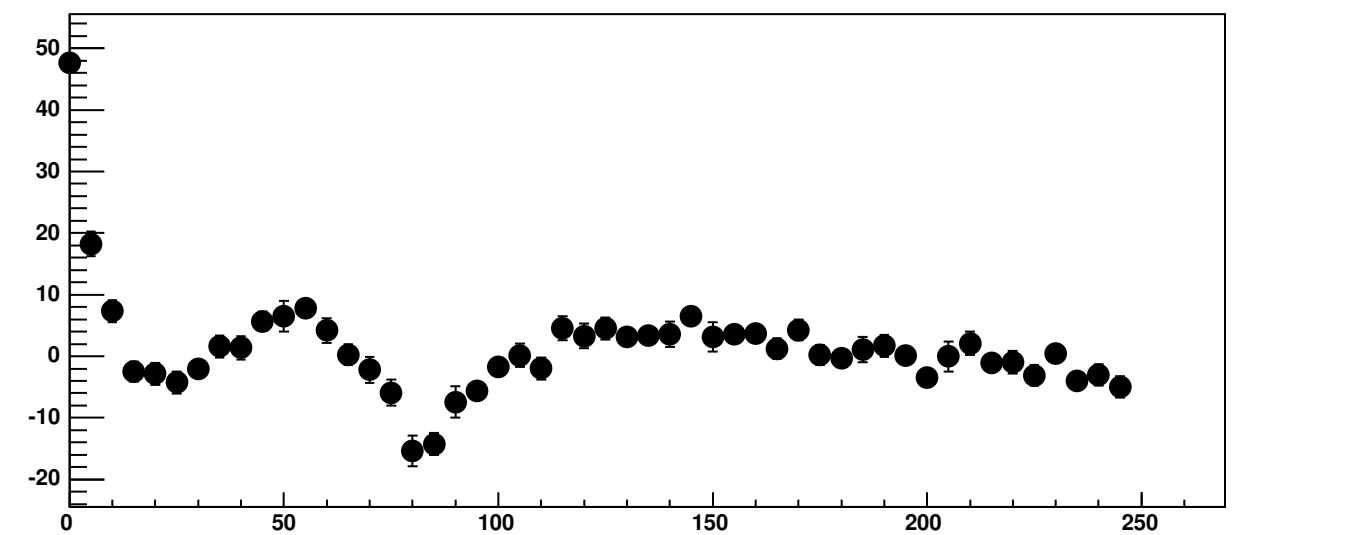
Chip 9, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

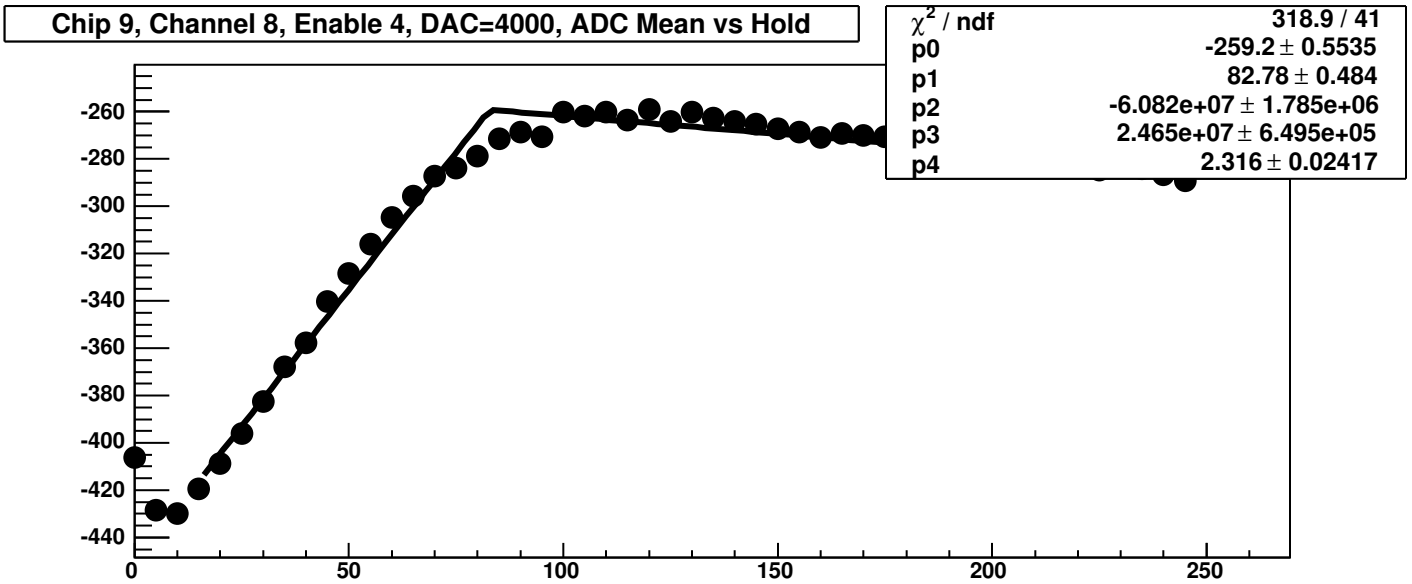


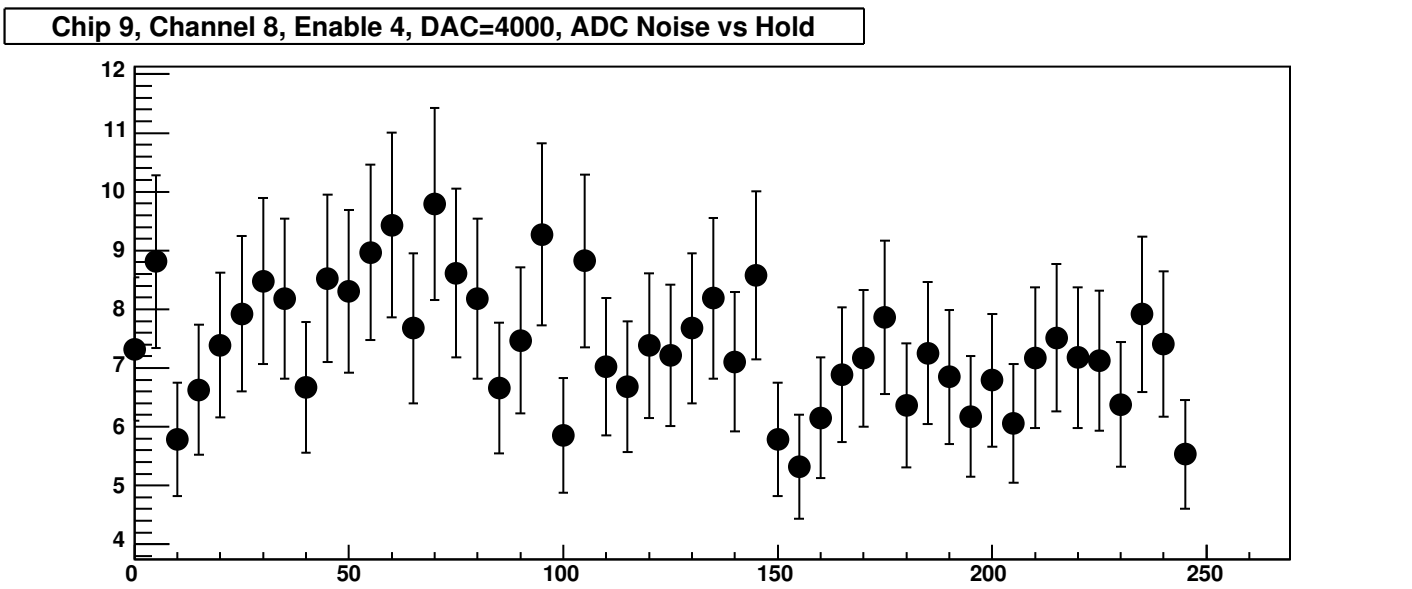
Chip 9, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold

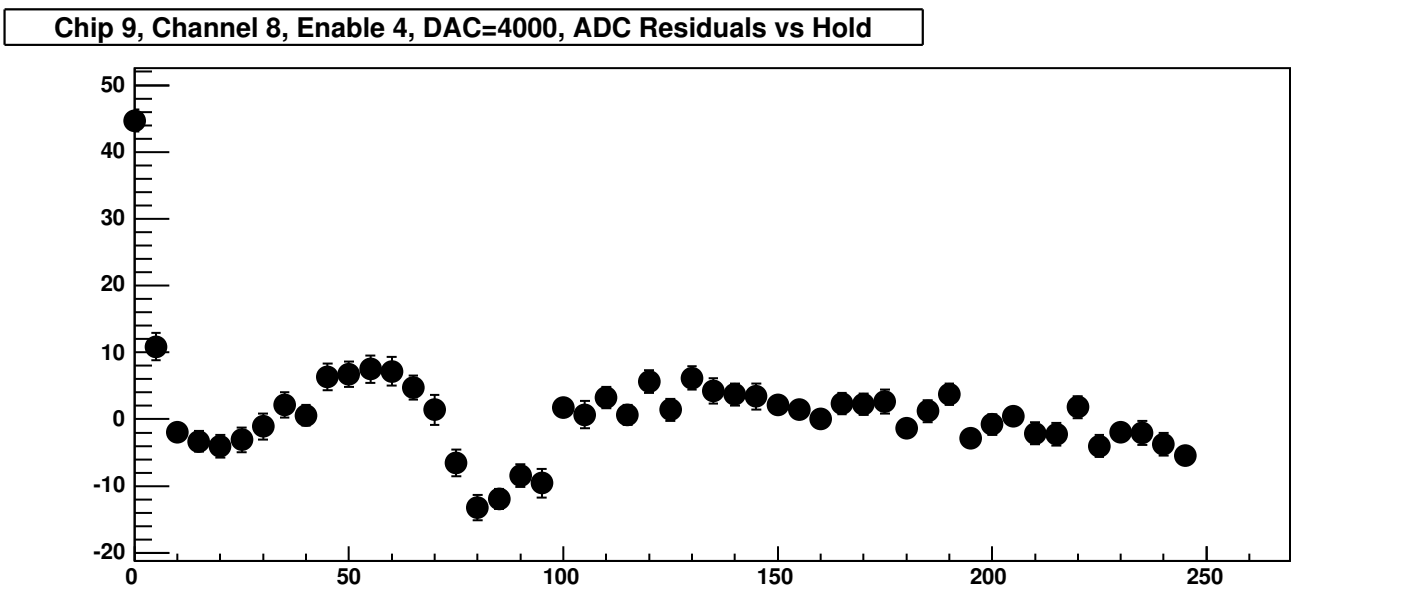


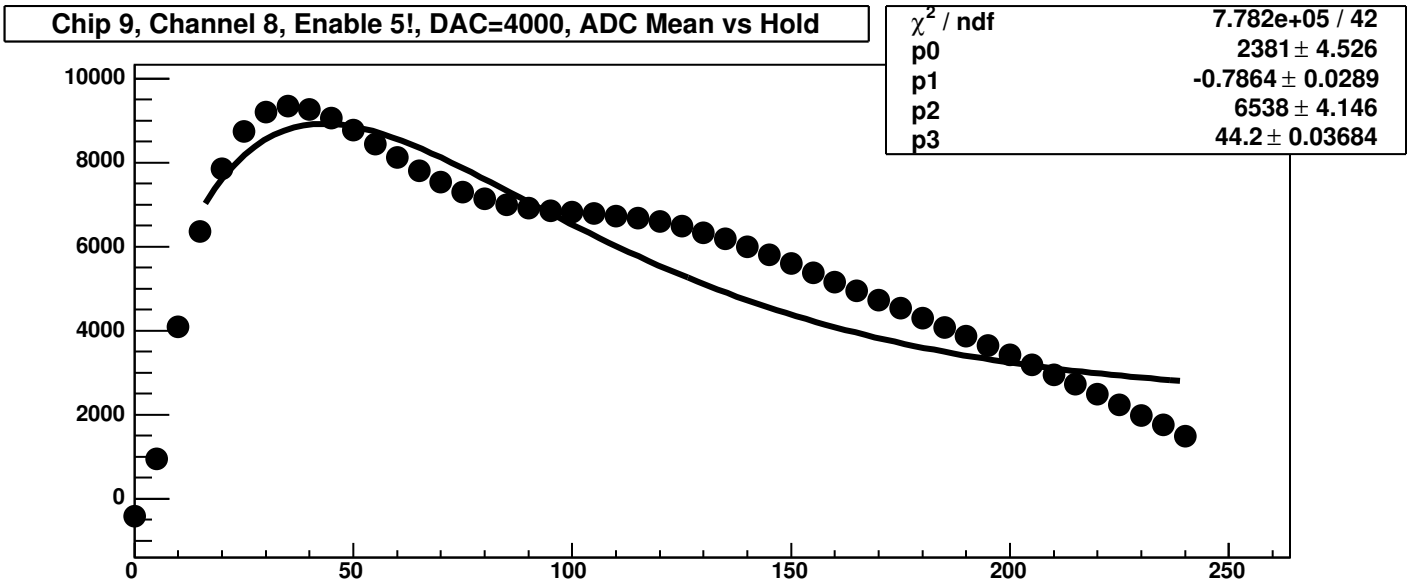
Chip 9, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

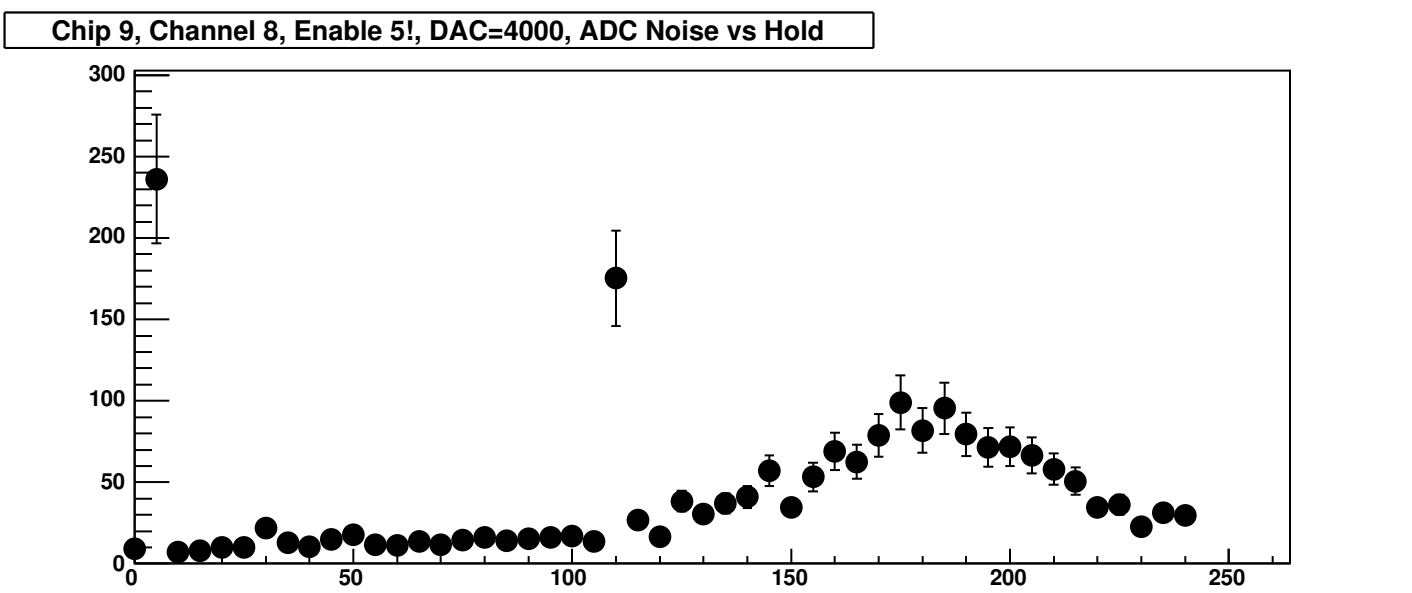


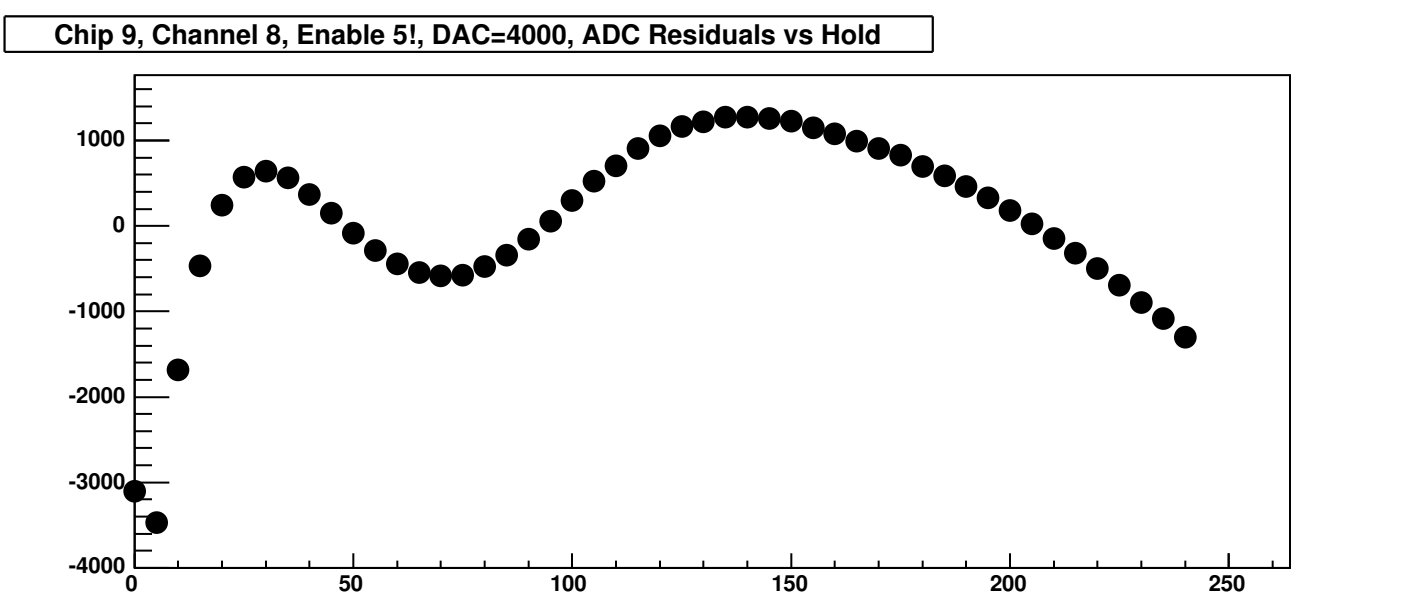

Chip 9, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 318.9 / 41
p0 -259.2 ± 0.5535
p1 82.78 ± 0.484
p2 -6.082e+07 ± 1.785e+06
p3 2.465e+07 ± 6.495e+05
p4 2.316 ± 0.02417
-260
-280
-300
-320
-340
-360
-380
-400
-420
-440
0
50
100
150
200
250

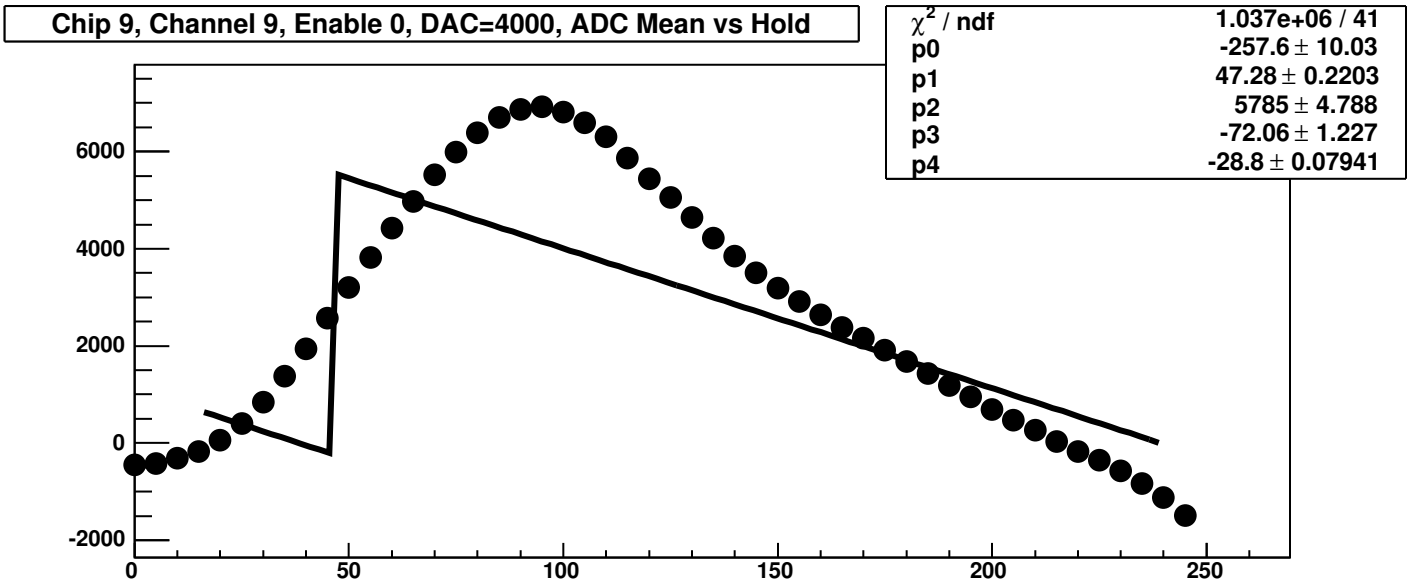

Chip 9, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

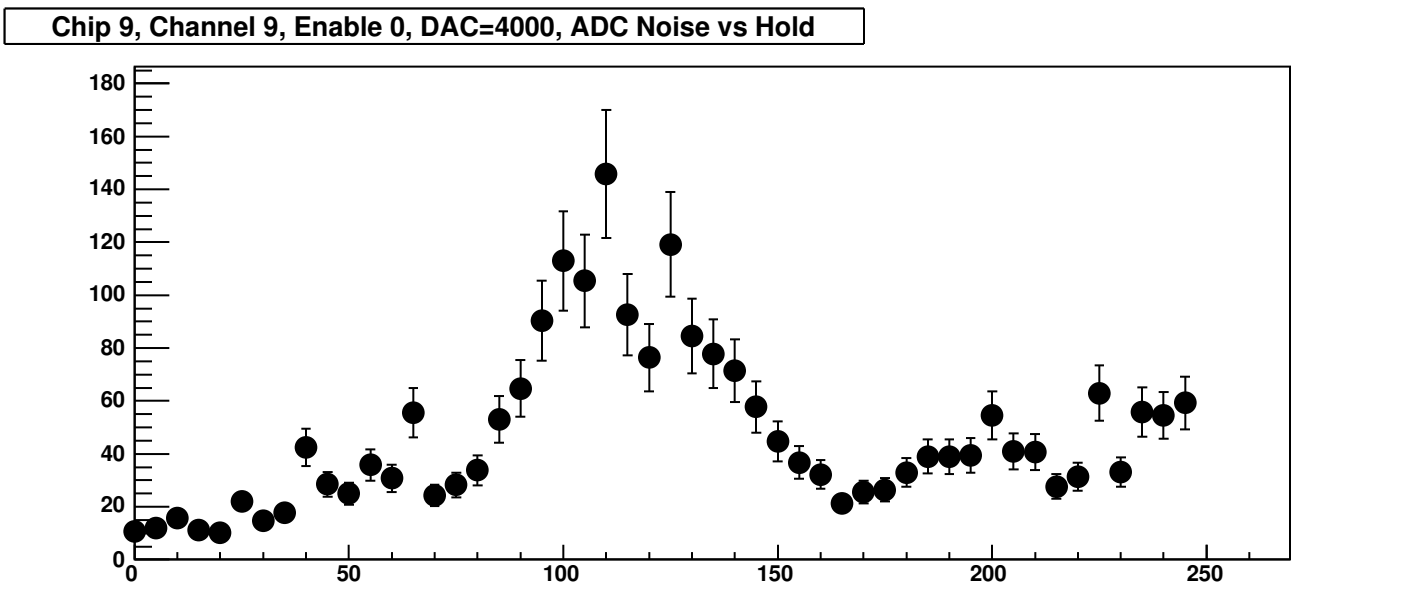

Chip 9, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
-20
0
50
100
150
200
250

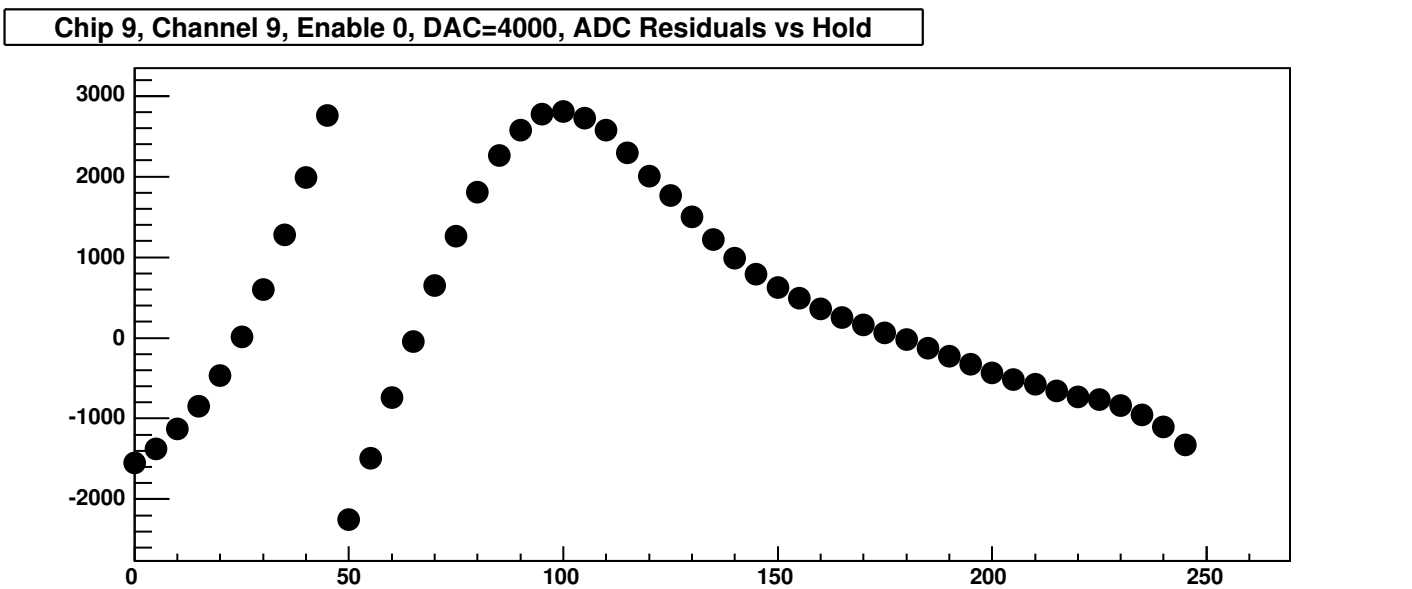

Chip 9, Channel 8, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 7.782e+05 / 42
p0 2381 ± 4.526
p1 -0.7864 ± 0.0289
p2 6538 ± 4.146
p3 44.2 ± 0.03684
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 9, Channel 8, Enable 5!, DAC=4000, ADC Noise vs Hold
300
250
200
150
100
50
0
0
50
100
150
200
250

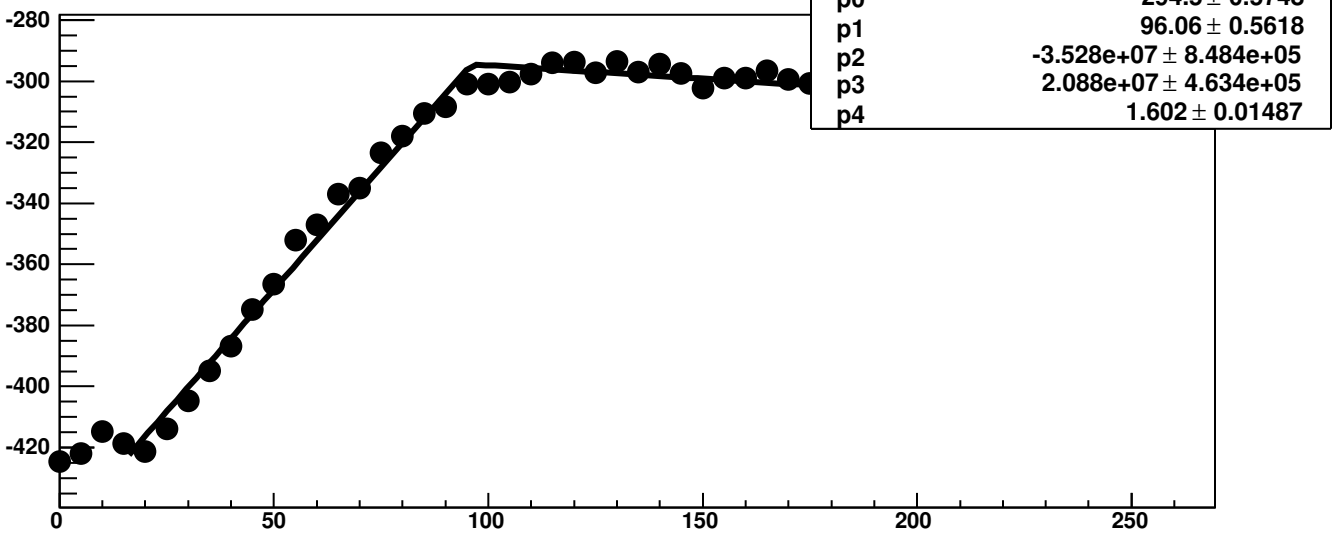

Chip 9, Channel 8, Enable 5!, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250


Chip 9, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.037e+06 / 41
p0 -257.6 ± 10.03
p1 47.28 ± 0.2203
p2 5785 ± 4.788
p3 -72.06 ± 1.227
p4 -28.8 ± 0.07941
6000
4000
2000
0
-2000
0
50
100
150
200
250

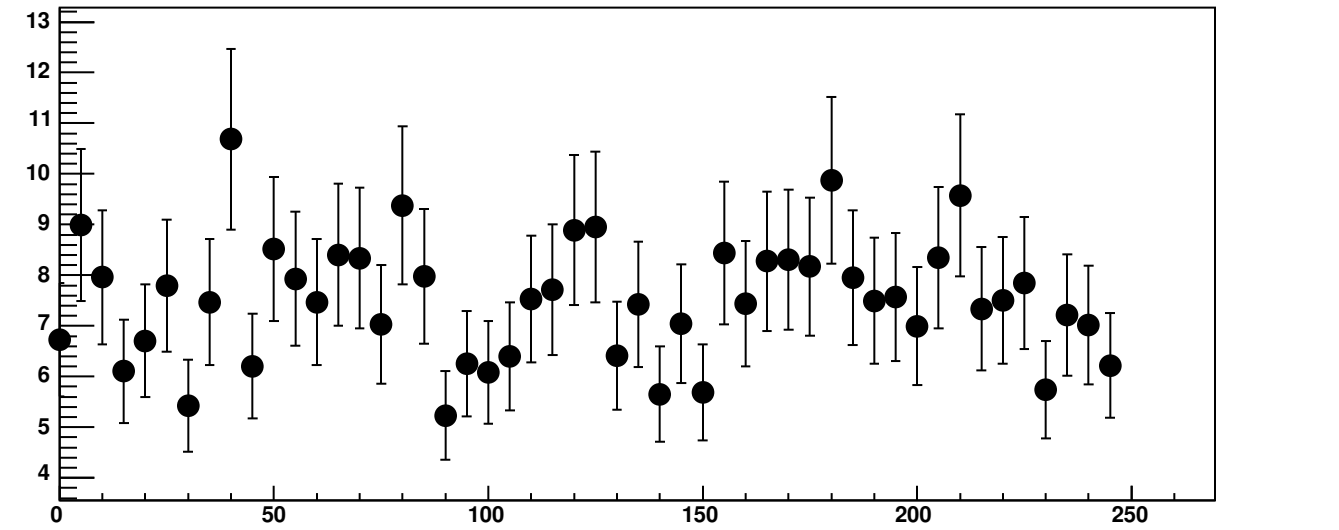

Chip 9, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 9, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
0
50
100
150
200
250

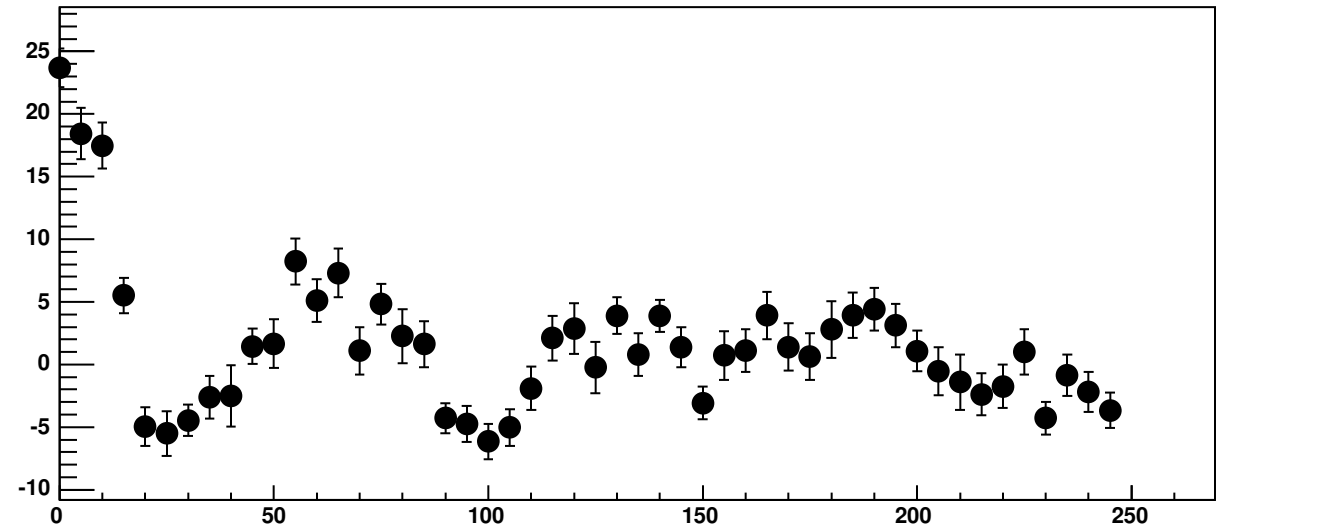
Chip 9, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold



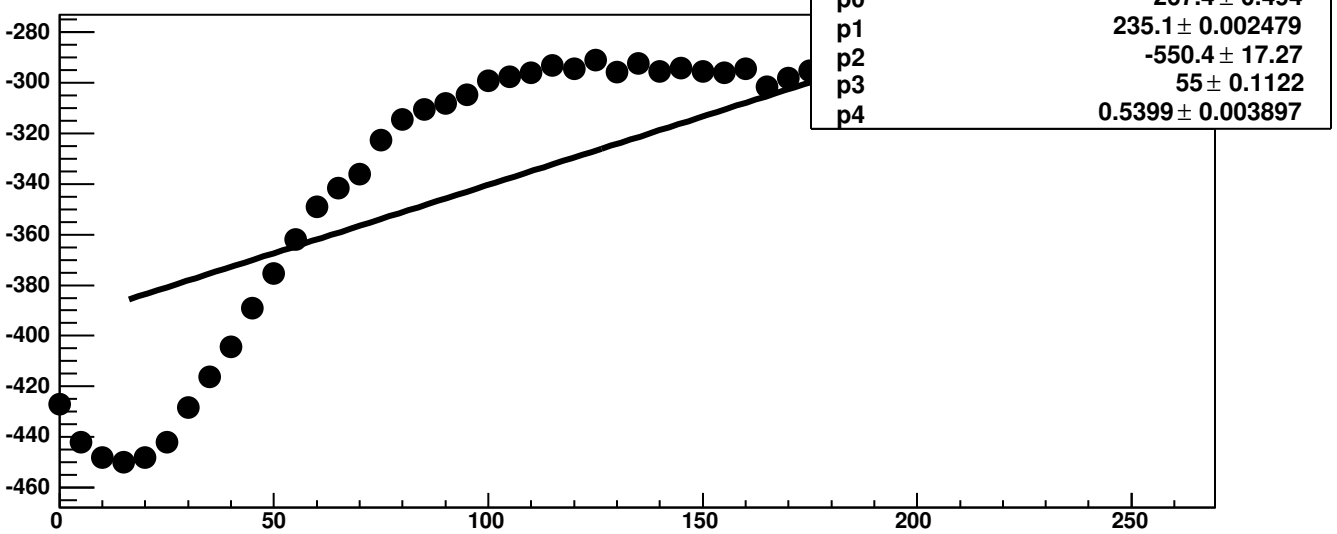
Chip 9, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



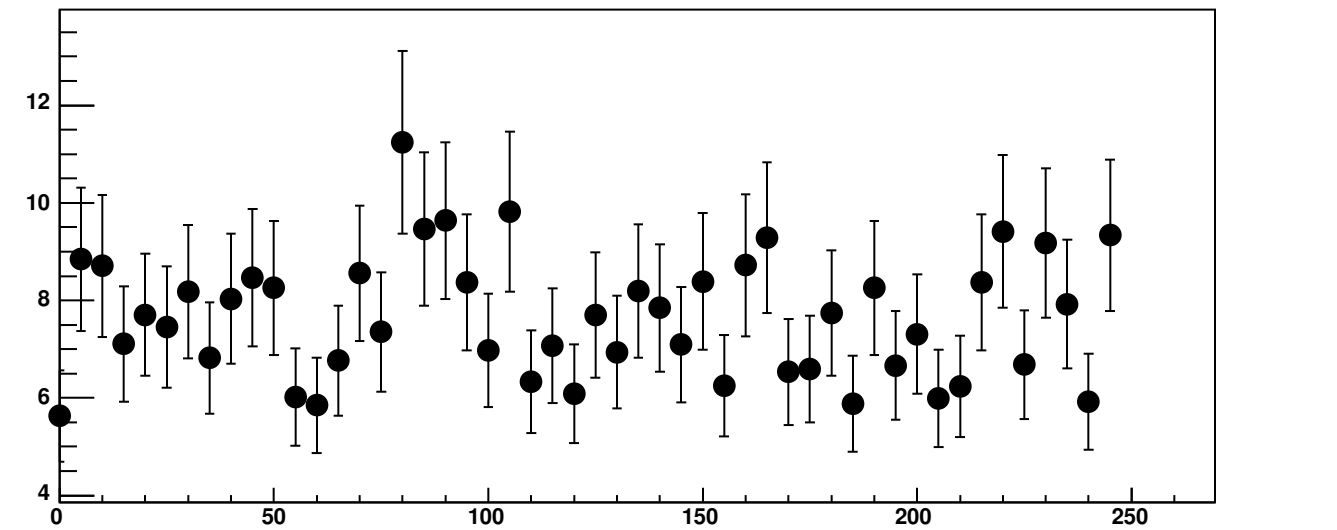
Chip 9, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold



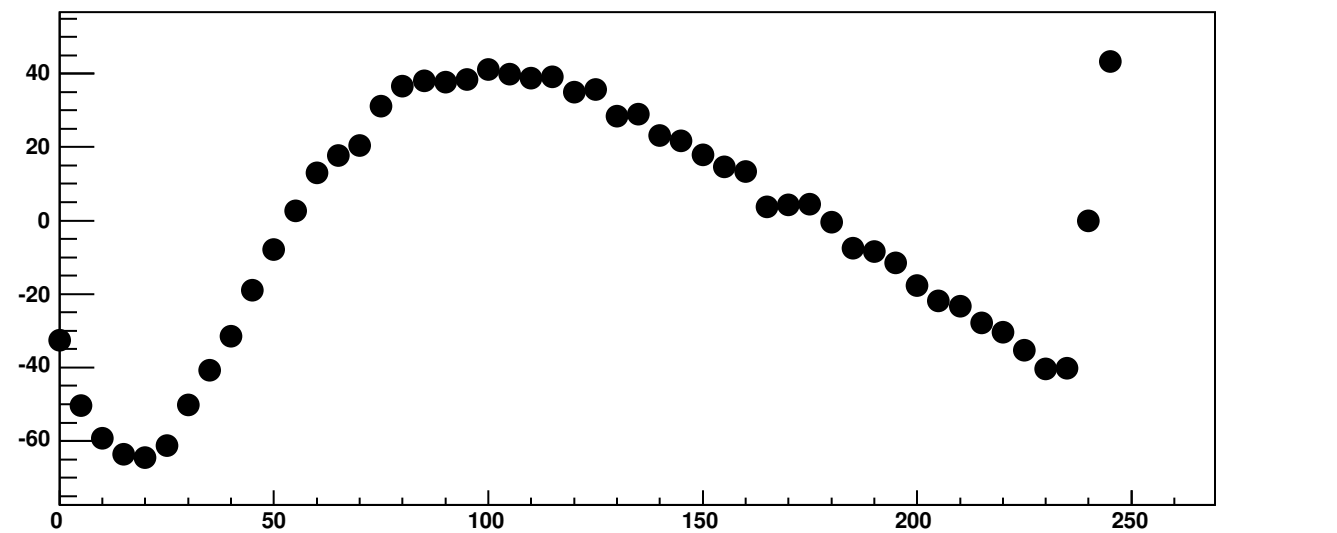
Chip 9, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold



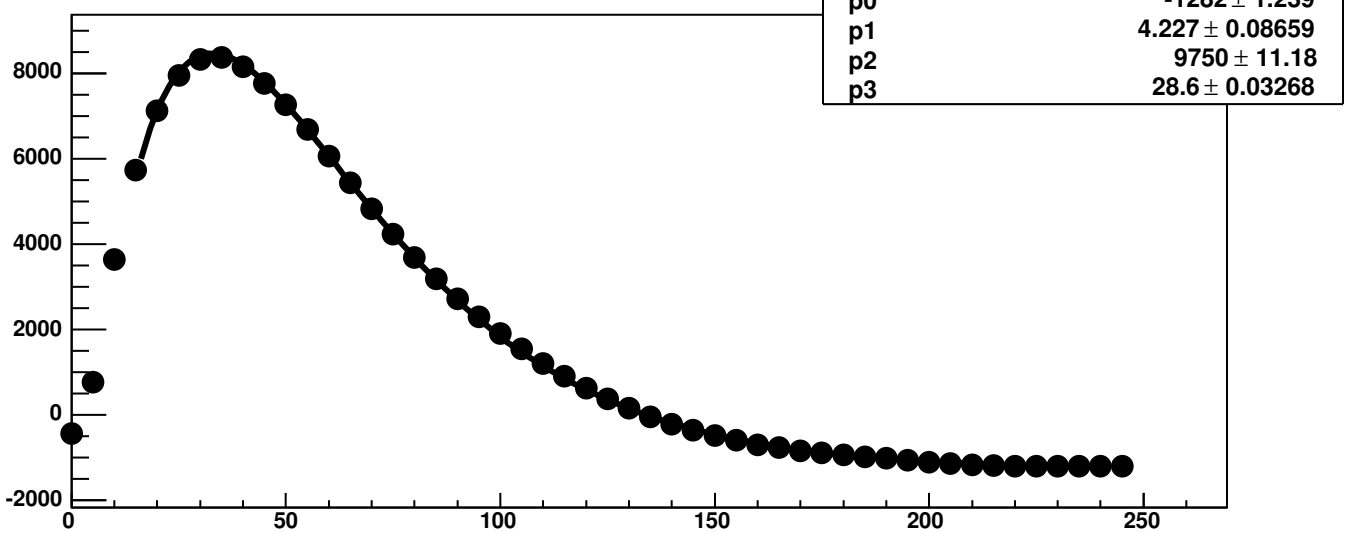
Chip 9, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



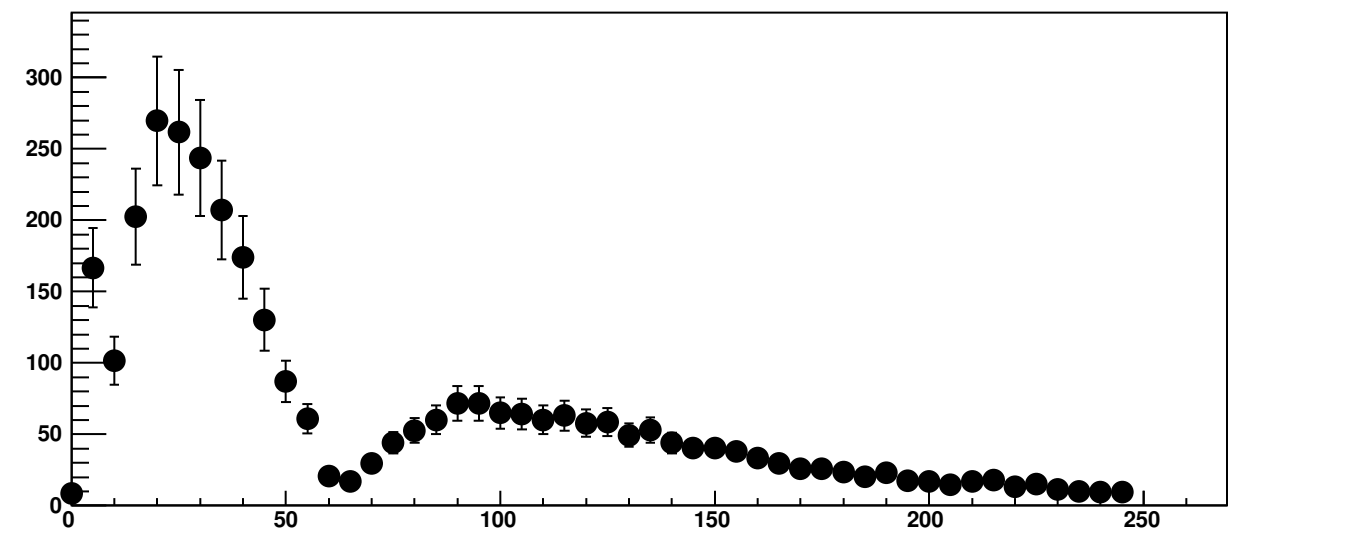
Chip 9, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold



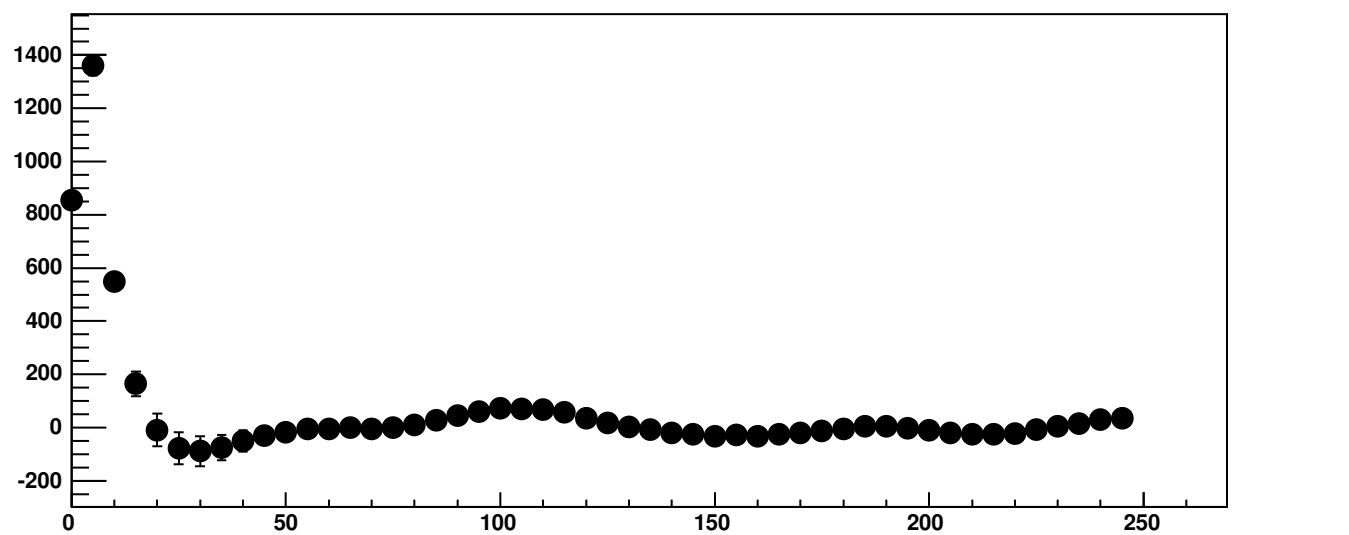
Chip 9, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold

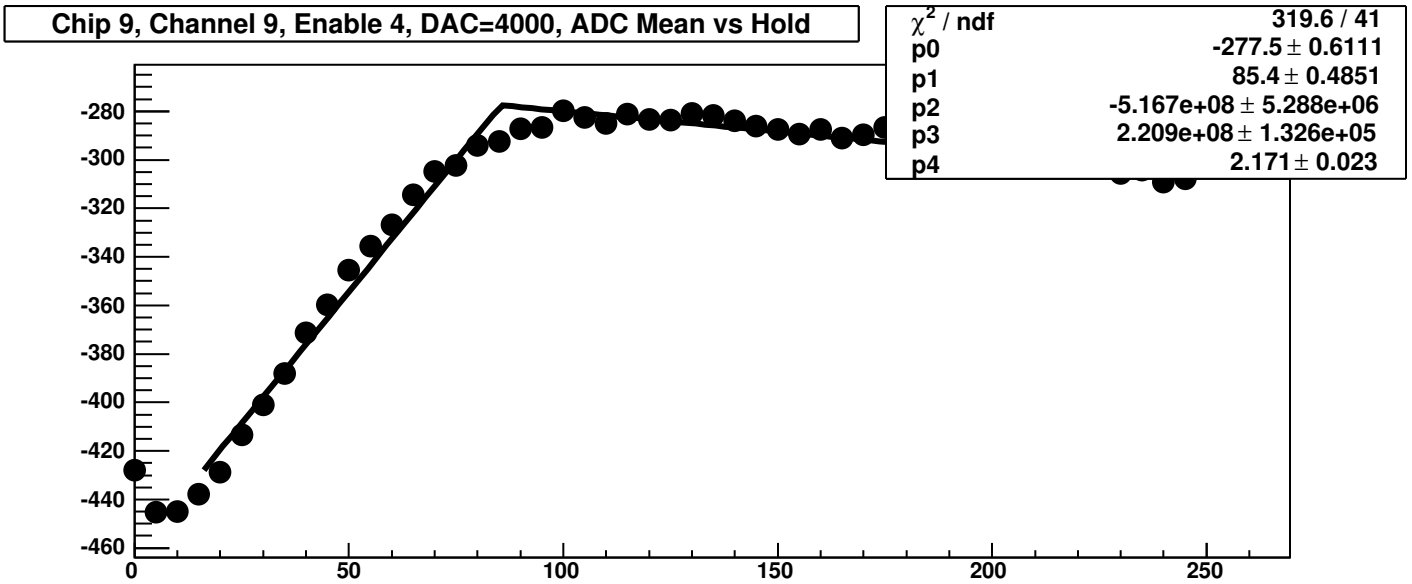


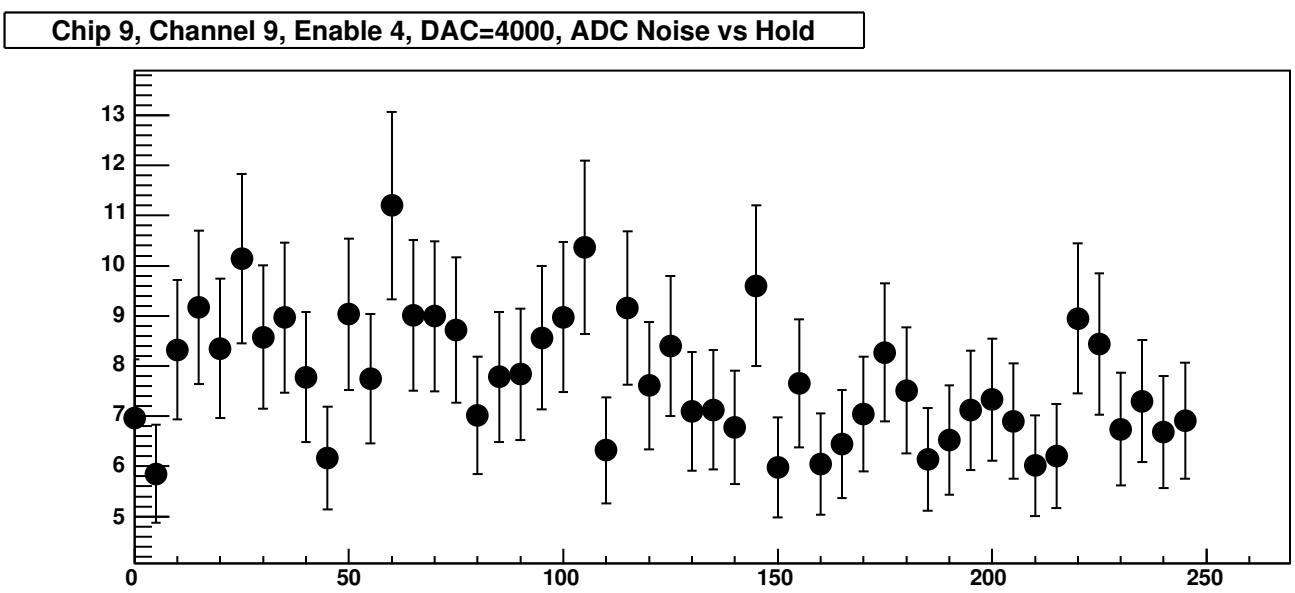
Chip 9, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold

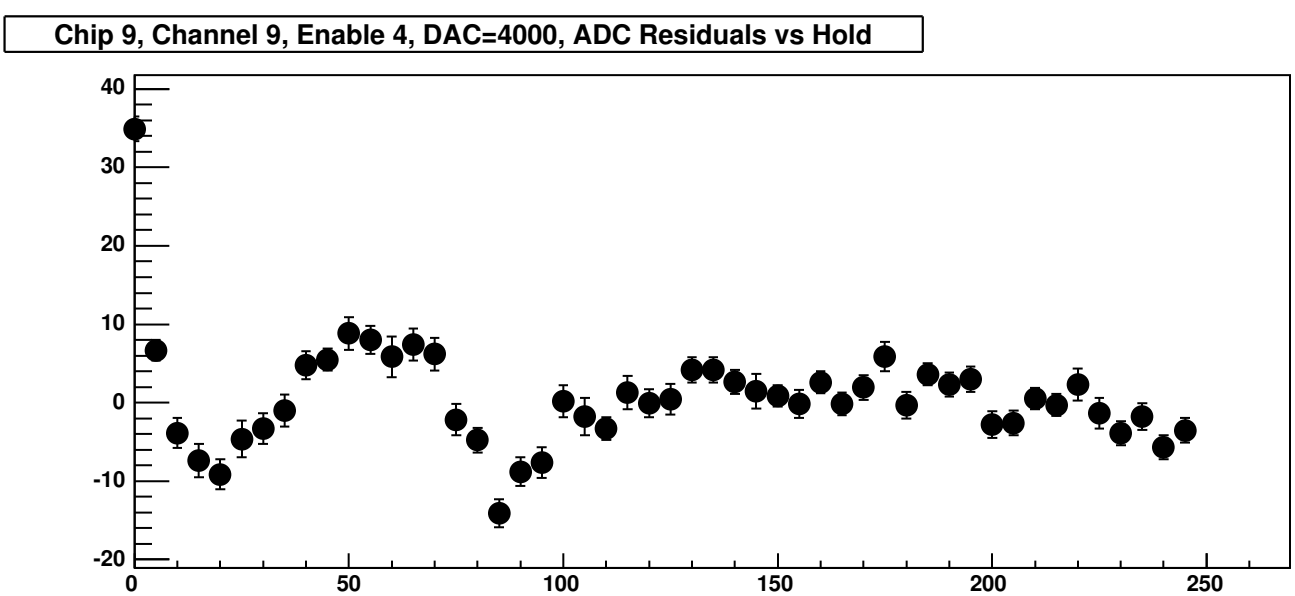


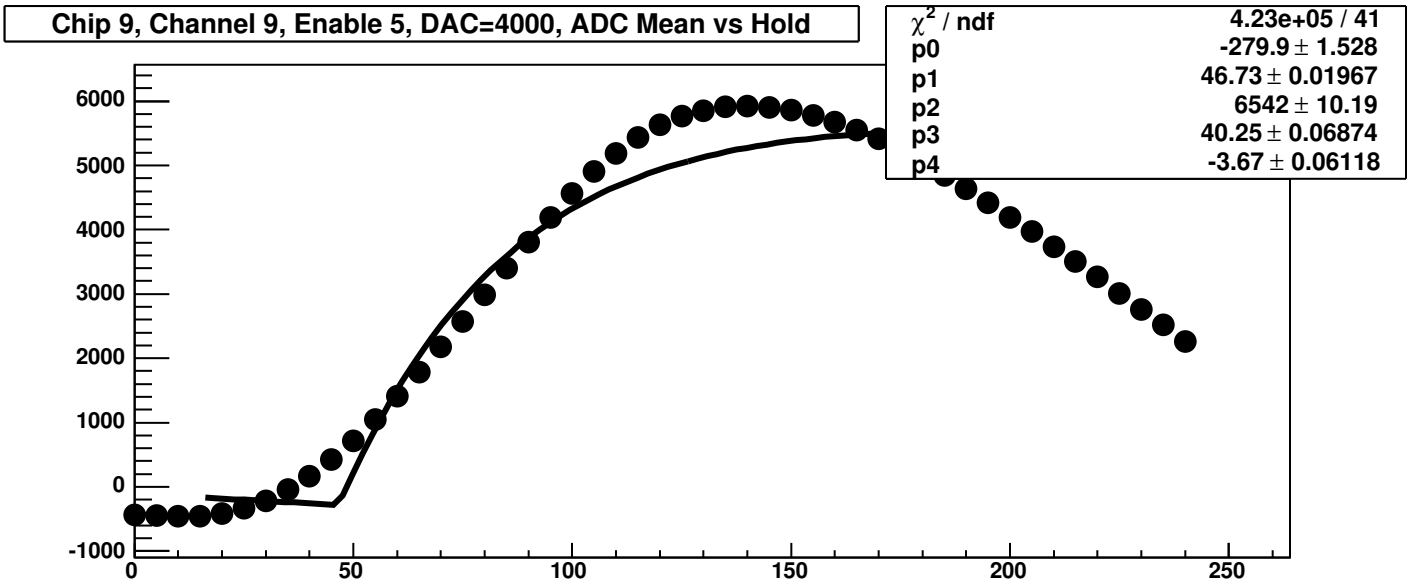
Chip 9, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold

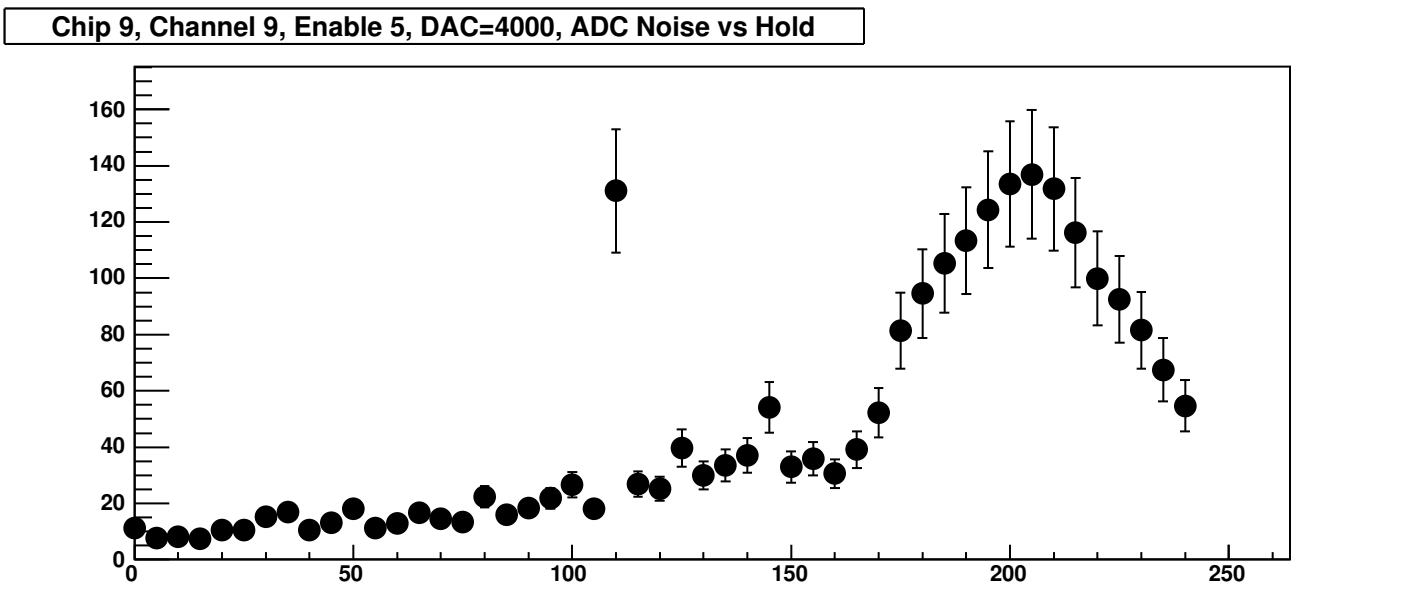


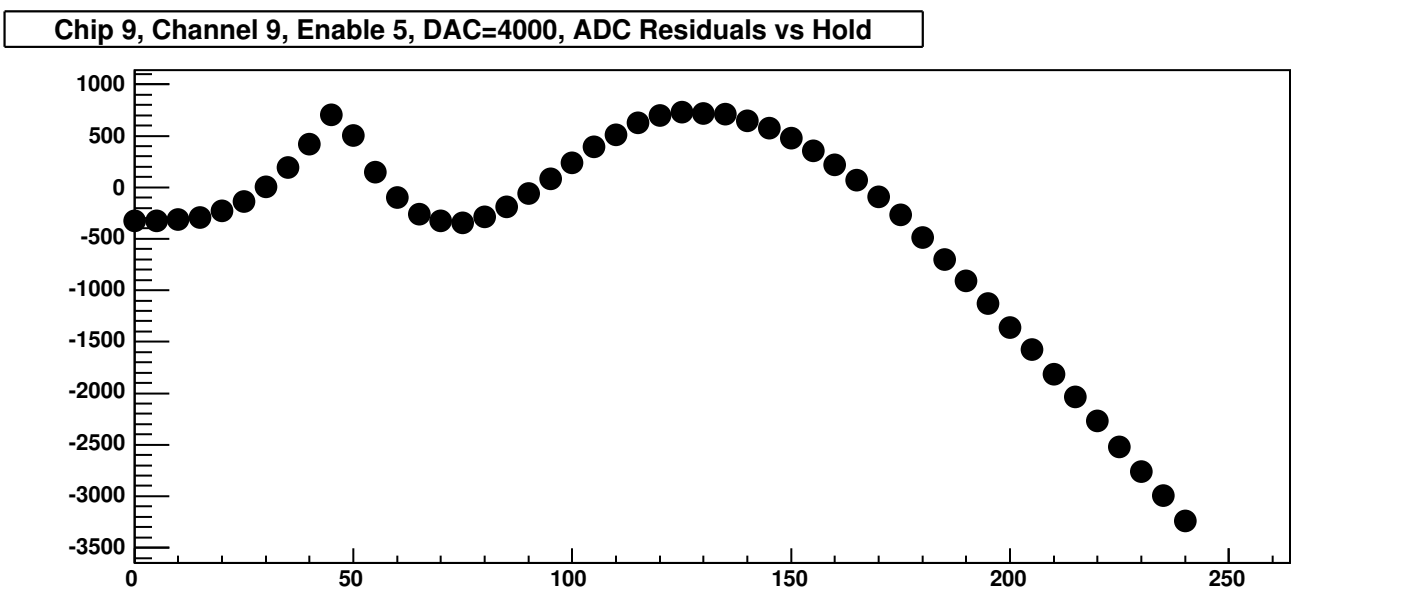

Chip 9, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 319.6 / 41
p0 -277.5 ± 0.6111
p1 85.4 ± 0.4851
p2 -5.167e+08 ± 5.288e+06
p3 2.209e+08 ± 1.326e+05
p4 2.171 ± 0.023
-280
-300
-320
-340
-360
-380
-400
-420
-440
-460
0
50
100
150
200
250


Chip 9, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
0
50
100
150
200
250


Chip 9, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
-20
0
50
100
150
200
250

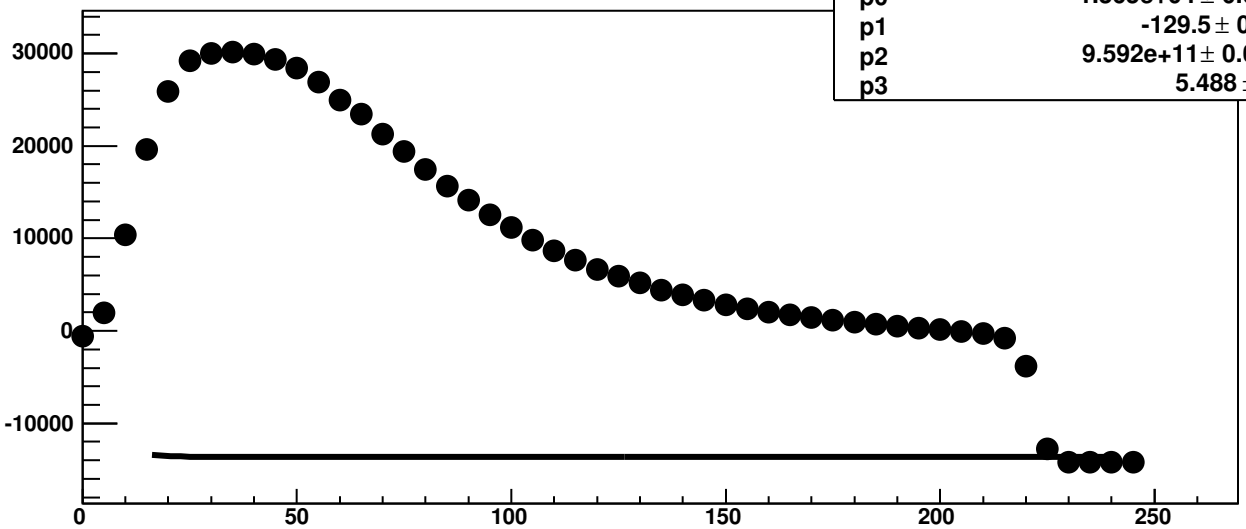

Chip 9, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.23e+05 / 41
p0 -279.9 ± 1.528
p1 46.73 ± 0.01967
p2 6542 ± 10.19
p3 40.25 ± 0.06874
p4 -3.67 ± 0.06118
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 9, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

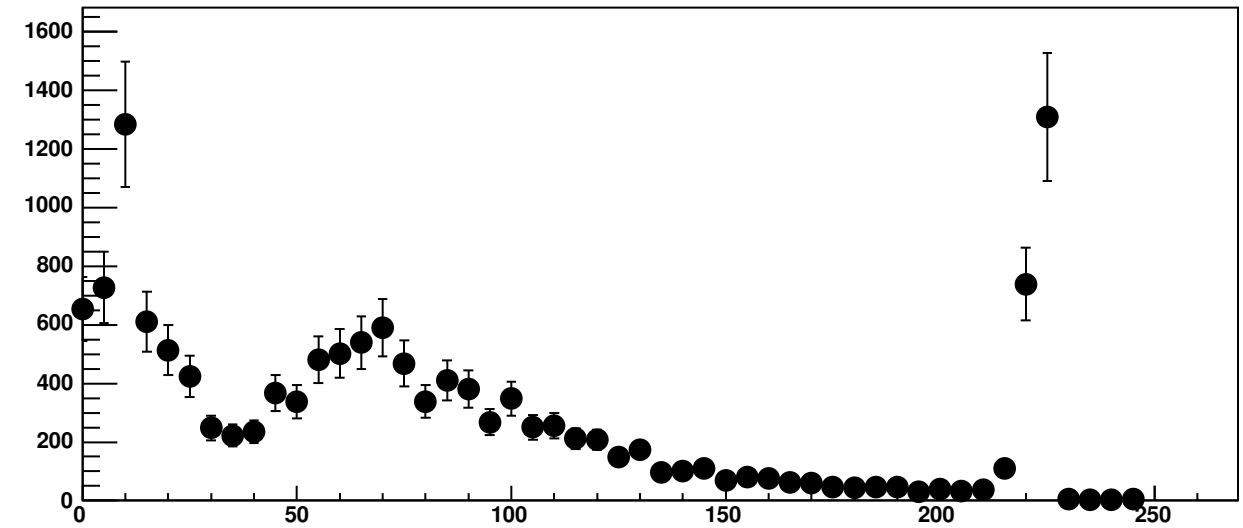

Chip 9, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
0
50
100
150
200
250

Chip 9, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold

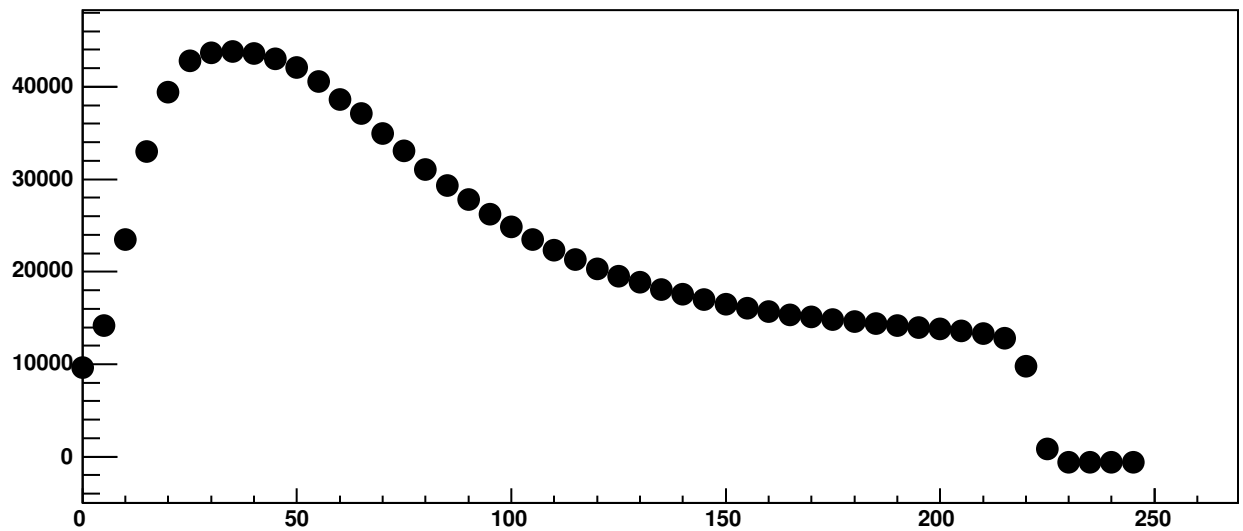
χ^2 / ndf	3.294e+07 / 42
p0	-1.365e+04 ± 0.001849
p1	-129.5 ± 0.03944
p2	9.592e+11 ± 0.001849
p3	5.488 ± 9.632



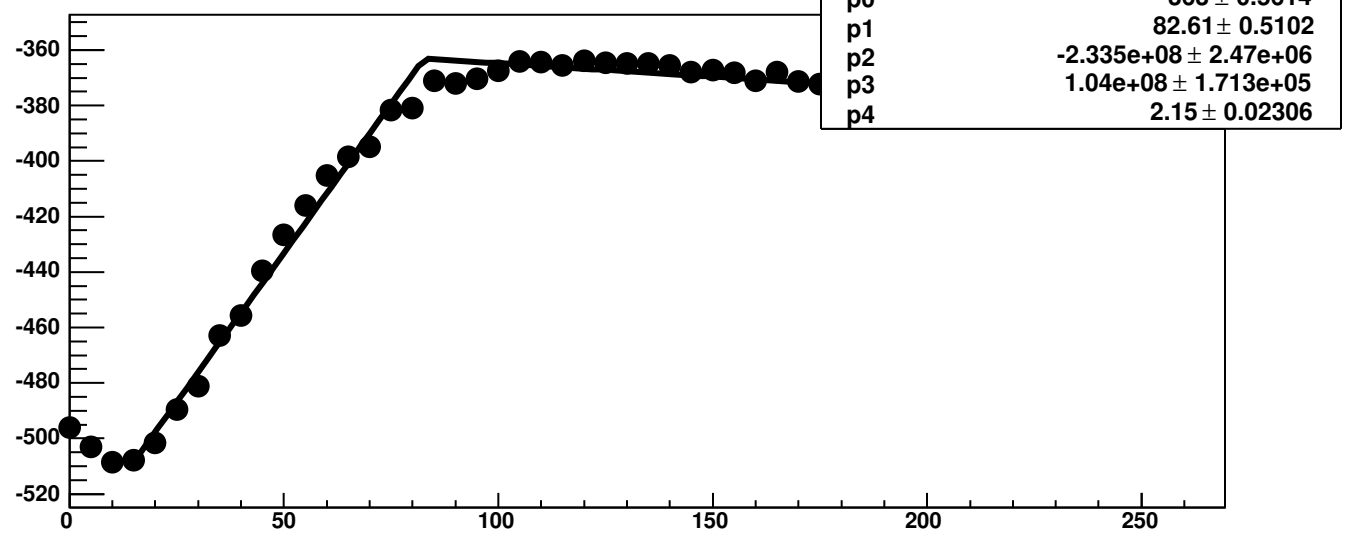
Chip 9, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold



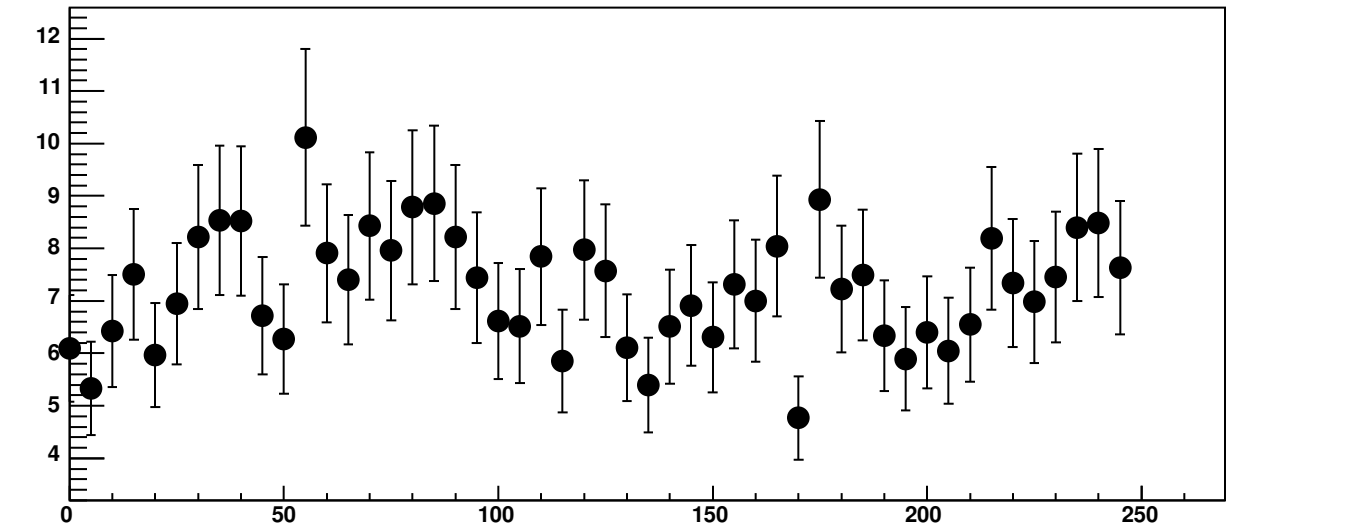
Chip 9, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold



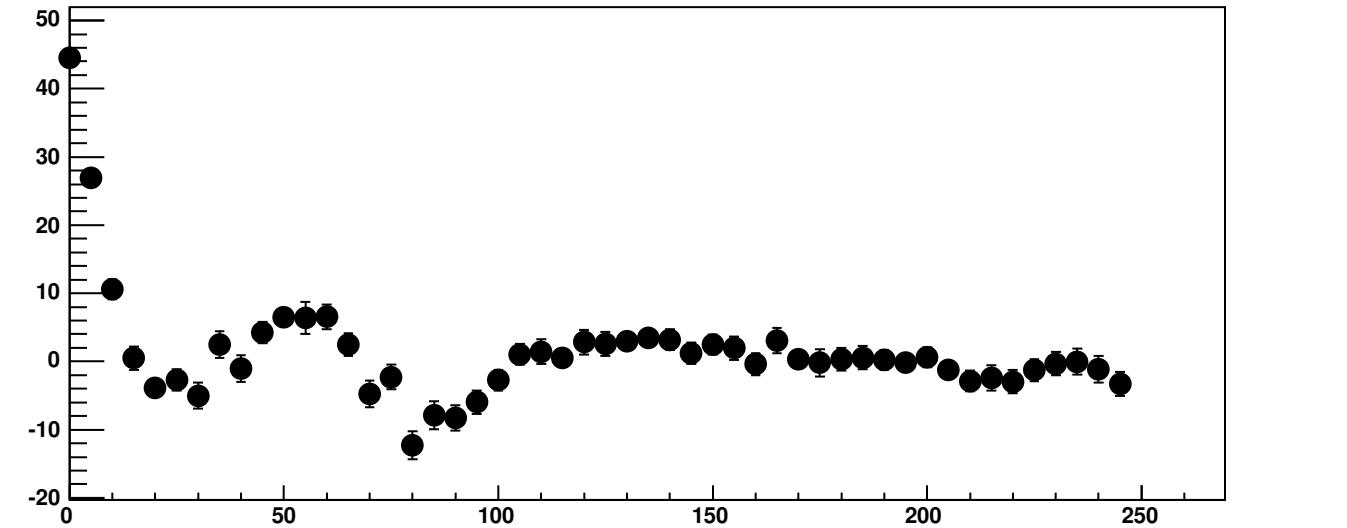
Chip 9, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold



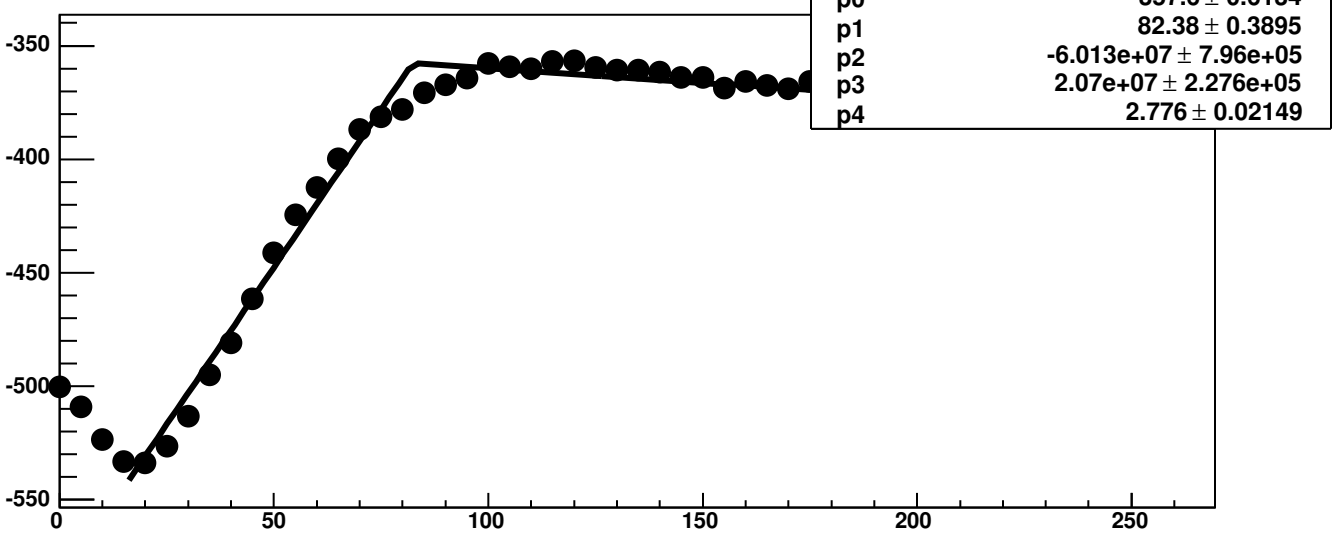
Chip 9, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



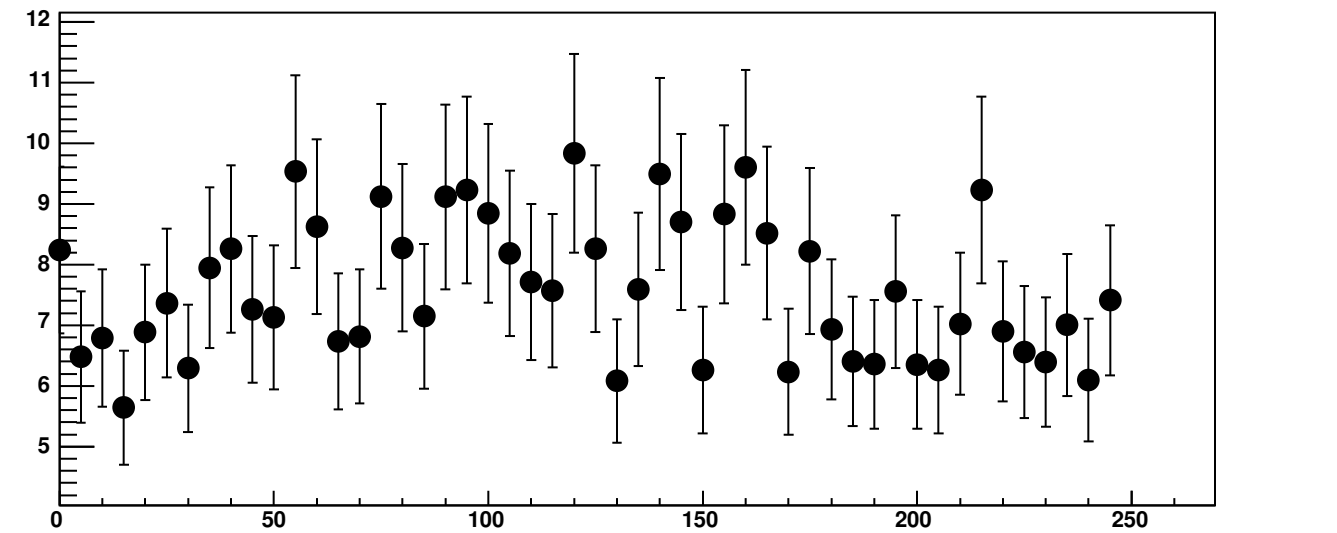
Chip 9, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold



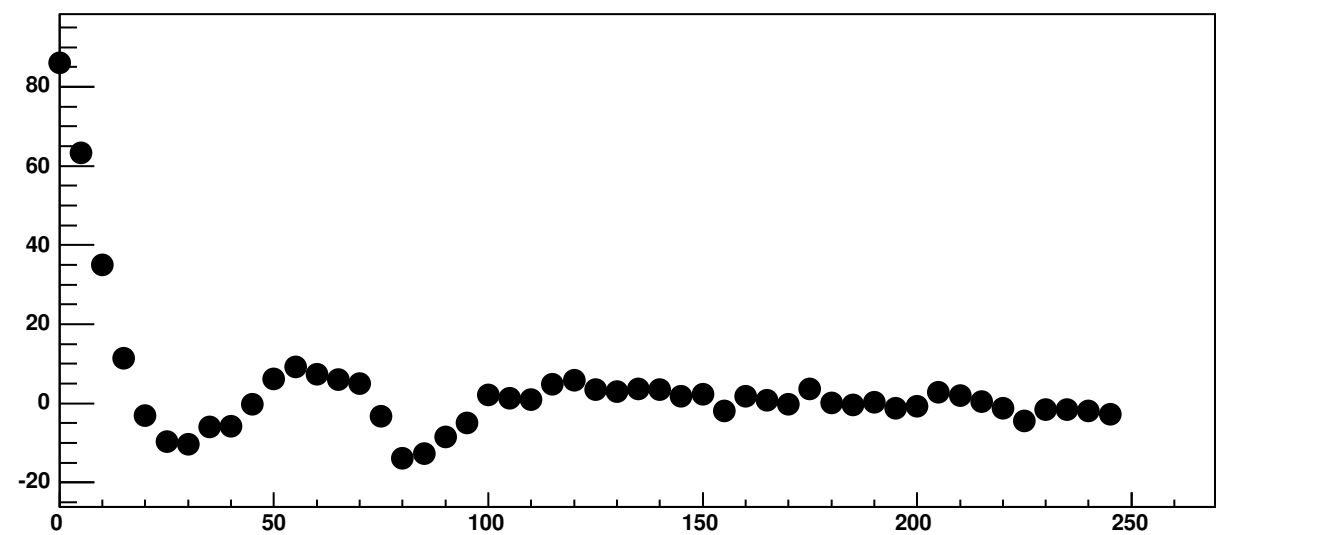
Chip 9, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold



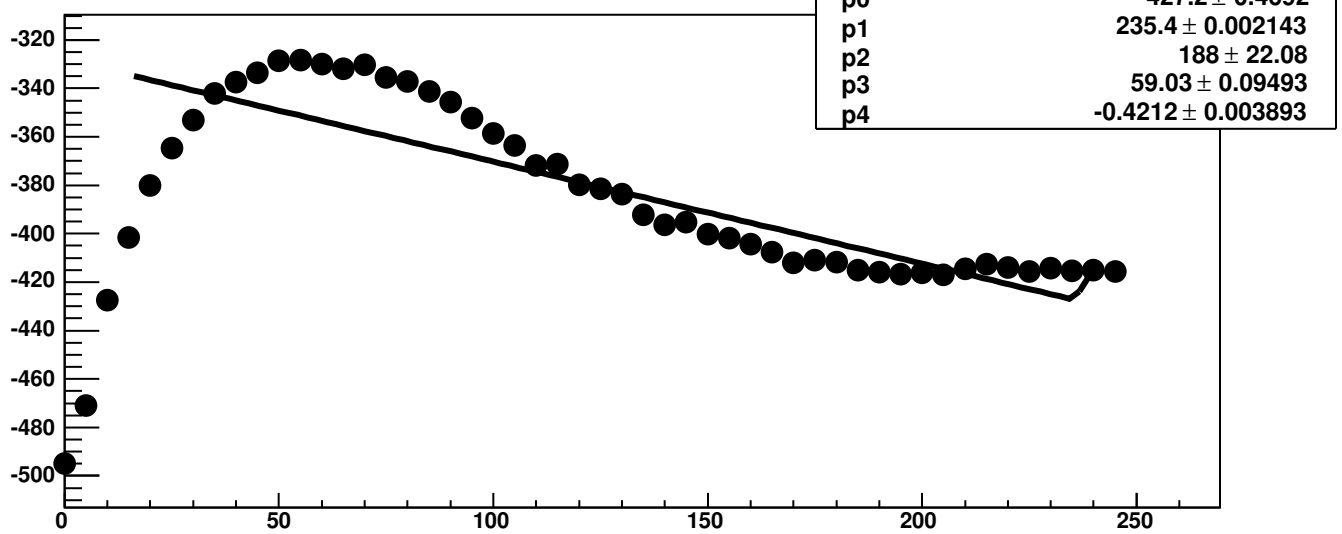
Chip 9, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



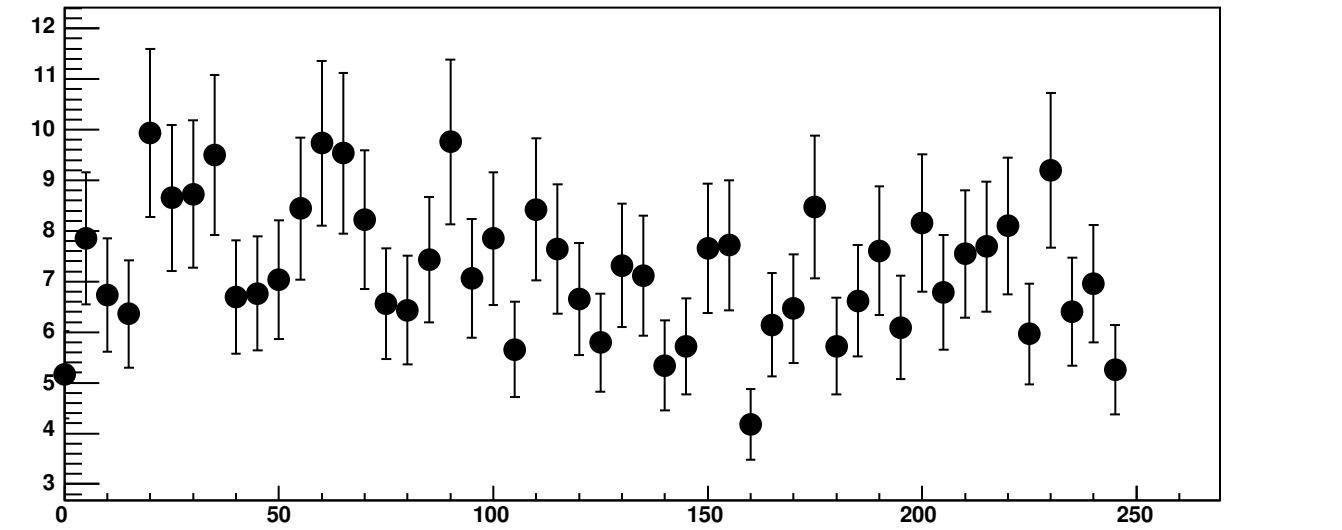
Chip 9, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold



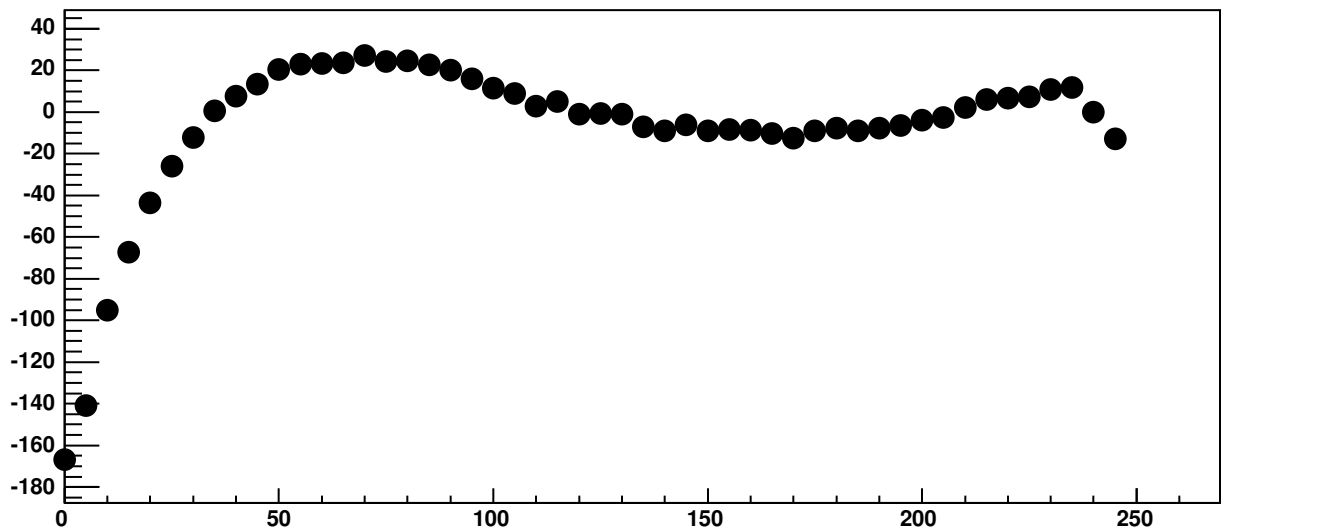
Chip 9, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold



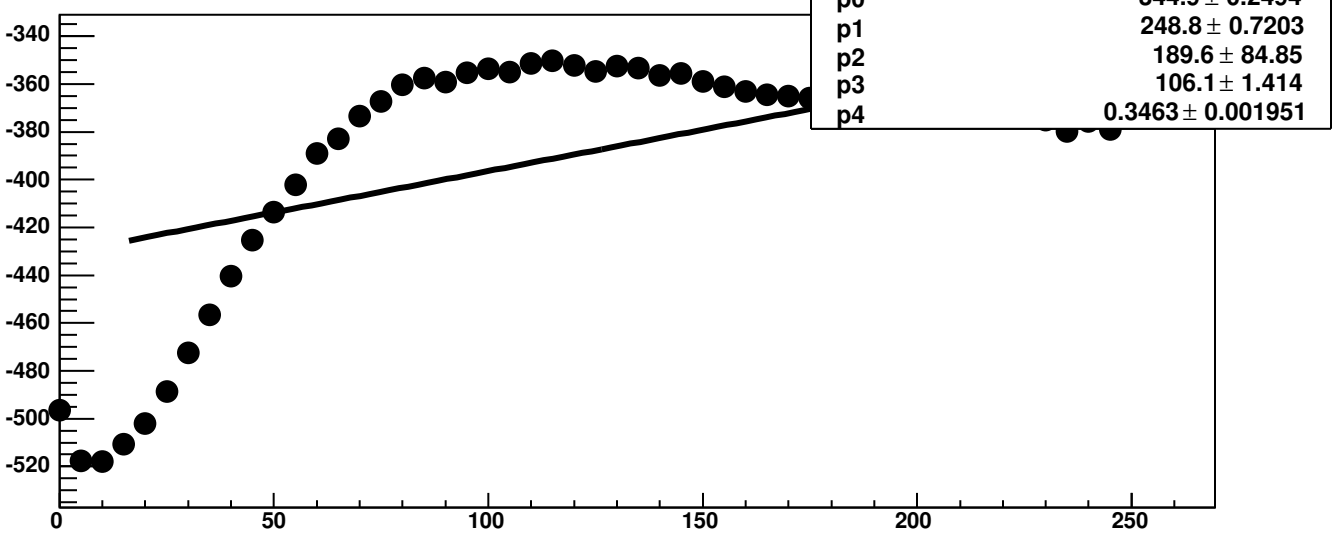
Chip 9, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold



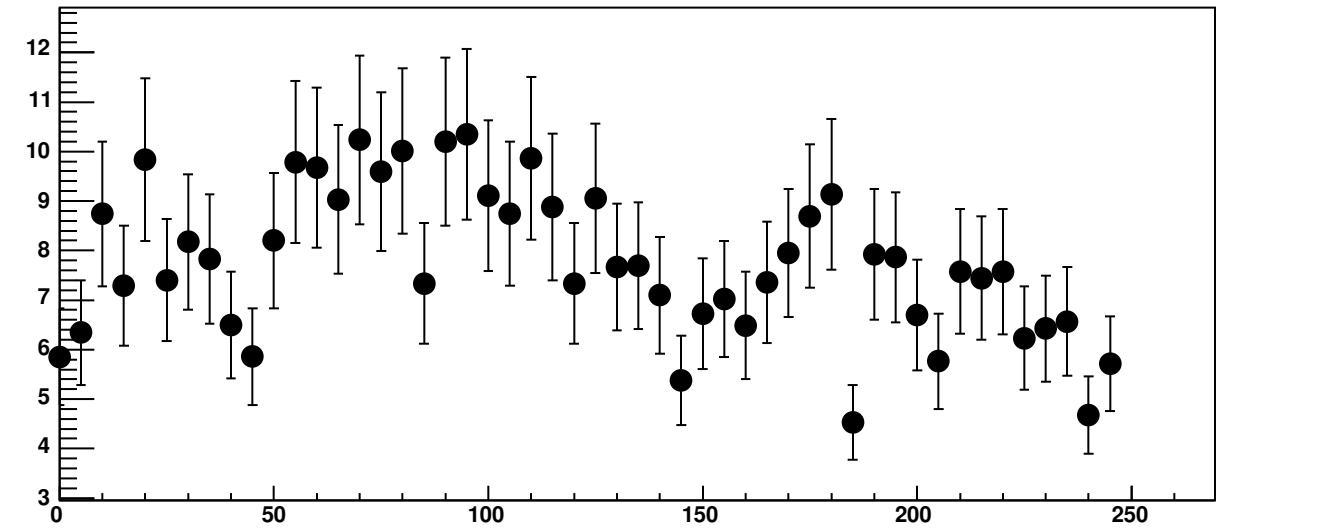
Chip 9, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold



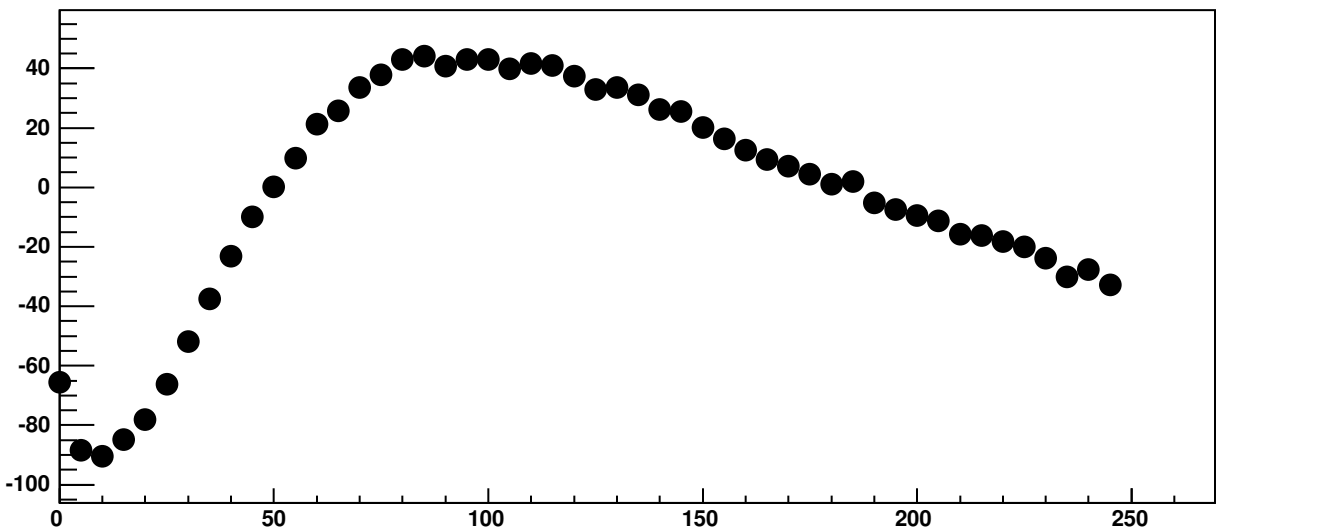
Chip 9, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

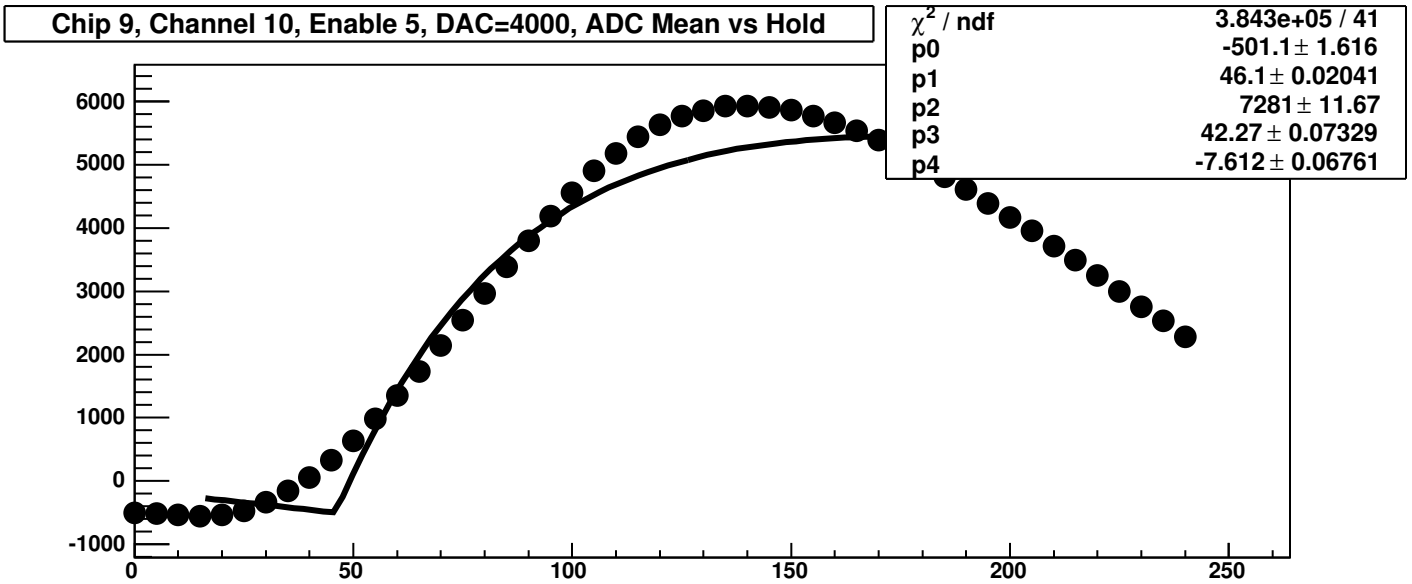


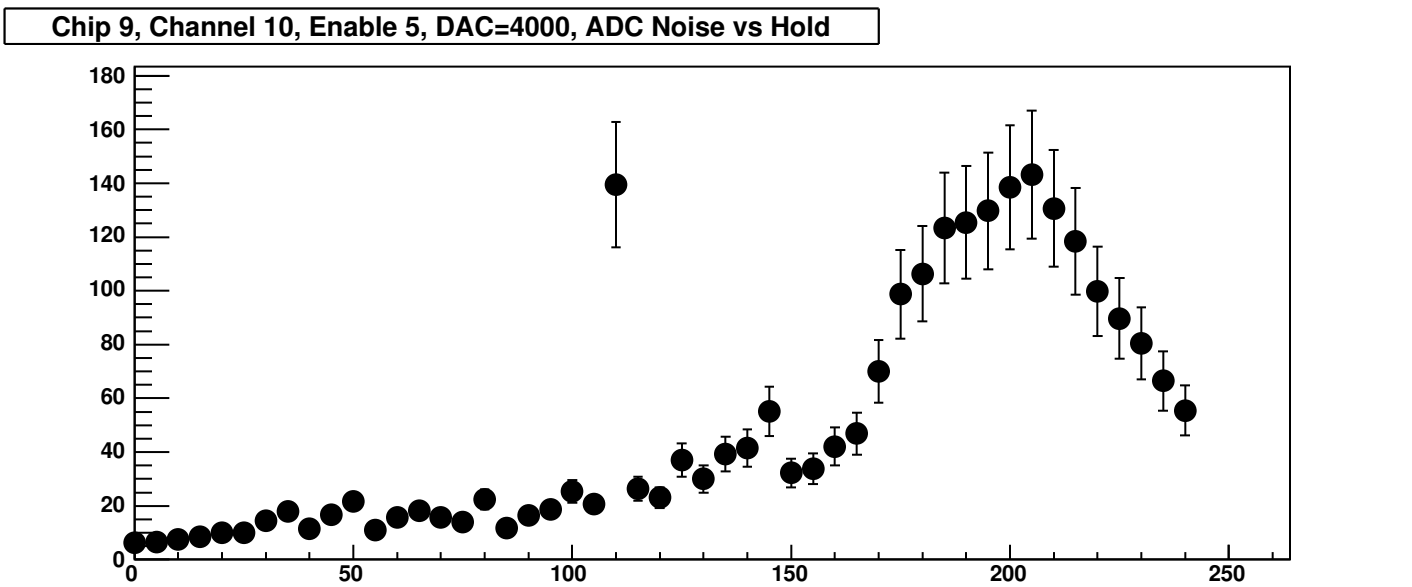
Chip 9, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold

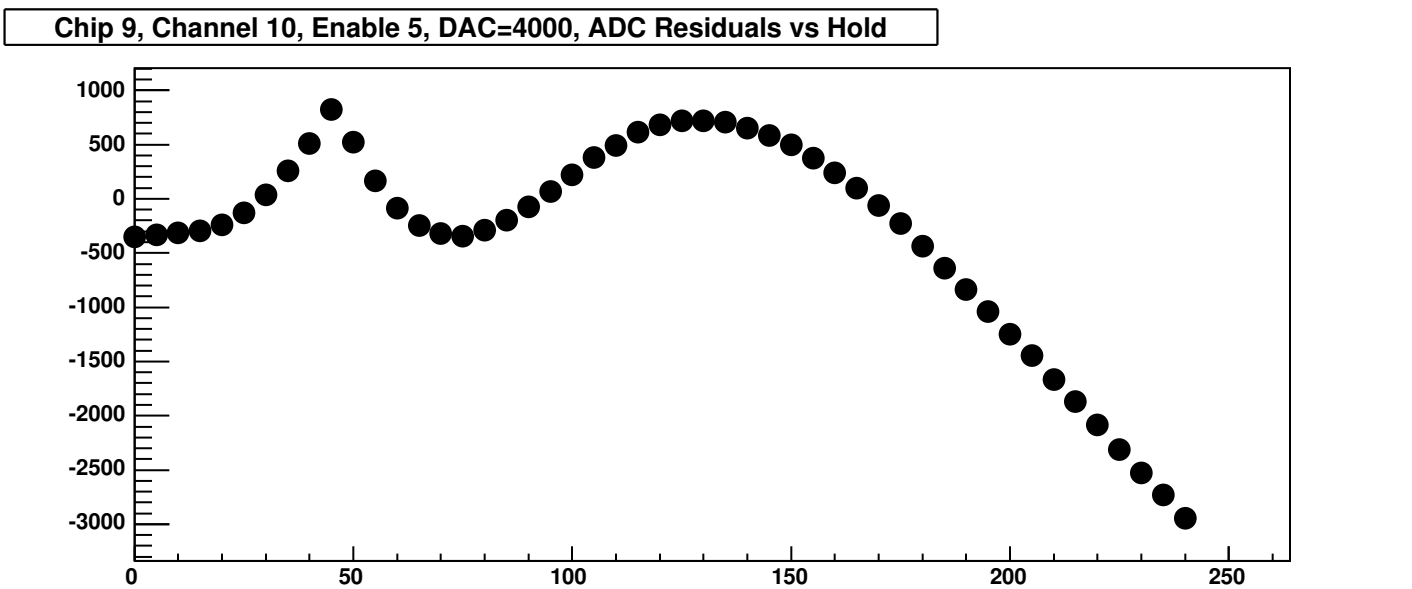


Chip 9, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

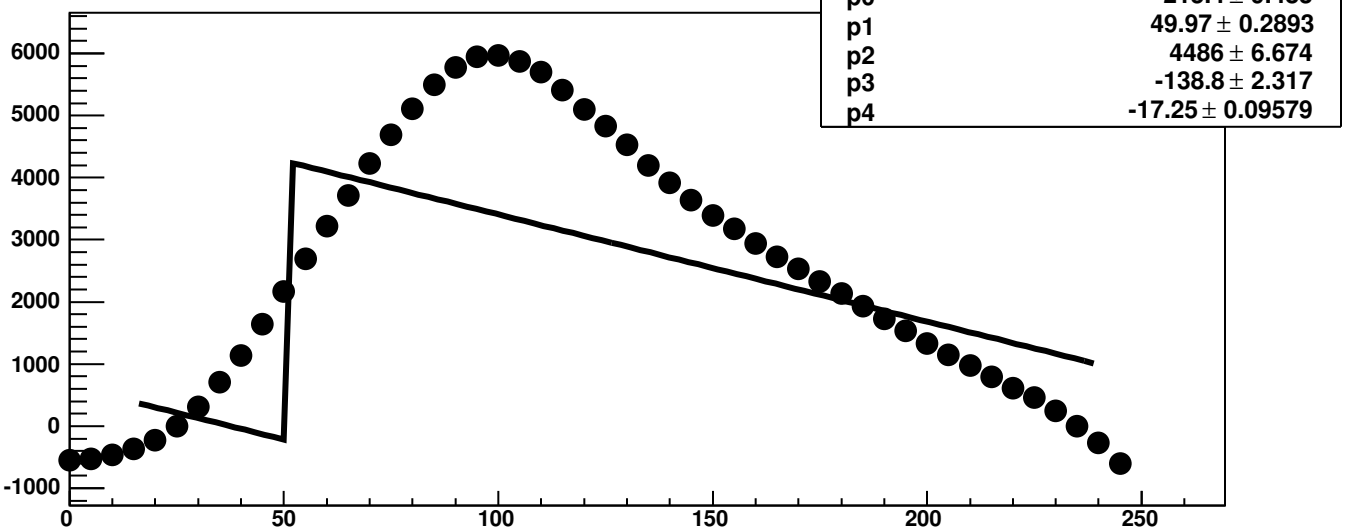



Chip 9, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.843e+05 / 41
p0 -501.1 ± 1.616
p1 46.1 ± 0.02041
p2 7281 ± 11.67
p3 42.27 ± 0.07329
p4 -7.612 ± 0.06761
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

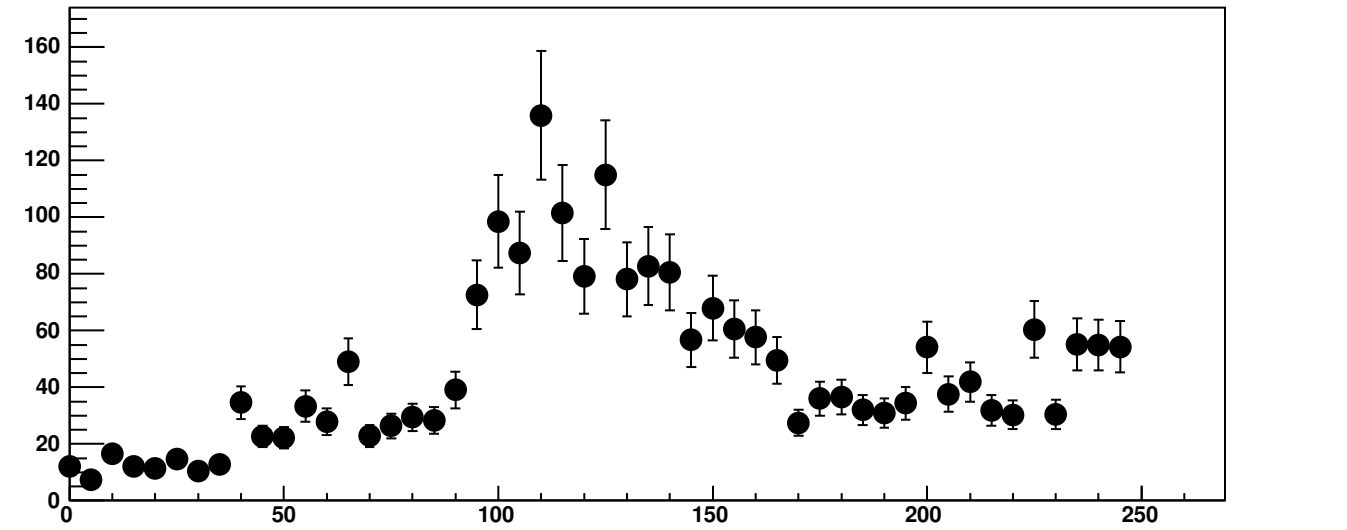

Chip 9, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 9, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250

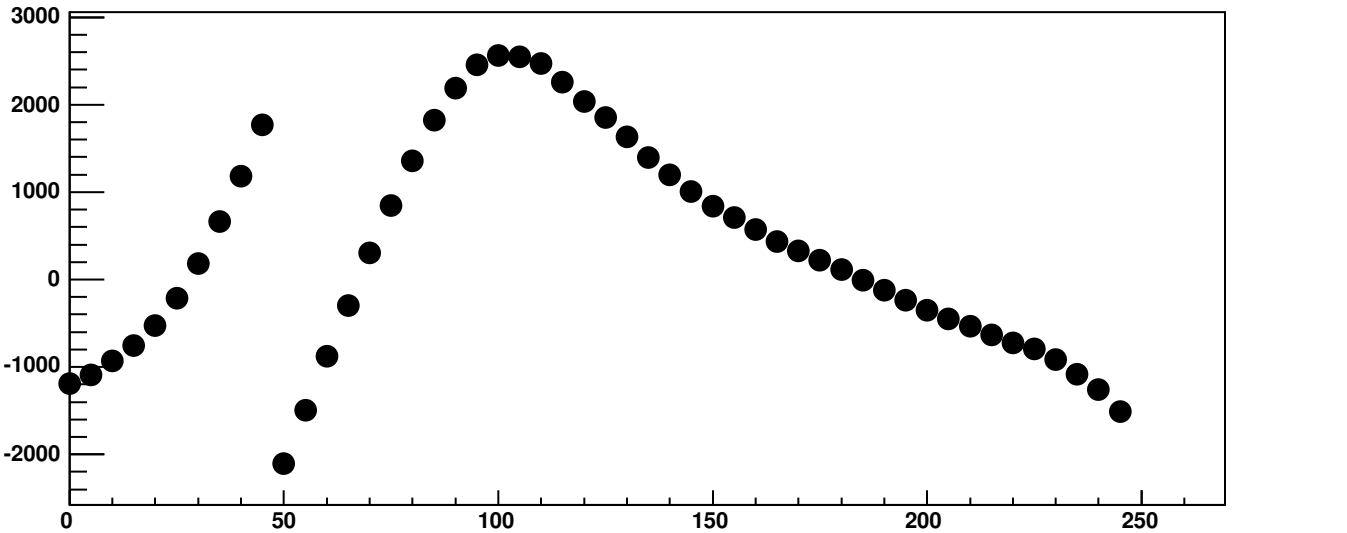
Chip 9, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold



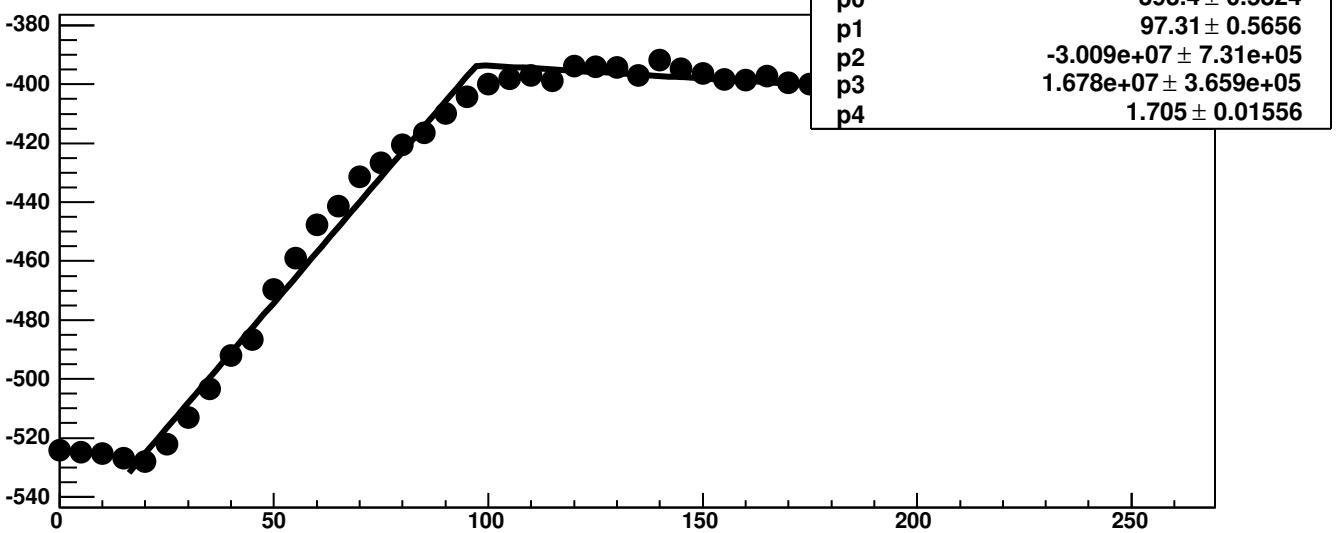
Chip 9, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold



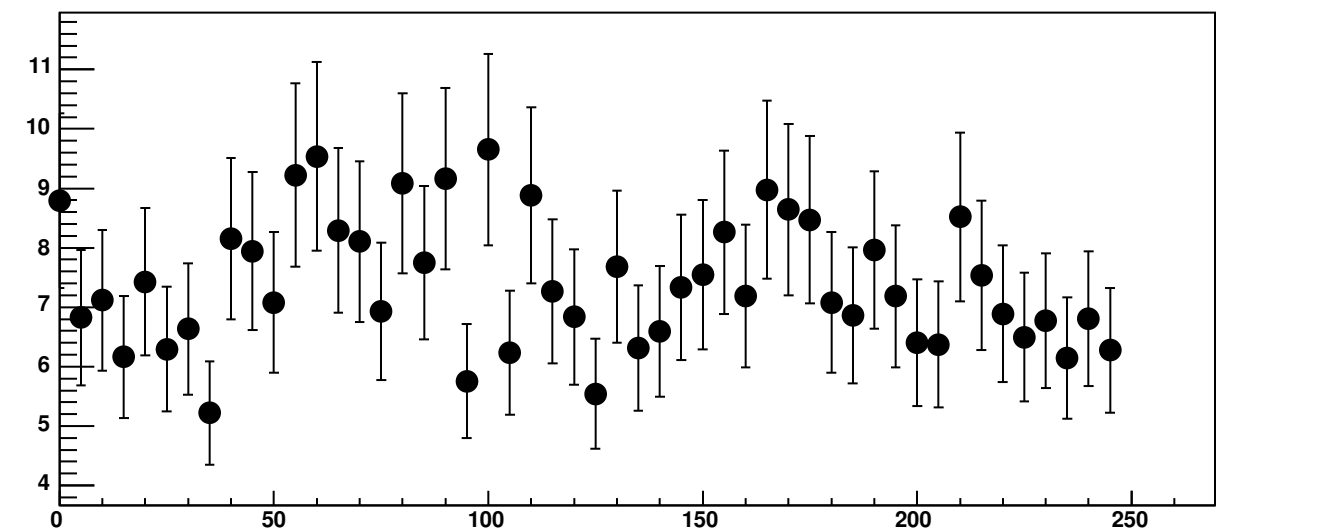
Chip 9, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold



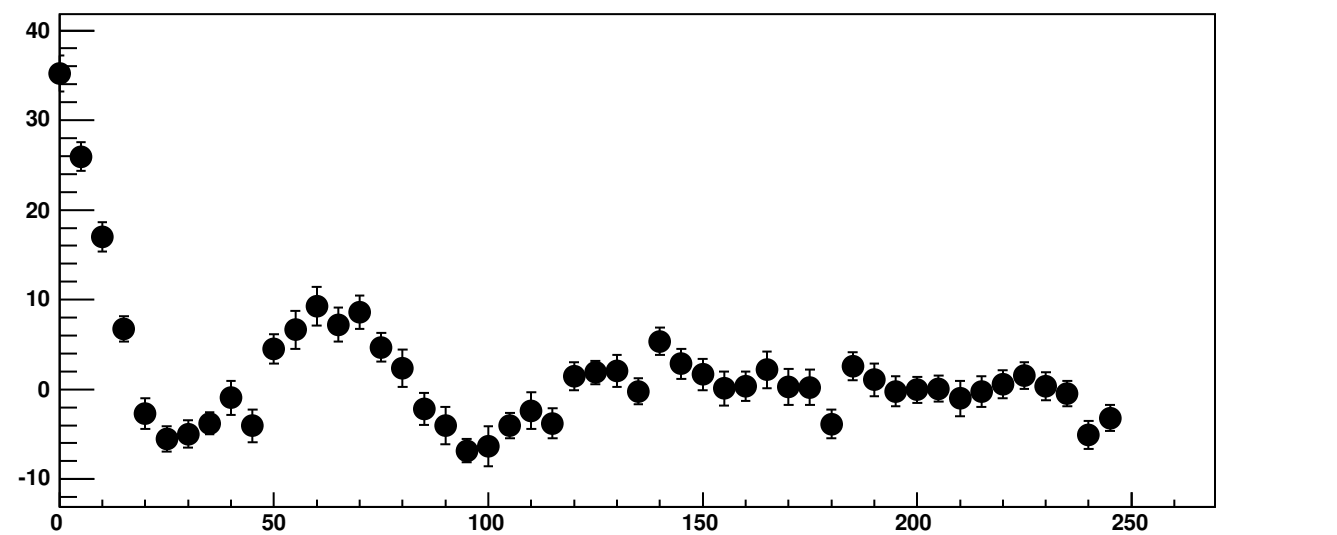
Chip 9, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold



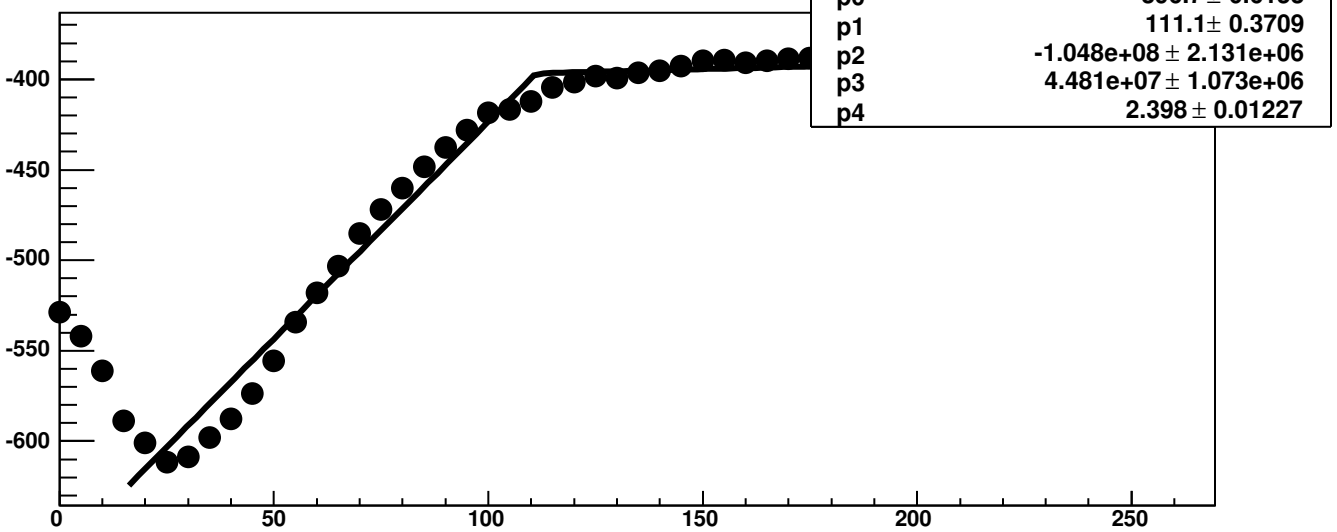
Chip 9, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



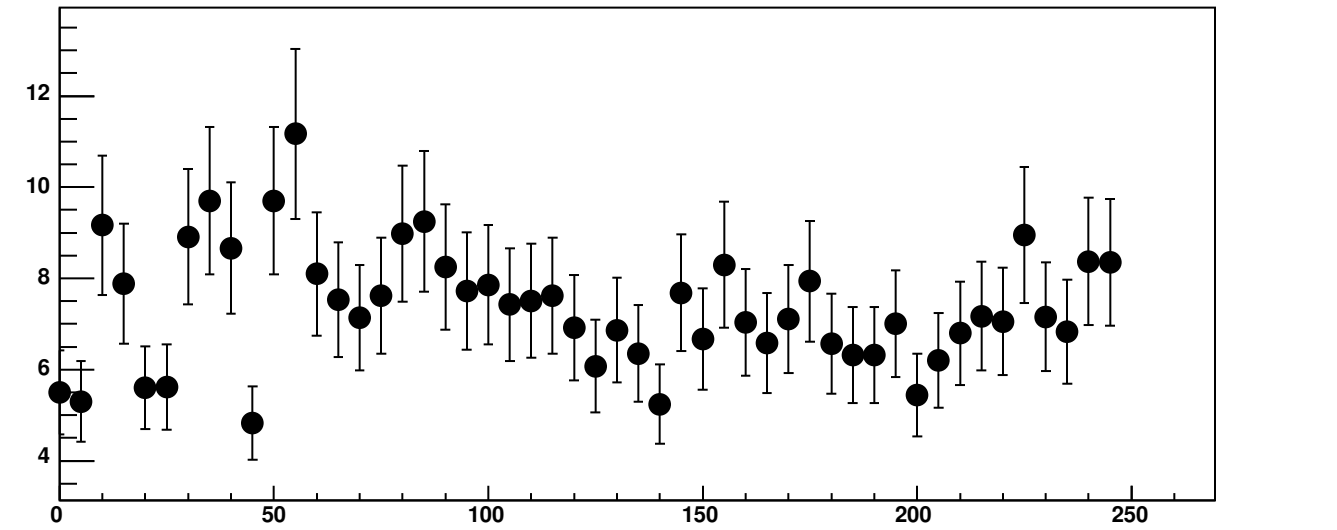
Chip 9, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold



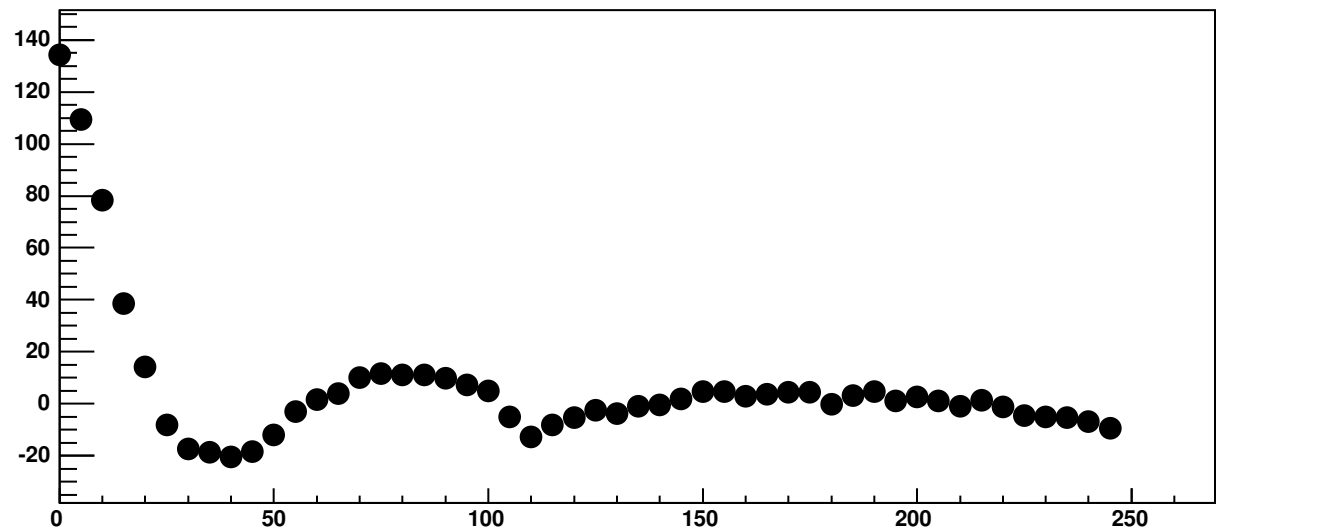
Chip 9, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold



Chip 9, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold

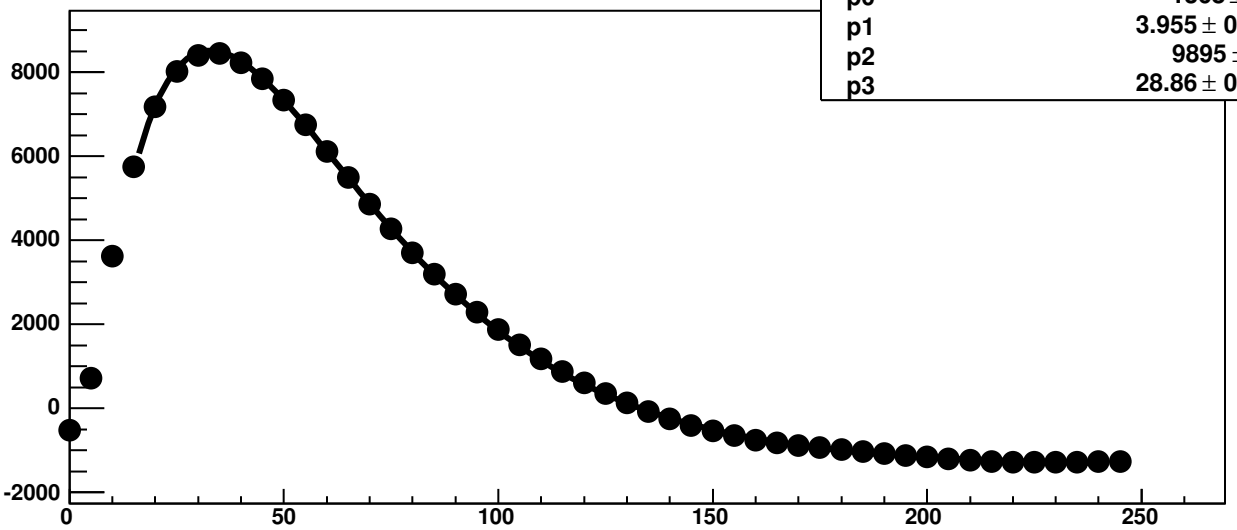


Chip 9, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

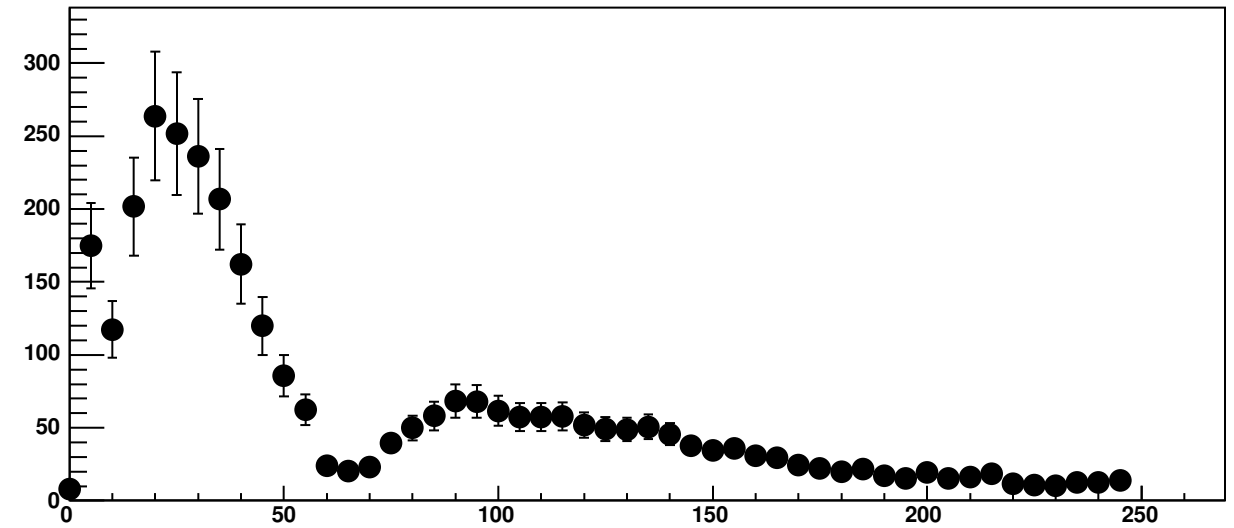


Chip 9, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold

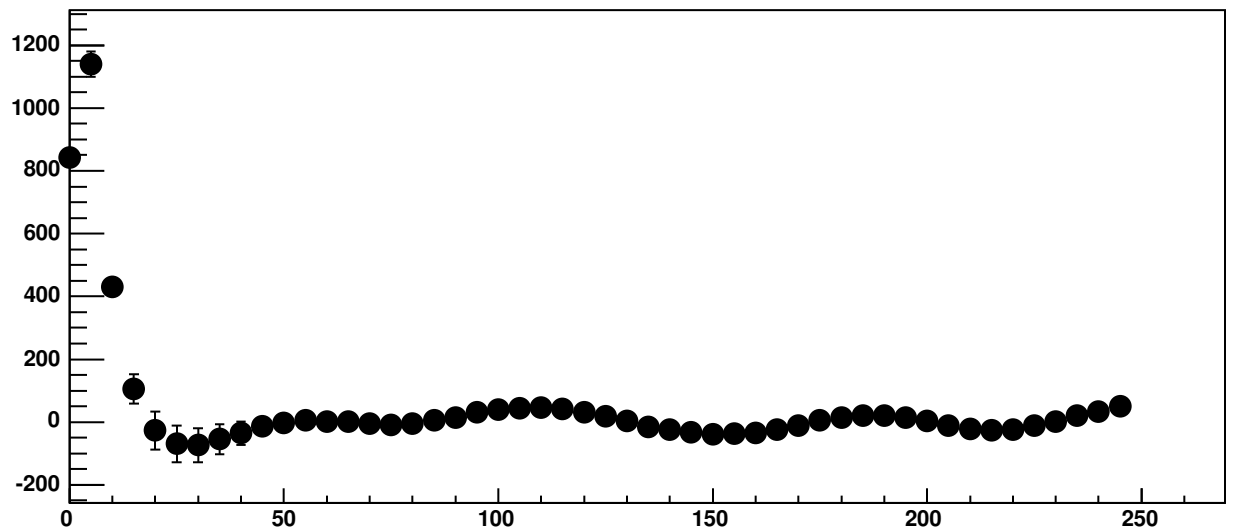
χ^2 / ndf	619.1 / 42
p0	-1368 ± 1.258
p1	3.955 ± 0.08559
p2	9895 ± 11.06
p3	28.86 ± 0.03155



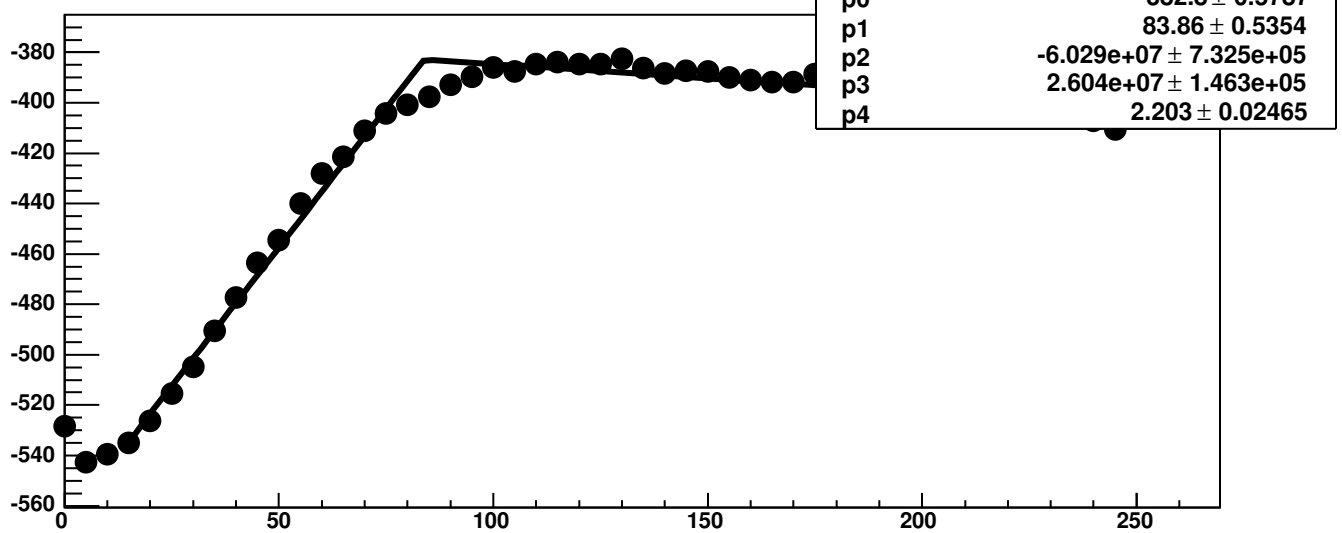
Chip 9, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold



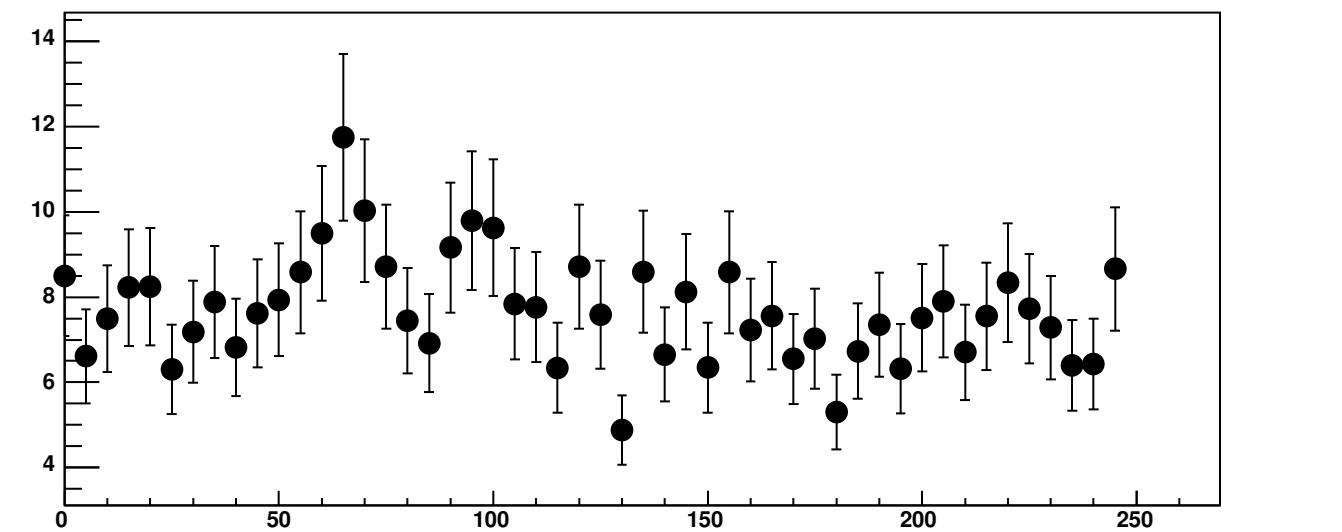
Chip 9, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold



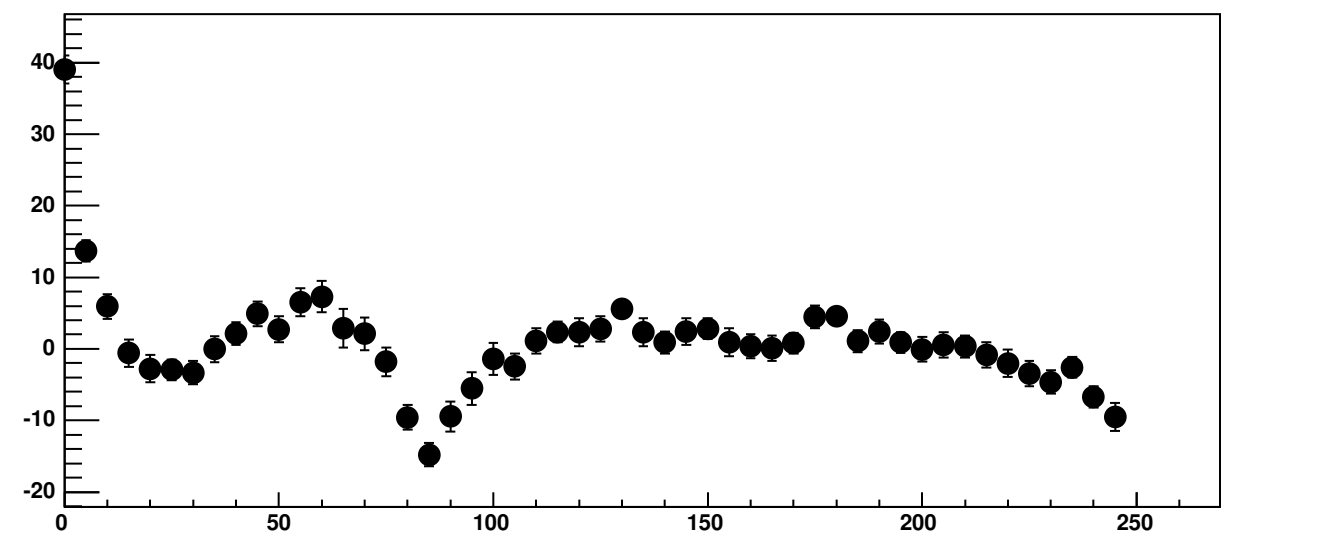
Chip 9, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold



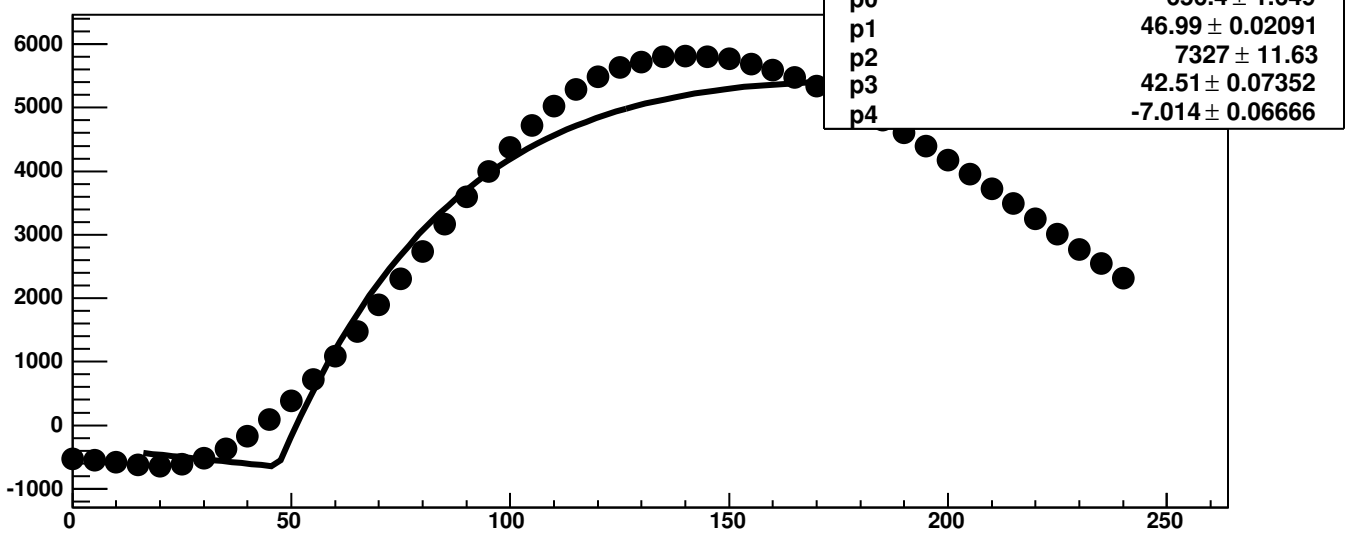
Chip 9, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



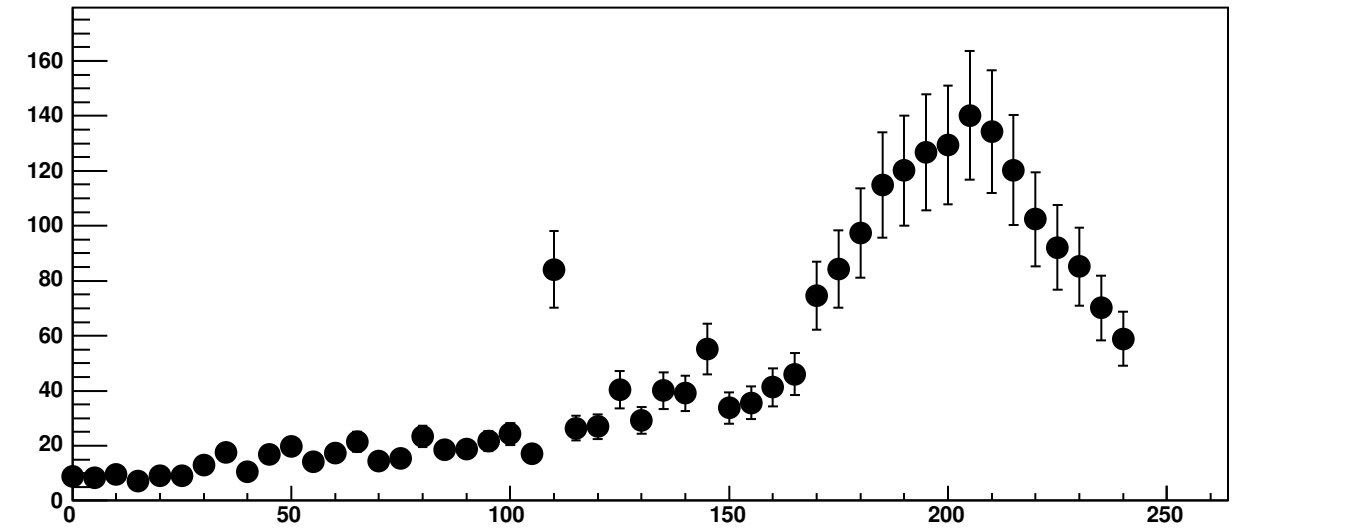
Chip 9, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold



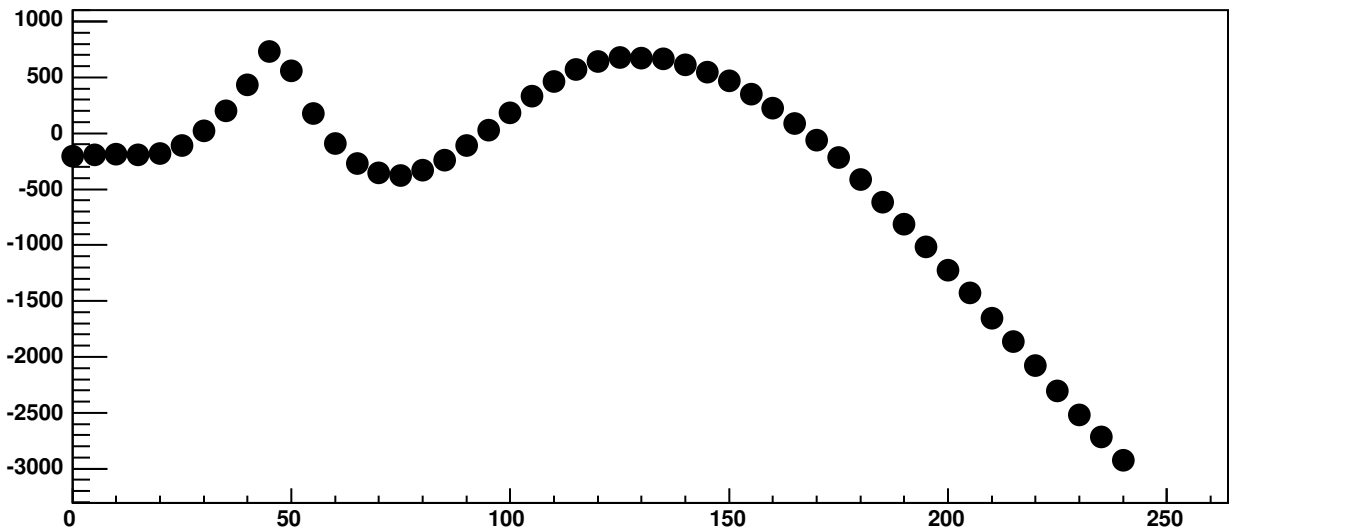
Chip 9, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold



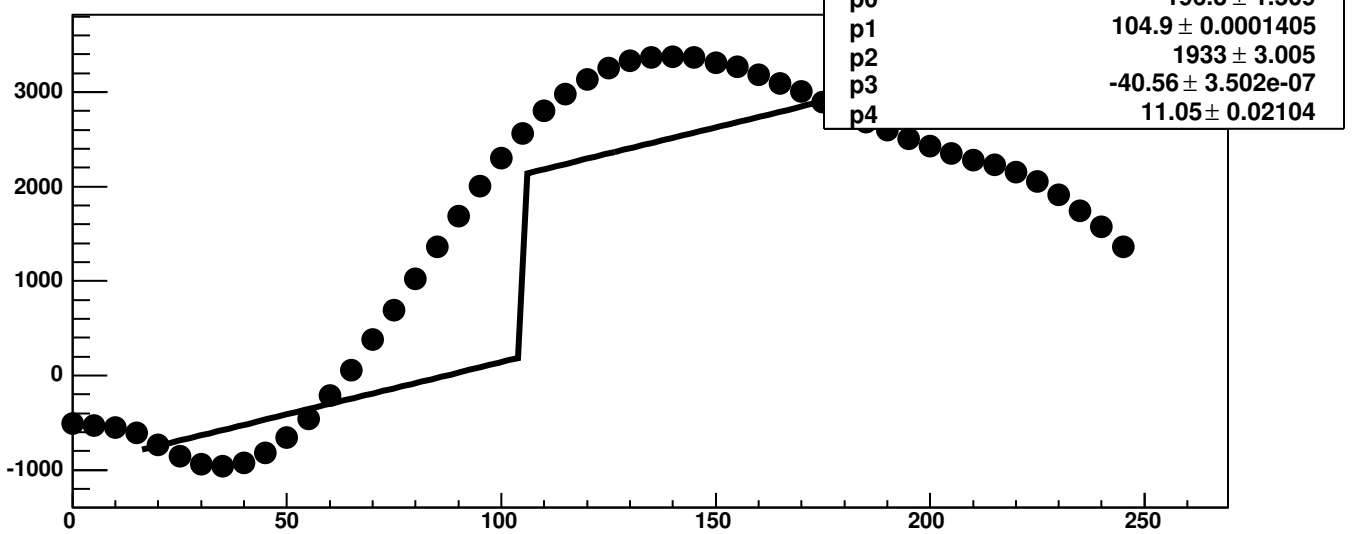
Chip 9, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



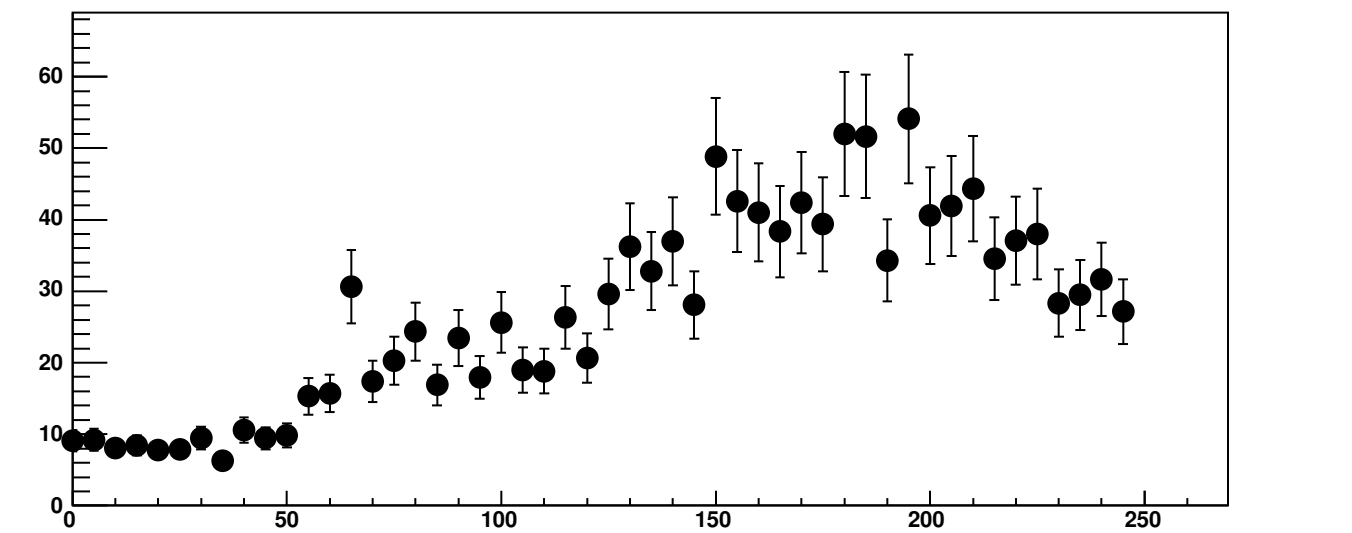
Chip 9, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold



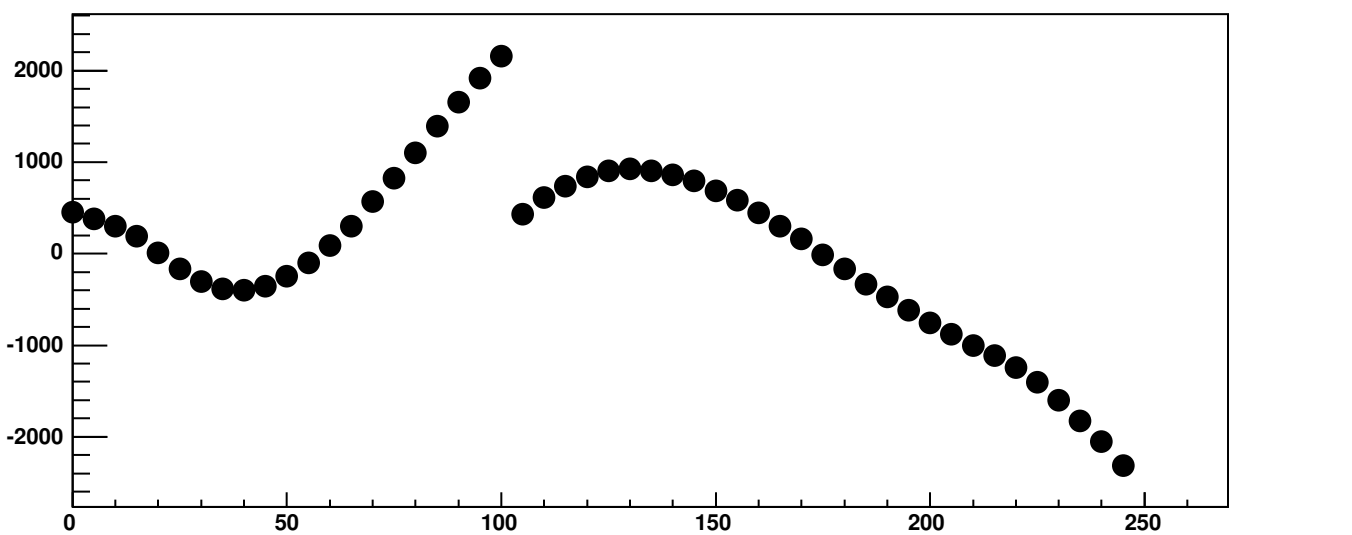
Chip 9, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold



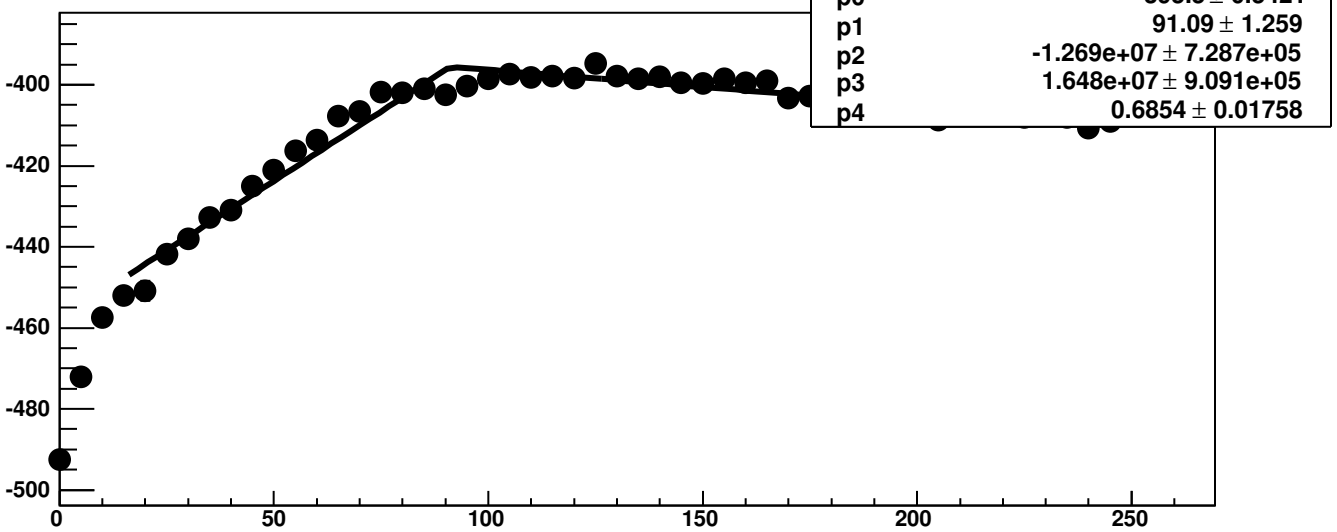
Chip 9, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



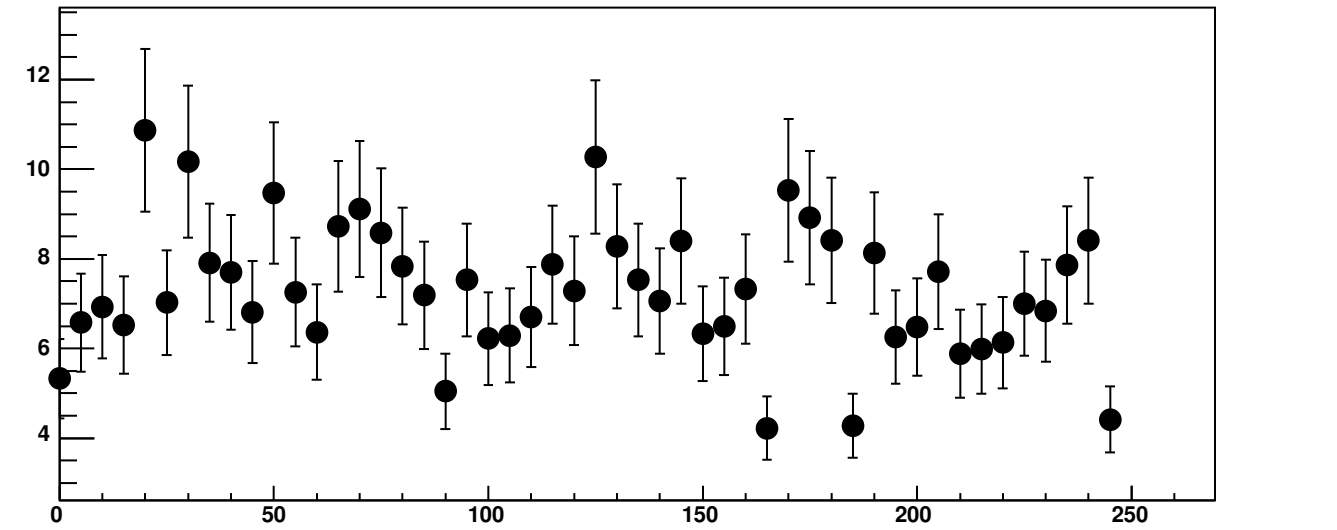
Chip 9, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold



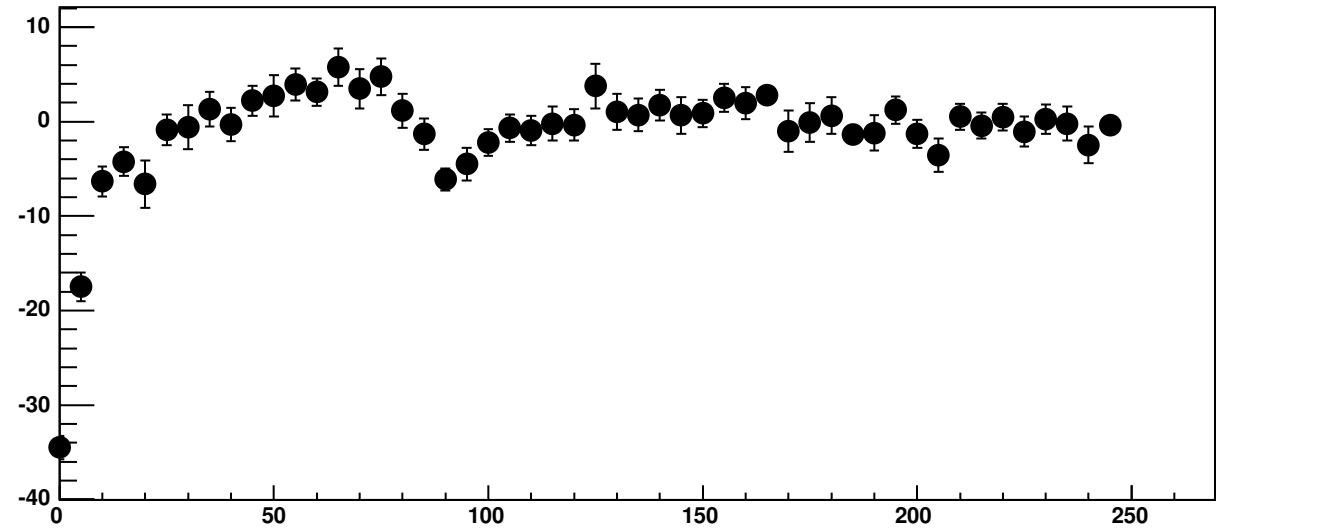
Chip 9, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold



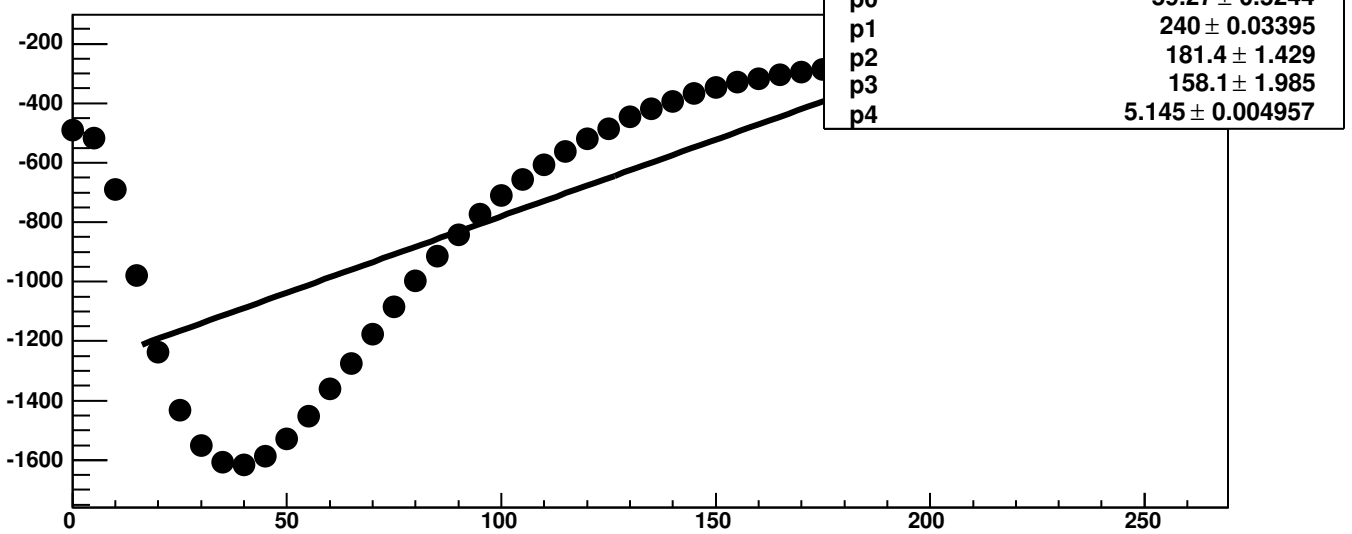
Chip 9, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold



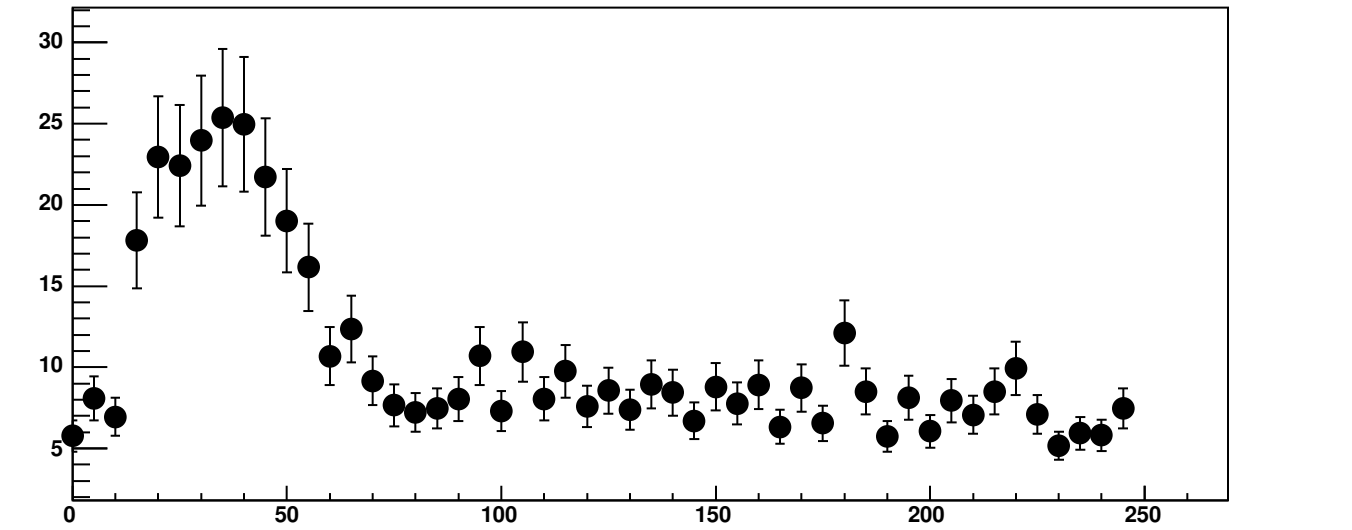
Chip 9, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold



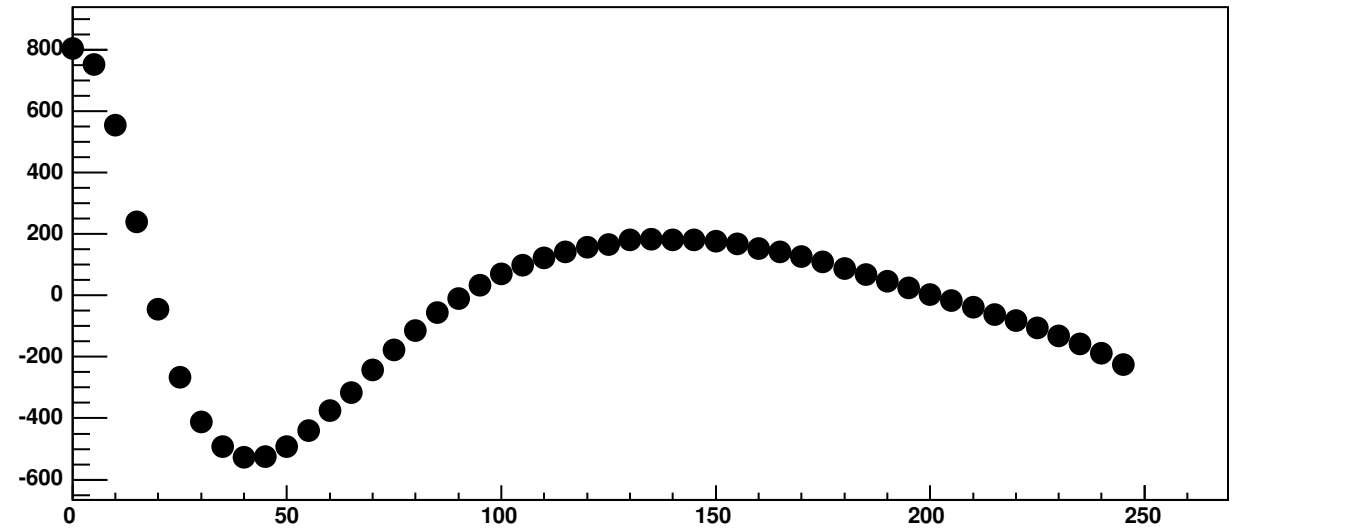
Chip 9, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold



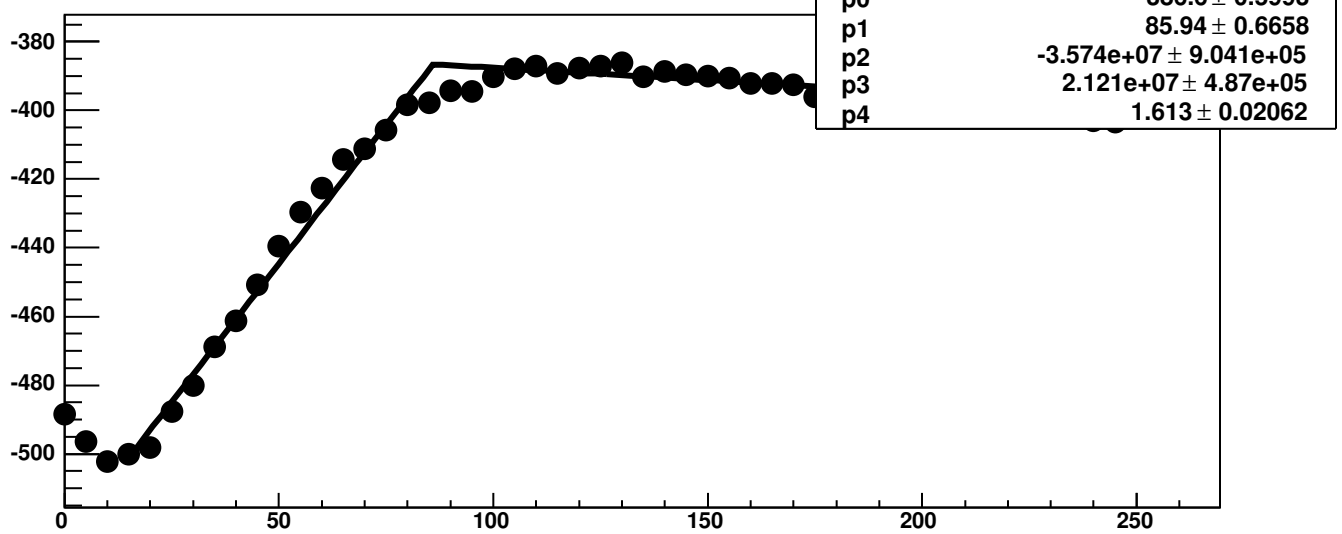
Chip 9, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



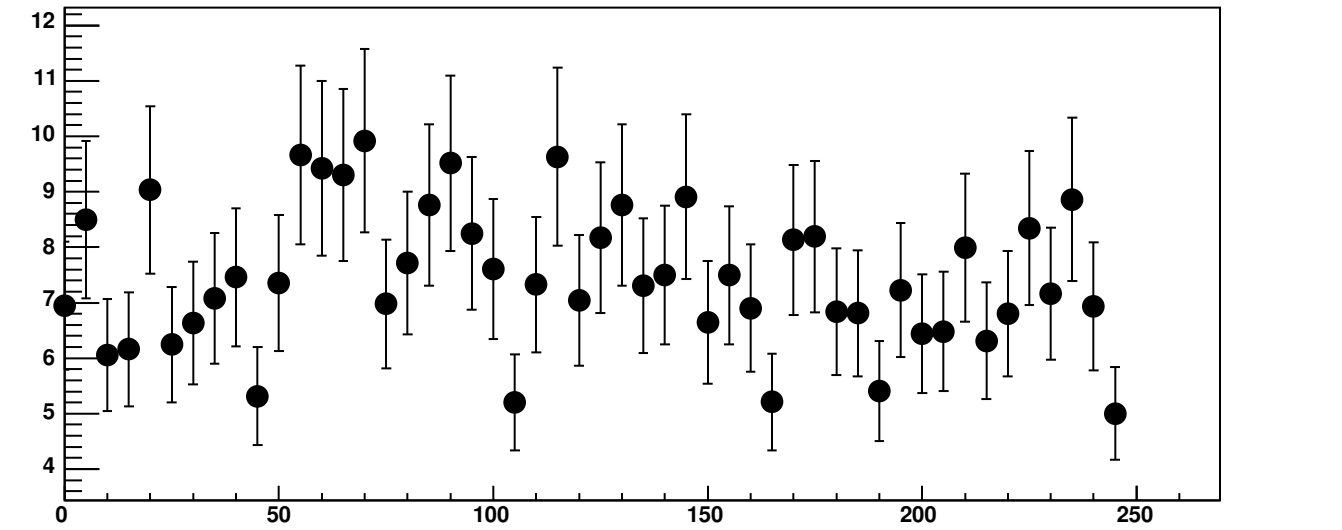
Chip 9, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold



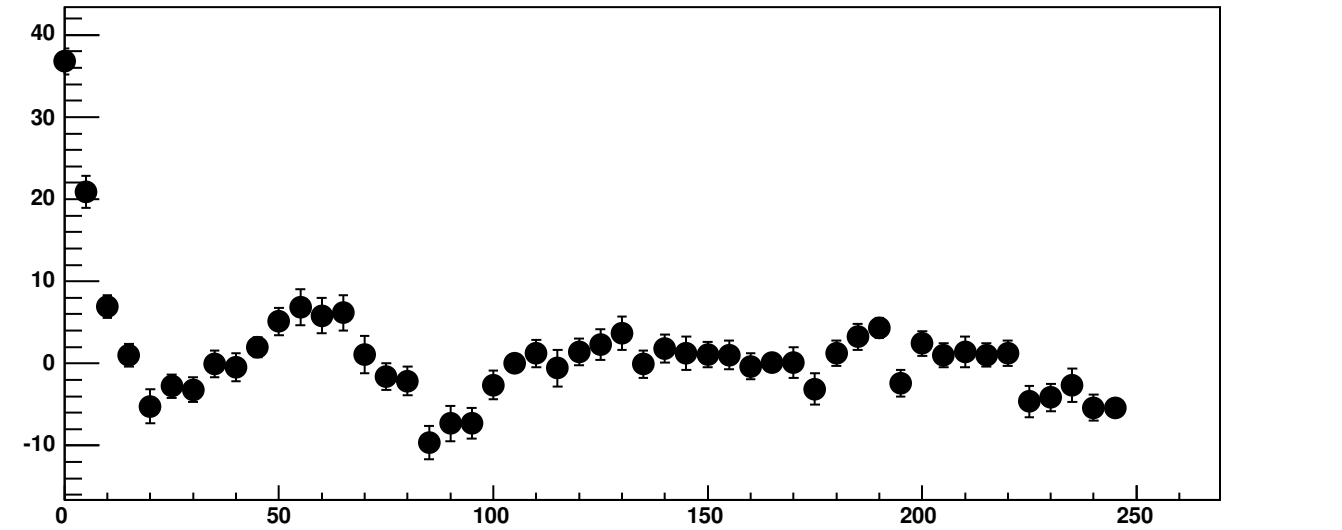
Chip 9, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold



Chip 9, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold

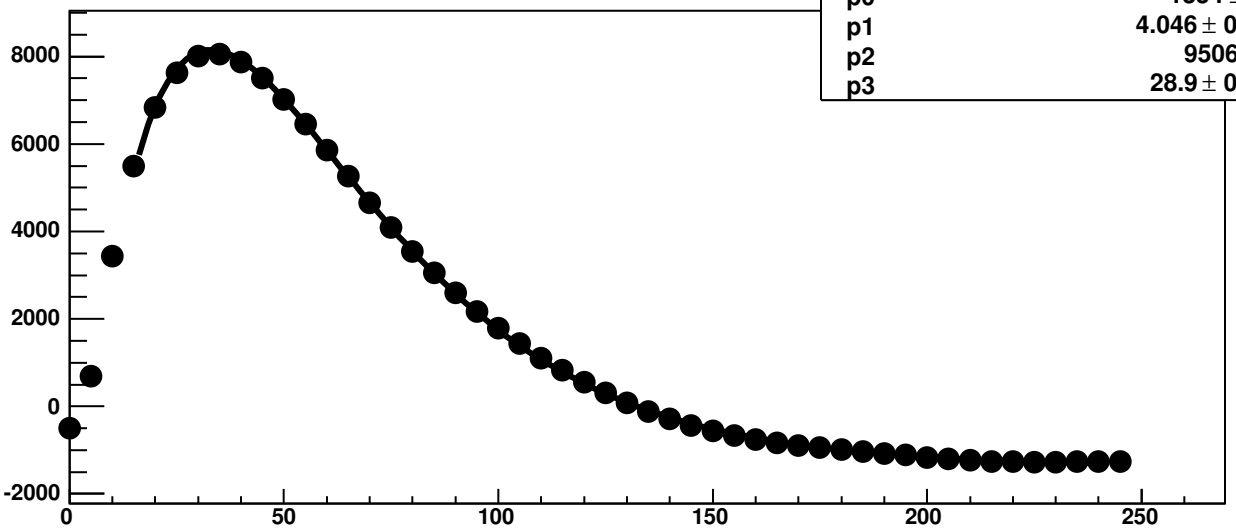


Chip 9, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

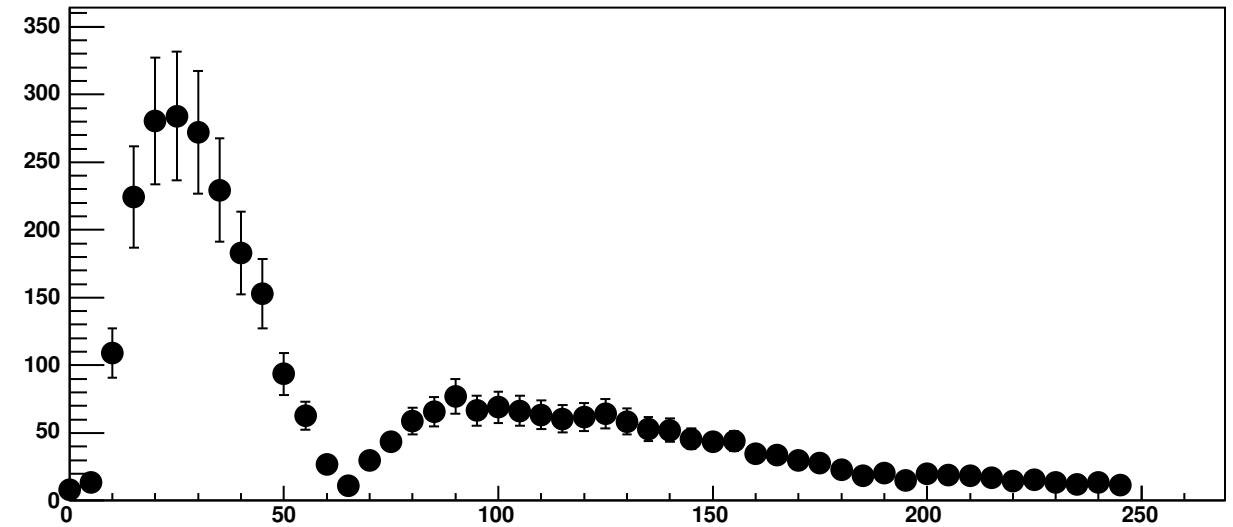


Chip 9, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold

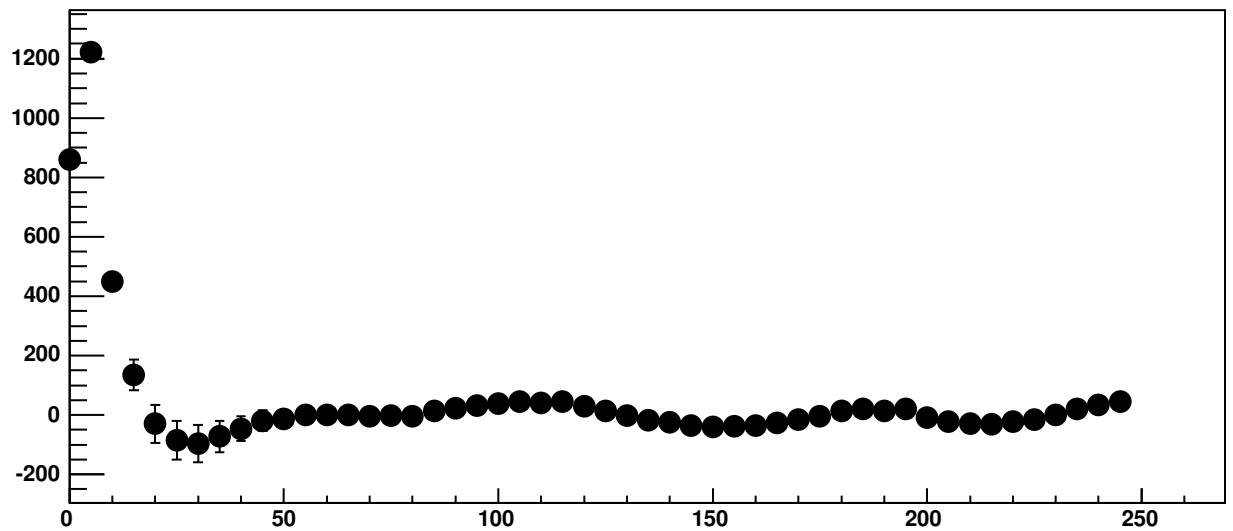
χ^2 / ndf	608 / 42
p0	-1354 ± 1.456
p1	4.046 ± 0.09744
p2	9506 ± 12
p3	28.9 ± 0.03714



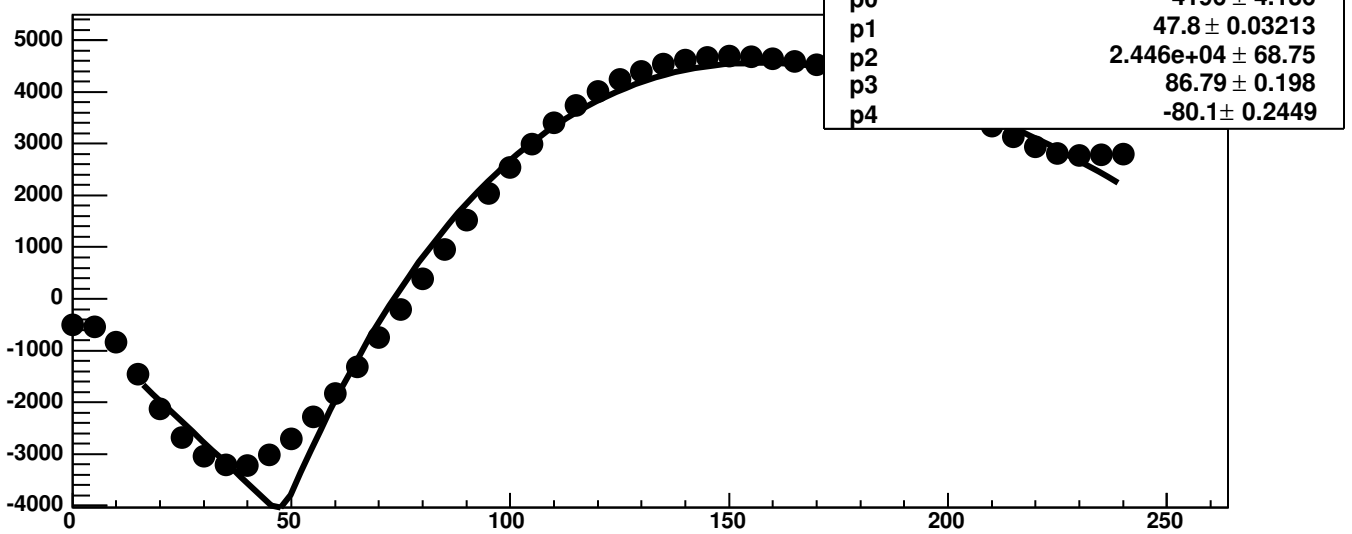
Chip 9, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold



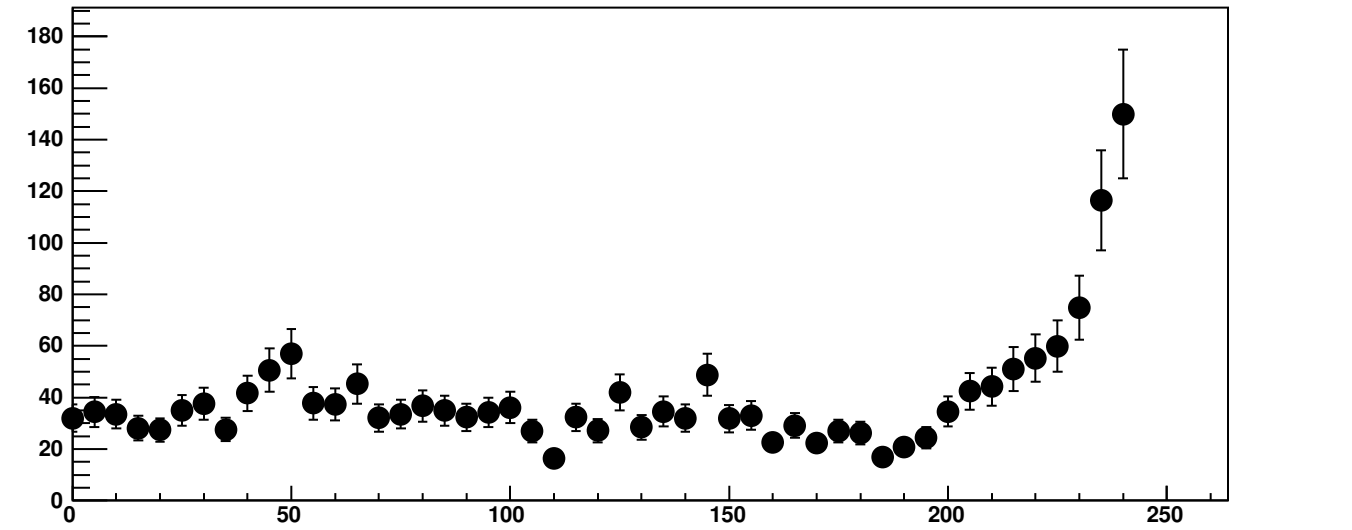
Chip 9, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold



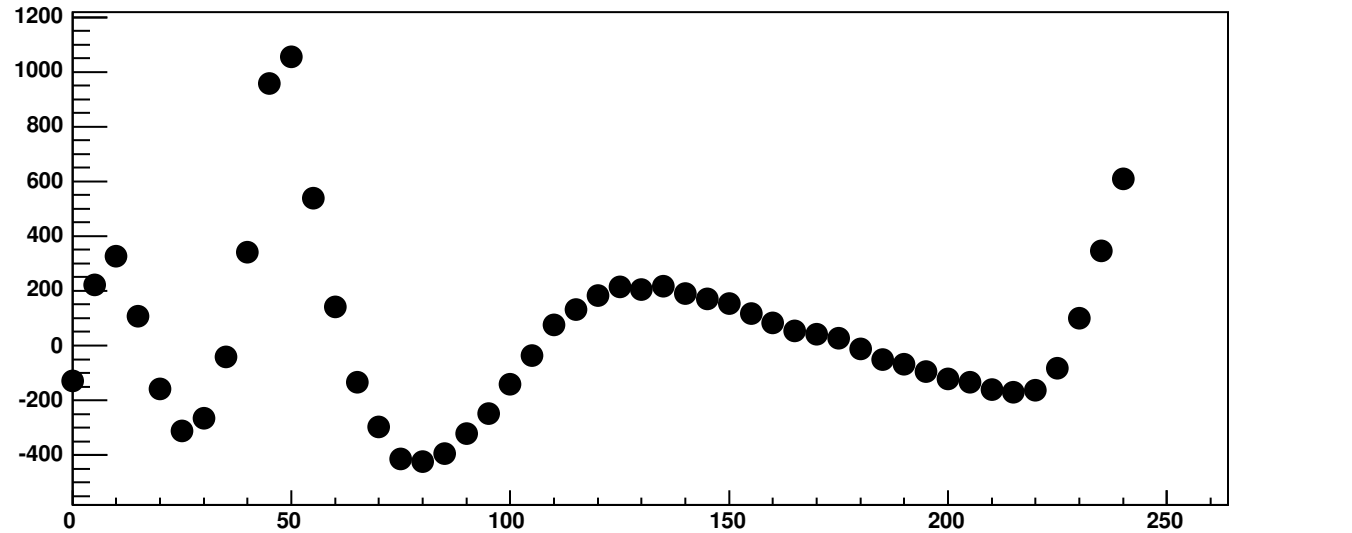
Chip 9, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold

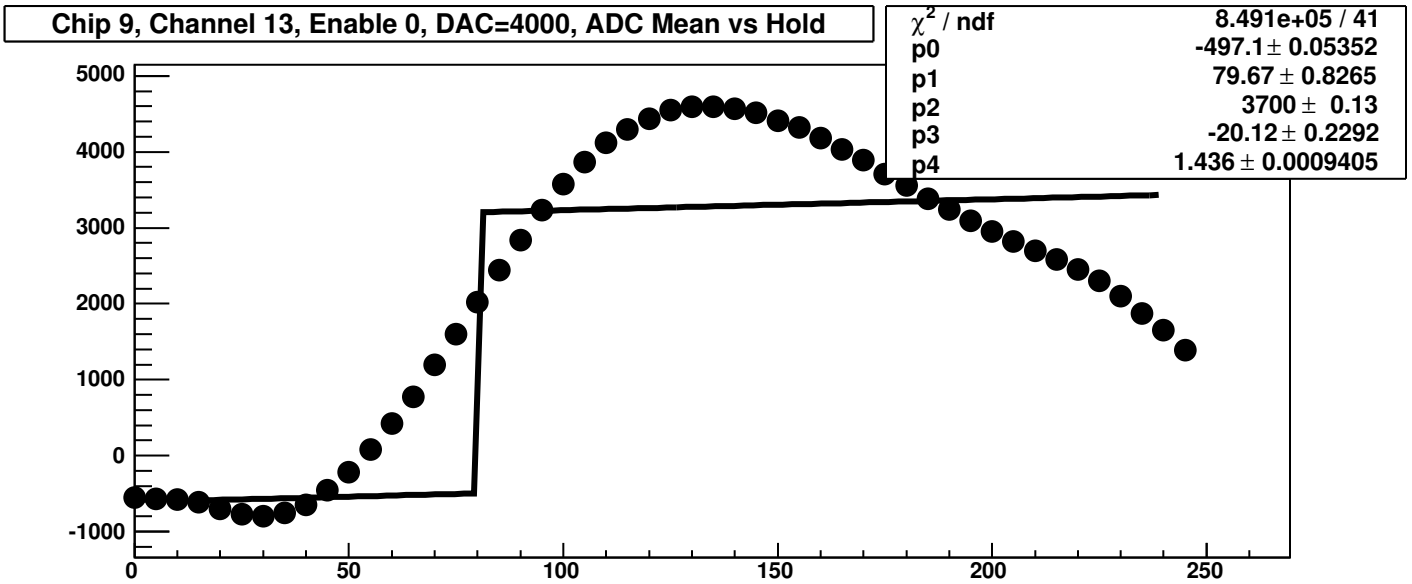


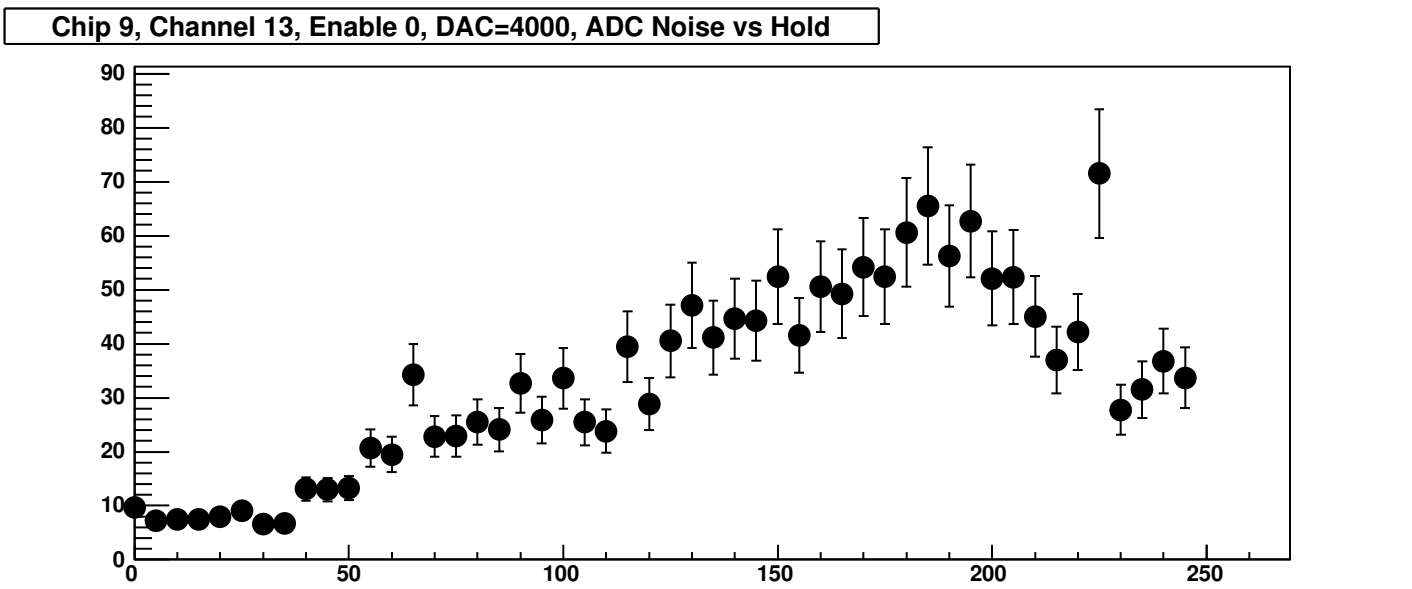
Chip 9, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold

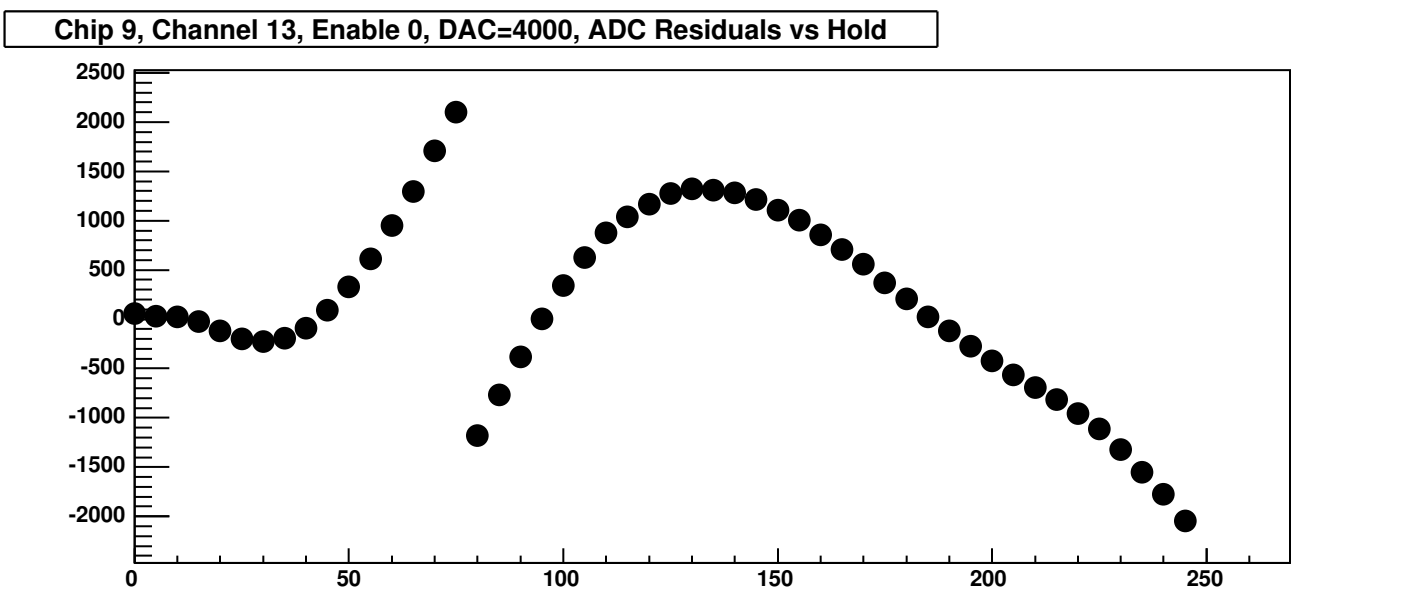


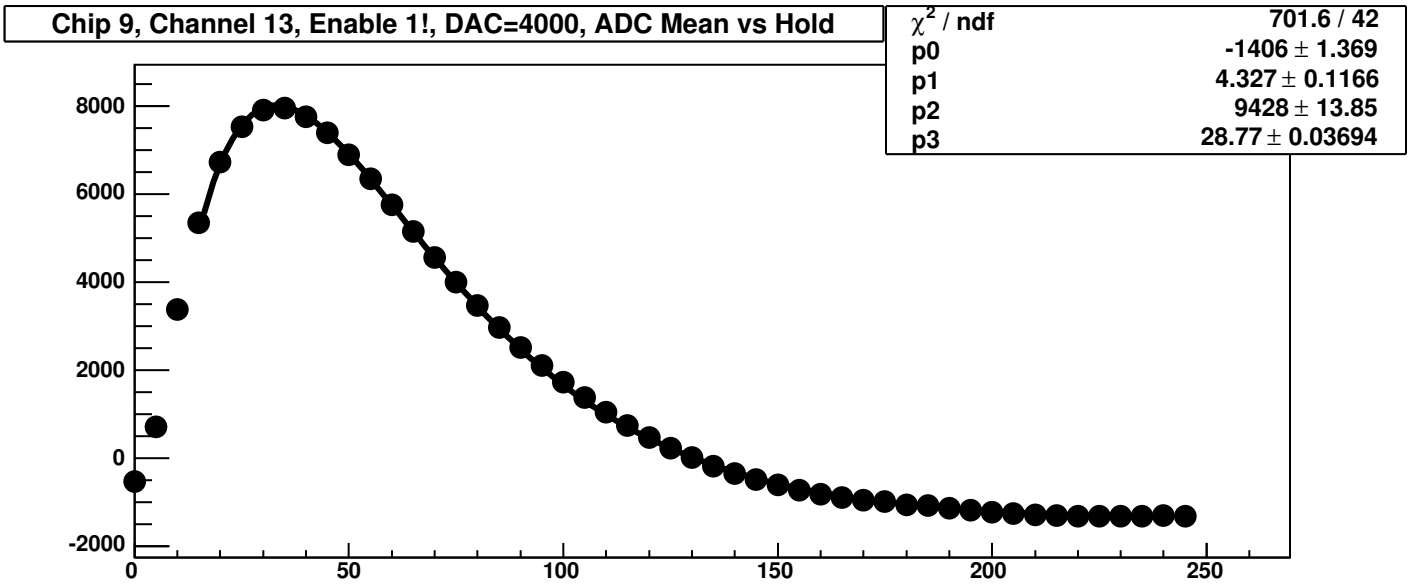
Chip 9, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold

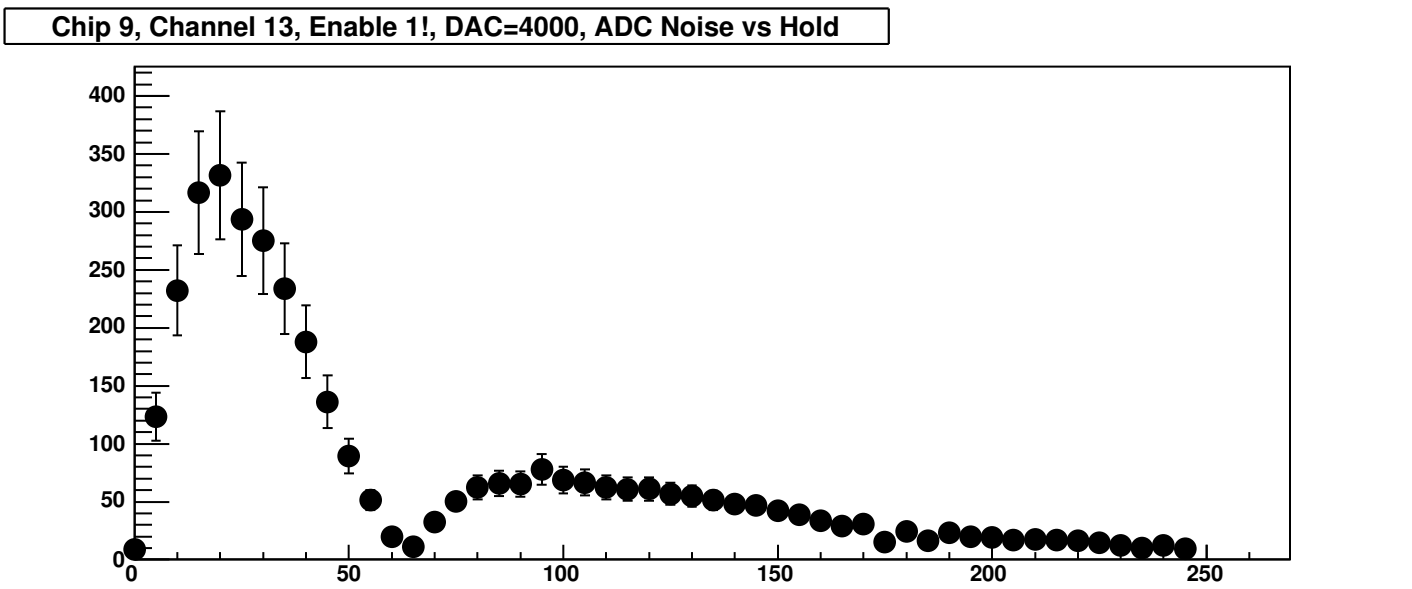


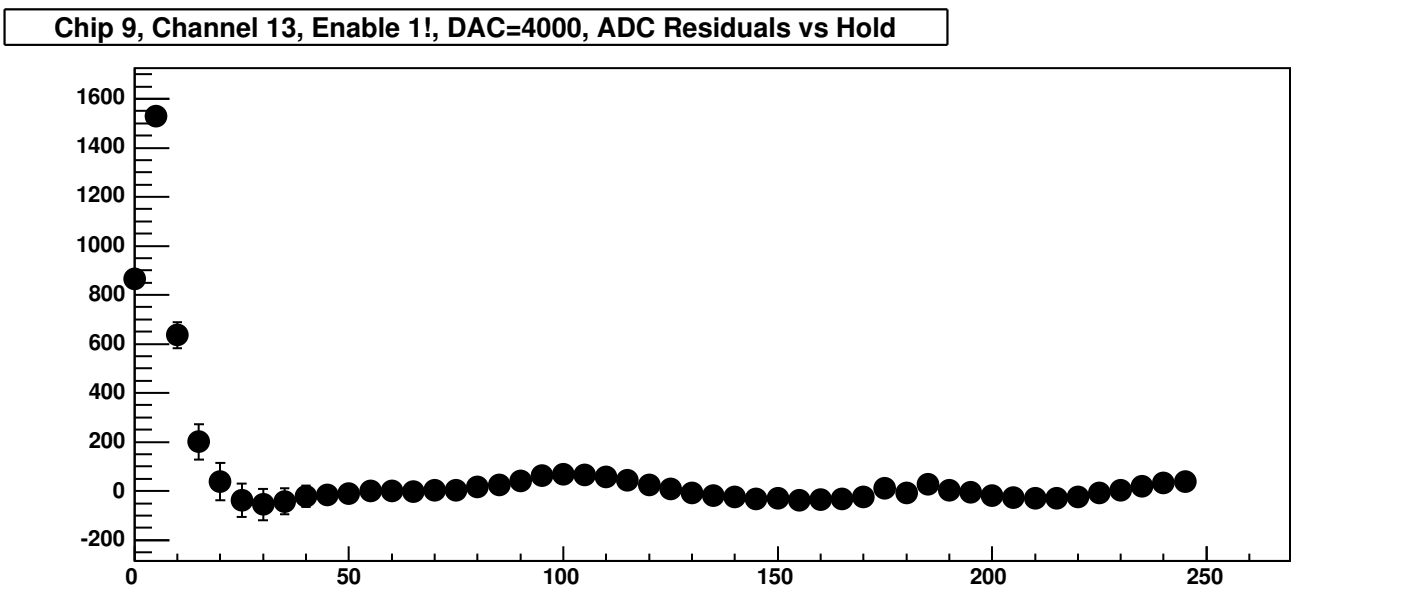

Chip 9, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 8.491e+05 / 41
p0 -497.1 ± 0.05352
p1 79.67 ± 0.8265
p2 3700 ± 0.13
p3 -20.12 ± 0.2292
p4 1.436 ± 0.0009405
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 9, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold
90
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250

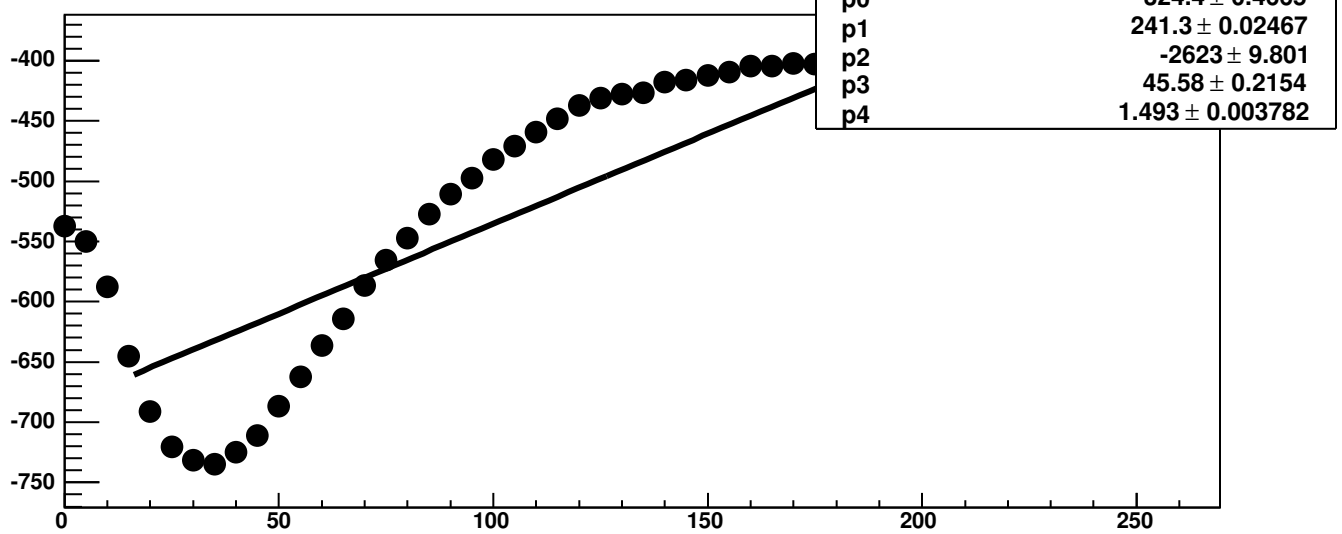

Chip 9, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold
2500
2000
1500
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250


Chip 9, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 701.6 / 42
p0 -1406 ± 1.369
p1 4.327 ± 0.1166
p2 9428 ± 13.85
p3 28.77 ± 0.03694
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

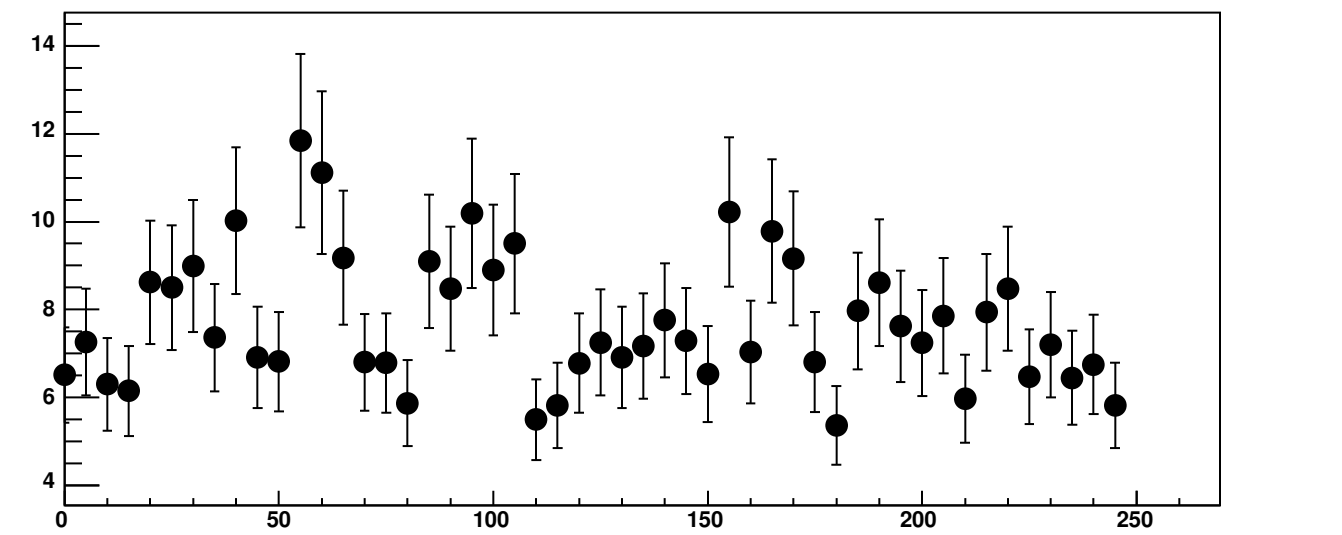

Chip 9, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold
400
350
300
250
200
150
100
50
0
0
50
100
150
200
250


Chip 9, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

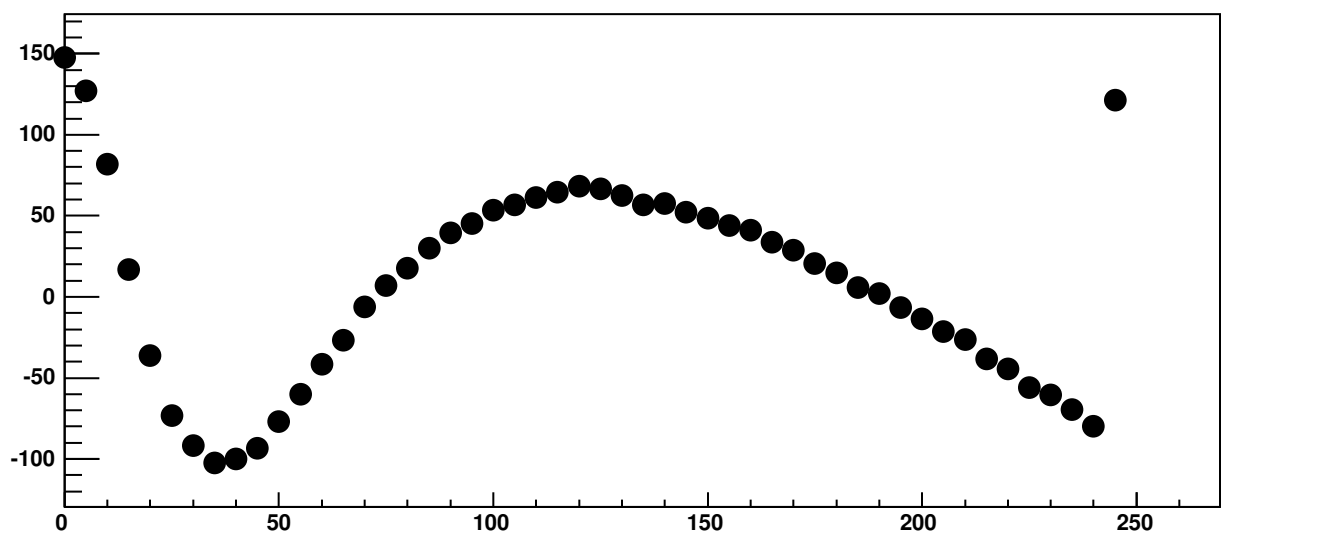
Chip 9, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

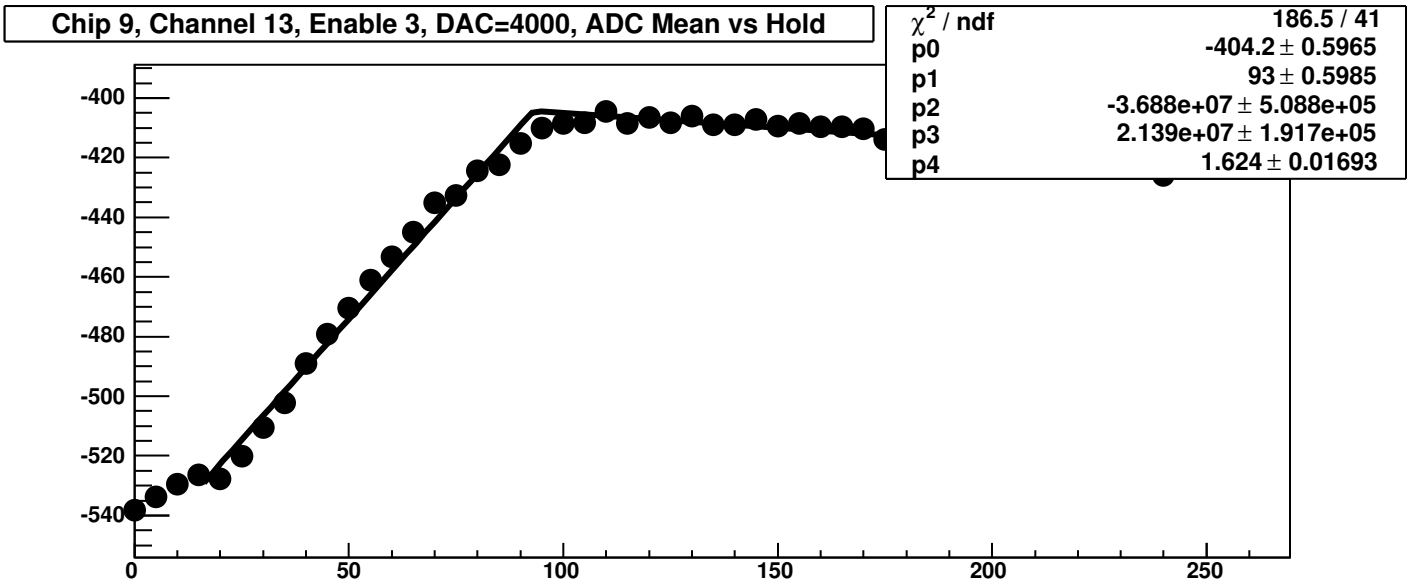


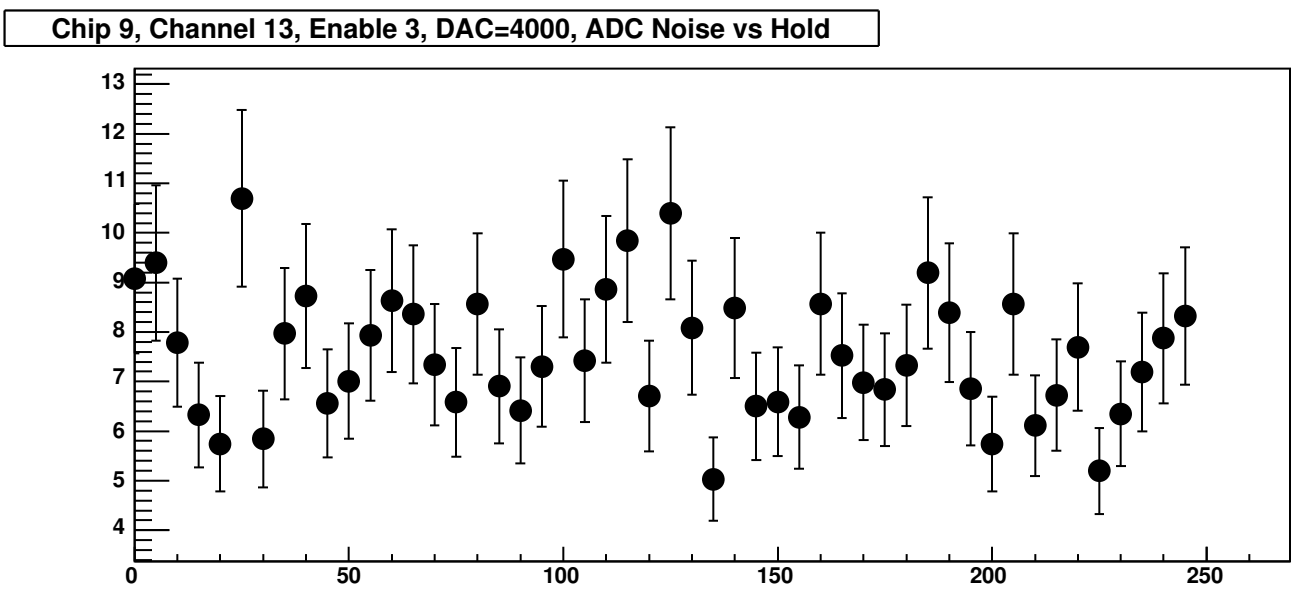
Chip 9, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold

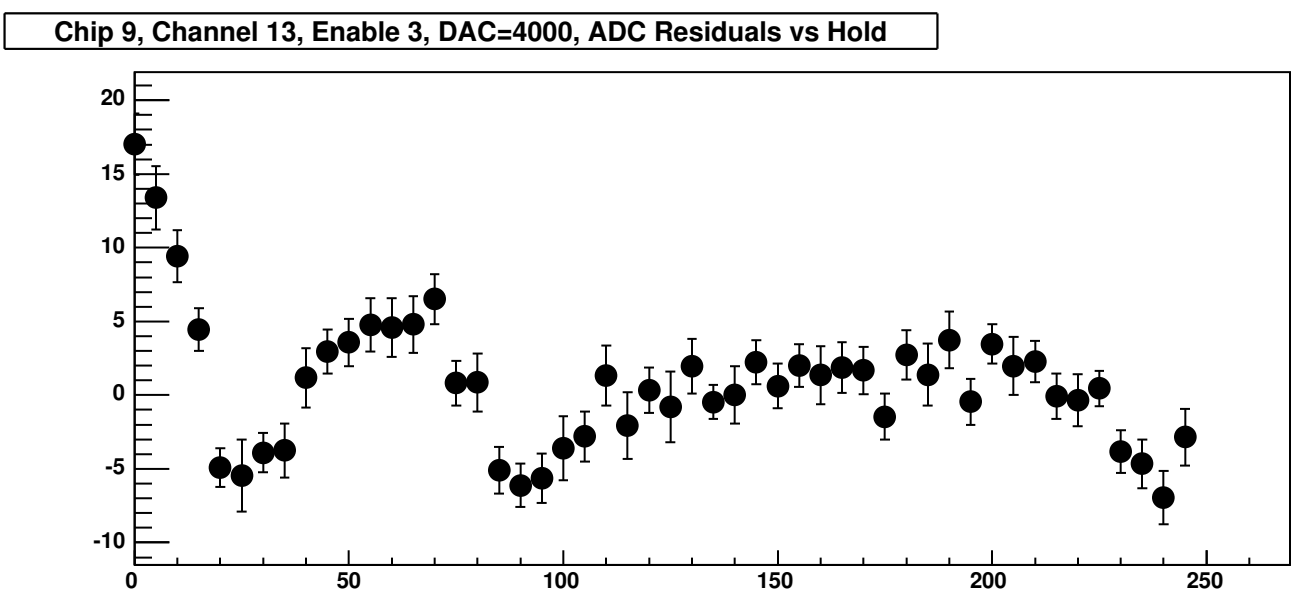


Chip 9, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

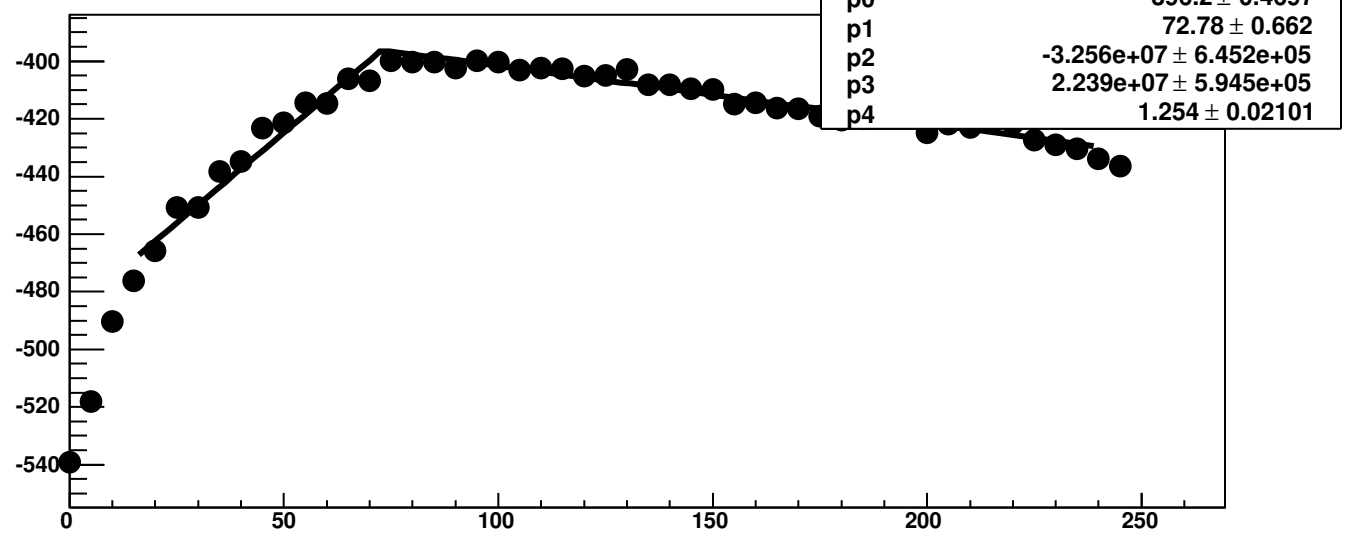



Chip 9, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 186.5 / 41
p0 -404.2 ± 0.5965
p1 93 ± 0.5985
p2 -3.688e+07 ± 5.088e+05
p3 2.139e+07 ± 1.917e+05
p4 1.624 ± 0.01693
-400
-420
-440
-460
-480
-500
-520
-540
0
50
100
150
200
250

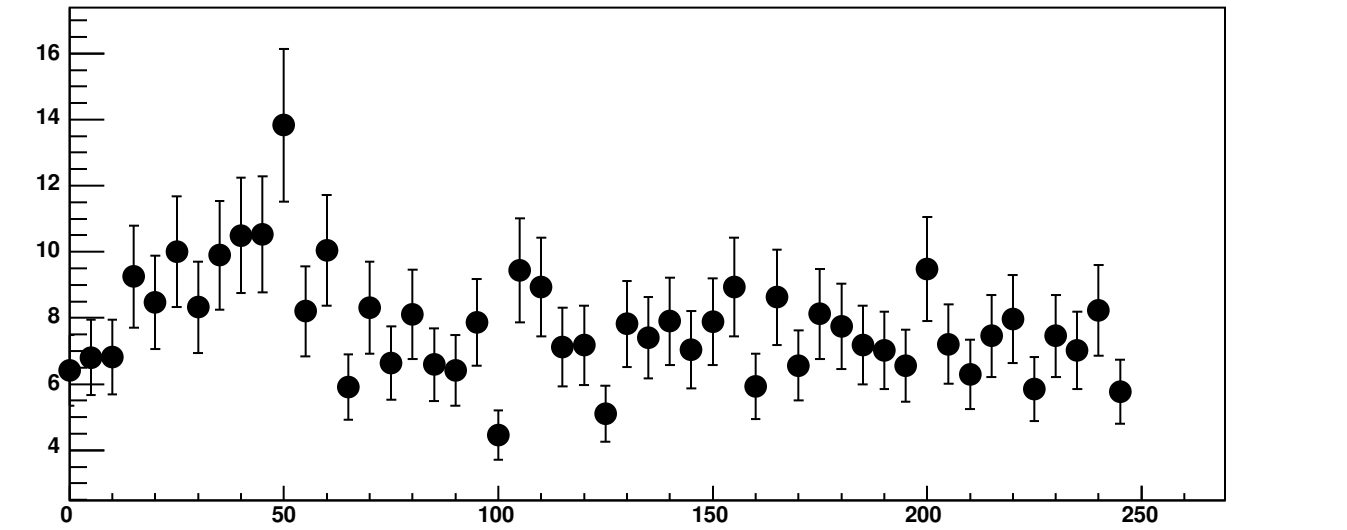

Chip 9, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 9, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
0
50
100
150
200
250

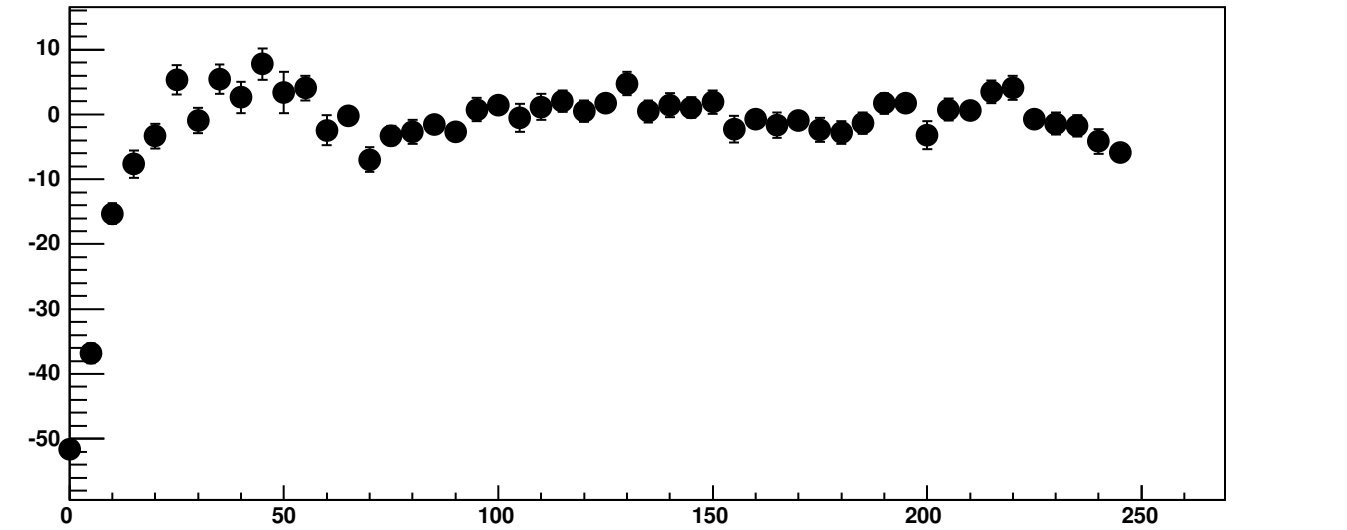
Chip 9, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold

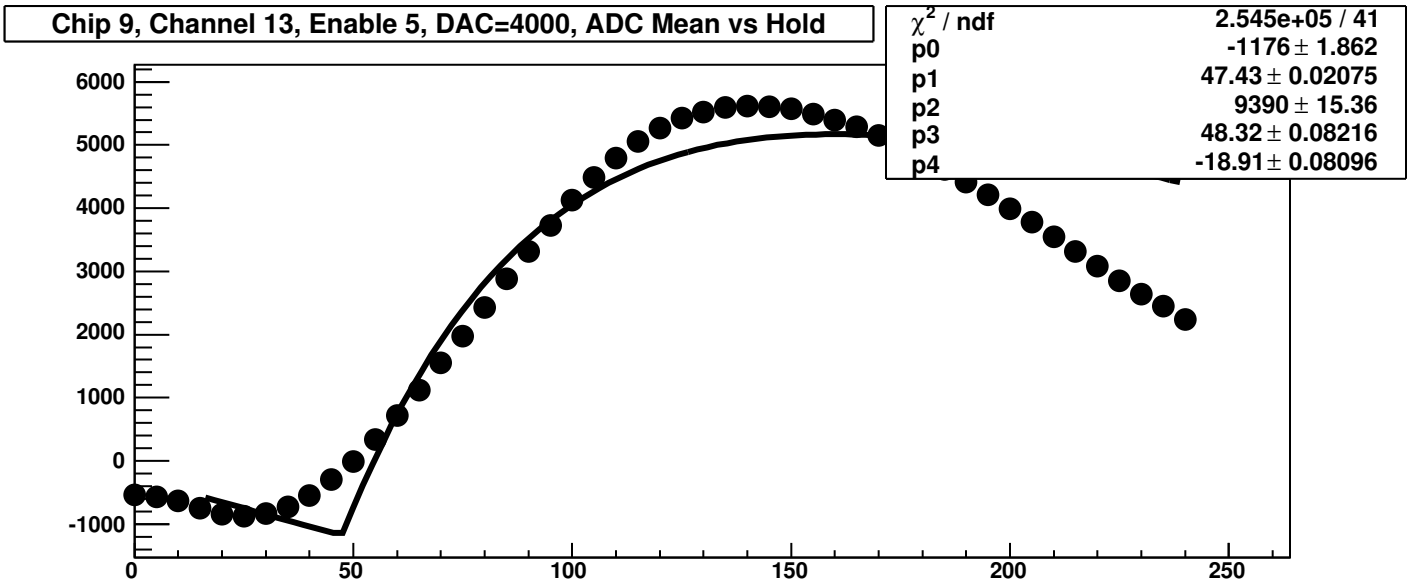


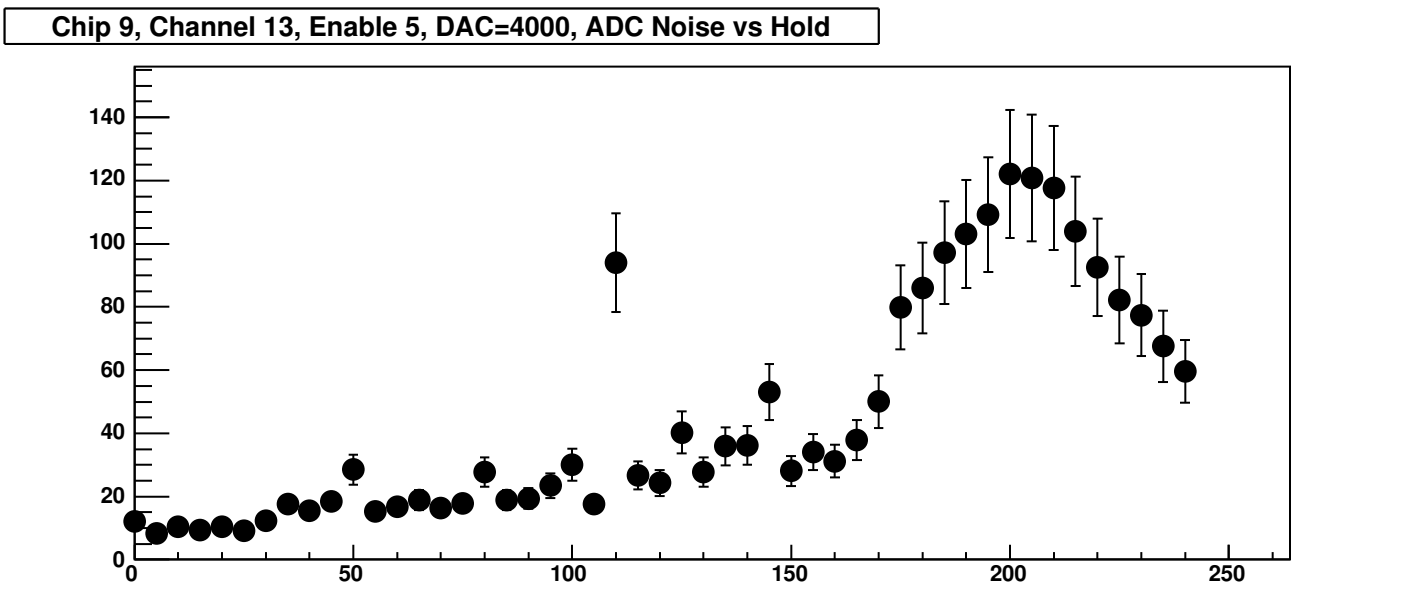
Chip 9, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold

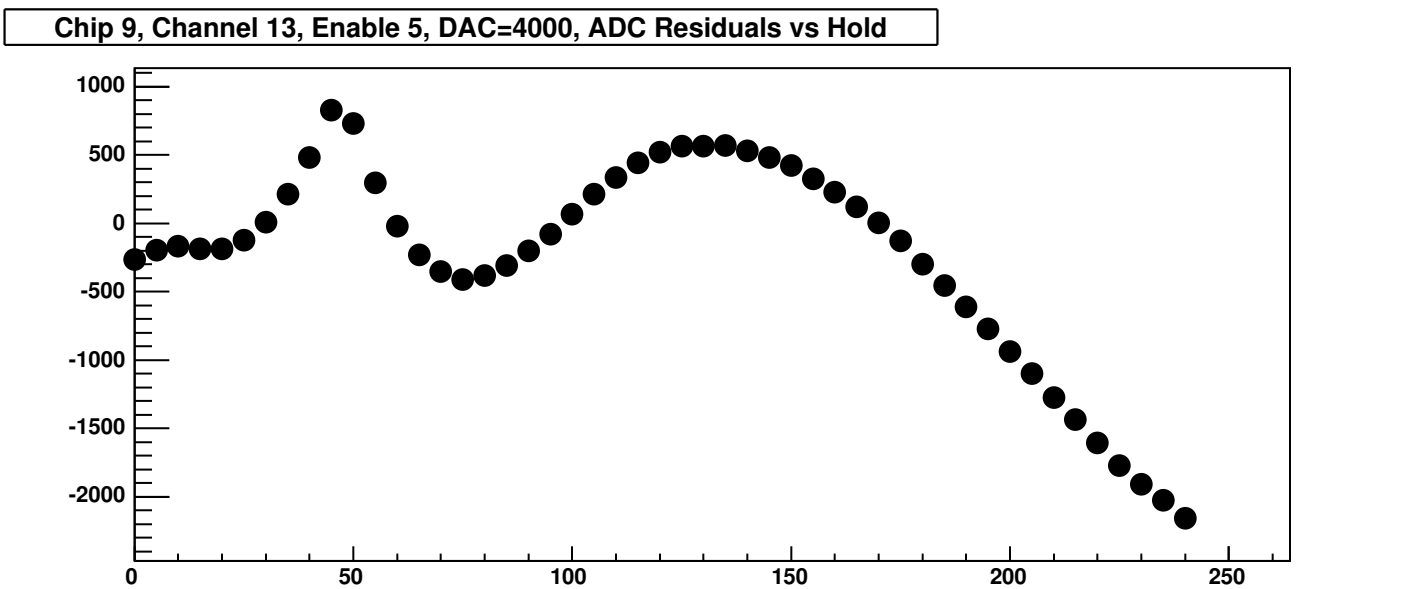


Chip 9, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold

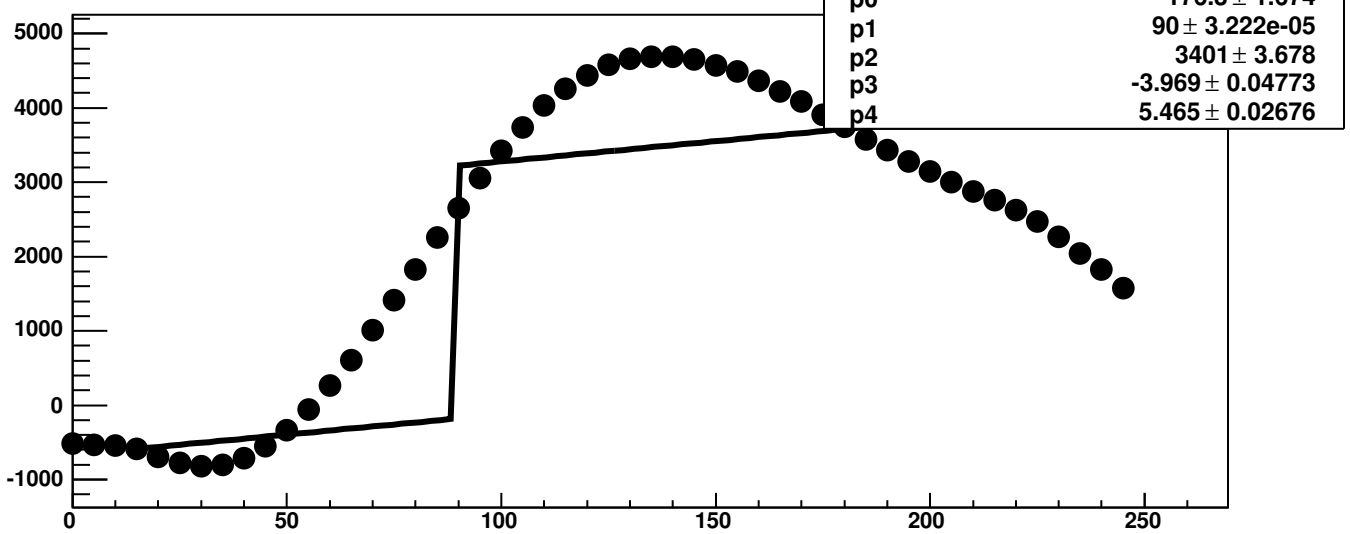



Chip 9, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.545e+05 / 41
p0 -1176 ± 1.862
p1 47.43 ± 0.02075
p2 9390 ± 15.36
p3 48.32 ± 0.08216
p4 -18.91 ± 0.08096
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

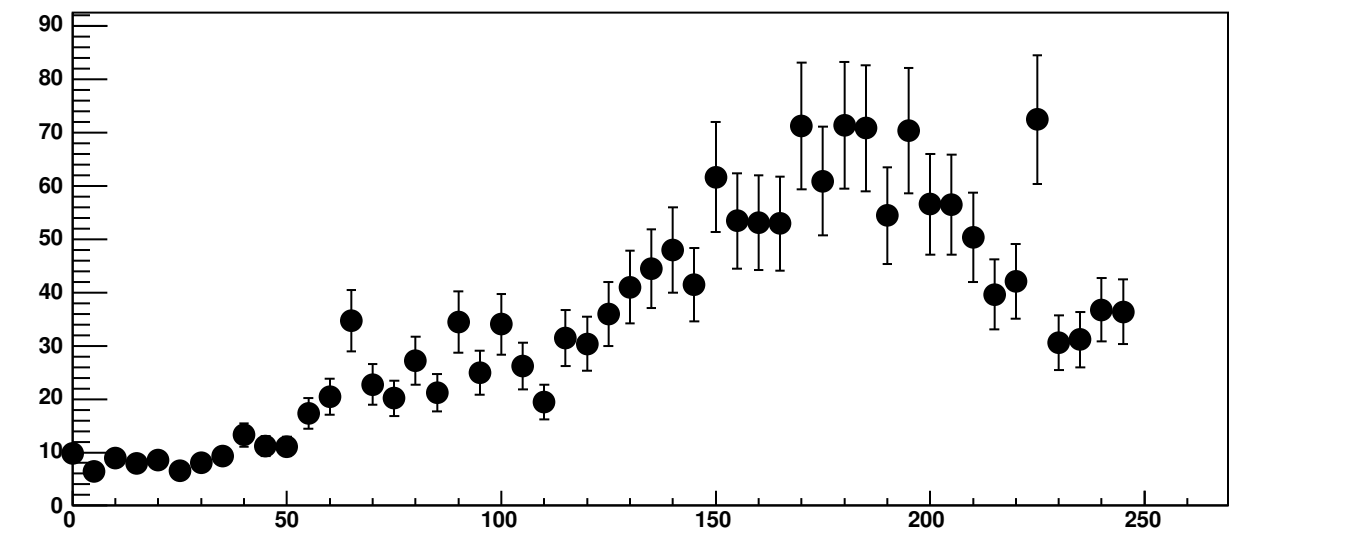

Chip 9, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 9, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

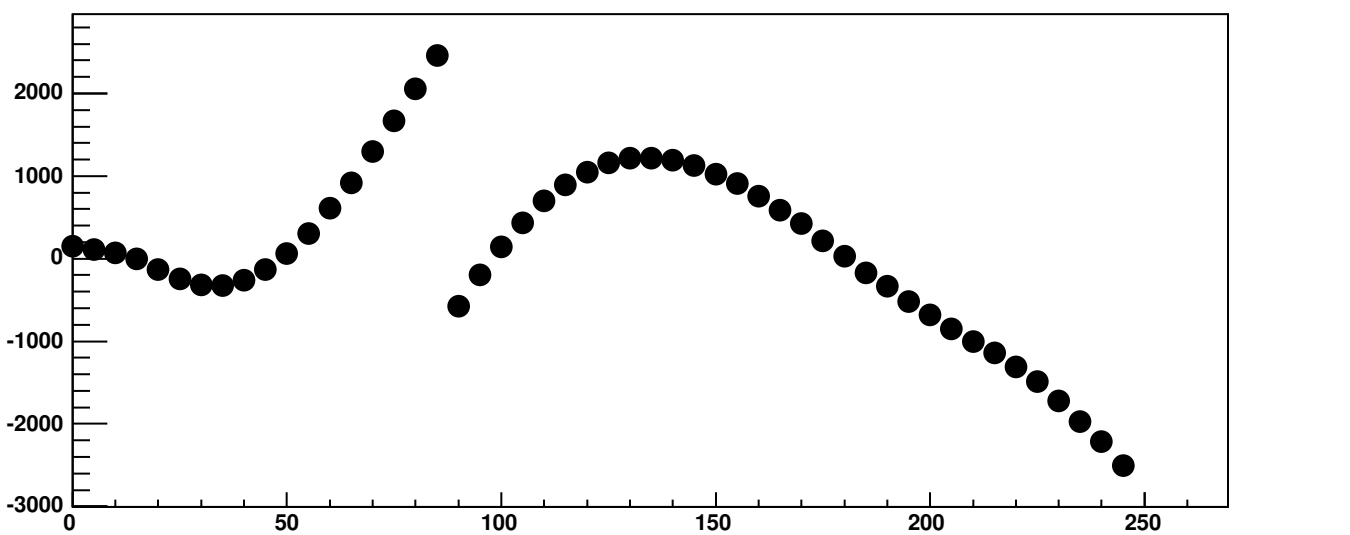
Chip 9, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold



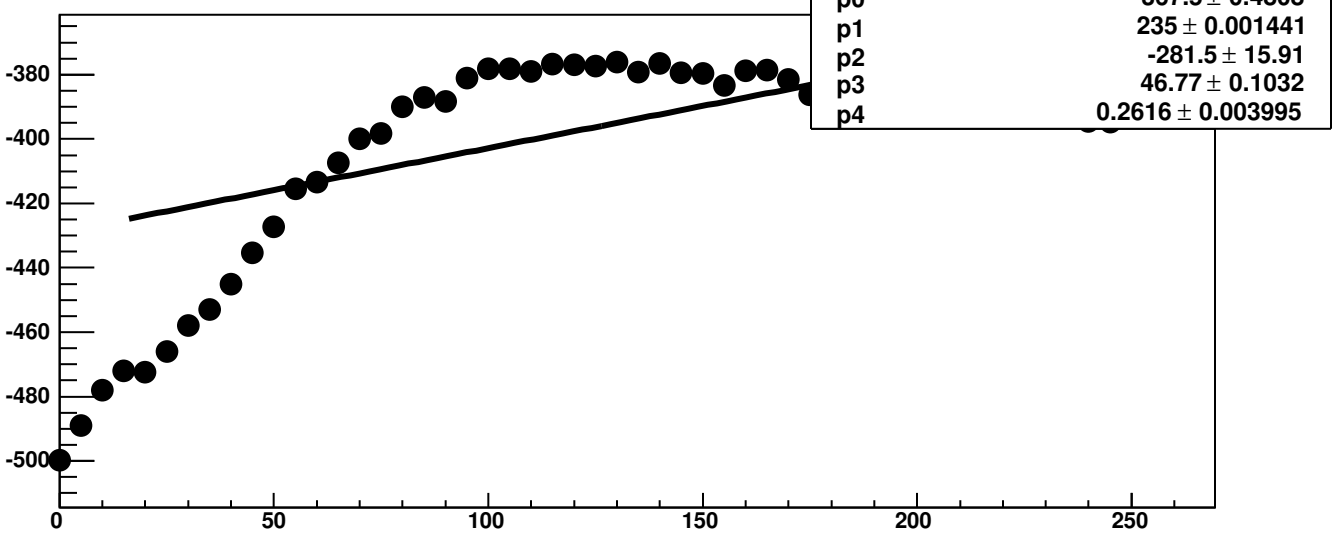
Chip 9, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



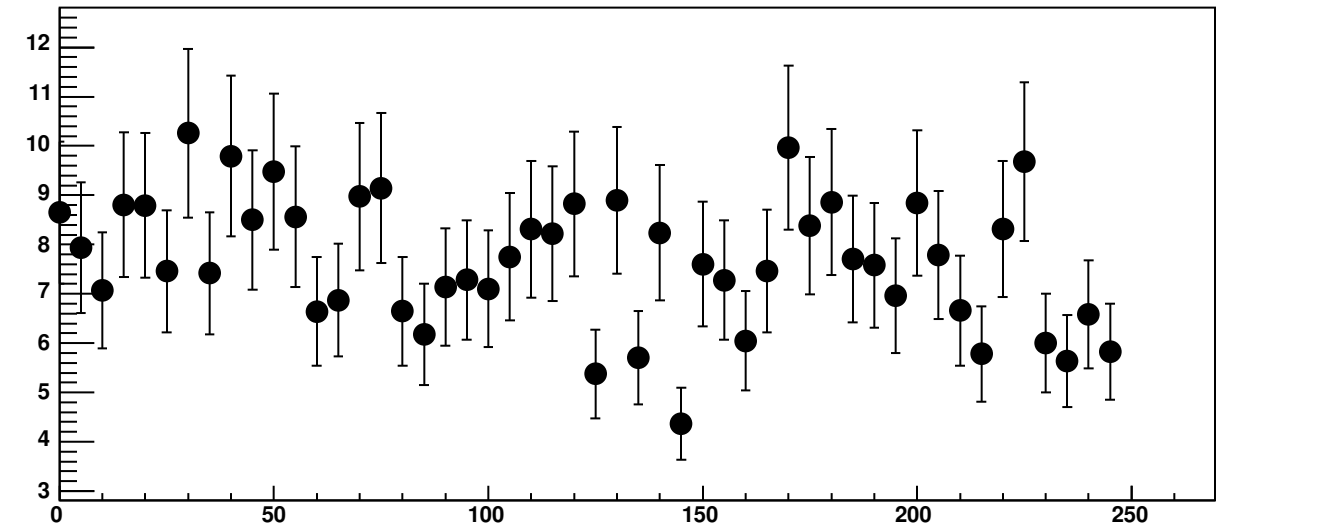
Chip 9, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold



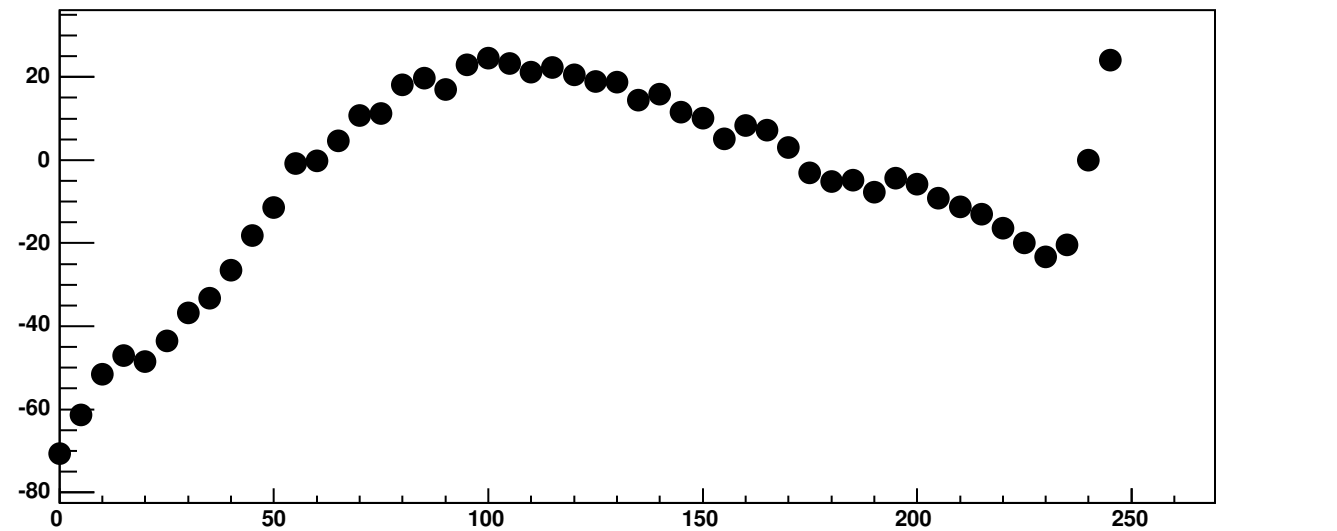
Chip 9, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold



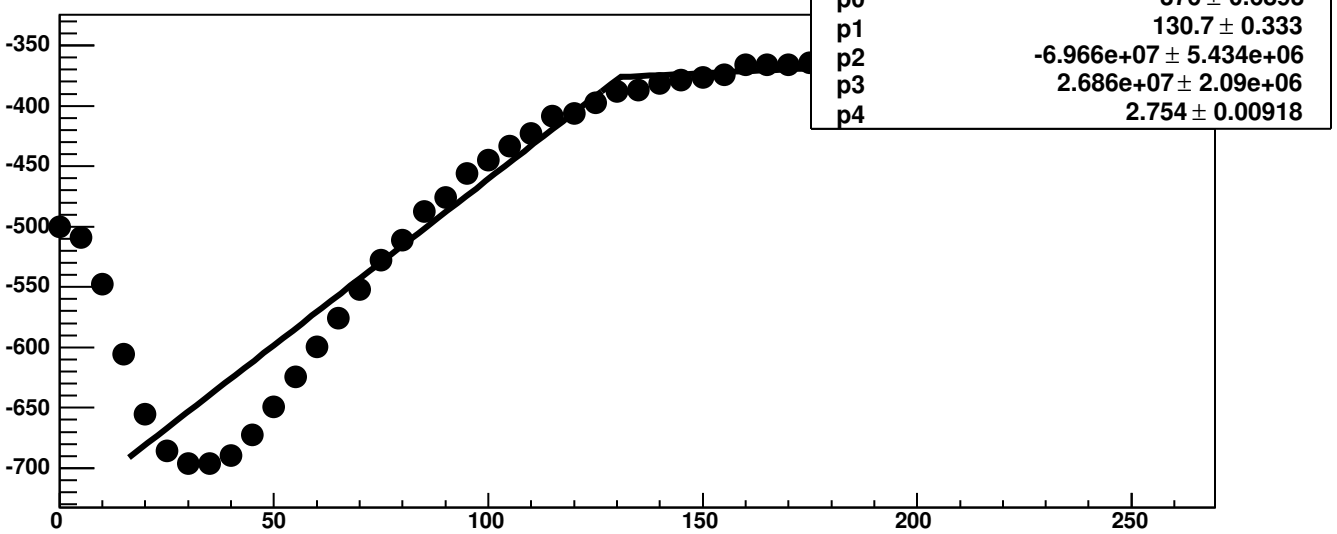
Chip 9, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold



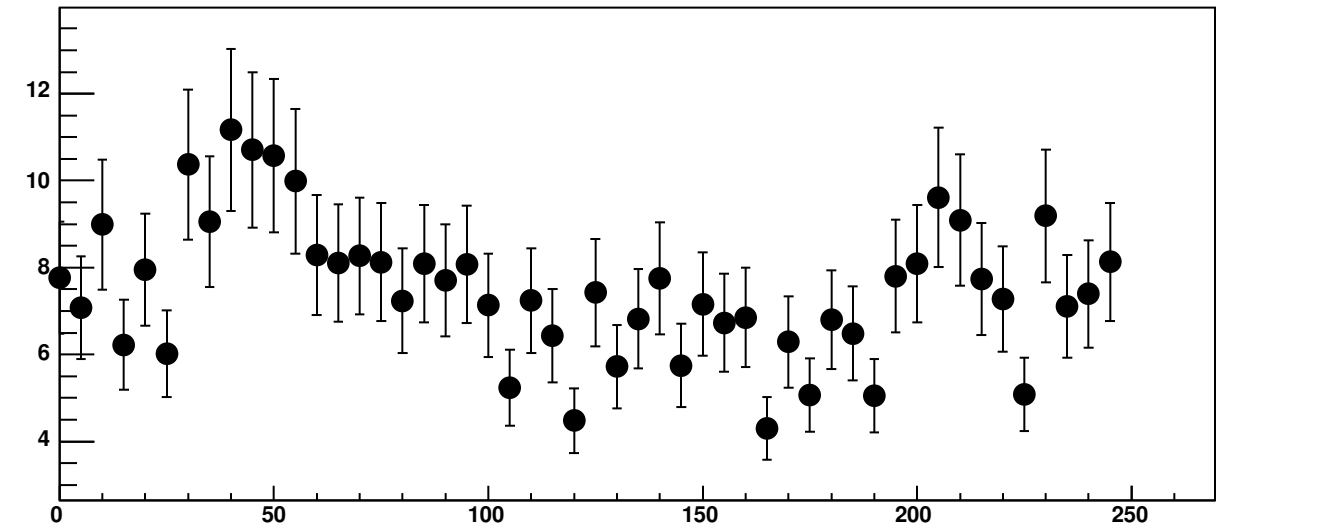
Chip 9, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold



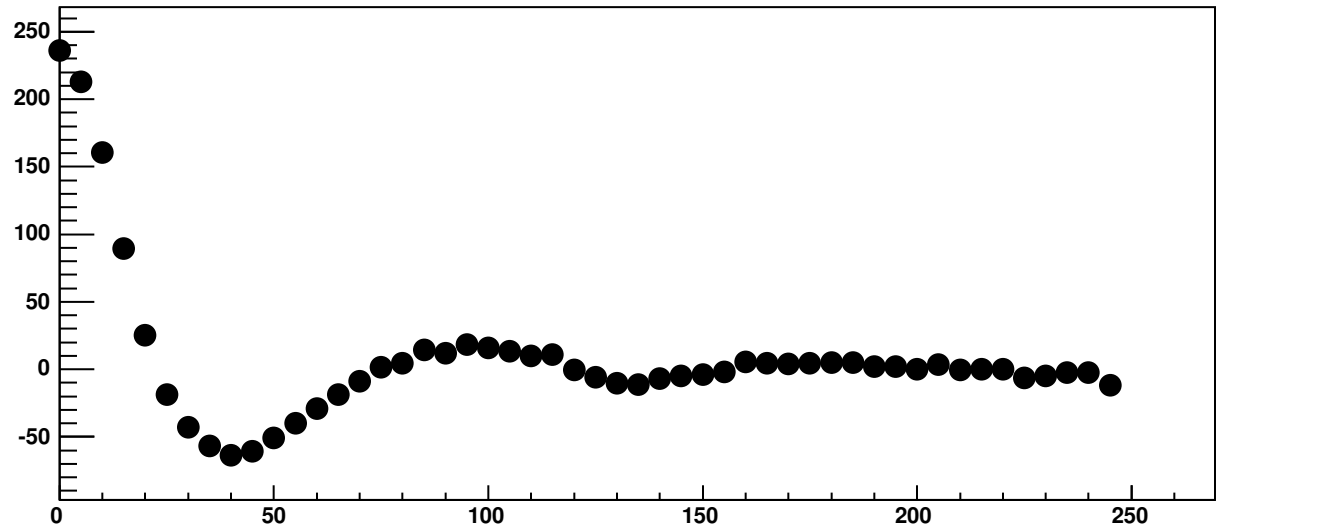
Chip 9, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold



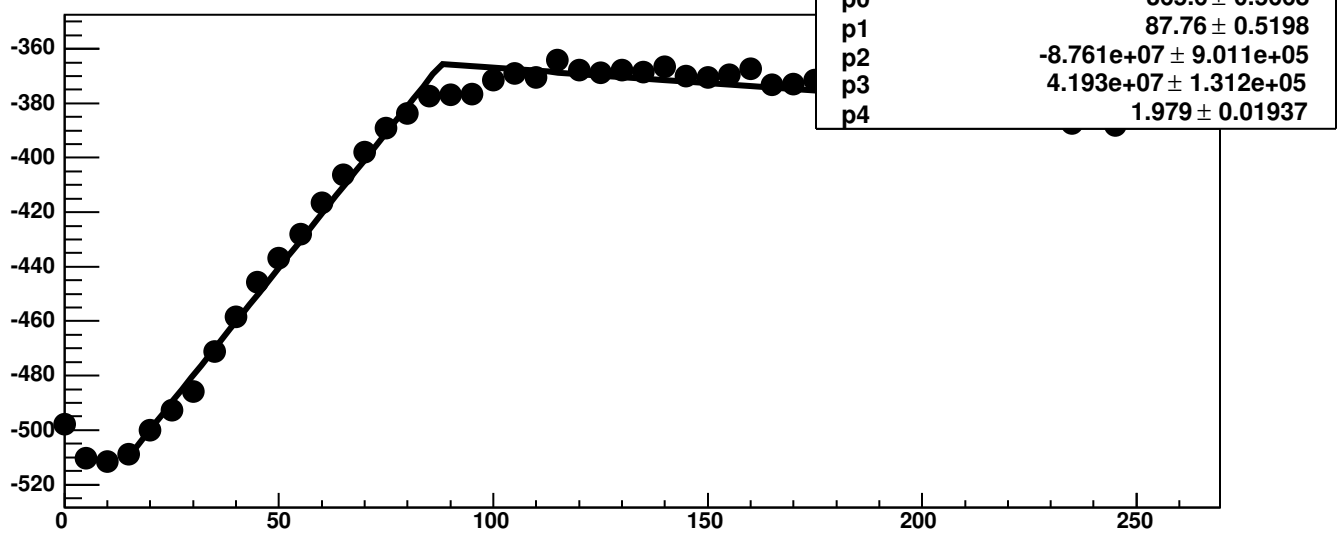
Chip 9, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



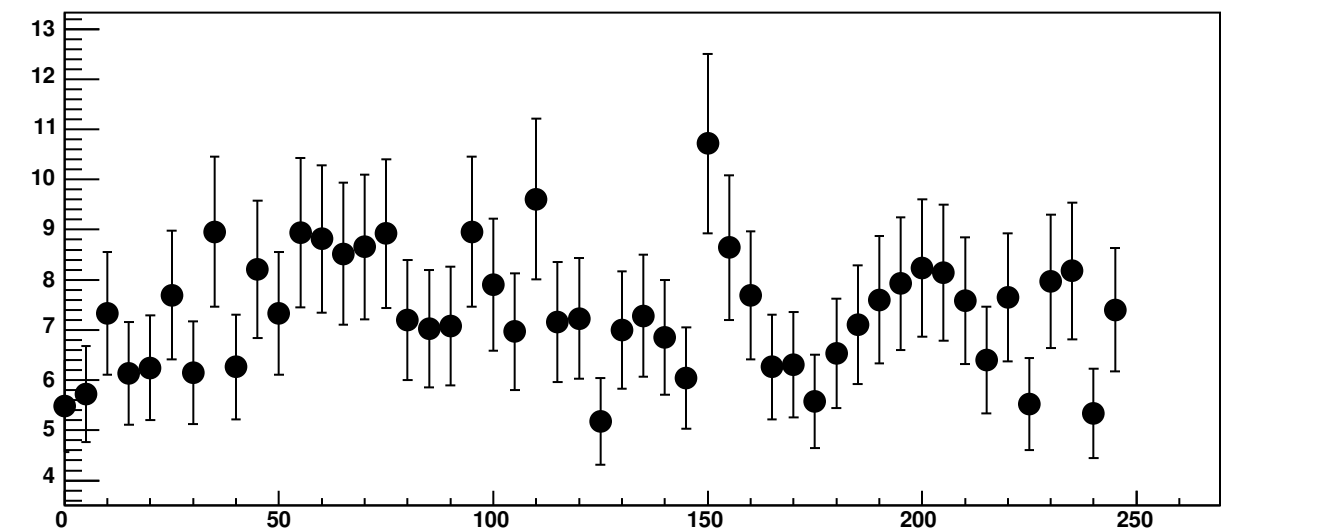
Chip 9, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold



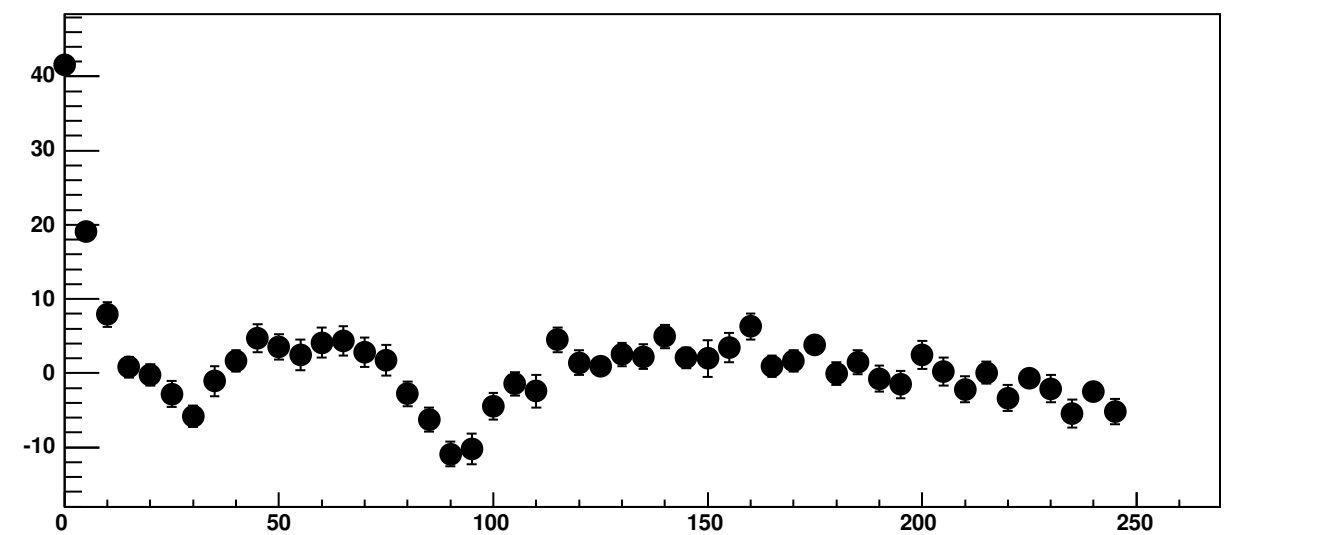
Chip 9, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

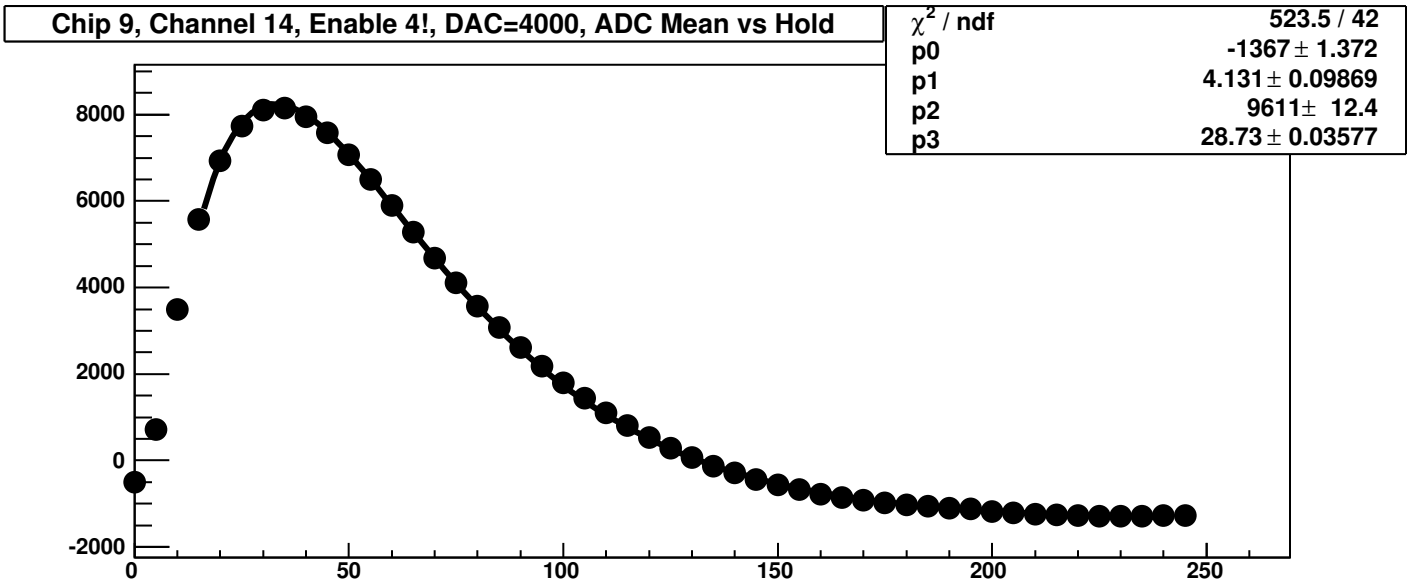


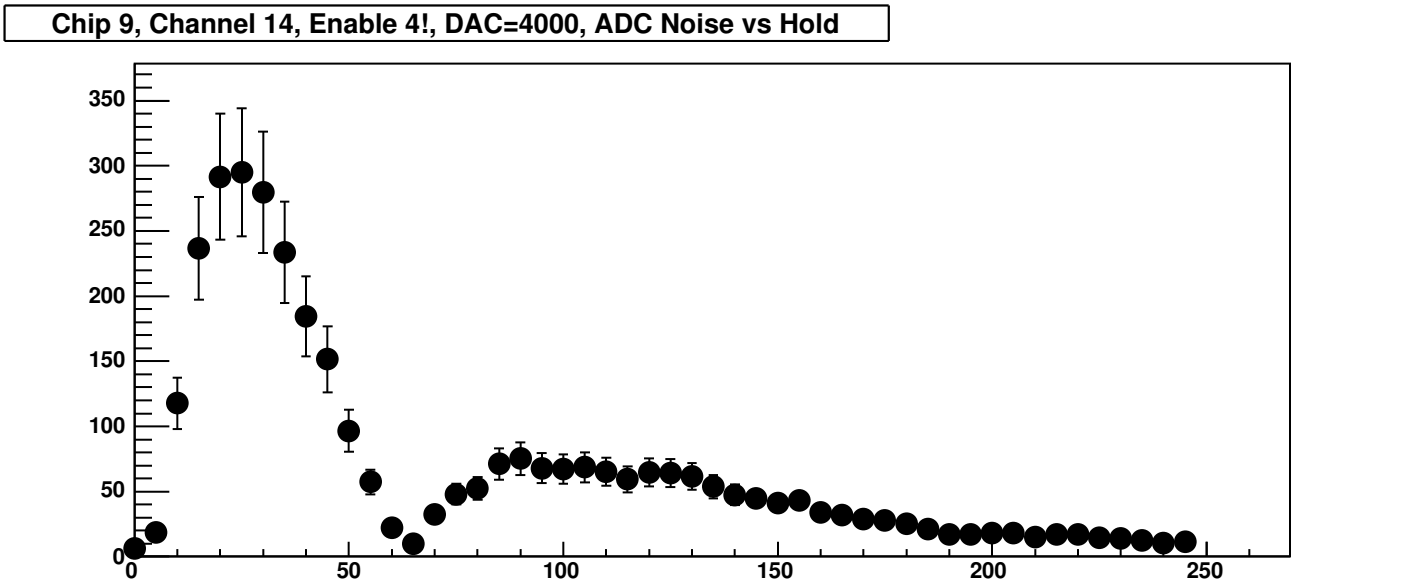
Chip 9, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold

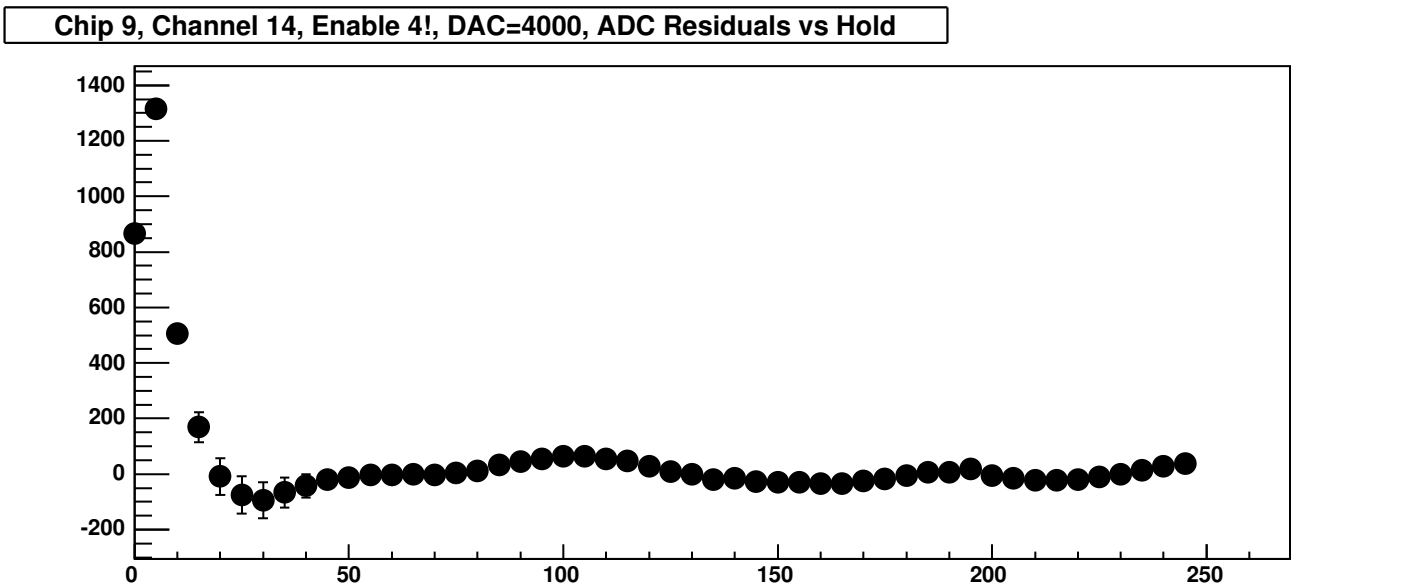


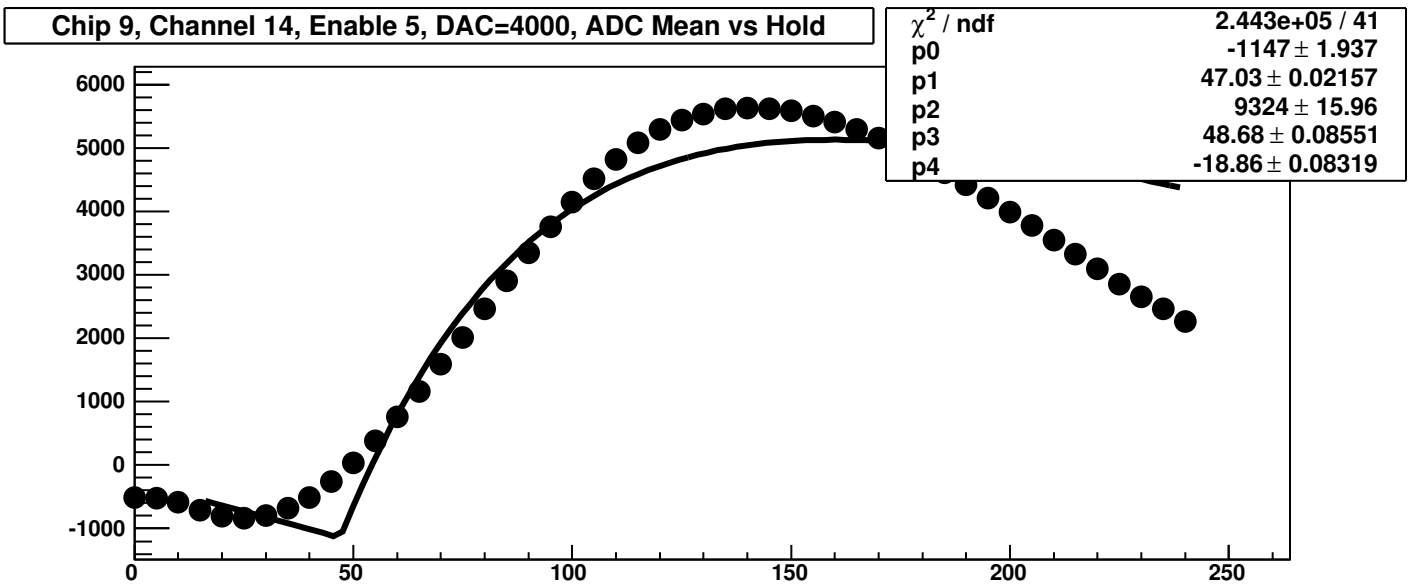
Chip 9, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

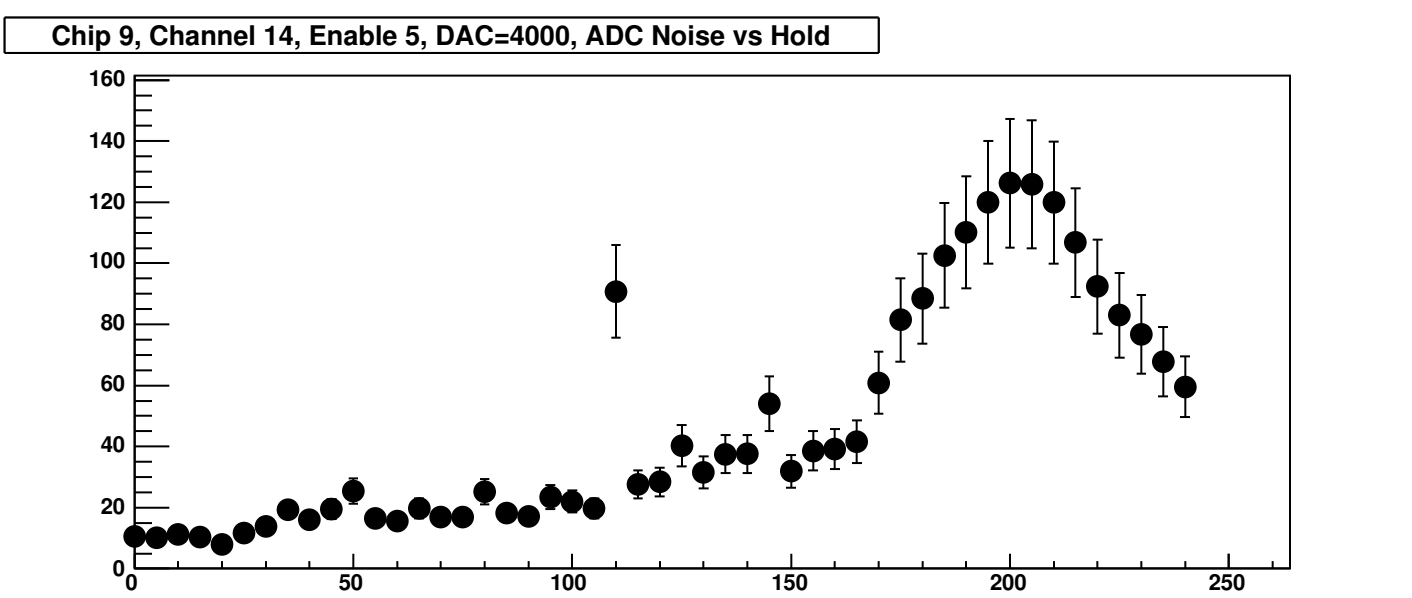


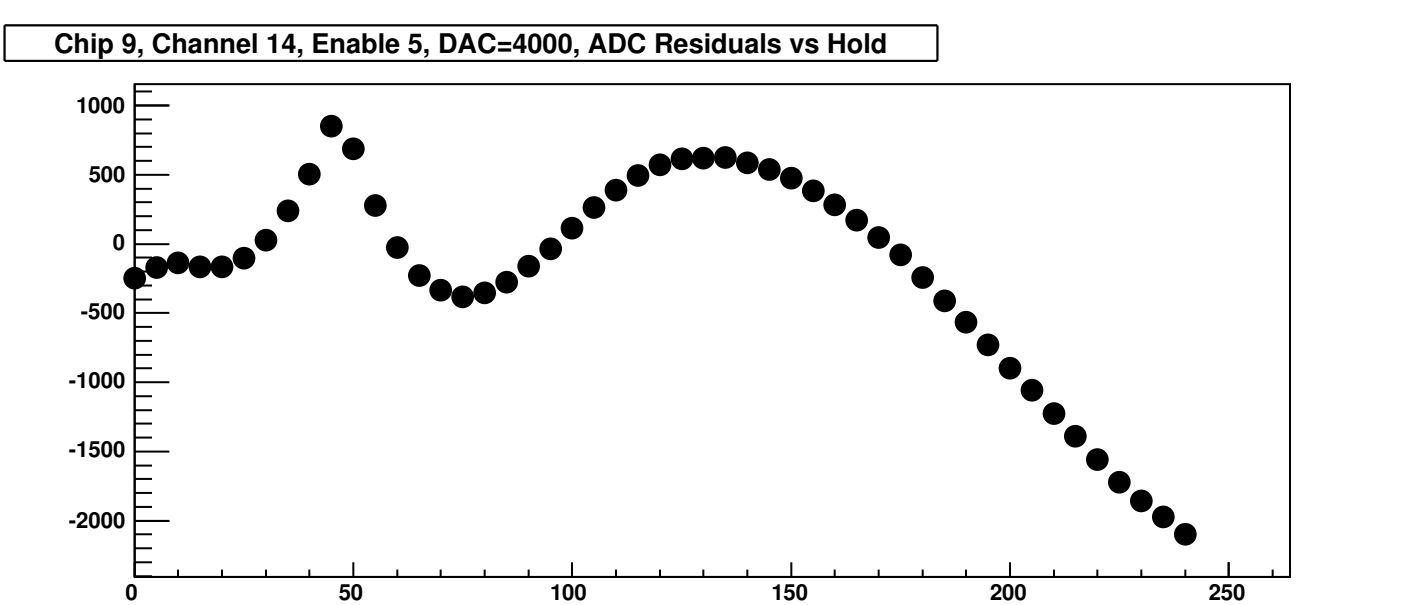

Chip 9, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 523.5 / 42
p0 -1367 ± 1.372
p1 4.131 ± 0.09869
p2 9611 ± 12.4
p3 28.73 ± 0.03577
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

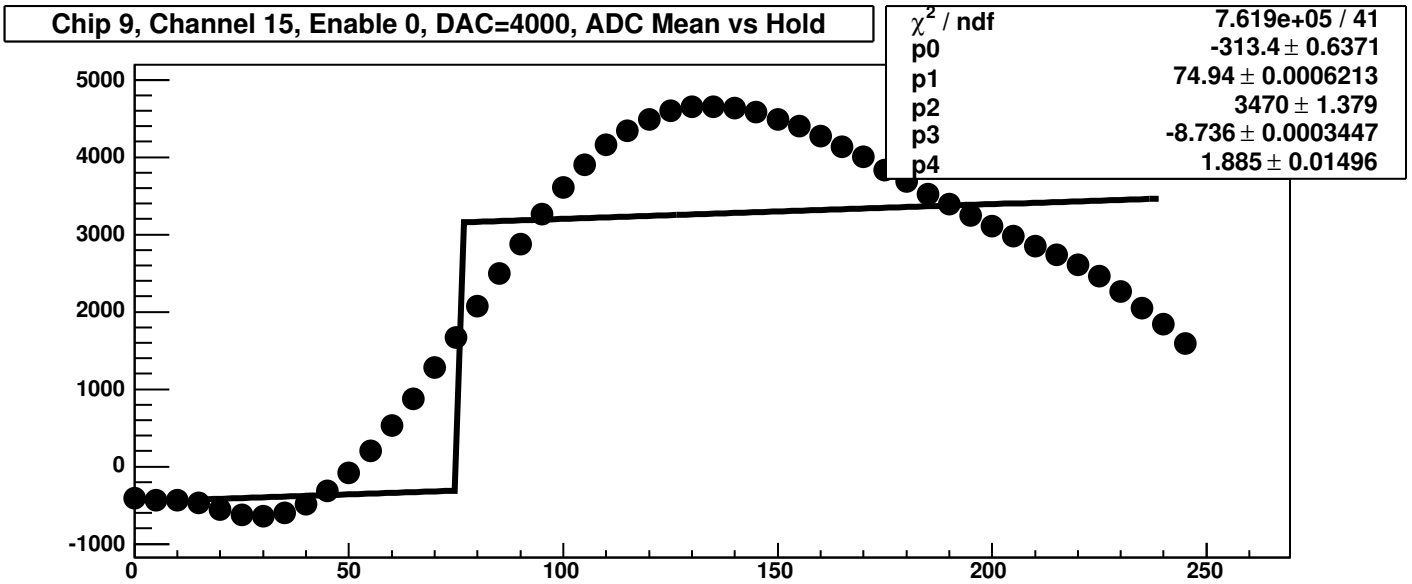

Chip 9, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold
350
300
250
200
150
100
50
0
0
50
100
150
200
250

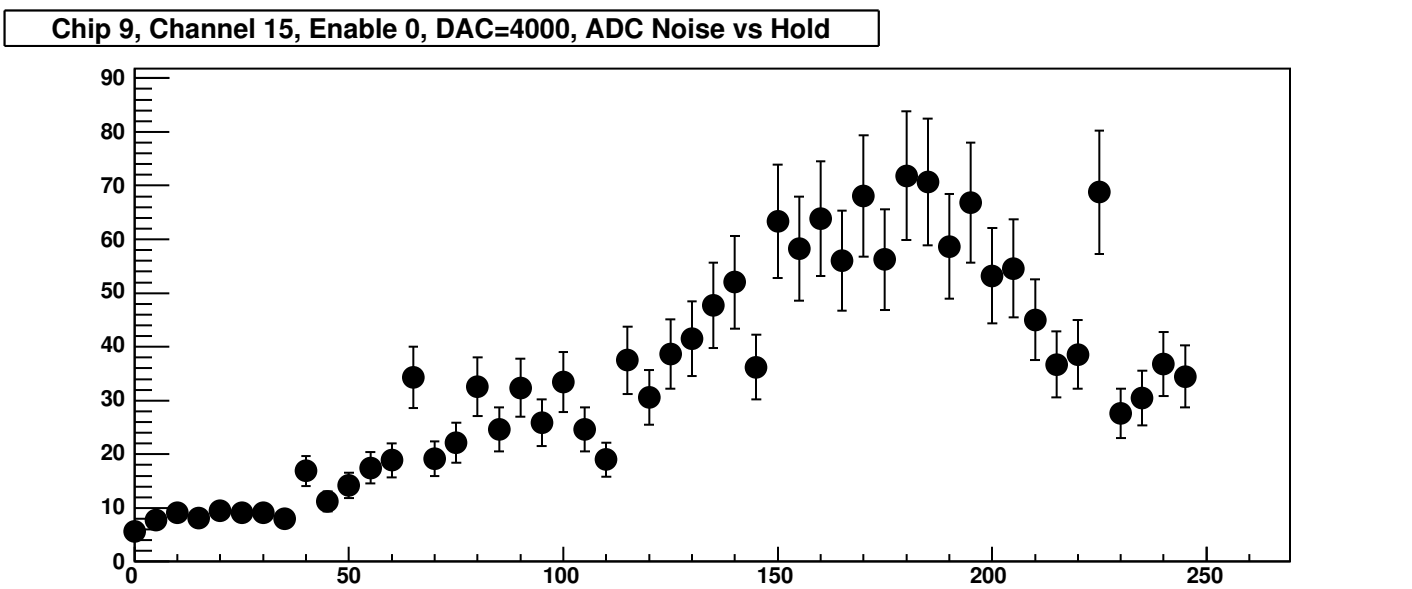

Chip 9, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

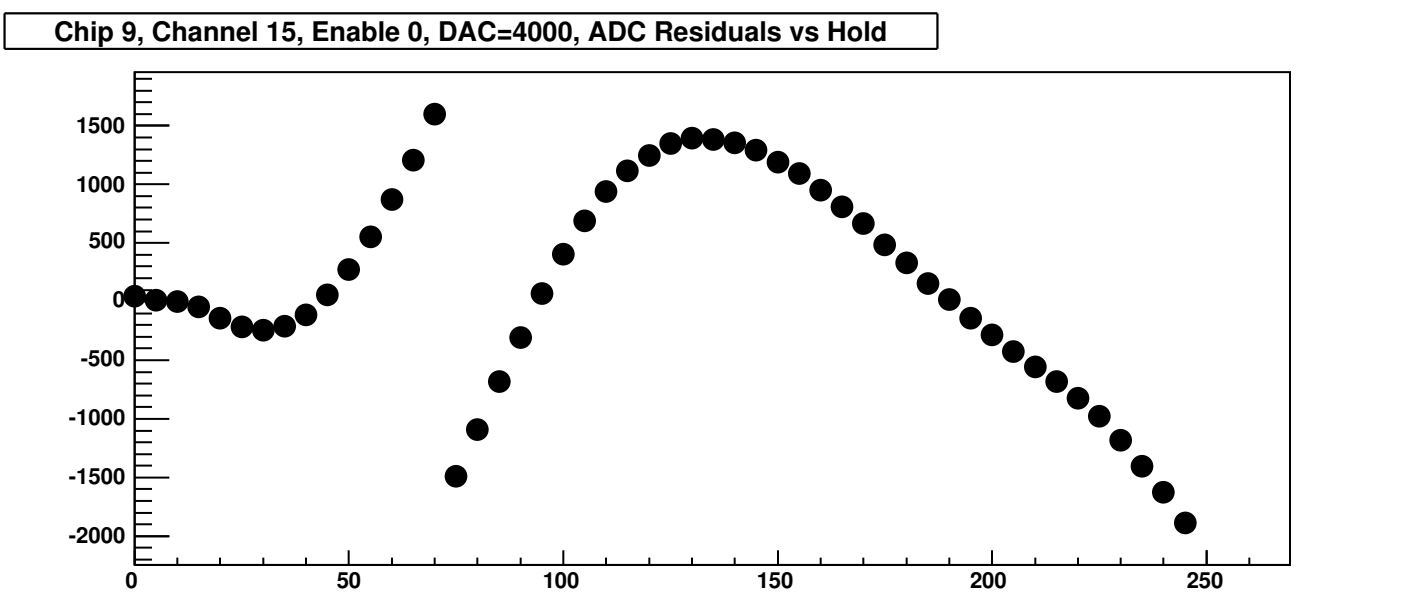

Chip 9, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.443e+05 / 41
p0 -1147 ± 1.937
p1 47.03 ± 0.02157
p2 9324 ± 15.96
p3 48.68 ± 0.08551
p4 -18.86 ± 0.08319
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

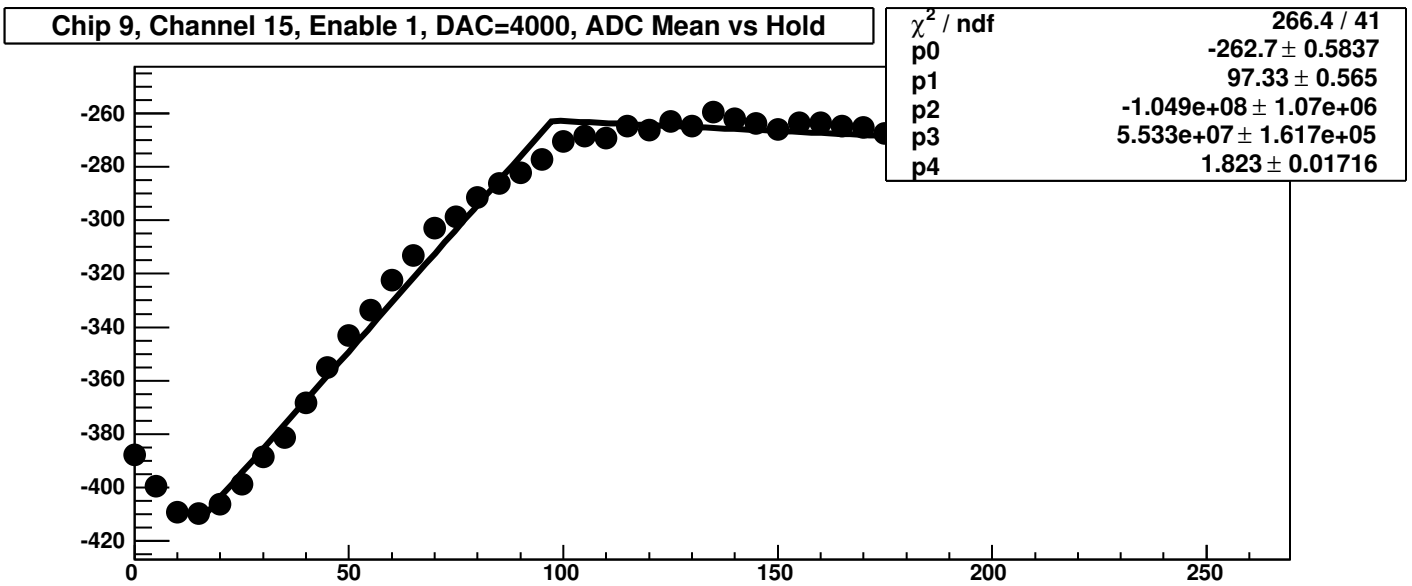

Chip 9, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

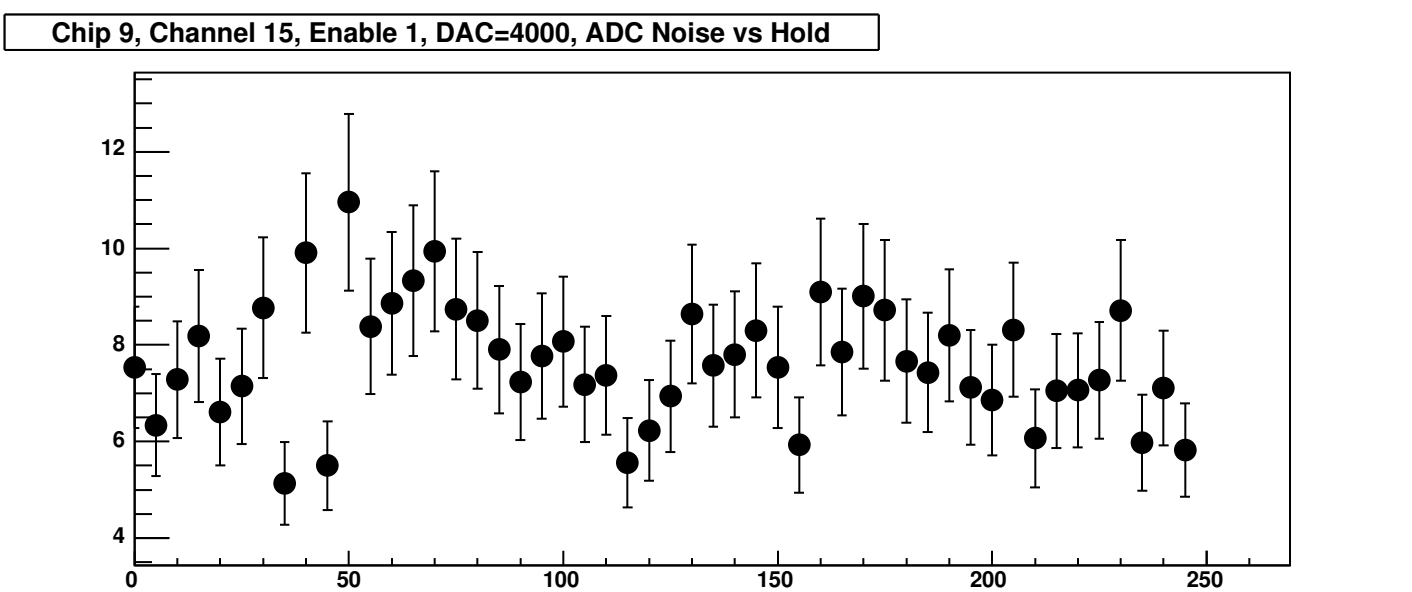

Chip 9, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

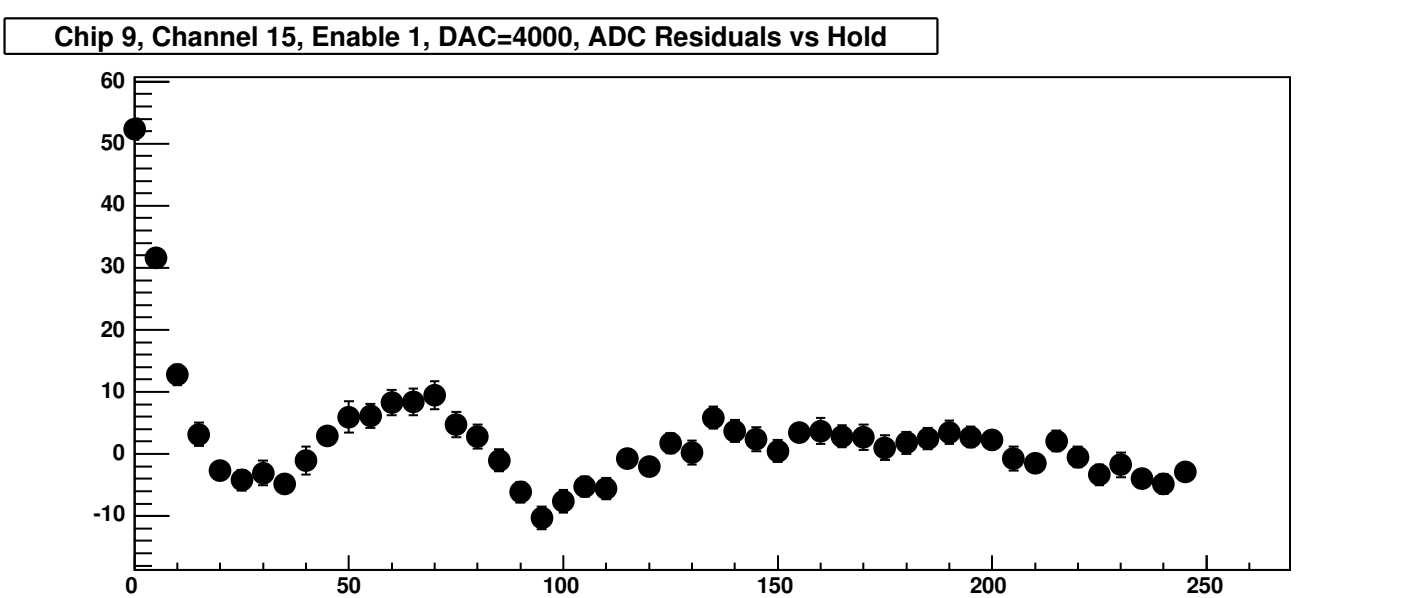

Chip 9, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 7.619e+05 / 41
p0 -313.4 ± 0.6371
p1 74.94 ± 0.0006213
p2 3470 ± 1.379
p3 -8.736 ± 0.0003447
p4 1.885 ± 0.01496
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 9, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold
90
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250

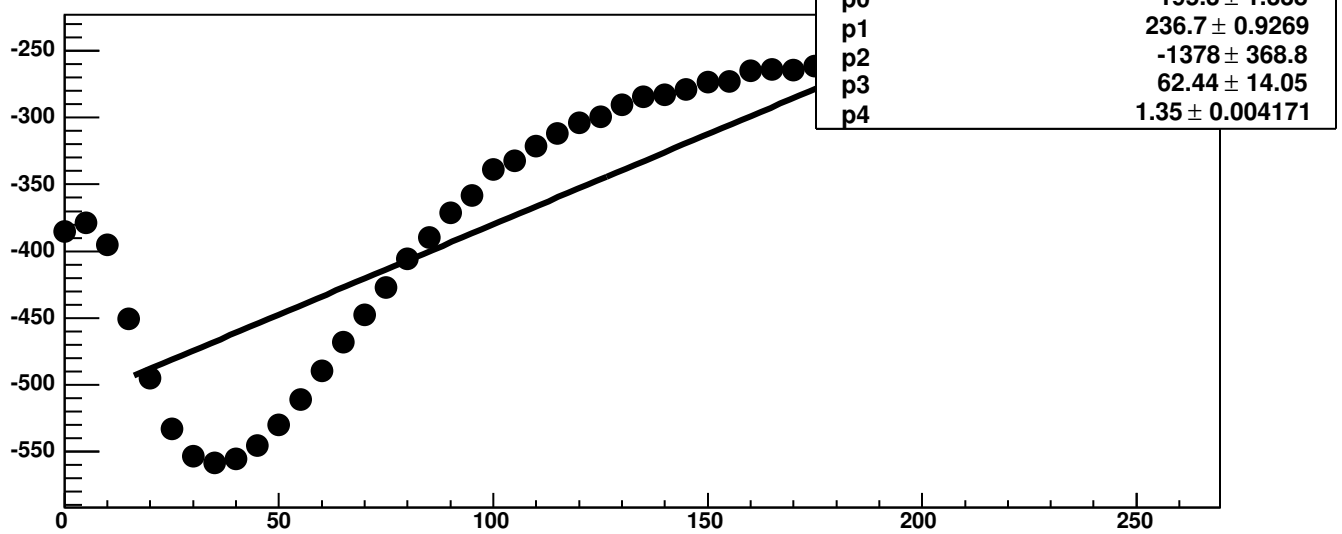

Chip 9, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold
1500
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250


Chip 9, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 266.4 / 41
p0 -262.7 ± 0.5837
p1 97.33 ± 0.565
p2 -1.049e+08 ± 1.07e+06
p3 5.533e+07 ± 1.617e+05
p4 1.823 ± 0.01716
-260
-280
-300
-320
-340
-360
-380
-400
-420
0
50
100
150
200
250

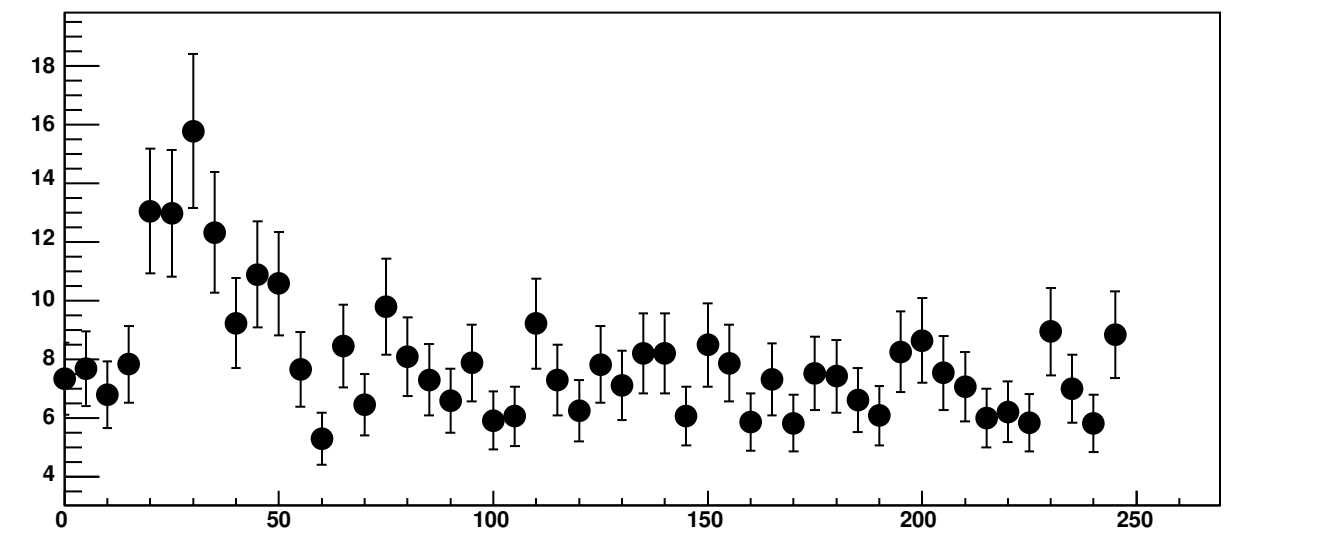

Chip 9, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 9, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

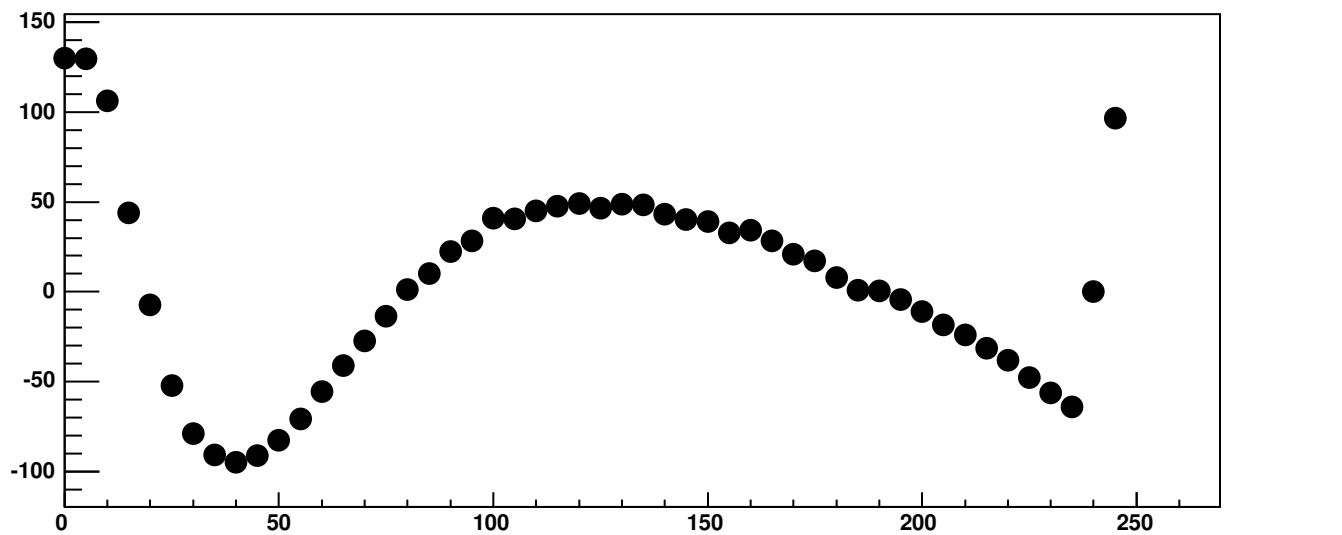
Chip 9, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold



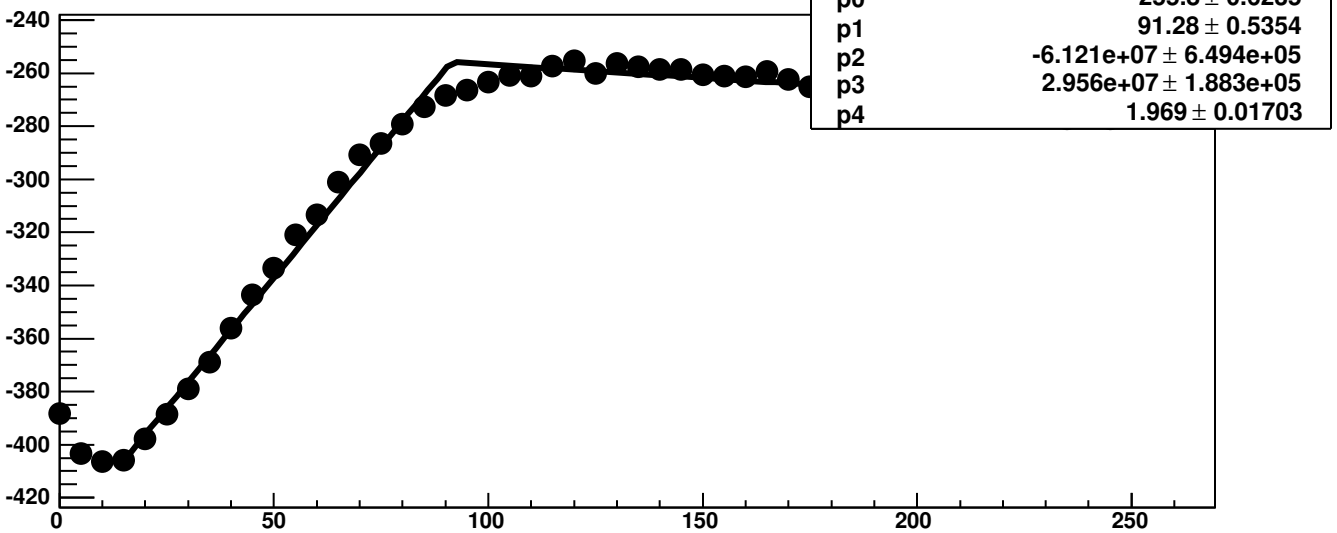
Chip 9, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold



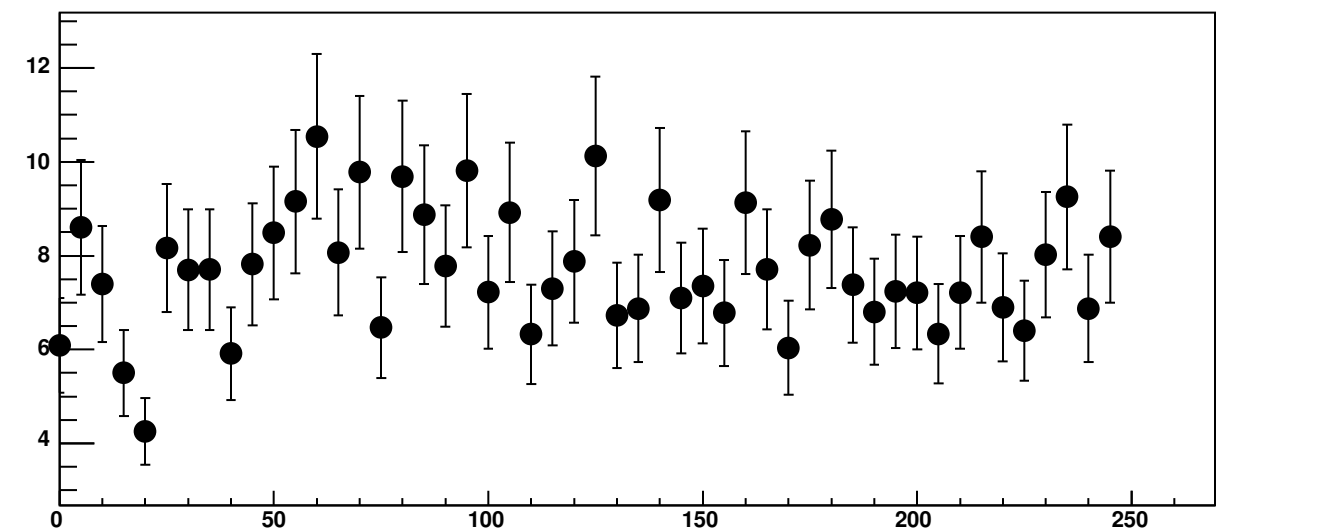
Chip 9, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold



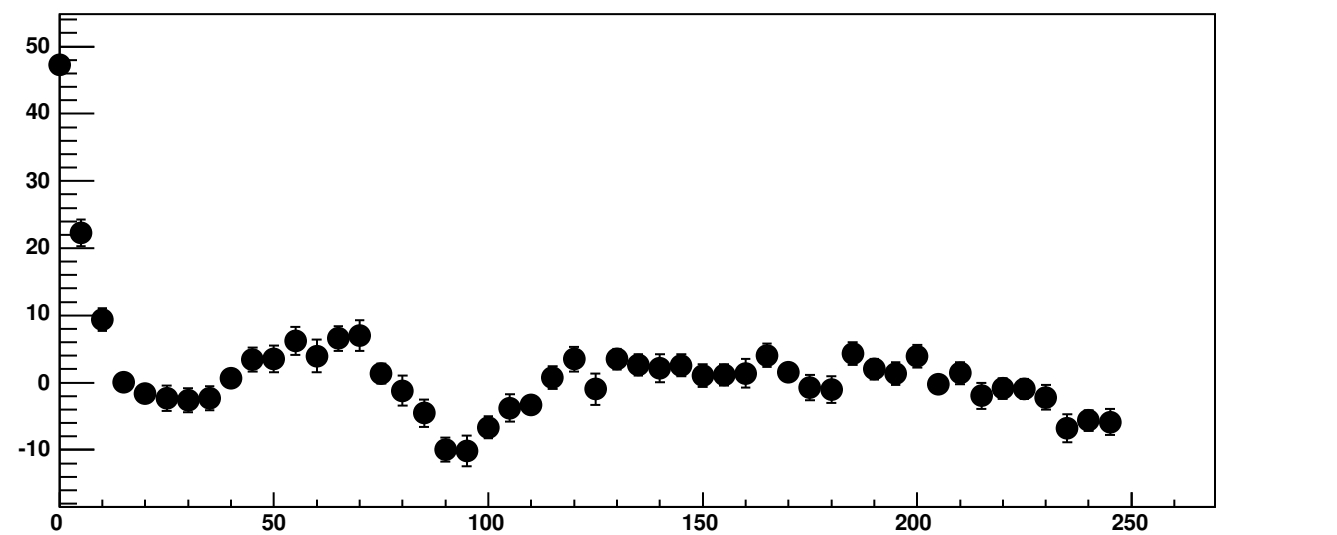
Chip 9, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

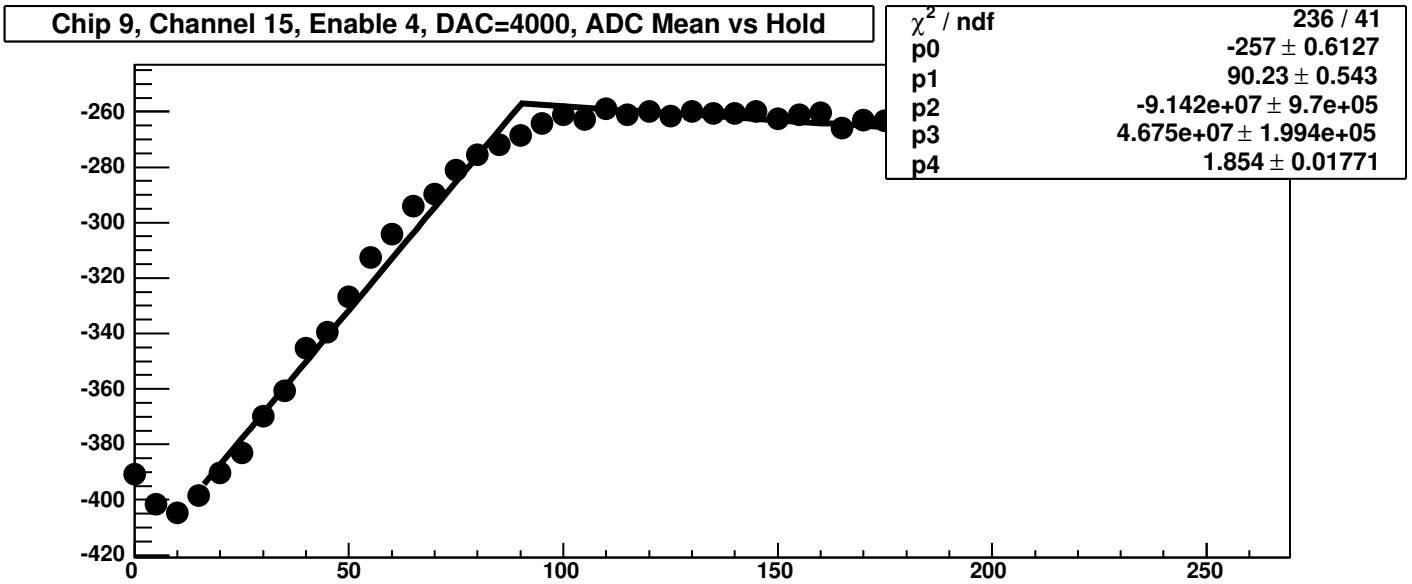


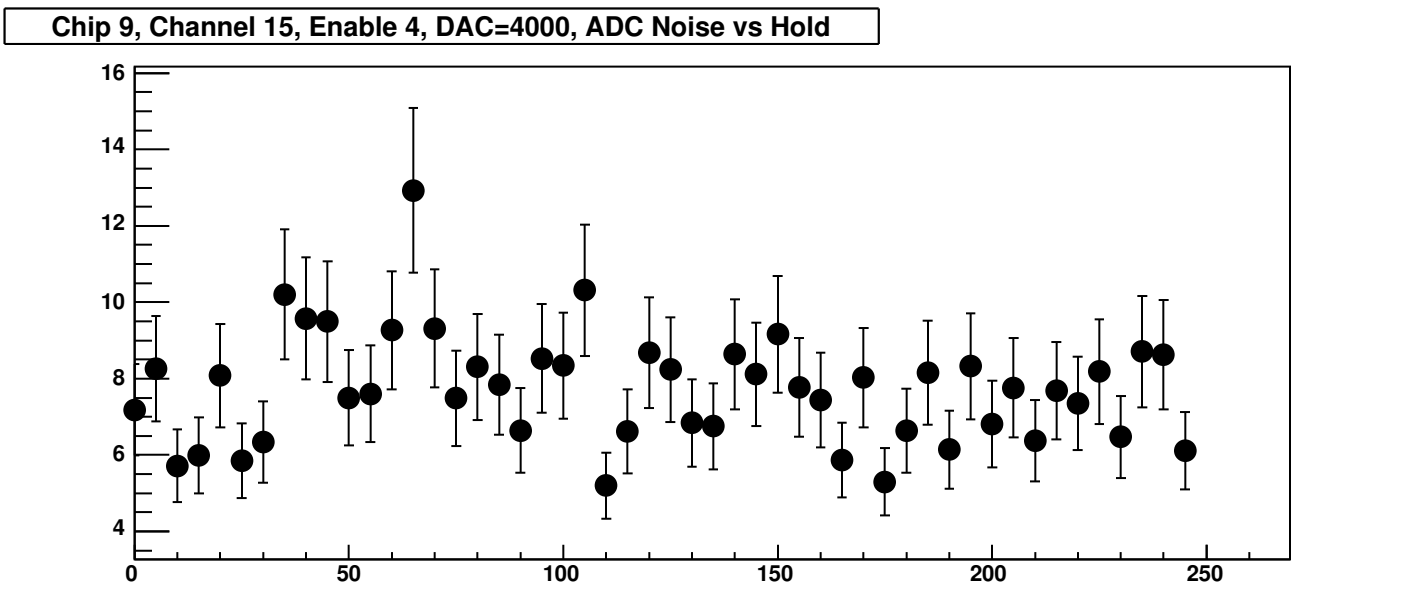
Chip 9, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold

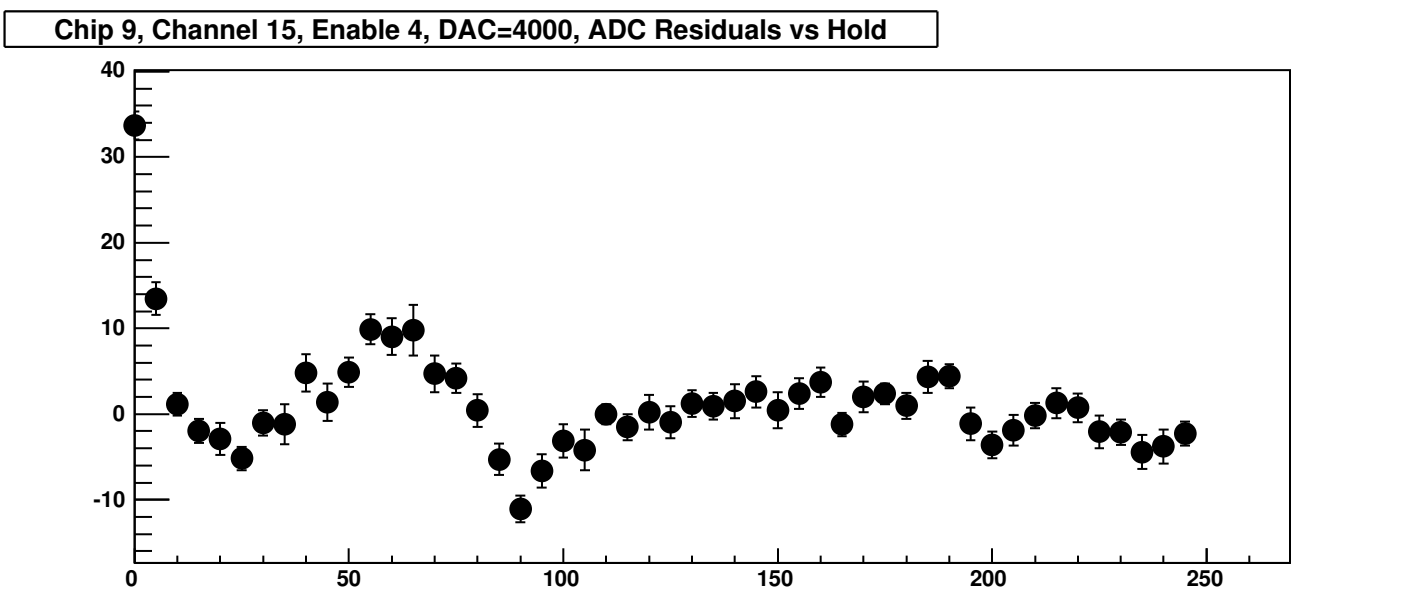


Chip 9, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold



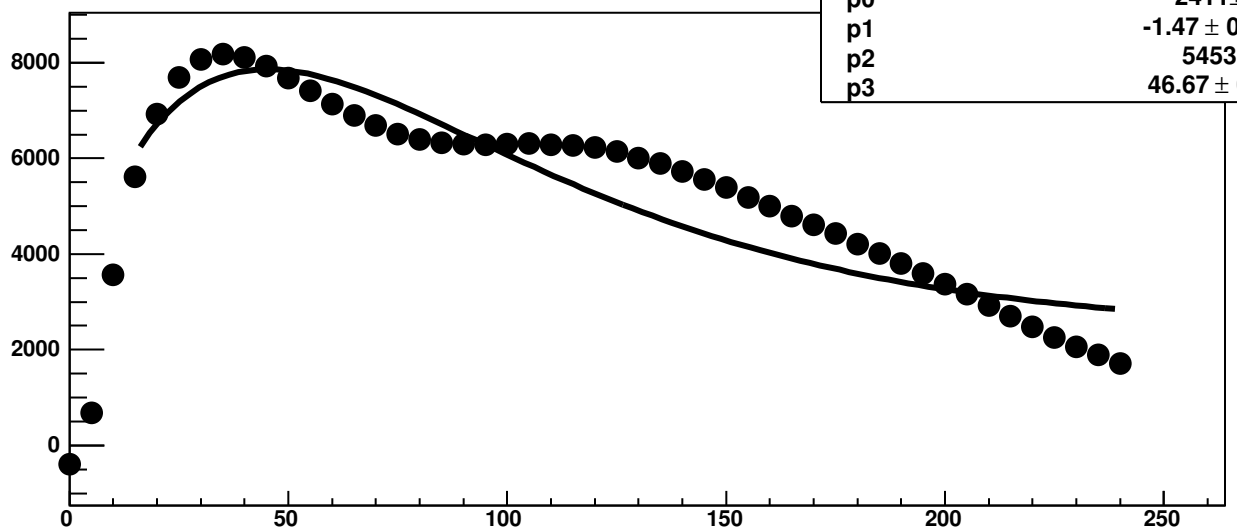

Chip 9, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 236 / 41
p0 -257 ± 0.6127
p1 90.23 ± 0.543
p2 -9.142e+07 ± 9.7e+05
p3 4.675e+07 ± 1.994e+05
p4 1.854 ± 0.01771
-260
-280
-300
-320
-340
-360
-380
-400
-420
0
50
100
150
200
250


Chip 9, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold
16
14
12
10
8
6
4
0
50
100
150
200
250

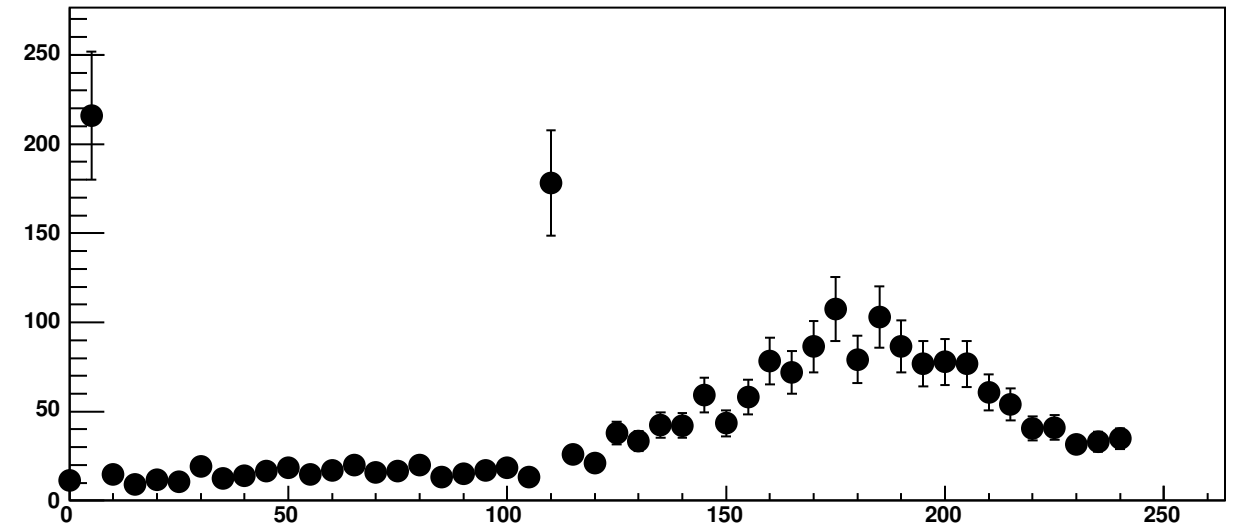

Chip 9, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

Chip 9, Channel 15, Enable 5!, DAC=4000, ADC Mean vs Hold

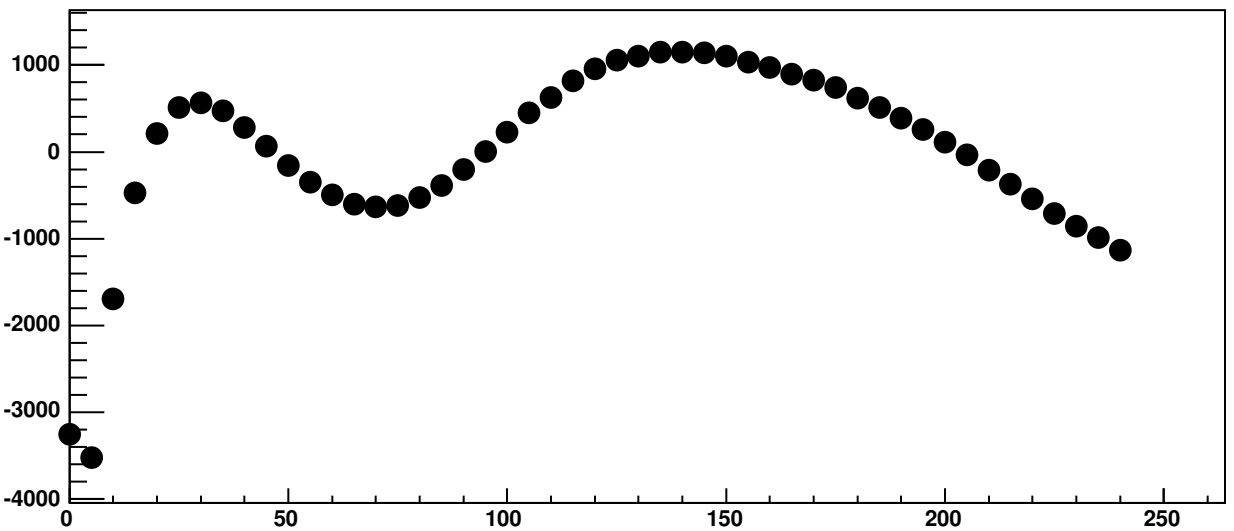
χ^2 / ndf	5.32e+05 / 42
p0	2411 ± 5.855
p1	-1.47 ± 0.04471
p2	5453 ± 5.4
p3	46.67 ± 0.0566

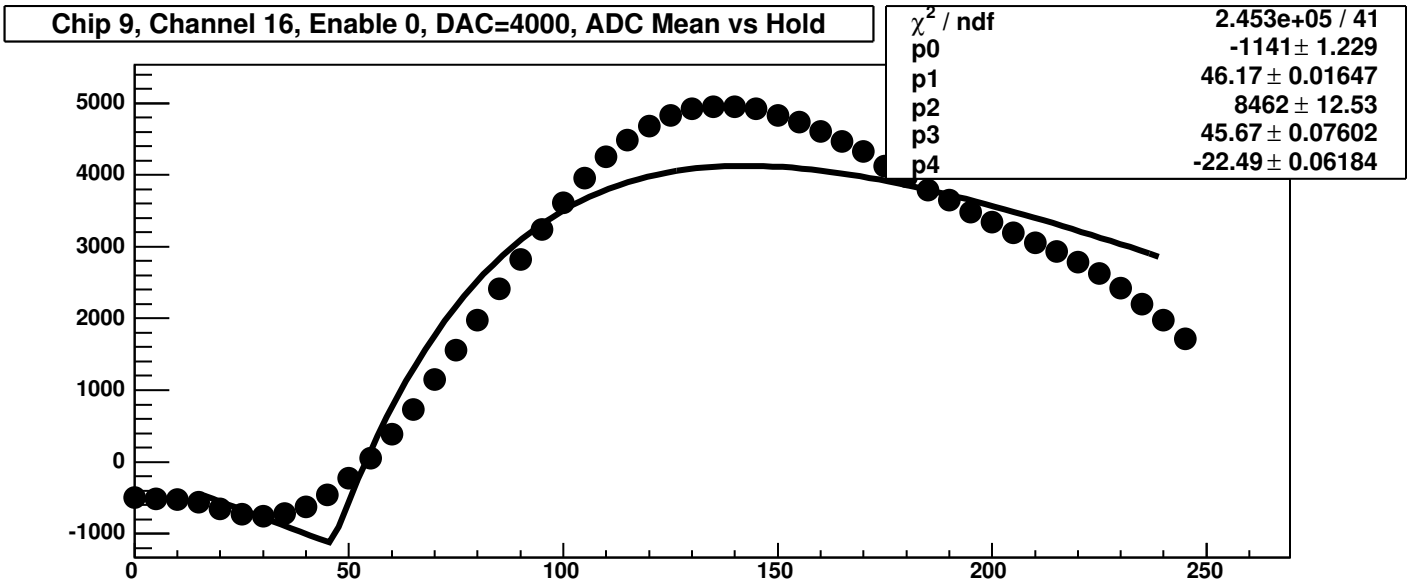


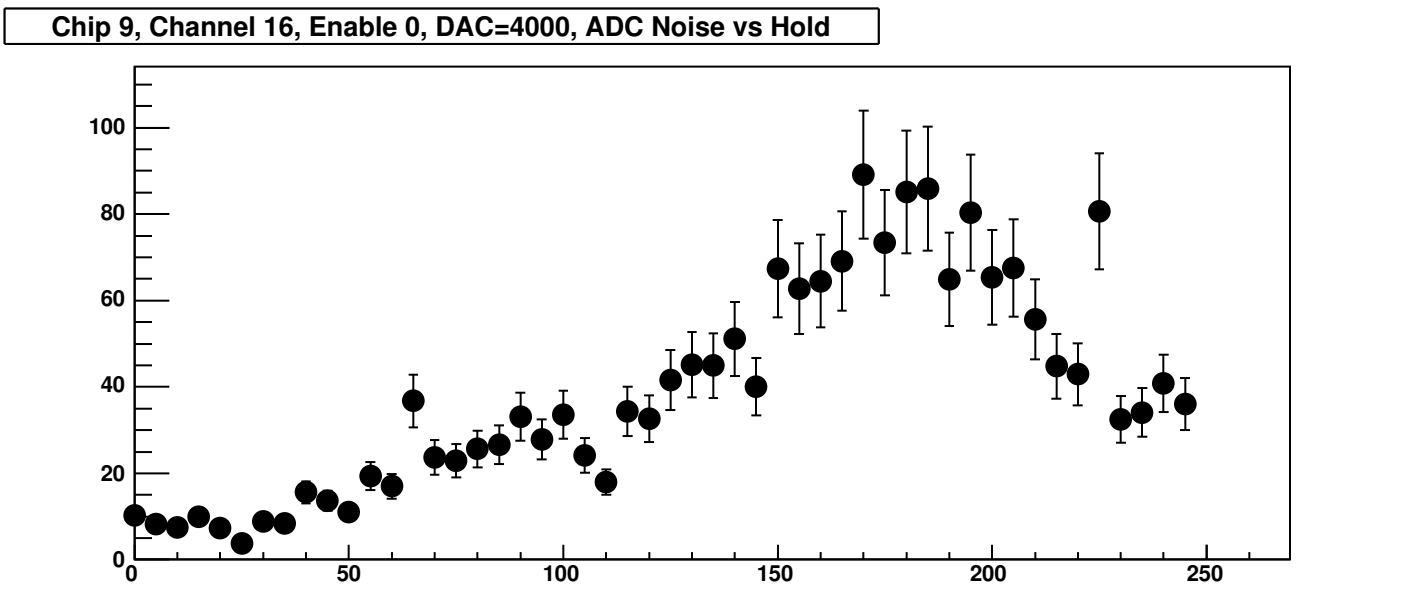
Chip 9, Channel 15, Enable 5!, DAC=4000, ADC Noise vs Hold

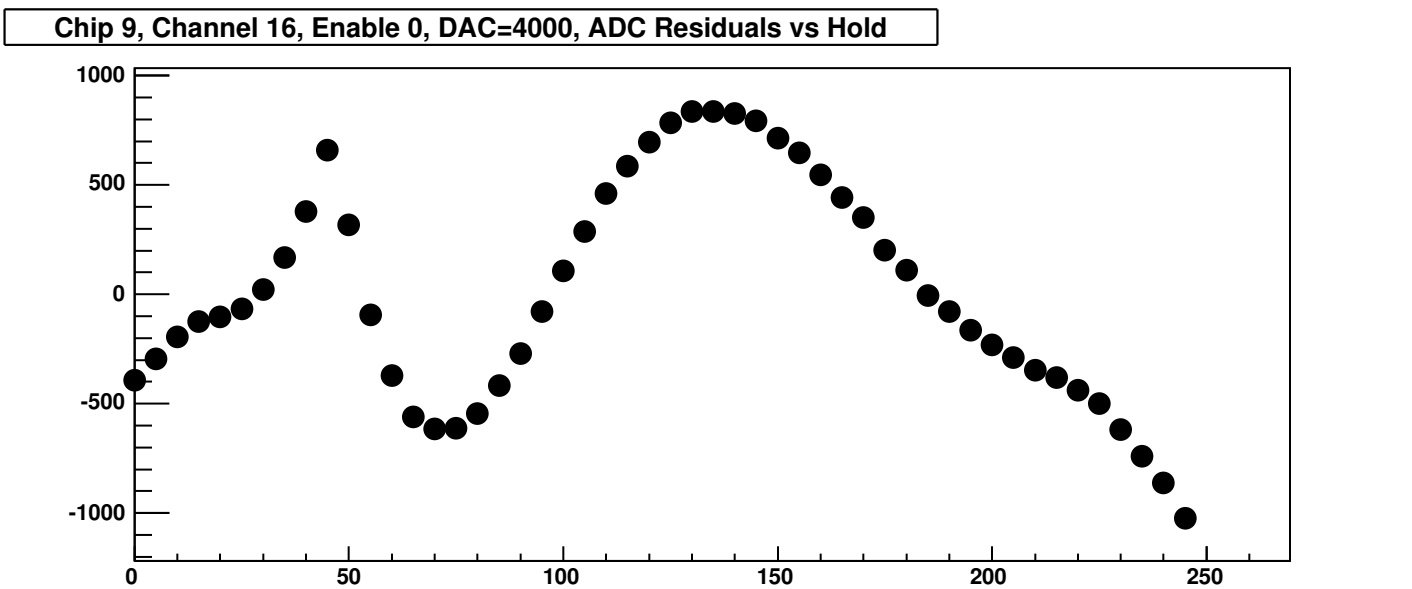


Chip 9, Channel 15, Enable 5!, DAC=4000, ADC Residuals vs Hold

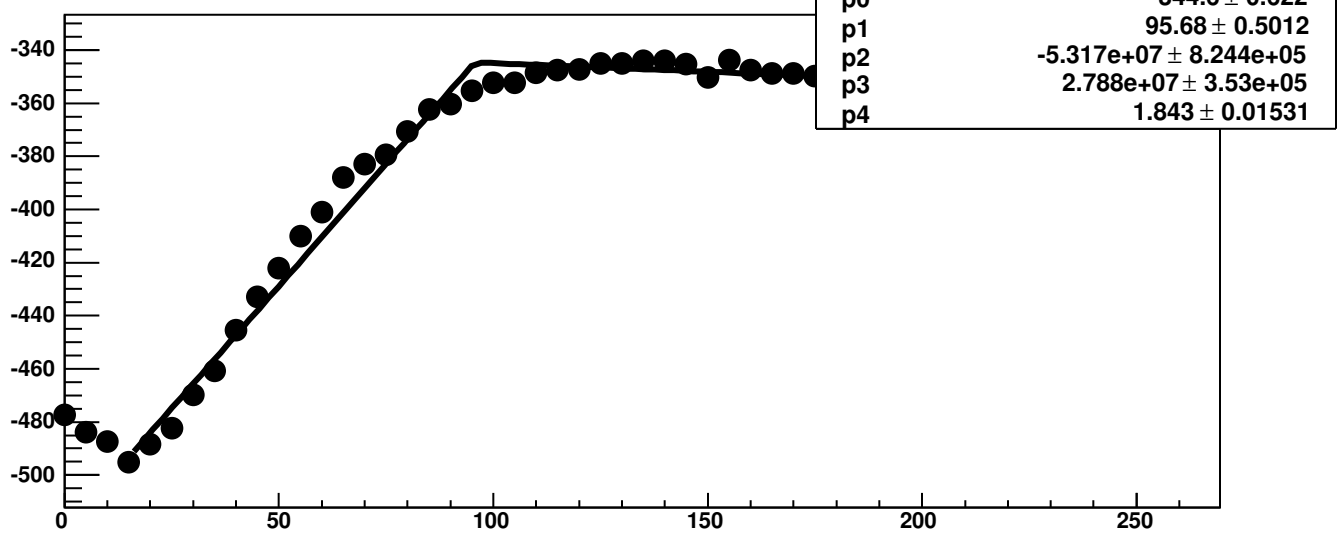



Chip 9, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.453e+05 / 41
p0 -1141 ± 1.229
p1 46.17 ± 0.01647
p2 8462 ± 12.53
p3 45.67 ± 0.07602
p4 -22.49 ± 0.06184
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

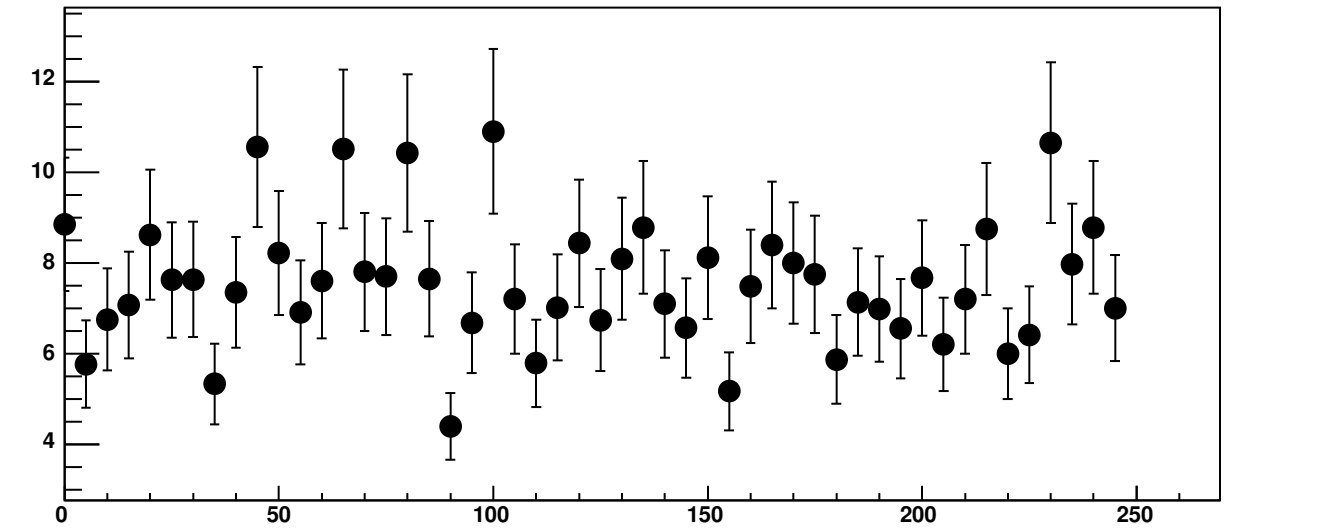

Chip 9, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250


Chip 9, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
0
50
100
150
200
250

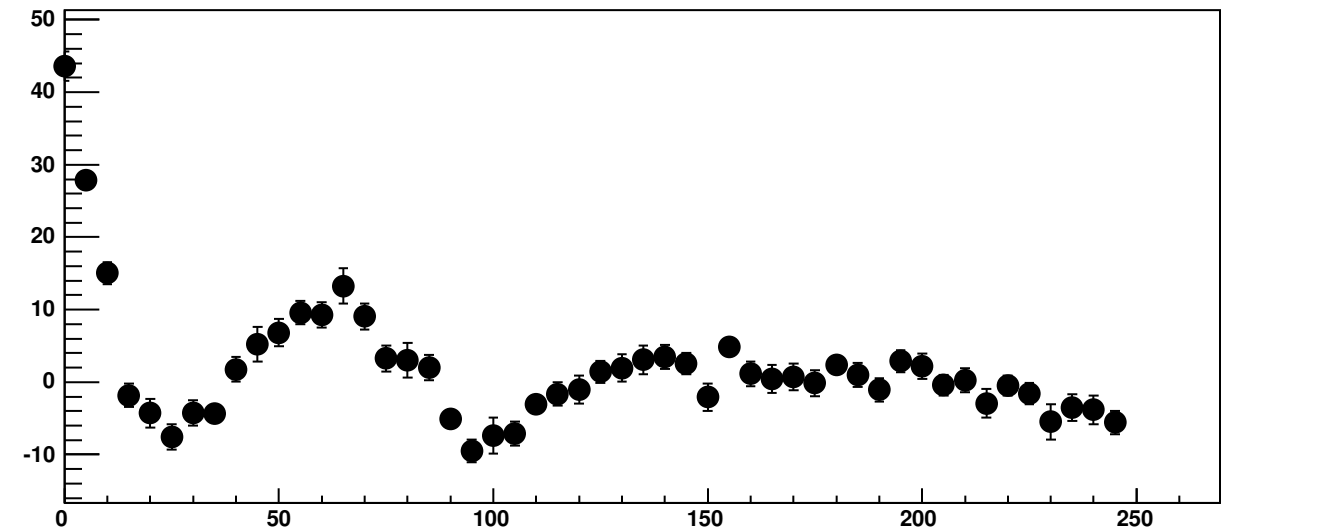
Chip 9, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 9, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold

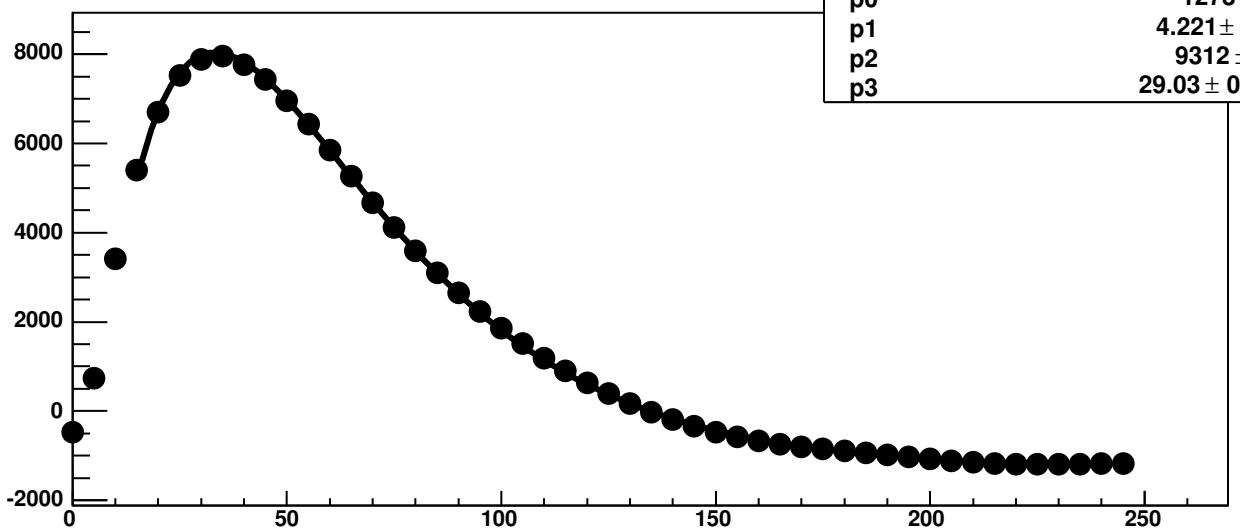


Chip 9, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold

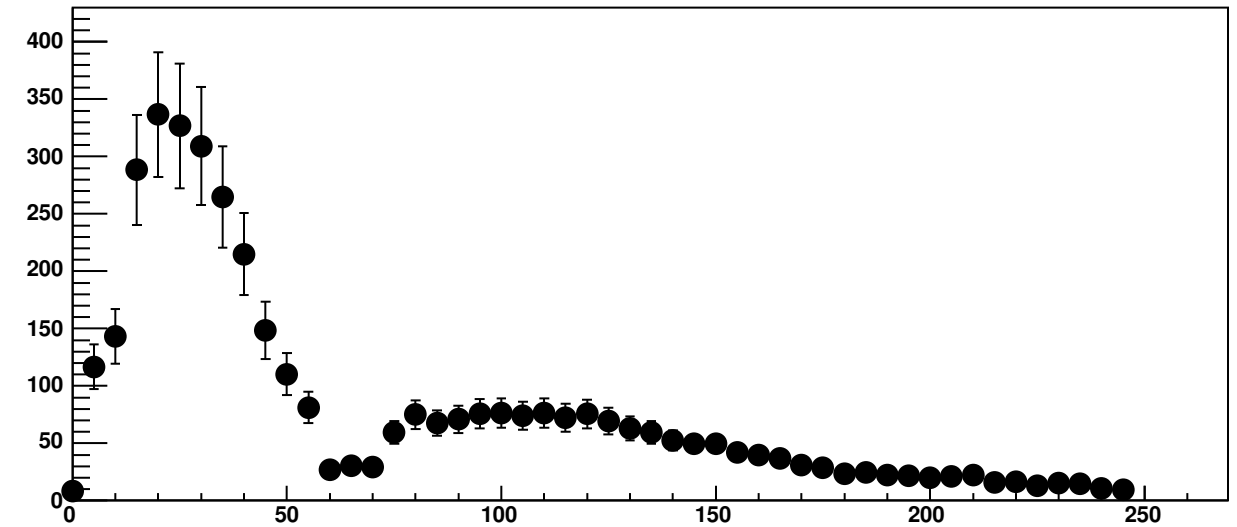


Chip 9, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold

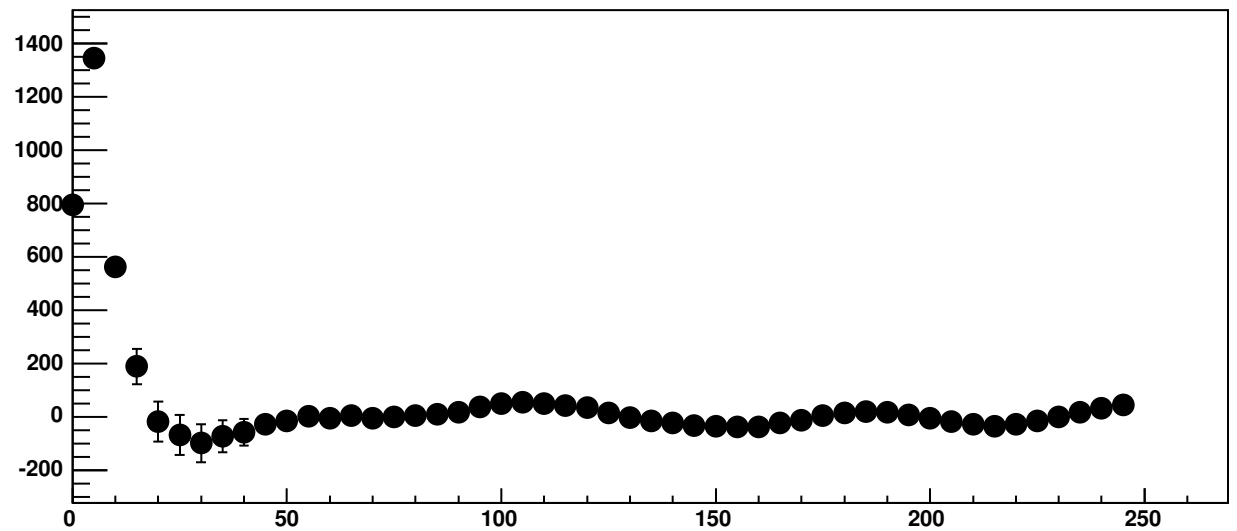
χ^2 / ndf	559.4 / 42
p0	-1273 ± 1.53
p1	4.221 ± 0.1202
p2	9312 ± 14.31
p3	29.03 ± 0.04219



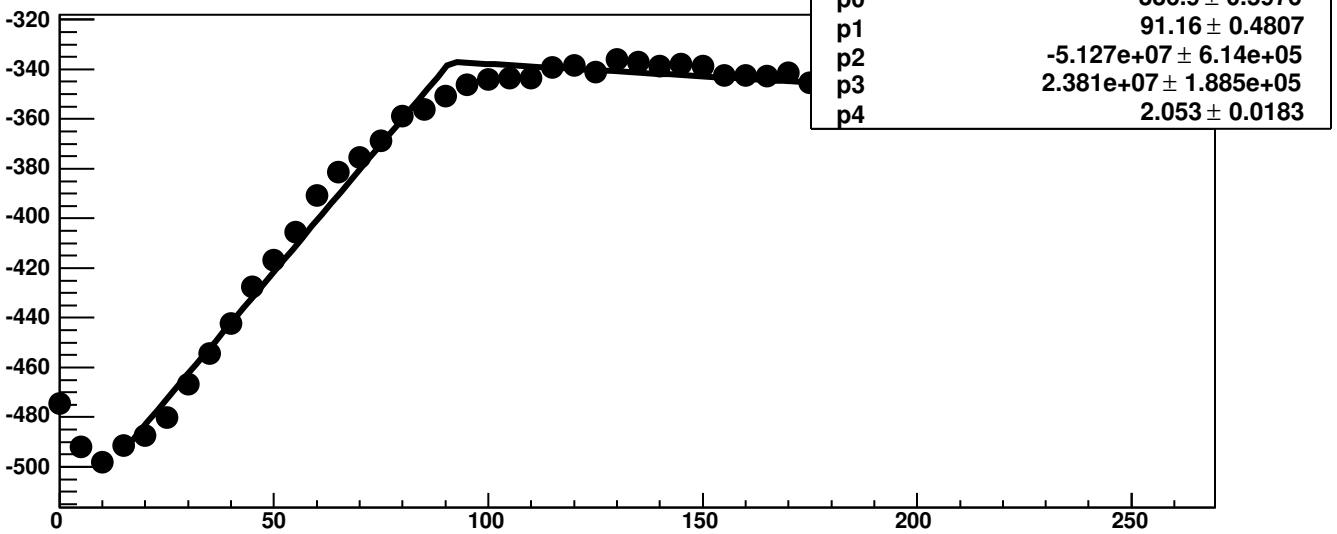
Chip 9, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold



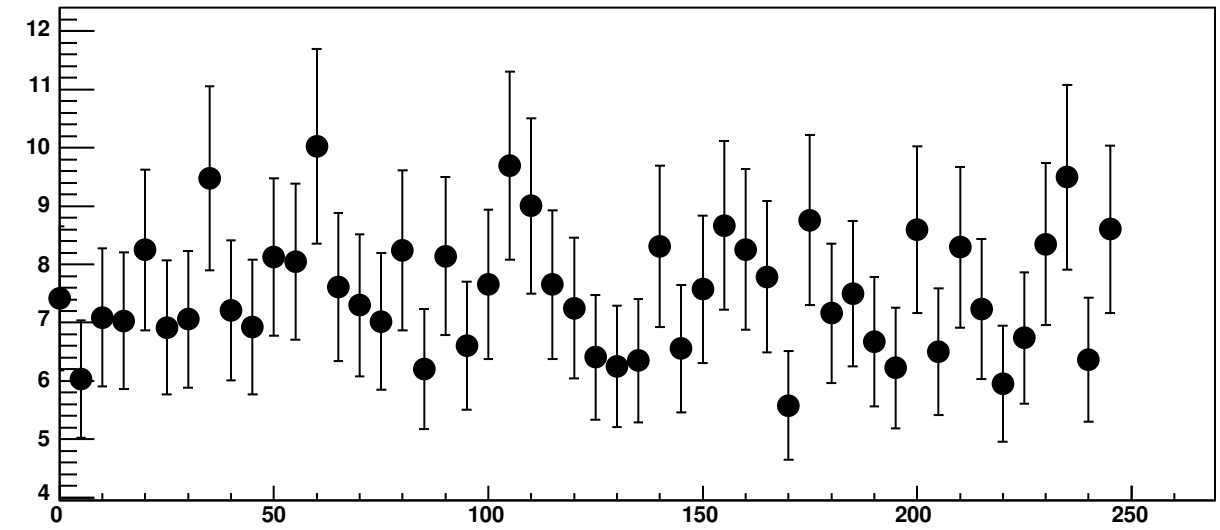
Chip 9, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold



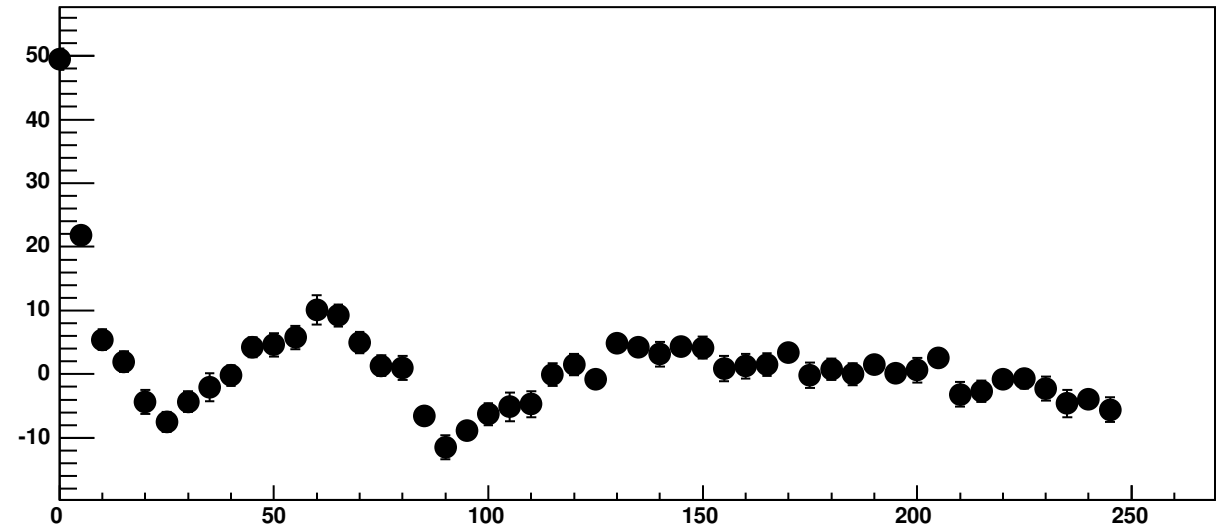
Chip 9, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

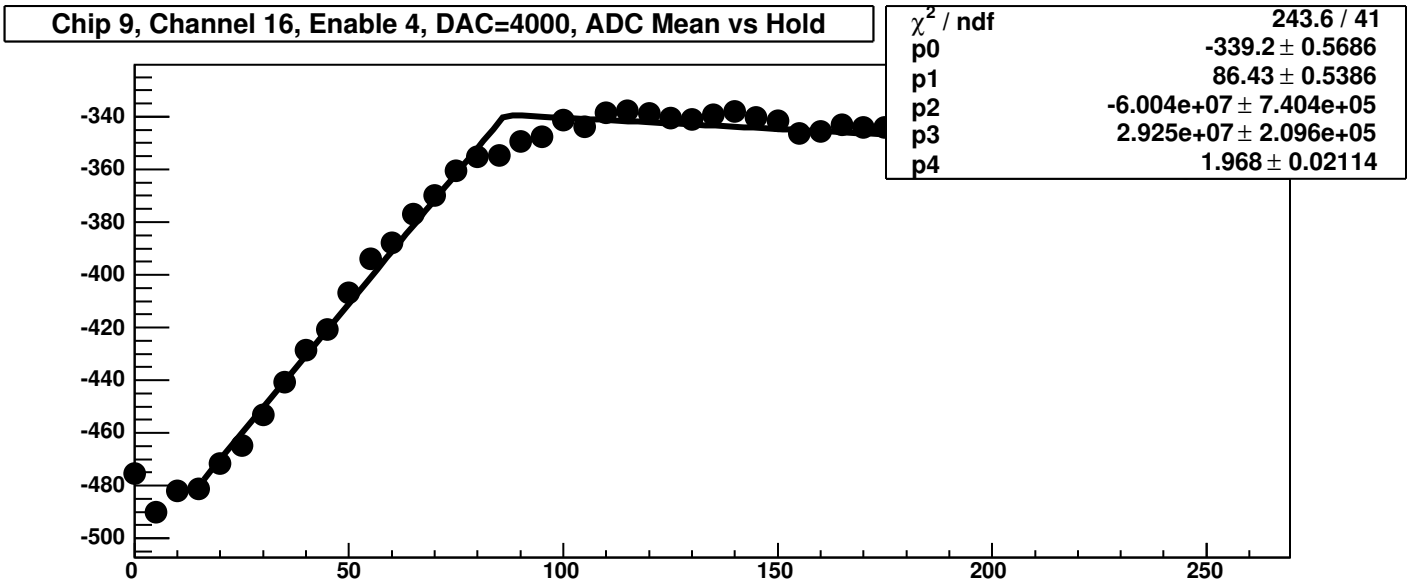


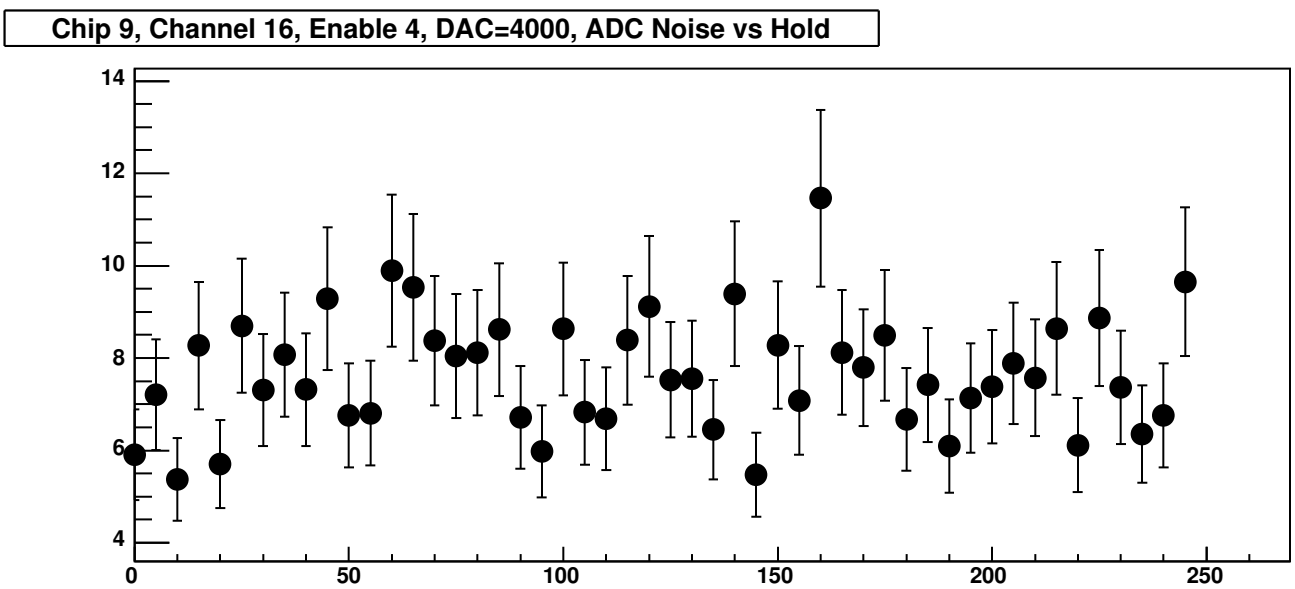
Chip 9, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold

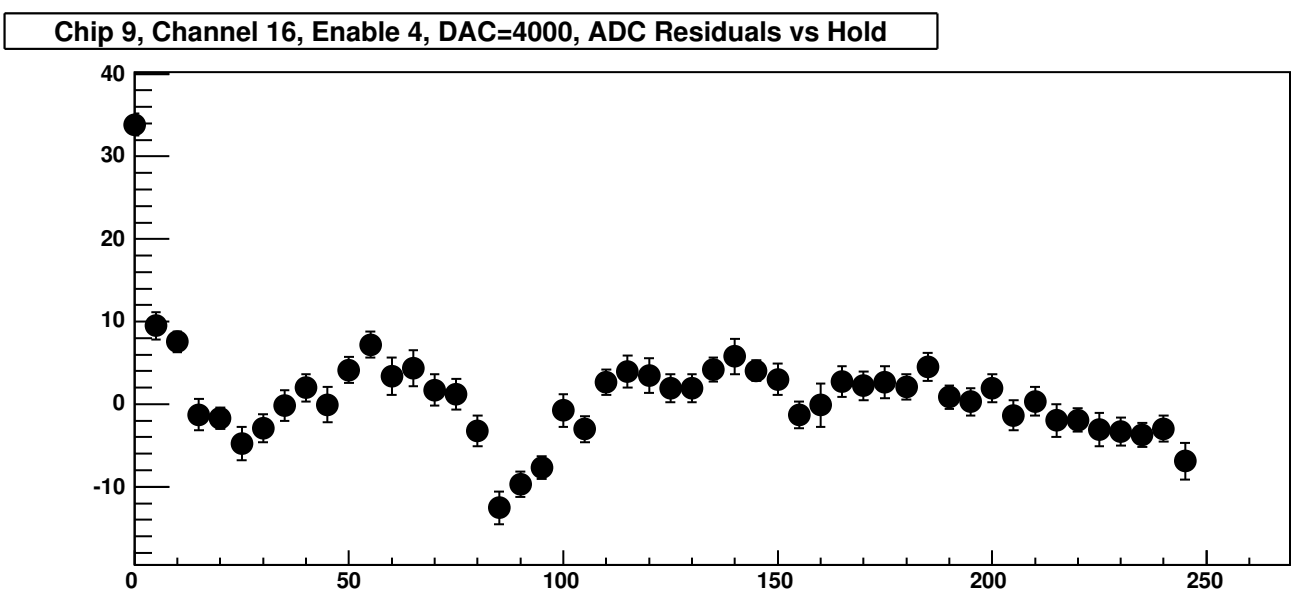


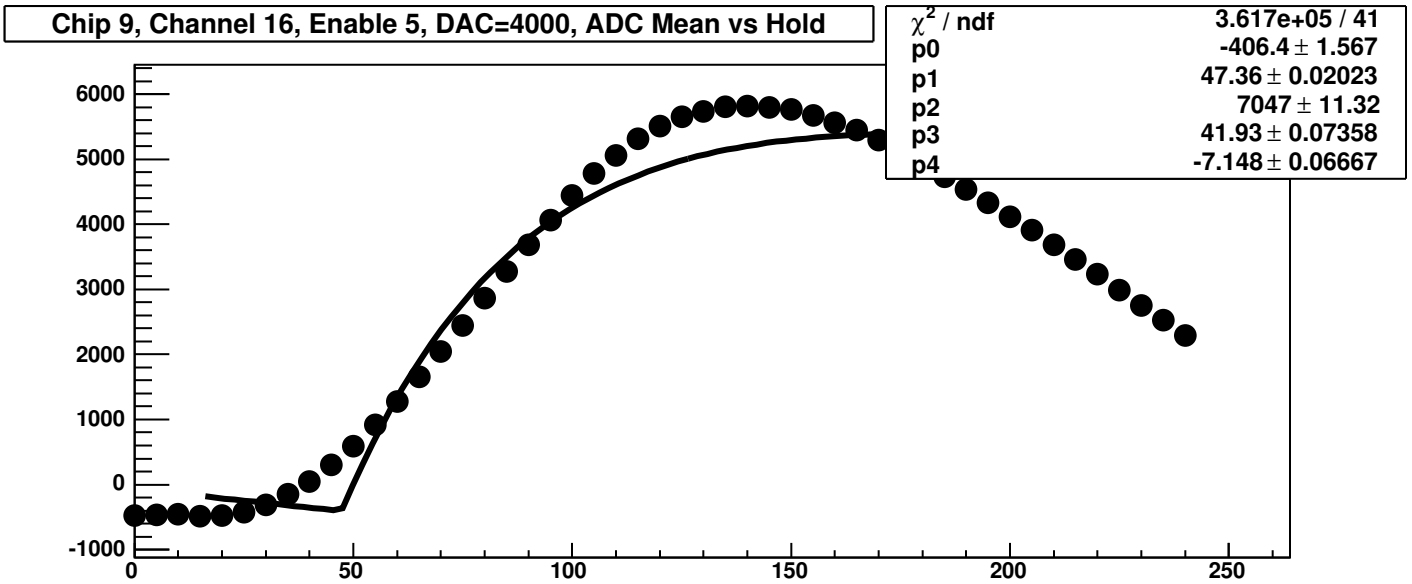
Chip 9, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

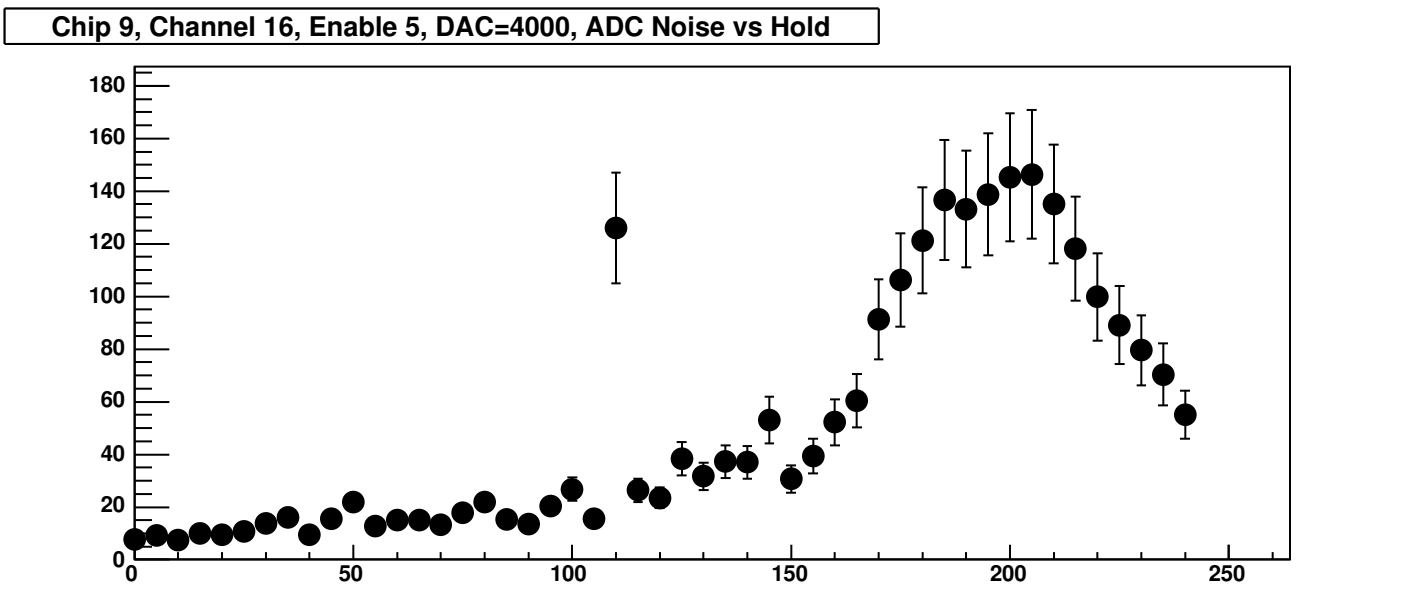


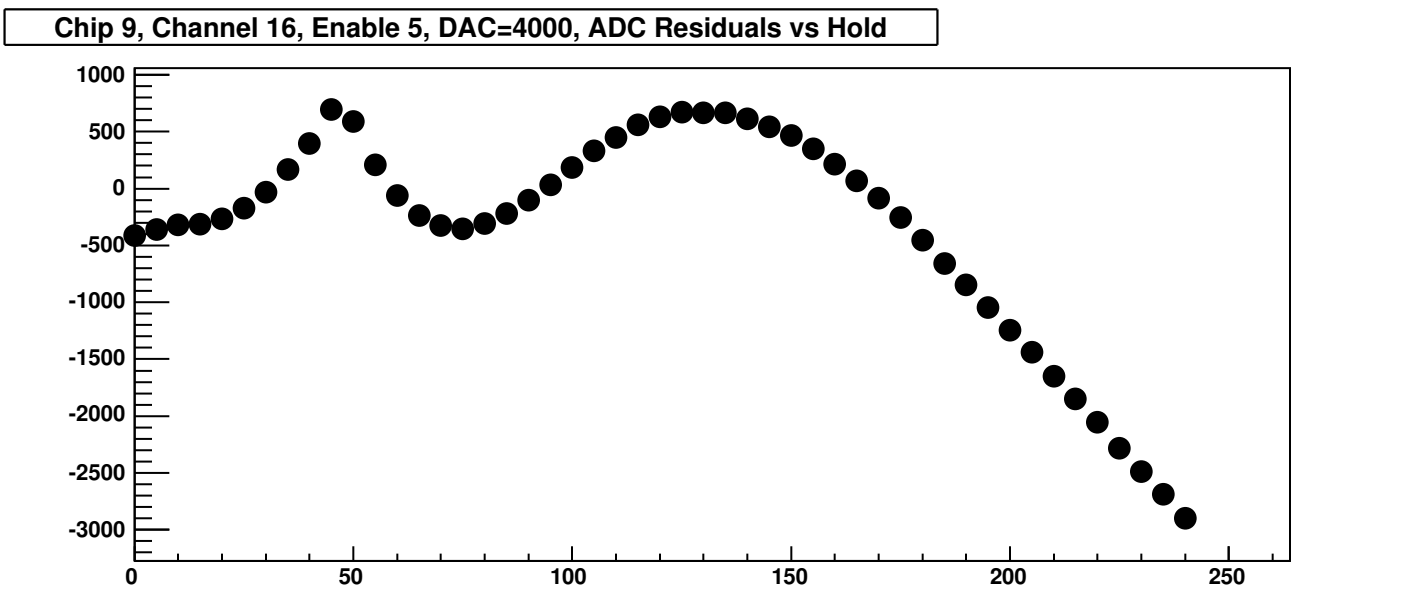

Chip 9, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 243.6 / 41
p0 -339.2 ± 0.5686
p1 86.43 ± 0.5386
p2 -6.004e+07 ± 7.404e+05
p3 2.925e+07 ± 2.096e+05
p4 1.968 ± 0.02114
-340
-360
-380
-400
-420
-440
-460
-480
-500
0
50
100
150
200
250


Chip 9, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

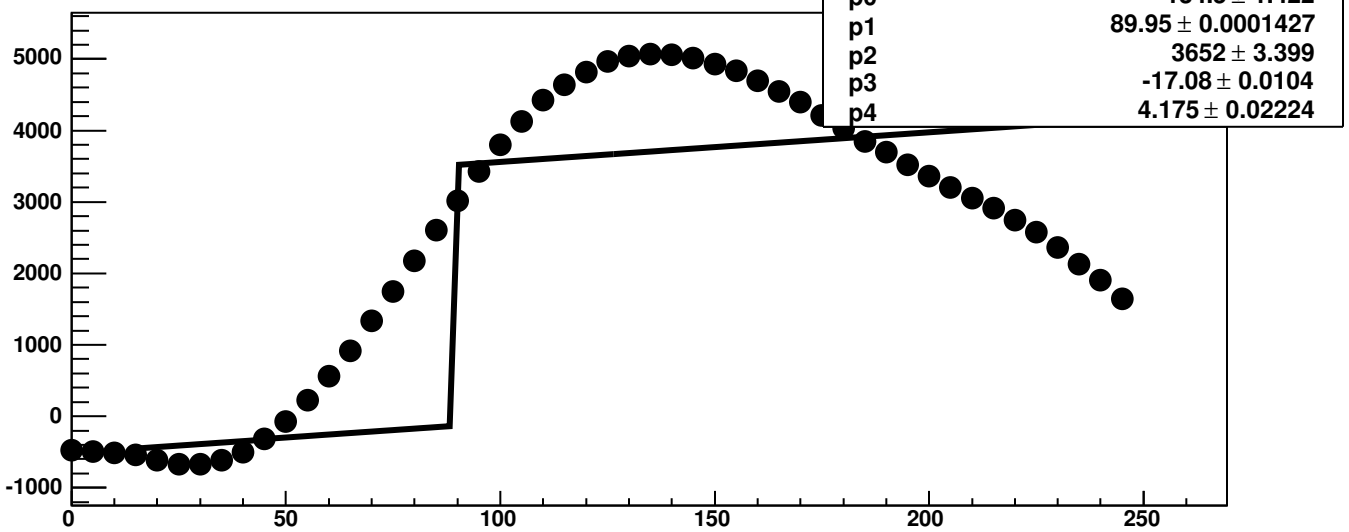

Chip 9, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250


Chip 9, Channel 16, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.617e+05 / 41
p0 -406.4 ± 1.567
p1 47.36 ± 0.02023
p2 7047 ± 11.32
p3 41.93 ± 0.07358
p4 -7.148 ± 0.06667
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

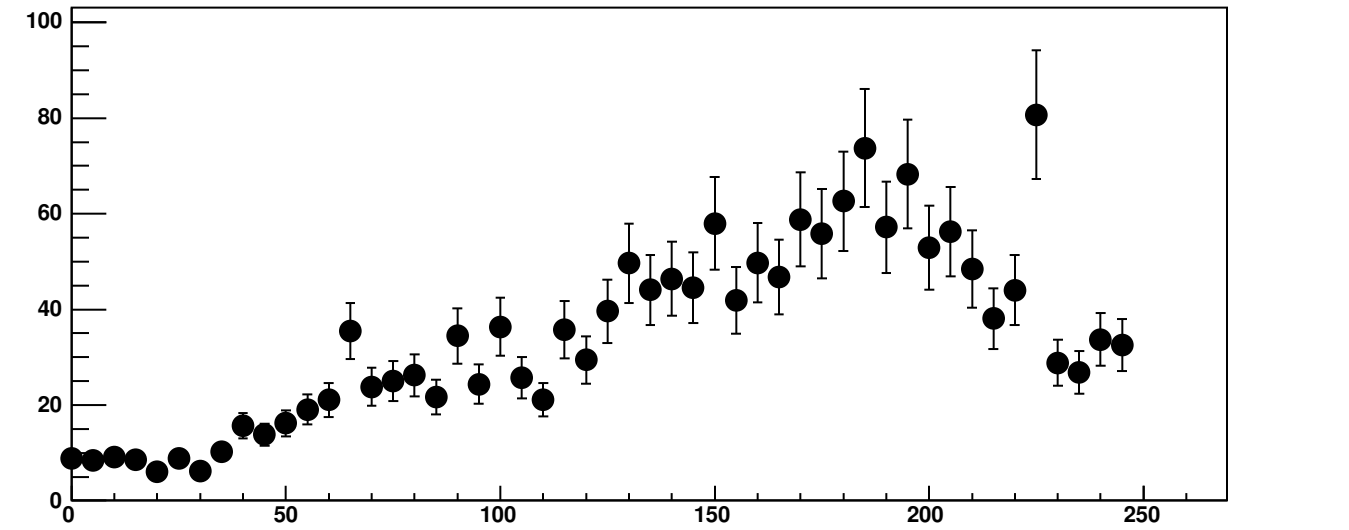

Chip 9, Channel 16, Enable 5, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 9, Channel 16, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250

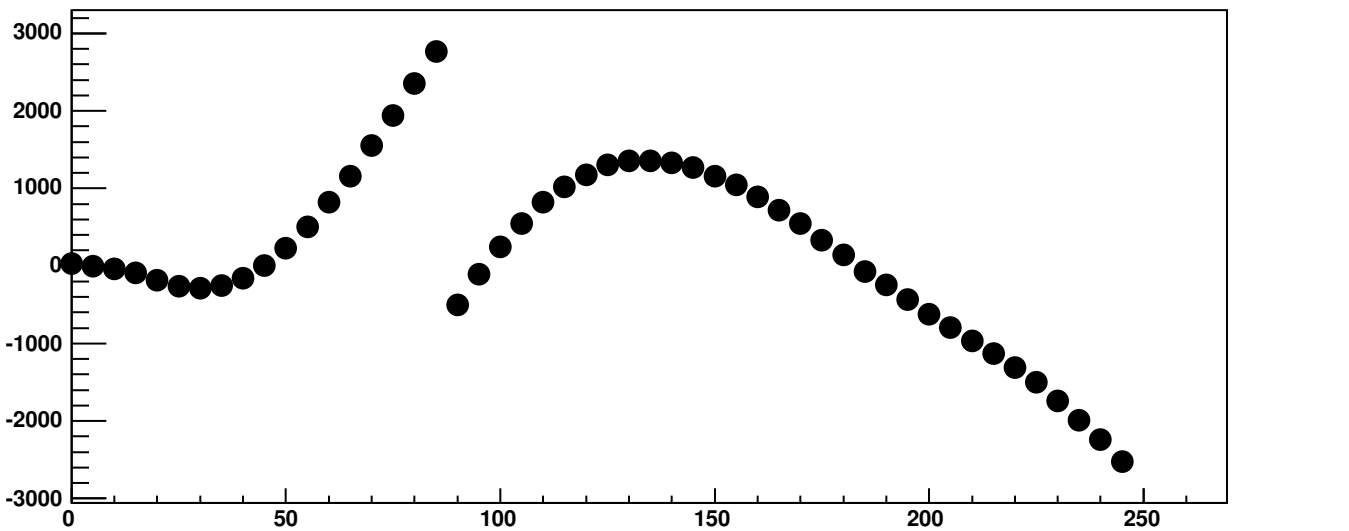
Chip 9, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold



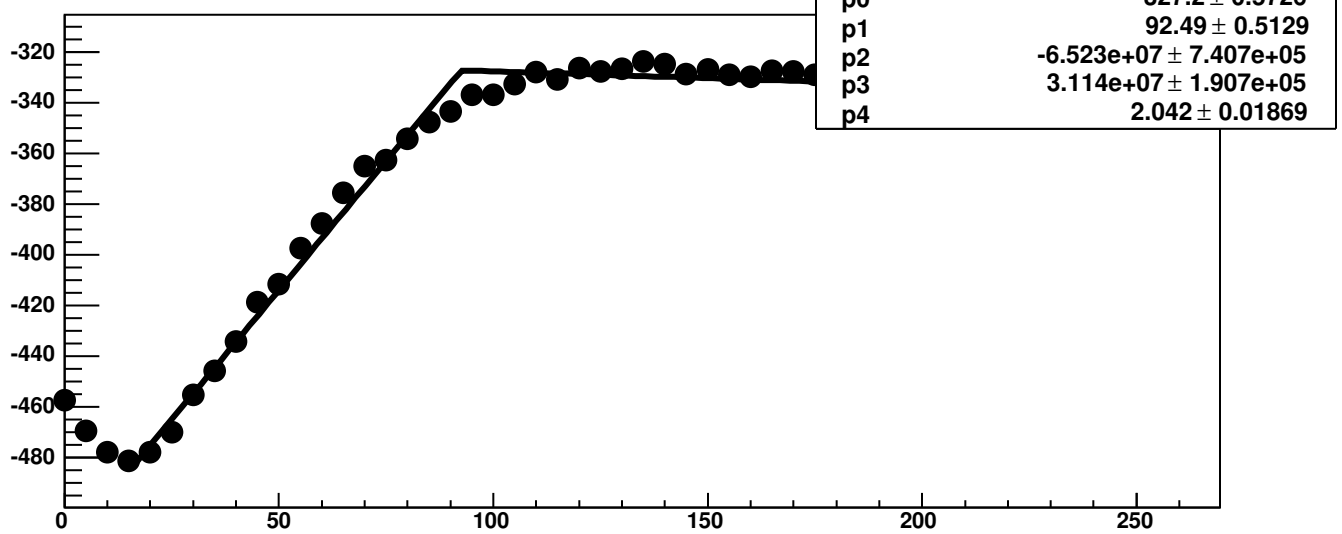
Chip 9, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



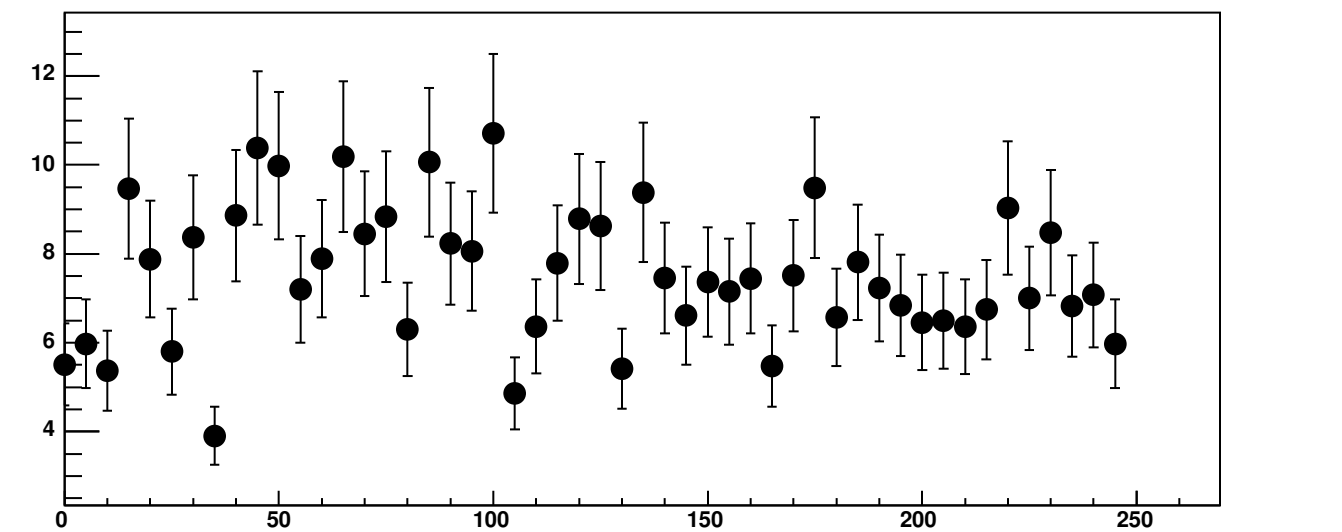
Chip 9, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold



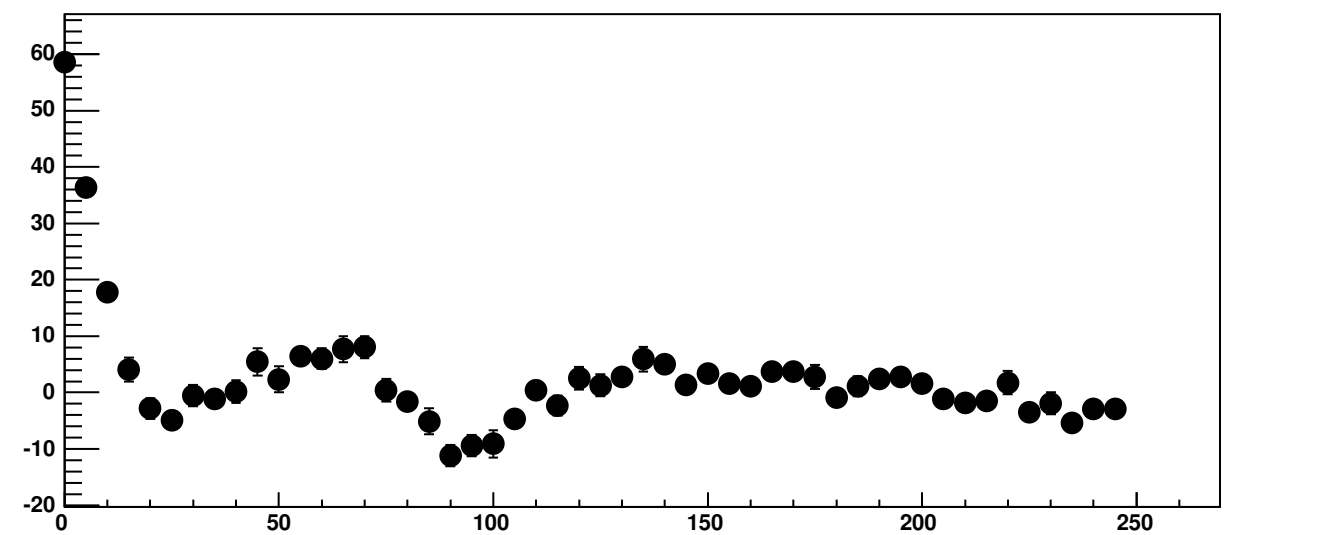
Chip 9, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold



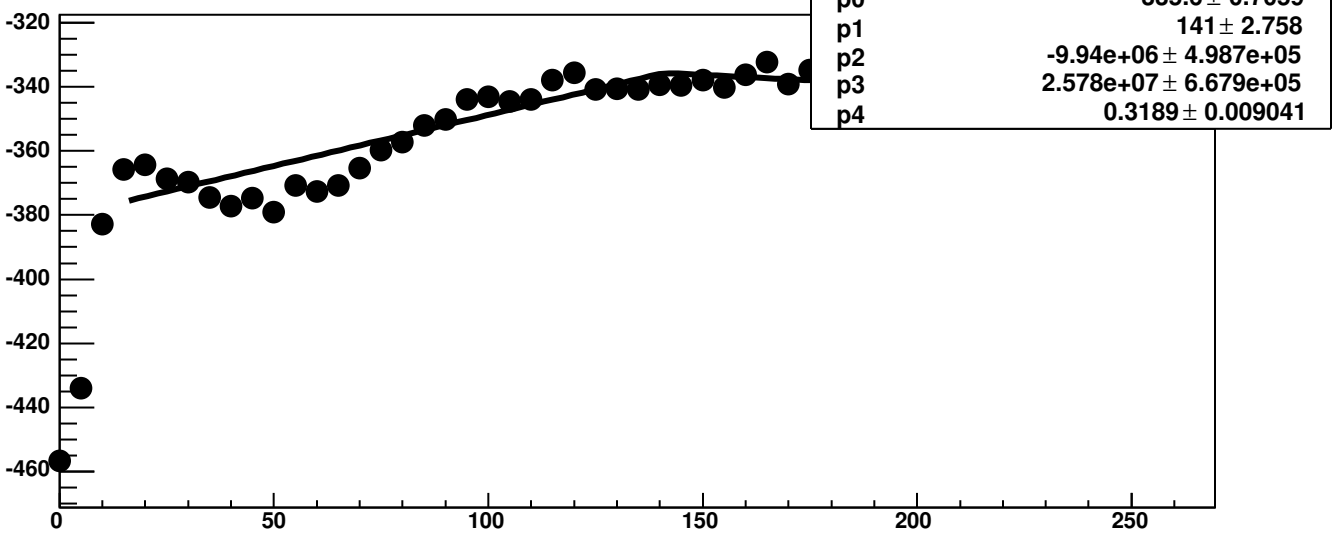
Chip 9, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



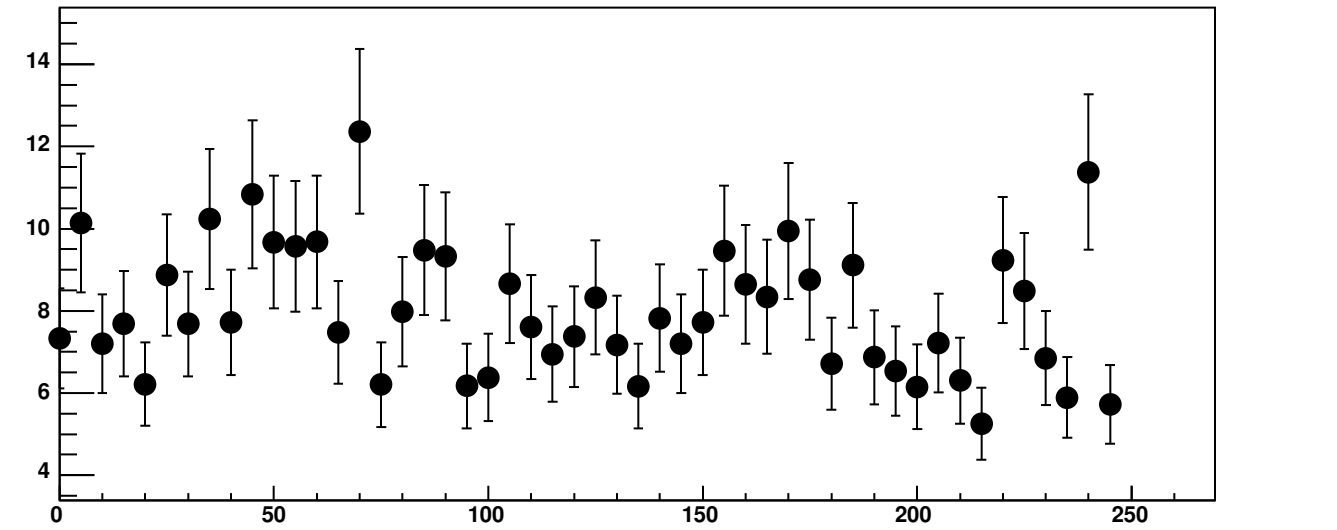
Chip 9, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold



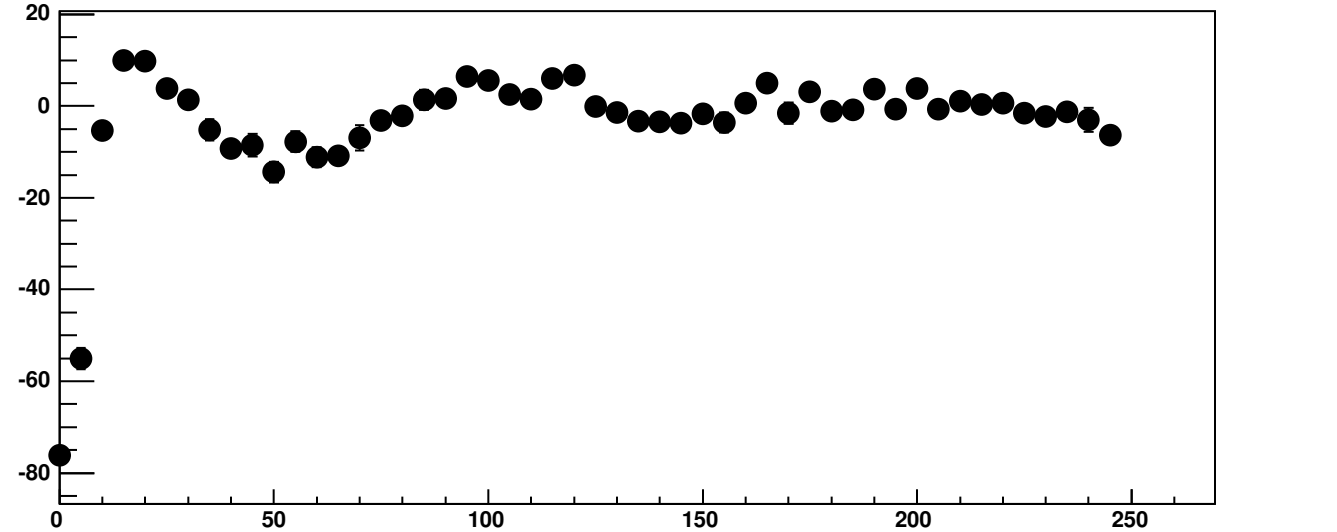
Chip 9, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold



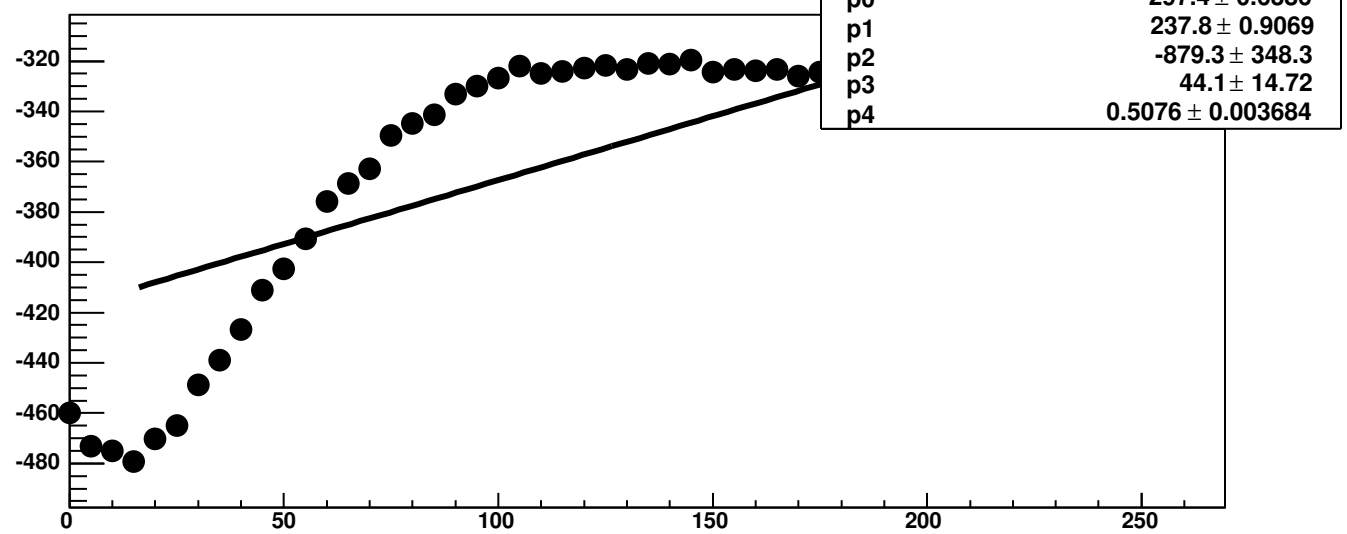
Chip 9, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold



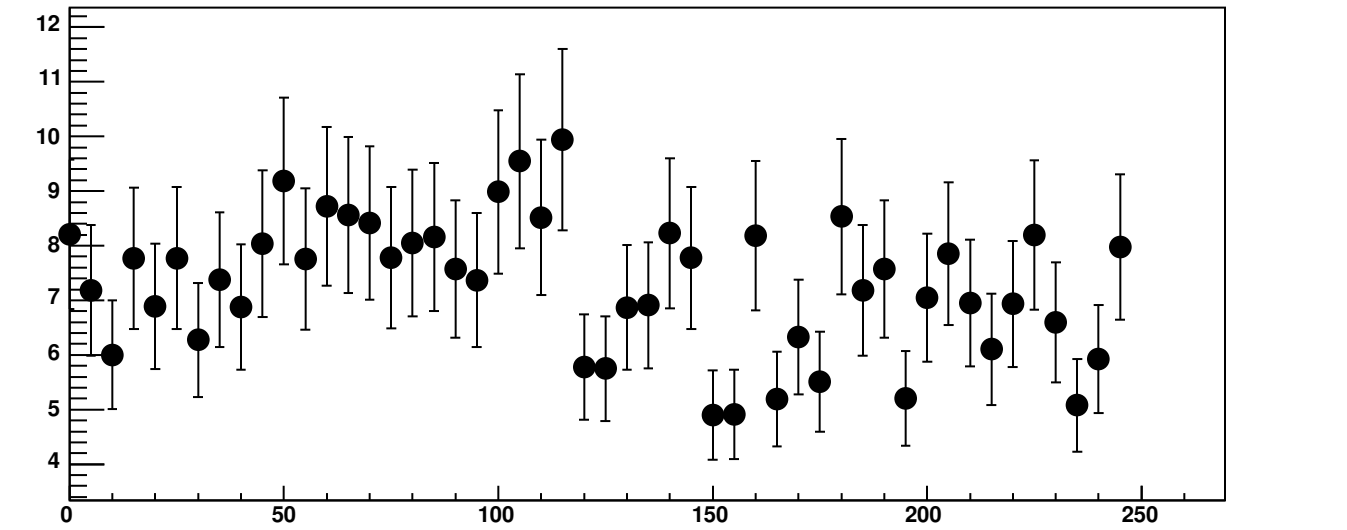
Chip 9, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold



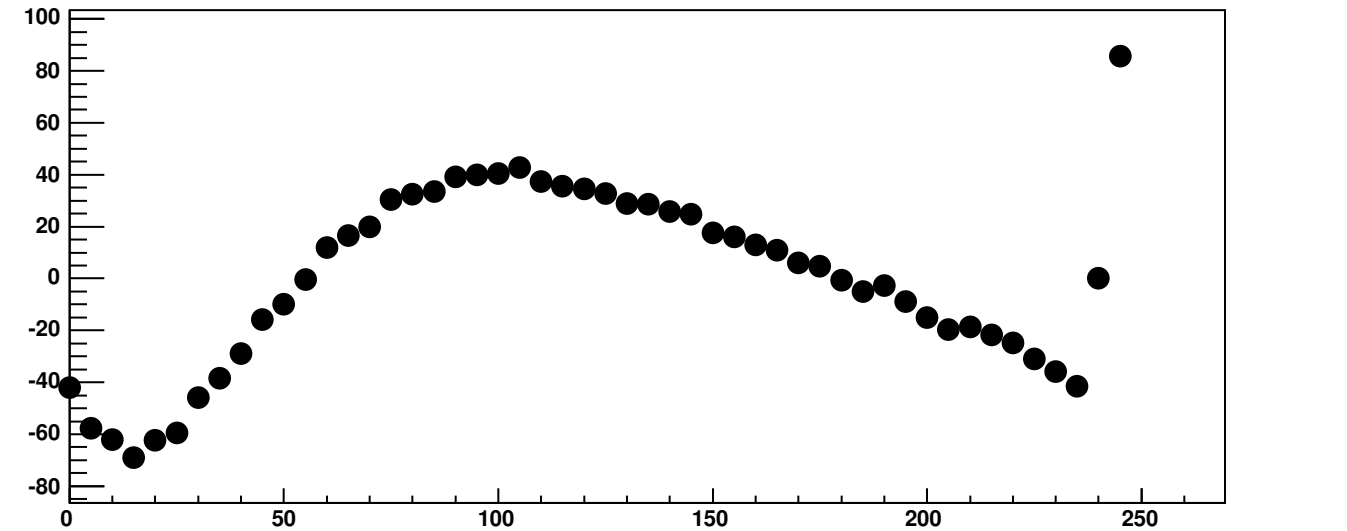
Chip 9, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



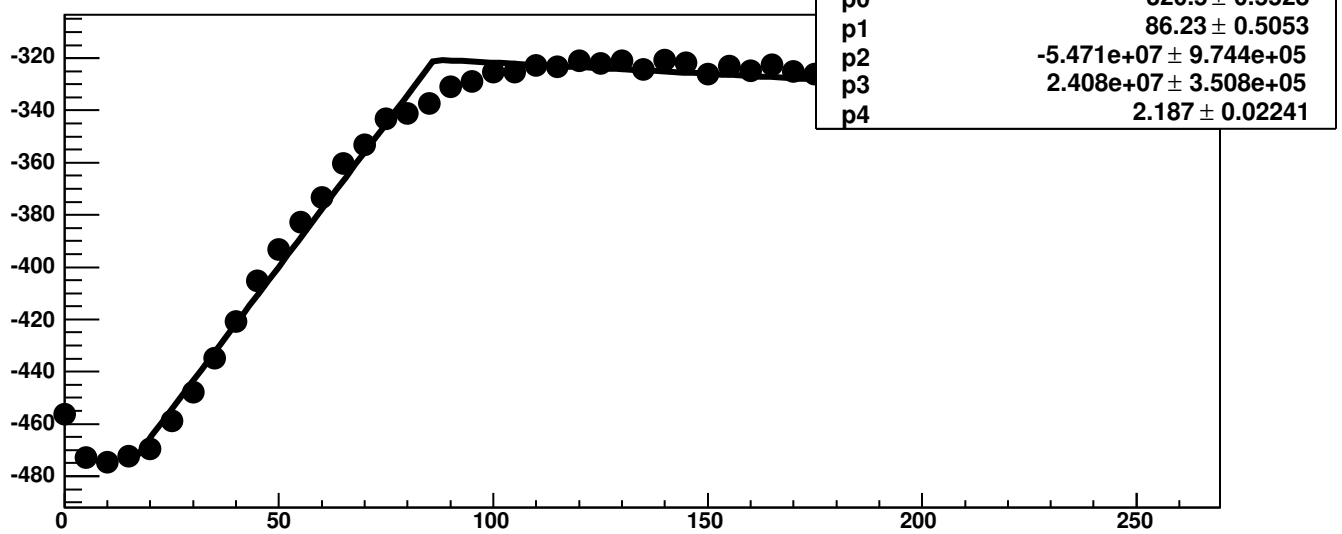
Chip 9, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



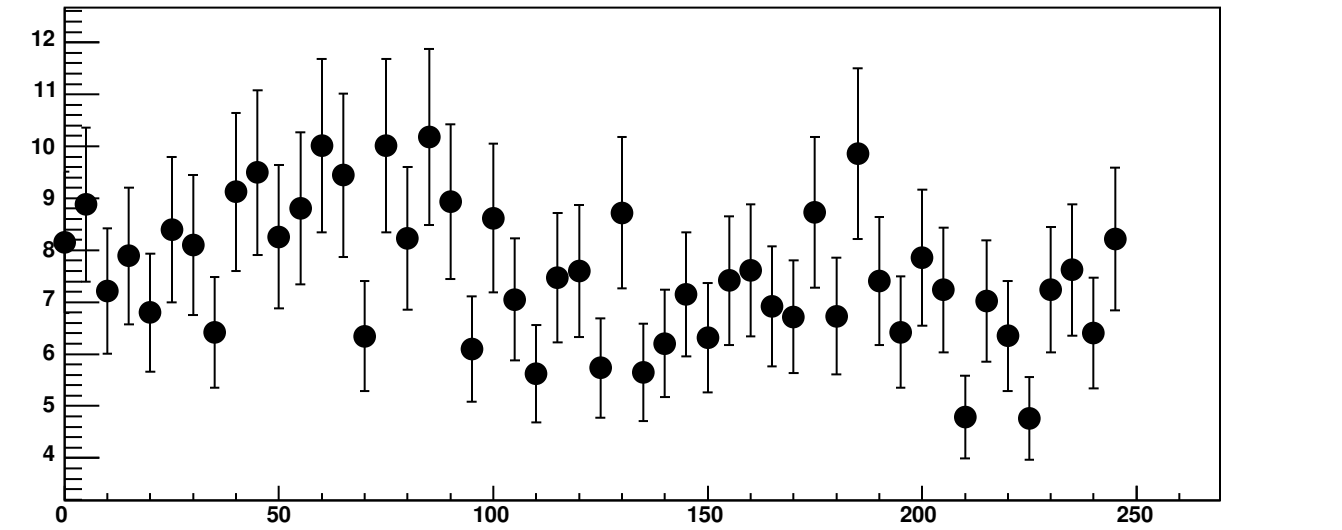
Chip 9, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold



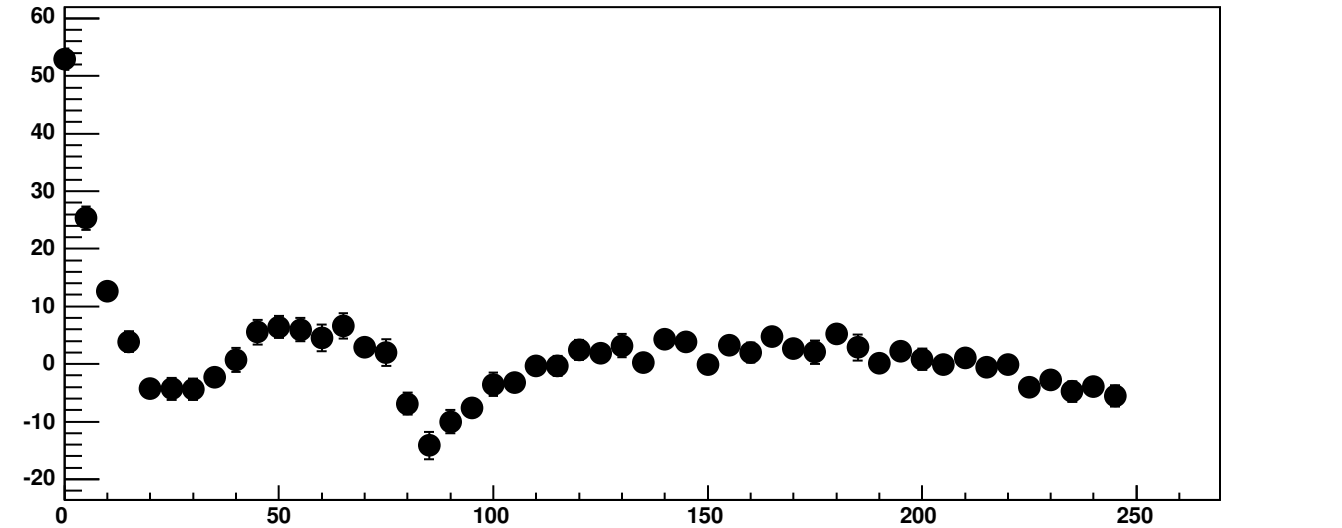
Chip 9, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



Chip 9, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold

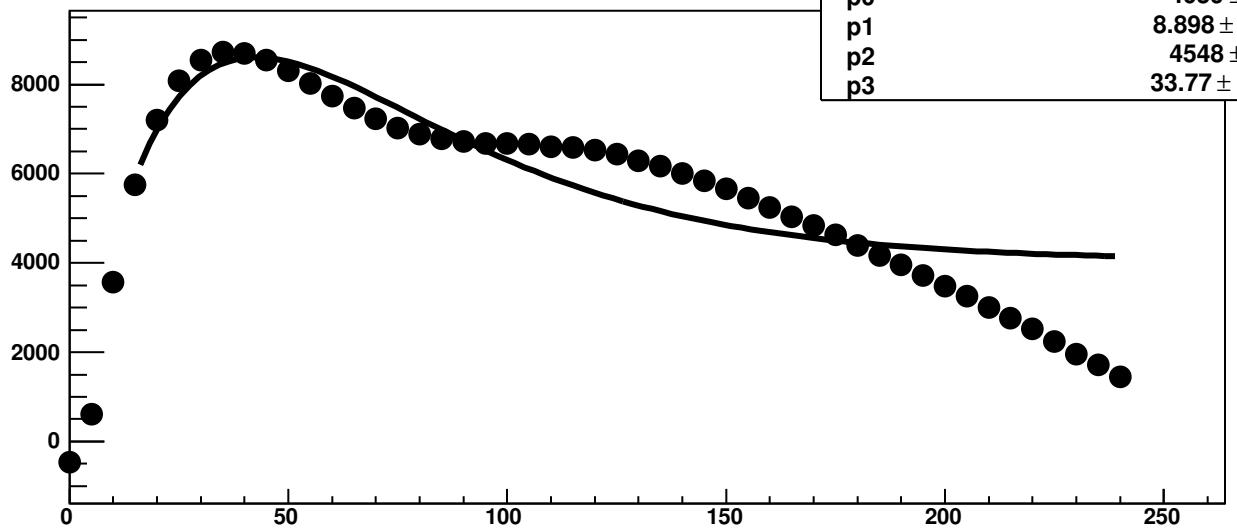


Chip 9, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

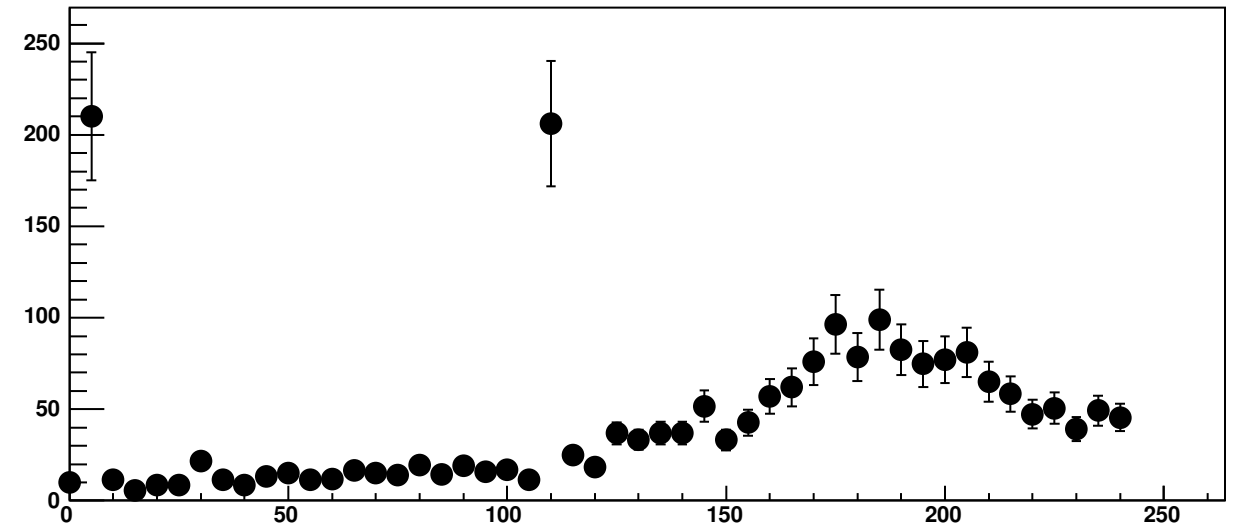


Chip 9, Channel 17, Enable 5!, DAC=4000, ADC Mean vs Hold

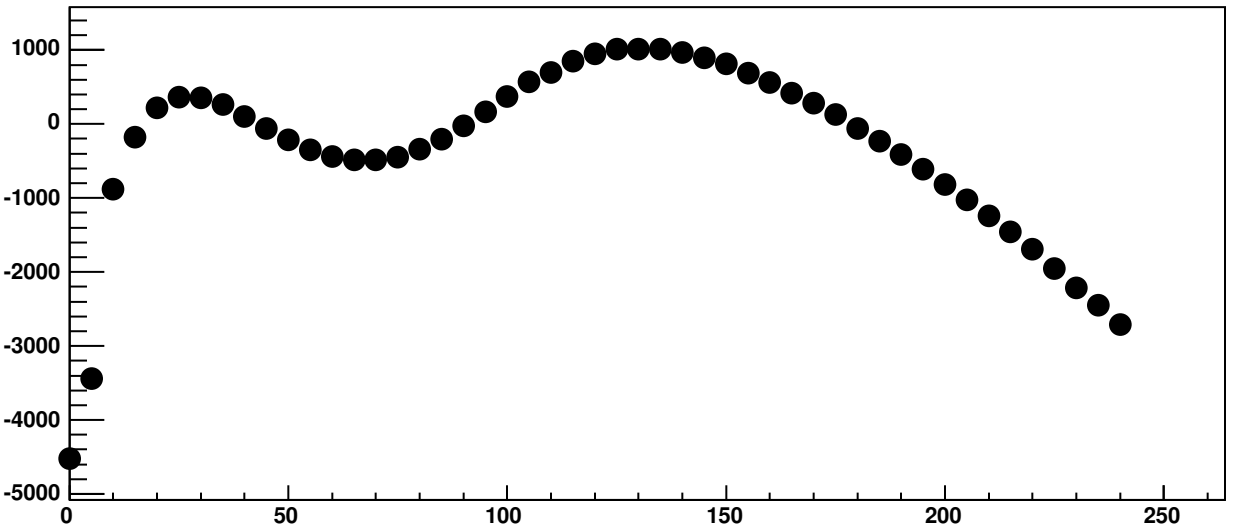
χ^2 / ndf	6.669e+05 / 42
p0	4059 ± 4.676
p1	8.898 ± 0.0201
p2	4548 ± 4.512
p3	33.77 ± 0.0348



Chip 9, Channel 17, Enable 5!, DAC=4000, ADC Noise vs Hold

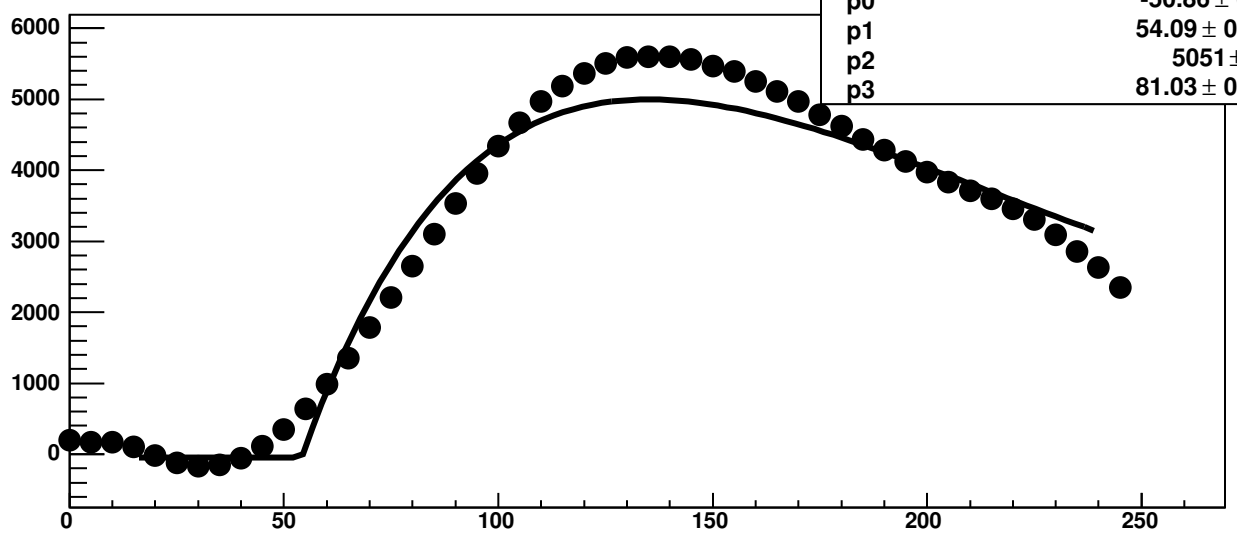


Chip 9, Channel 17, Enable 5!, DAC=4000, ADC Residuals vs Hold

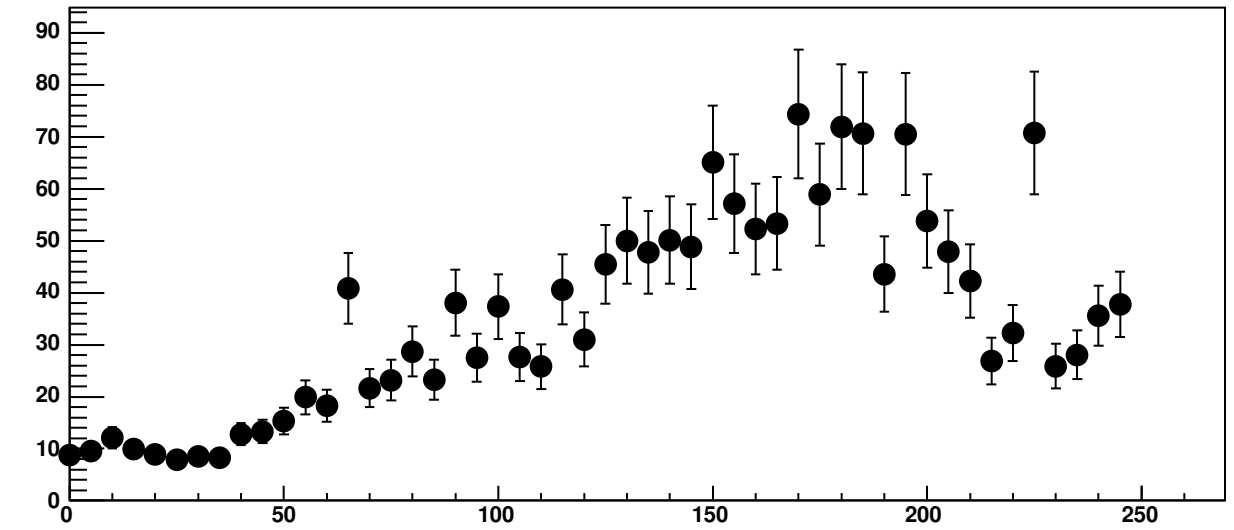


Chip 10, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold

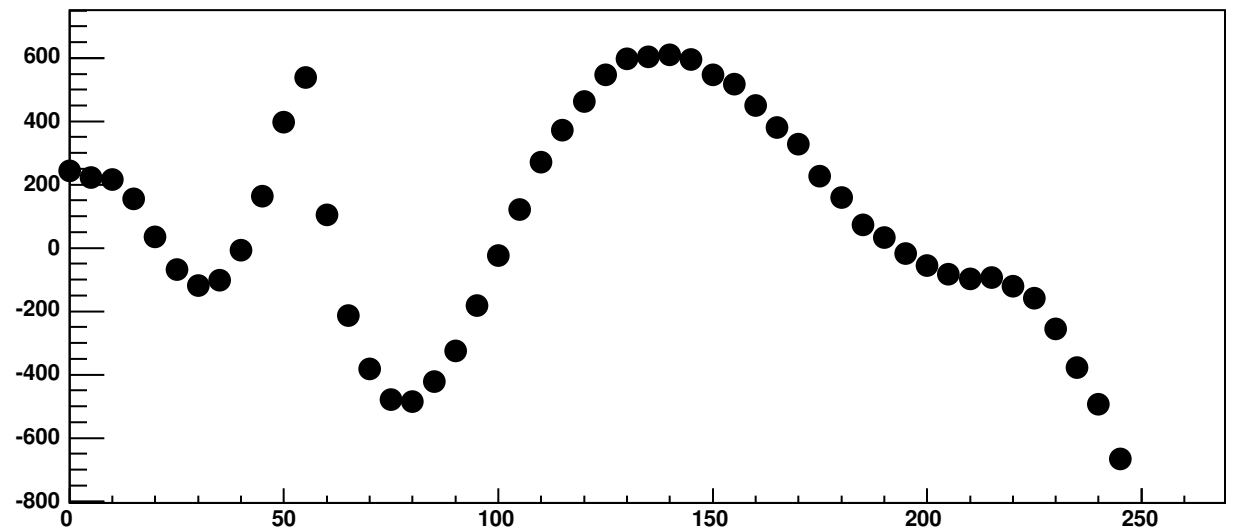
χ^2 / ndf	1.099e+05 / 42
p0	-50.86 ± 0.7945
p1	54.09 ± 0.01956
p2	5051 ± 2.106
p3	81.03 ± 0.05095

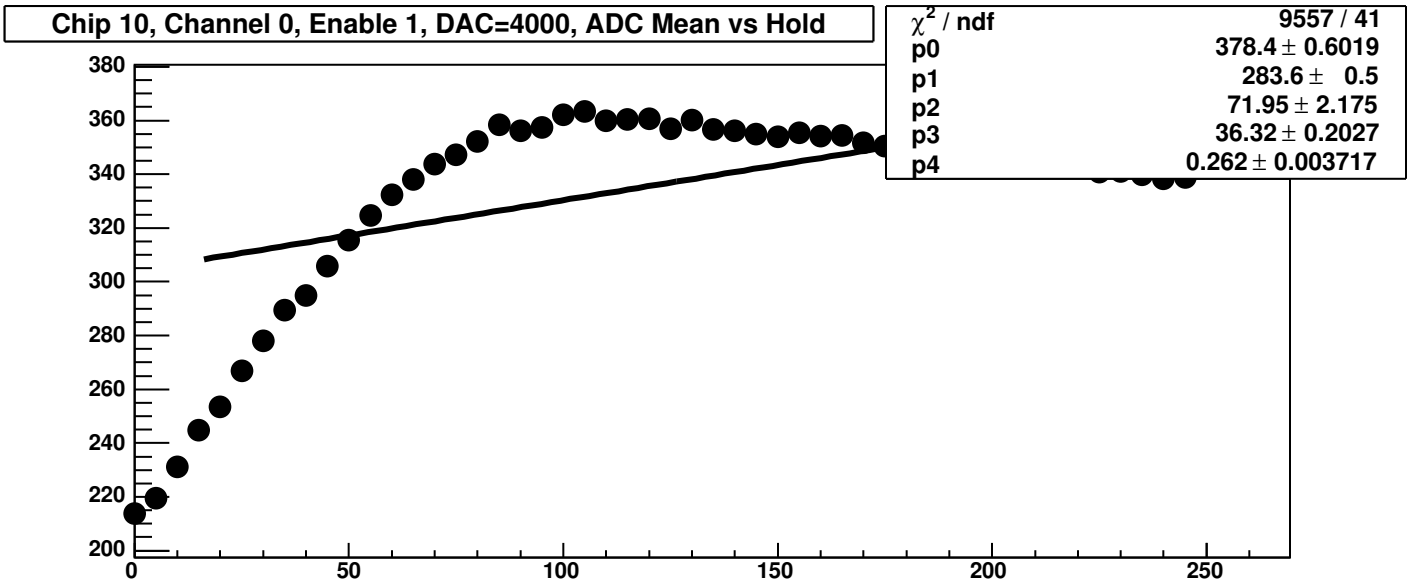


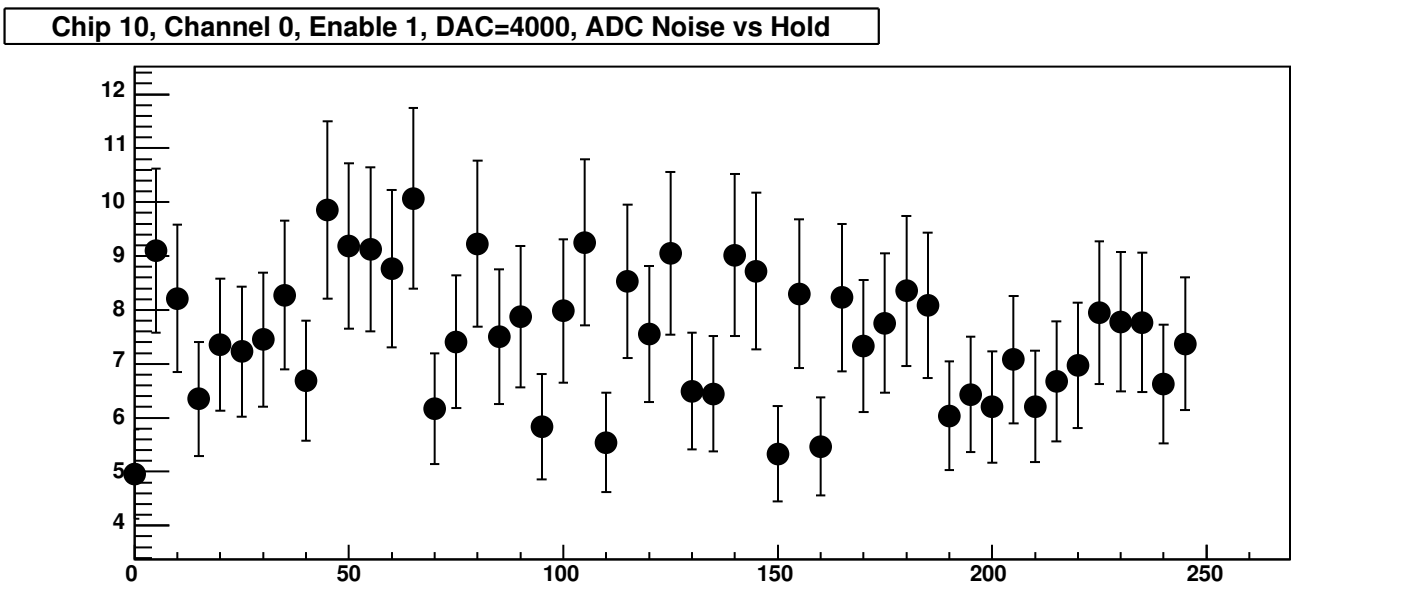
Chip 10, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold

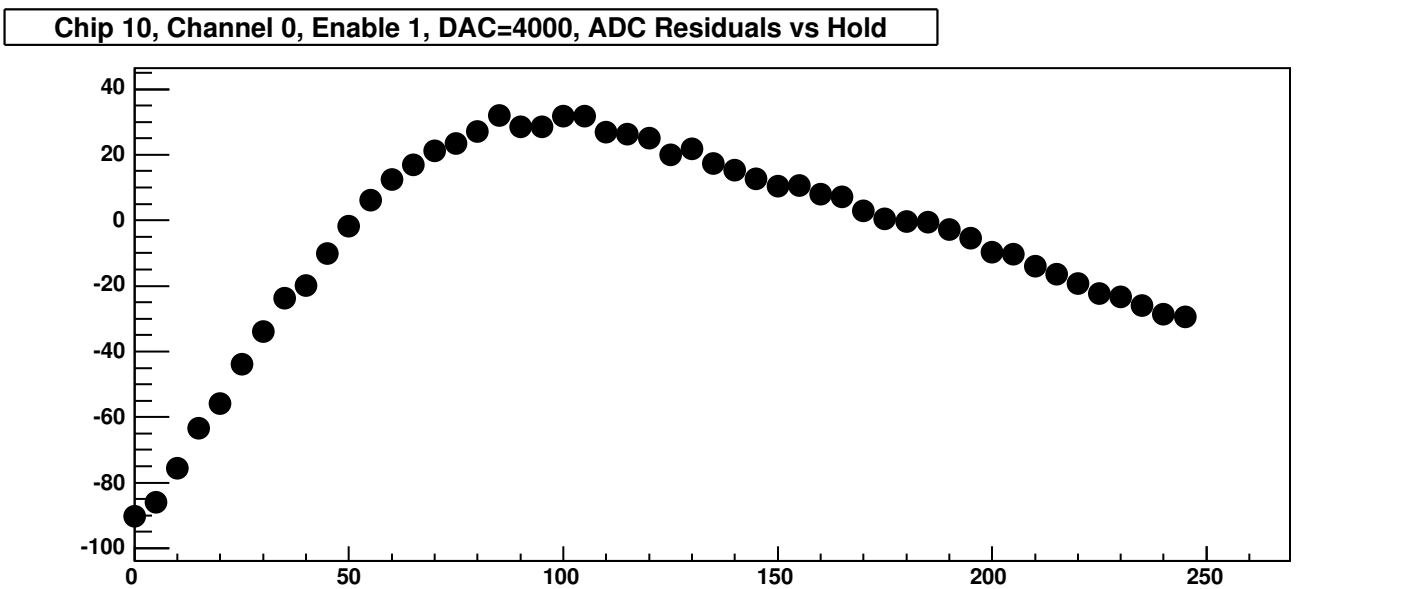


Chip 10, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold

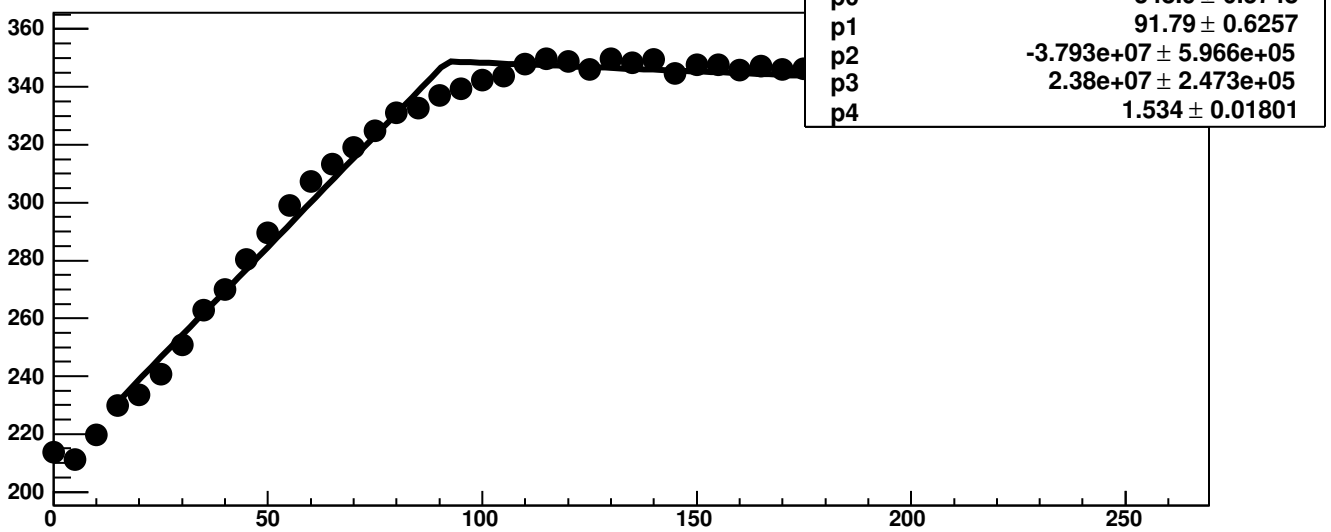



Chip 10, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 9557 / 41
p0 378.4 ± 0.6019
p1 283.6 ± 0.5
p2 71.95 ± 2.175
p3 36.32 ± 0.2027
p4 0.262 ± 0.003717
380
360
340
320
300
280
260
240
220
200
0
50
100
150
200
250

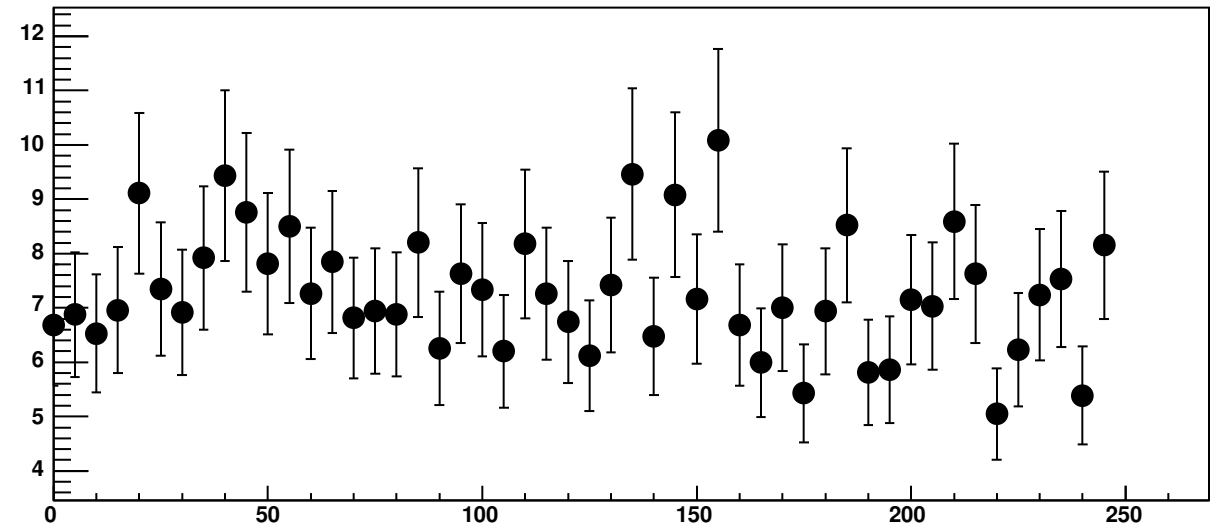

Chip 10, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 10, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
-100
0
50
100
150
200
250

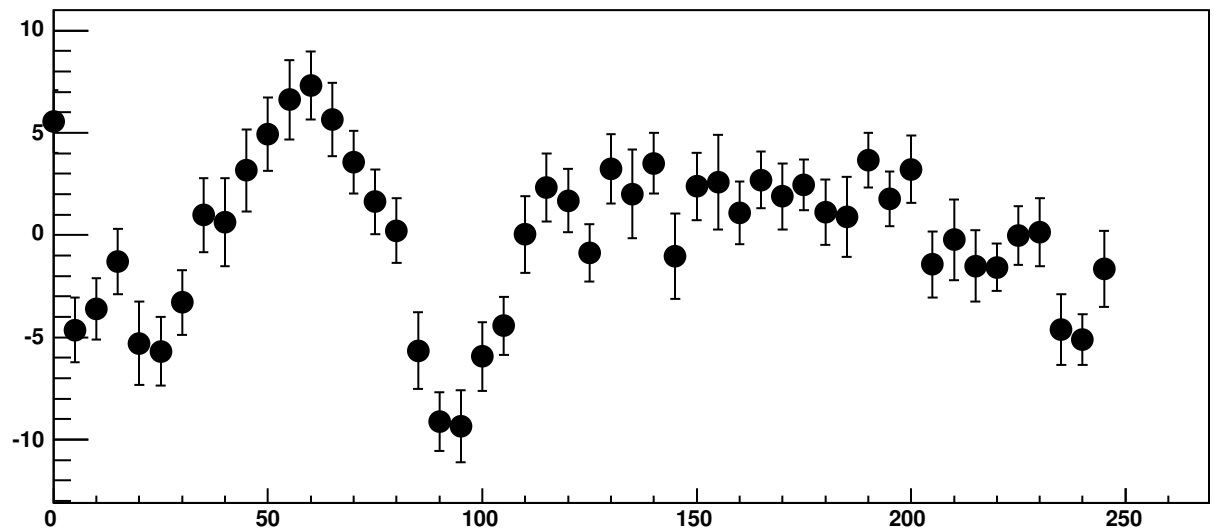
Chip 10, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold

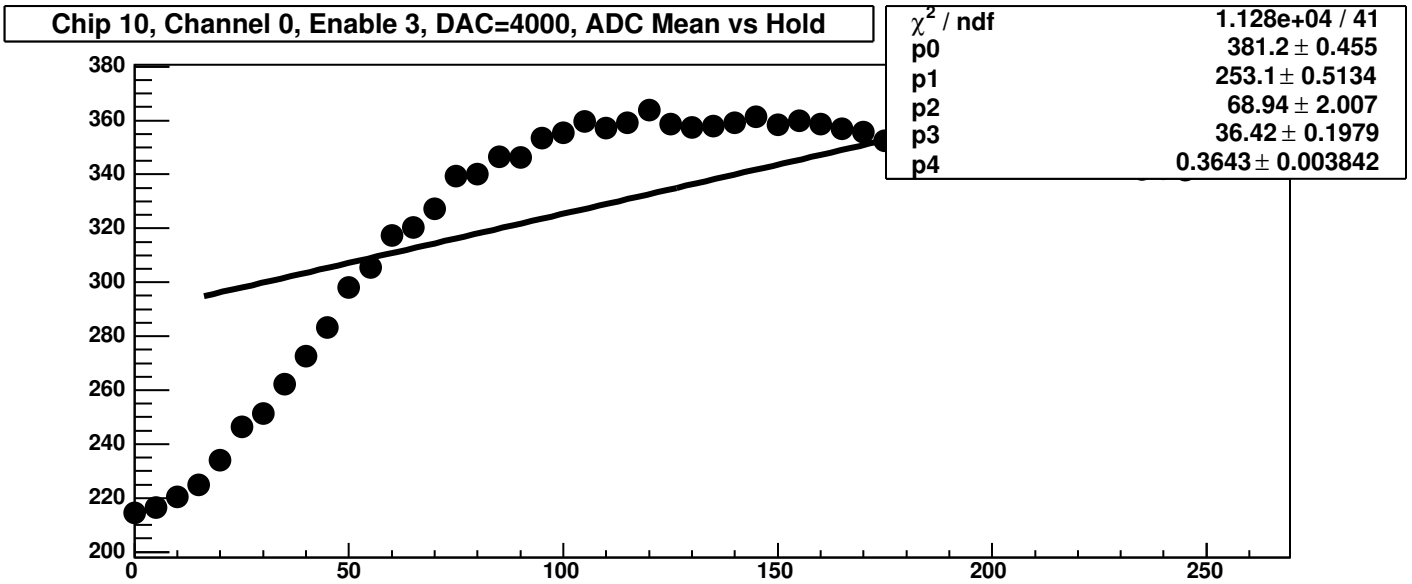


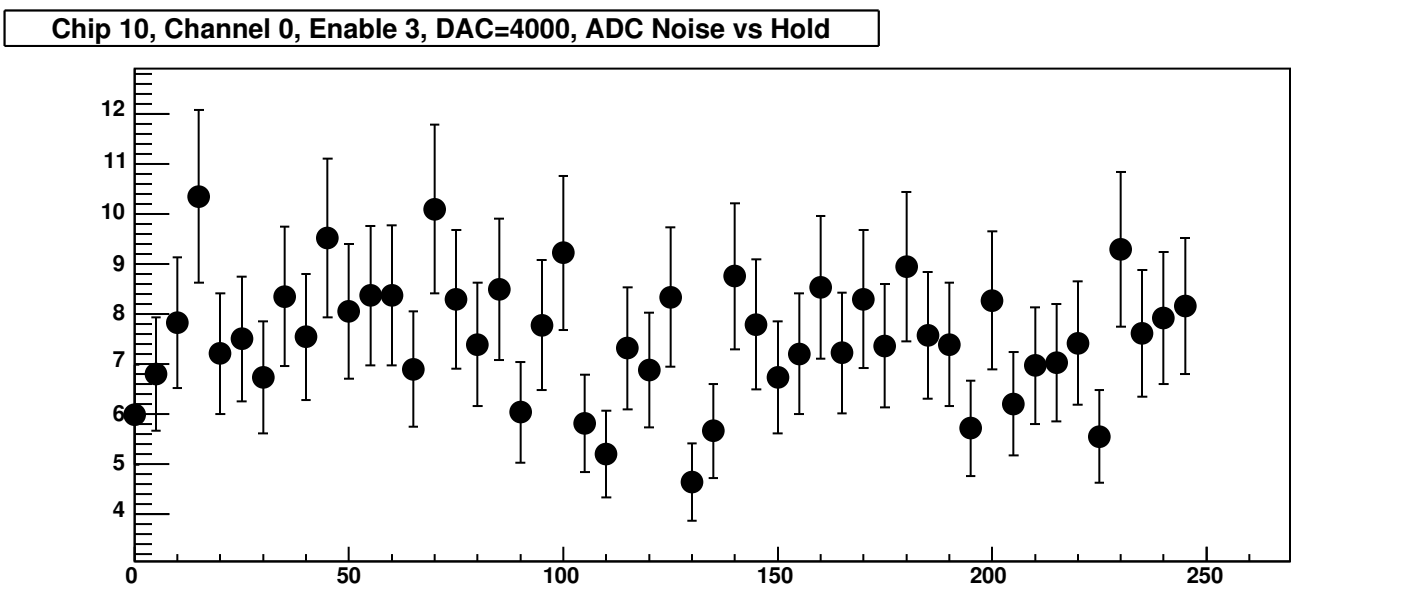
Chip 10, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold

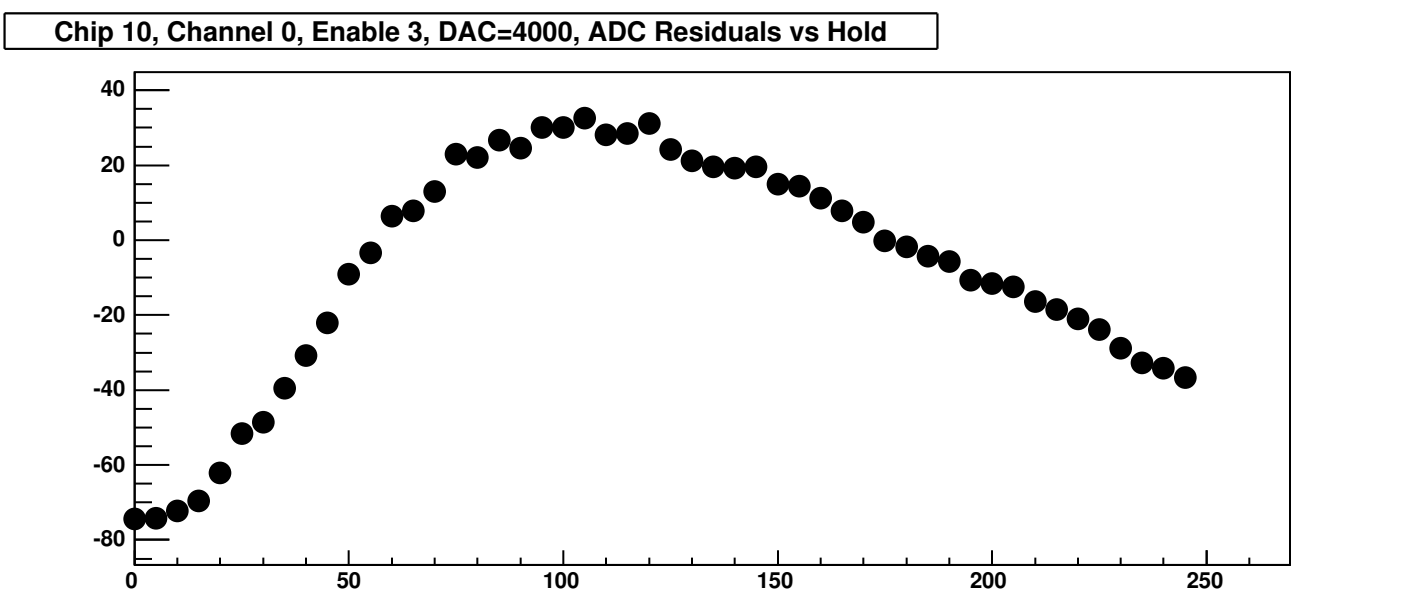


Chip 10, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold

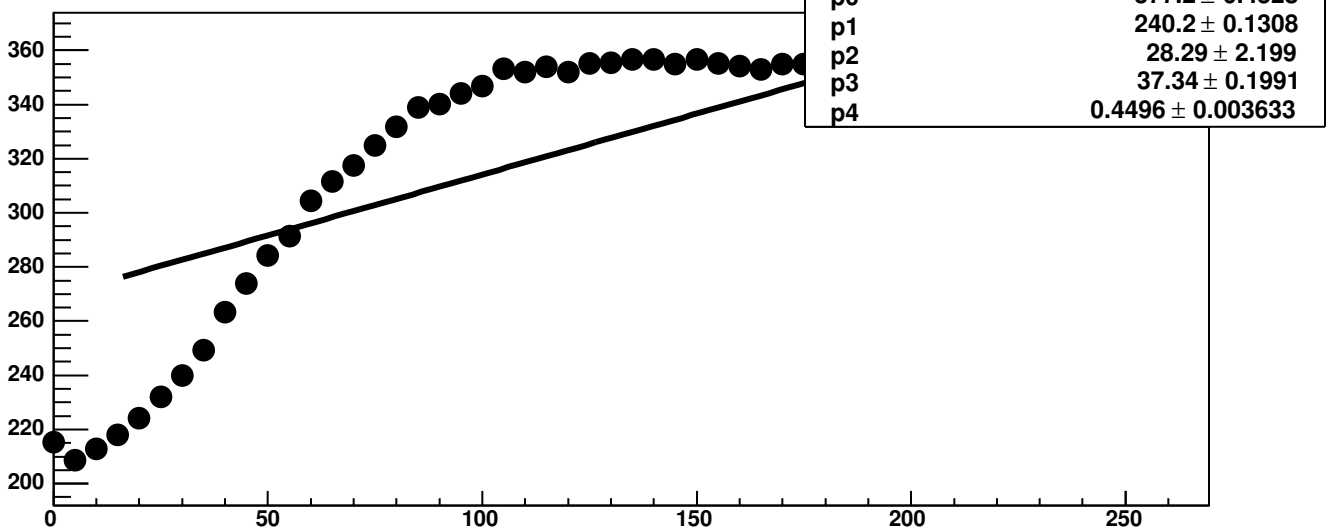



Chip 10, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.128e+04 / 41
p0 381.2 ± 0.455
p1 253.1 ± 0.5134
p2 68.94 ± 2.007
p3 36.42 ± 0.1979
p4 0.3643 ± 0.003842
380
360
340
320
300
280
260
240
220
200
0
50
100
150
200
250

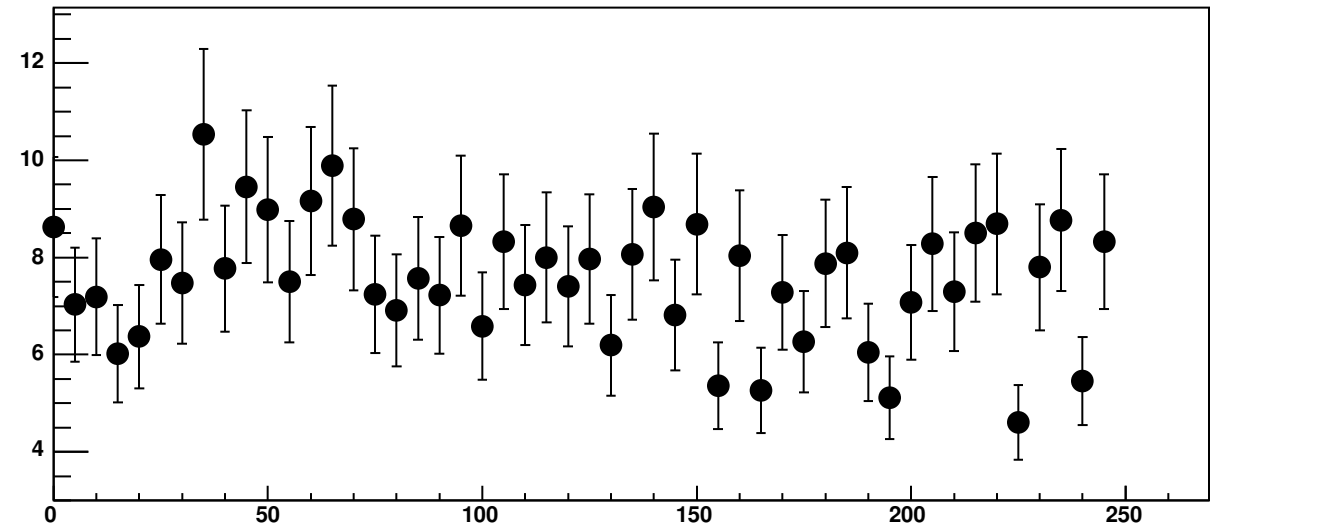

Chip 10, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 10, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
0
50
100
150
200
250

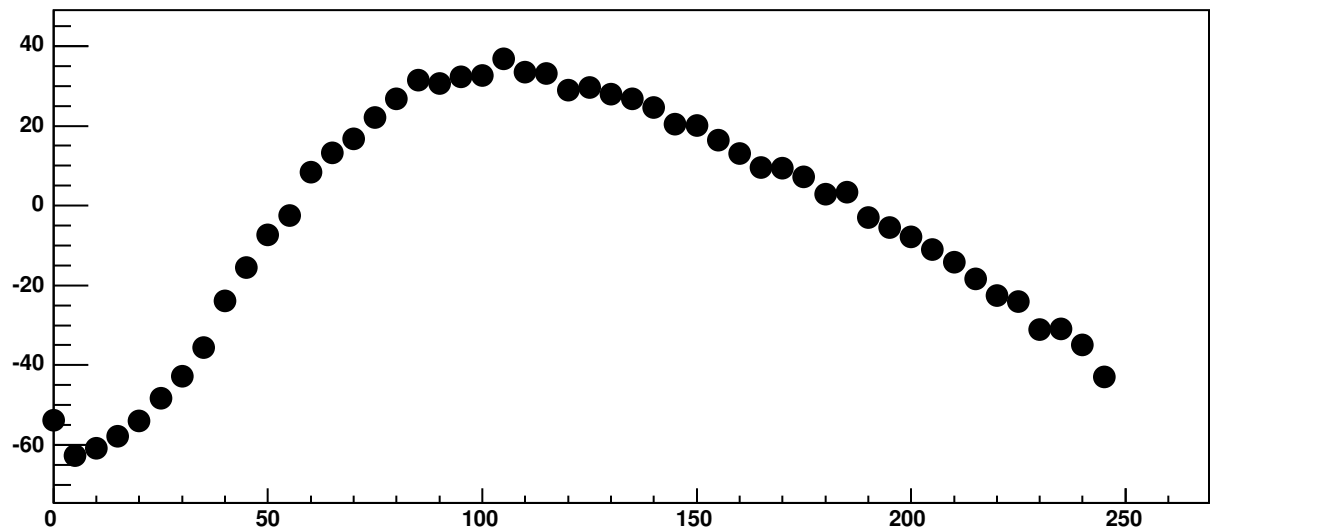
Chip 10, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

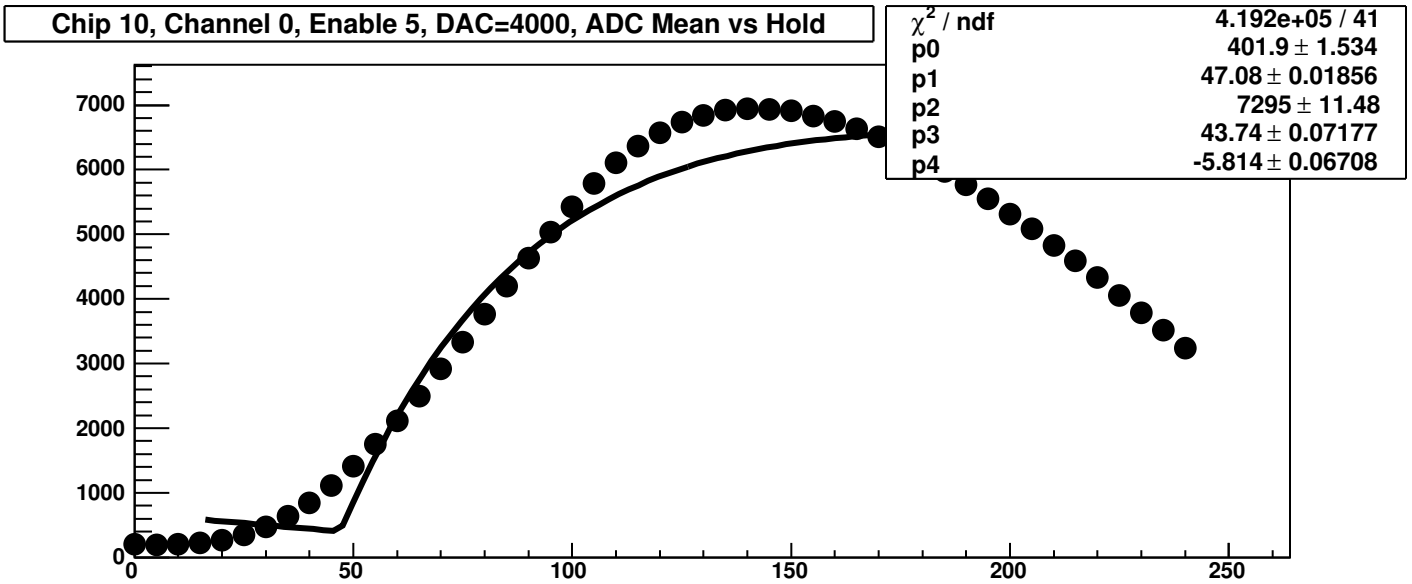


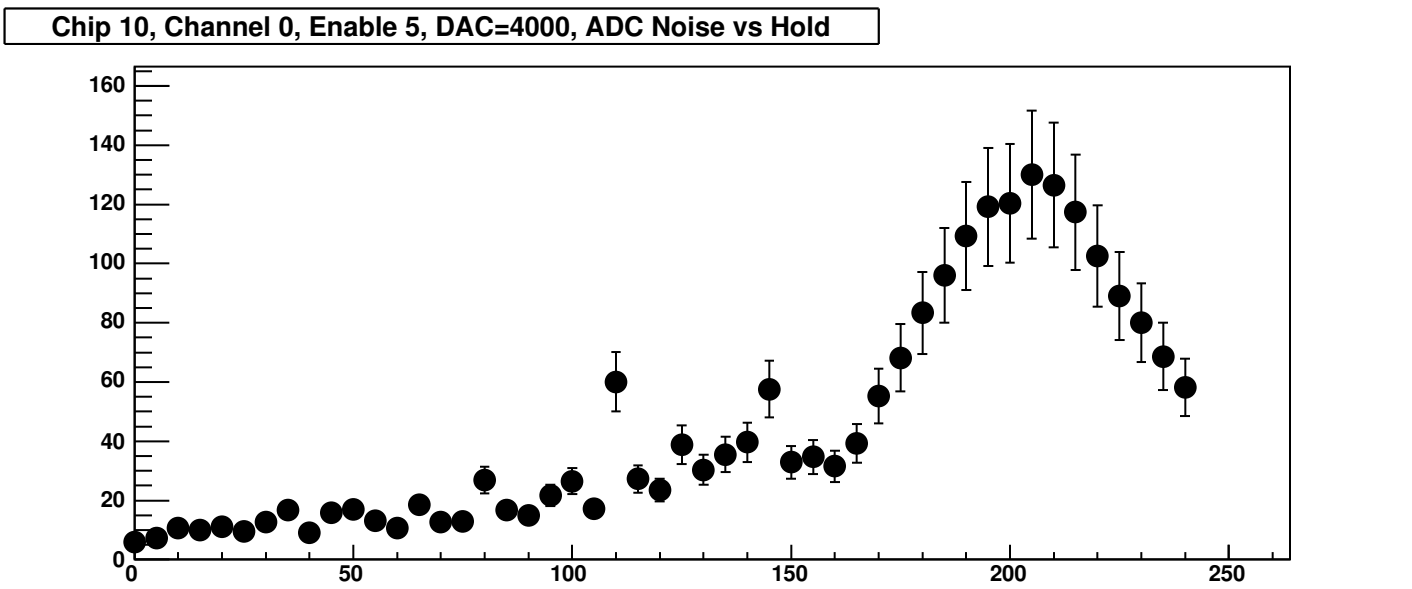
Chip 10, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold

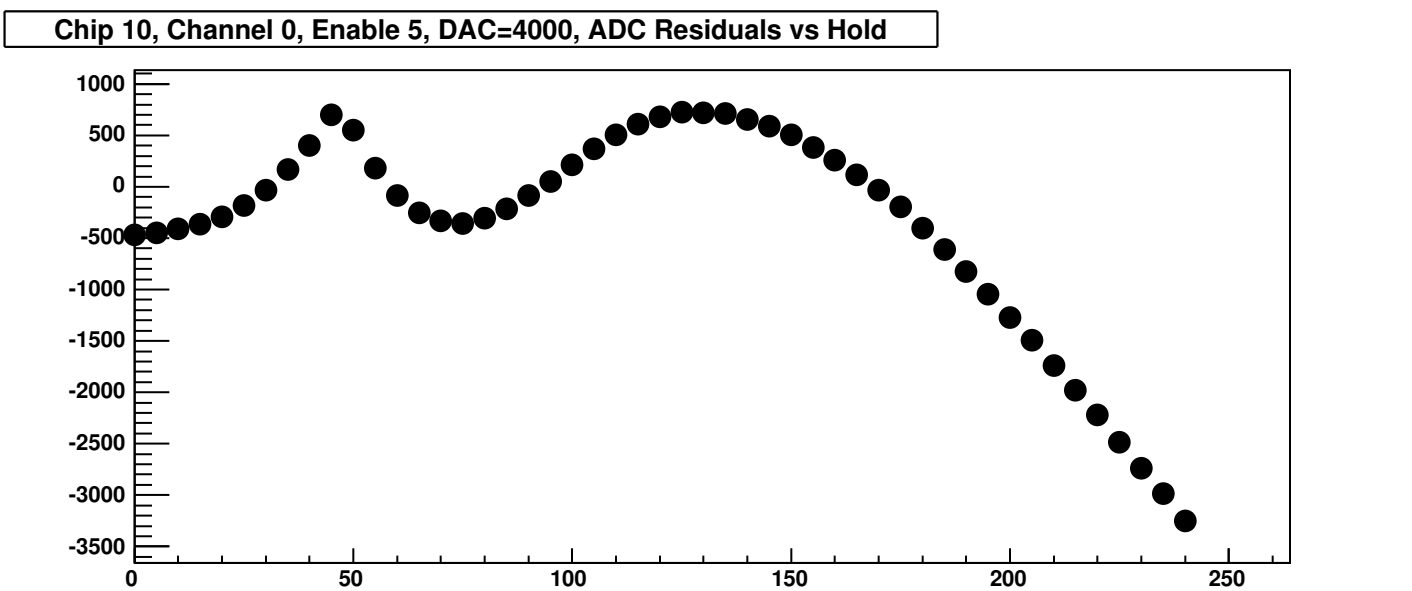


Chip 10, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

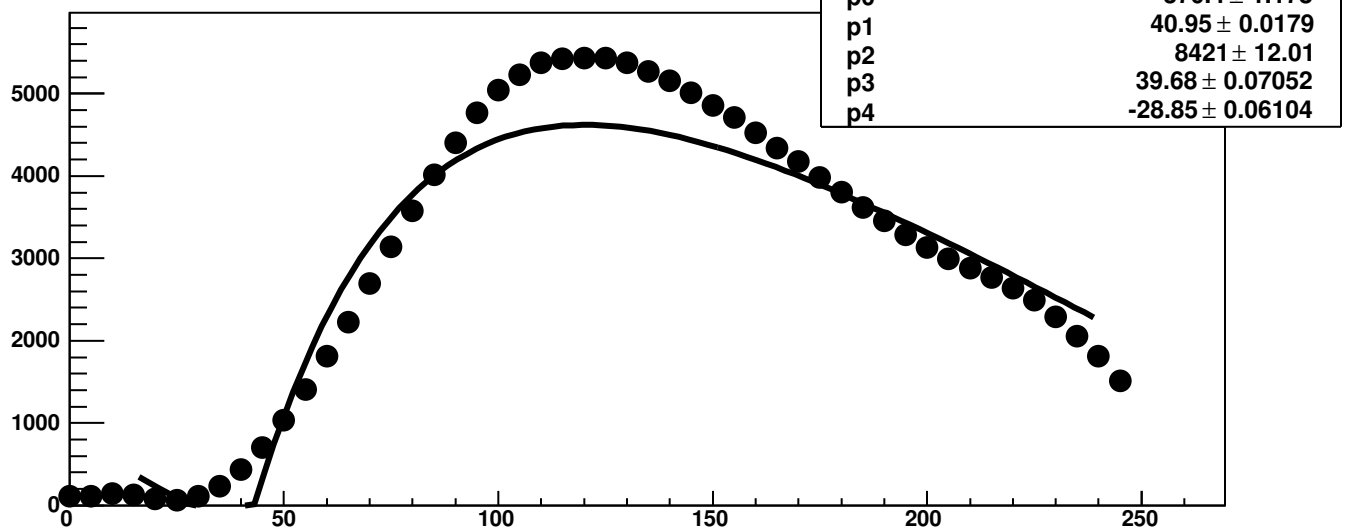



Chip 10, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.192e+05 / 41
p0 401.9 ± 1.534
p1 47.08 ± 0.01856
p2 7295 ± 11.48
p3 43.74 ± 0.07177
p4 -5.814 ± 0.06708
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

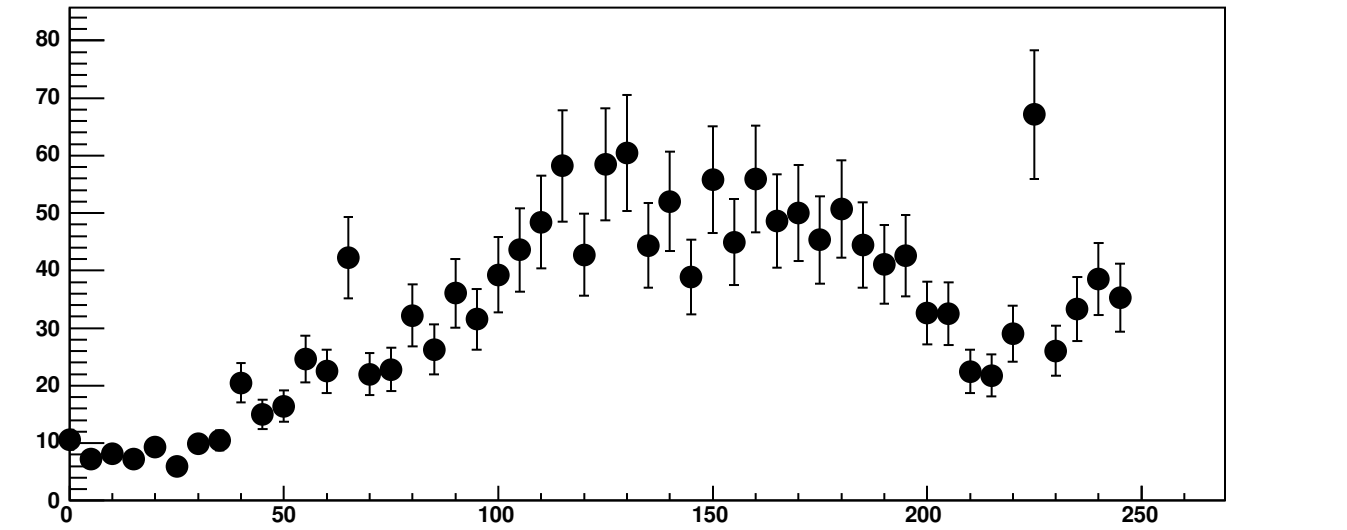

Chip 10, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 10, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
0
50
100
150
200
250

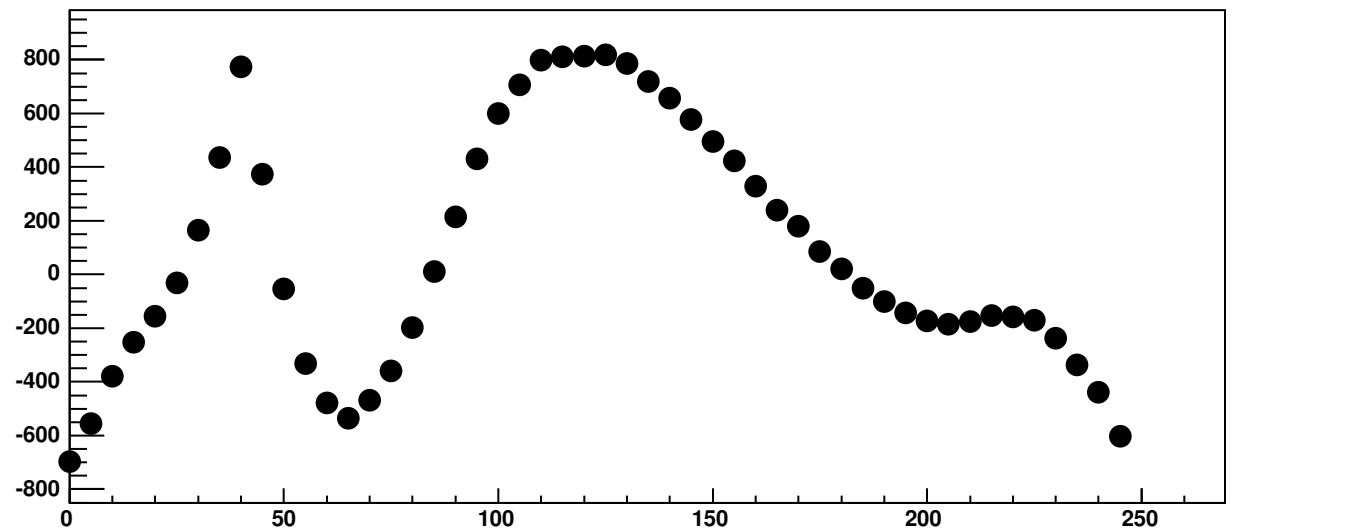
Chip 10, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold



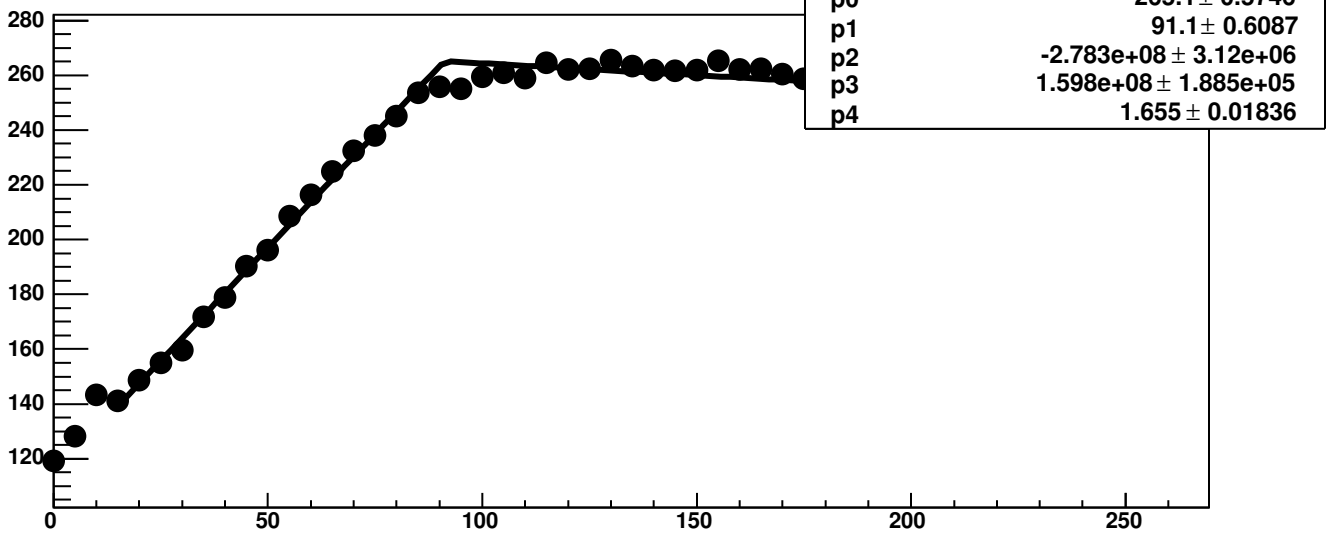
Chip 10, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold



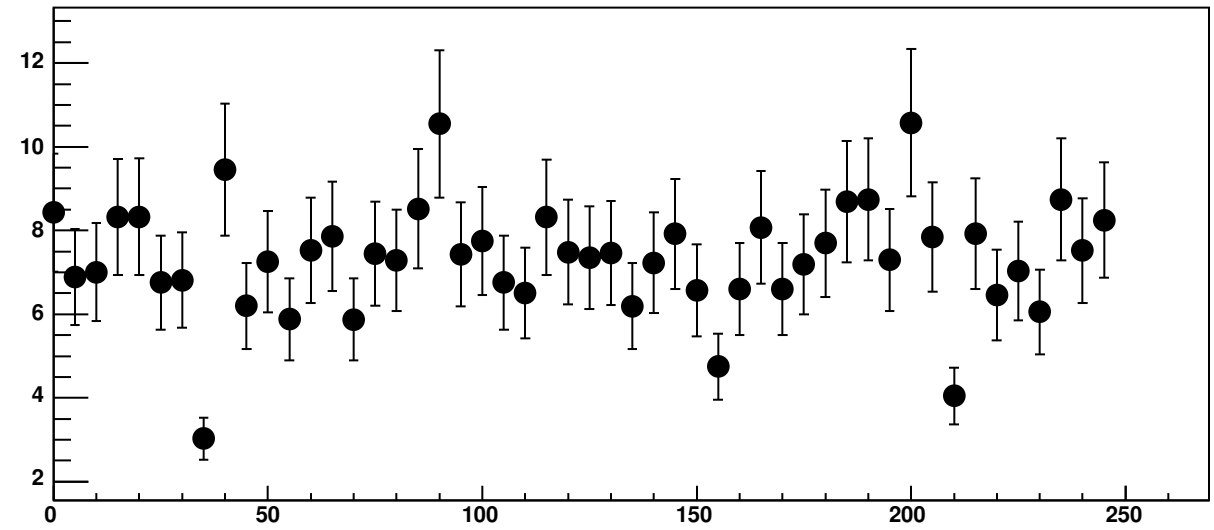
Chip 10, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold



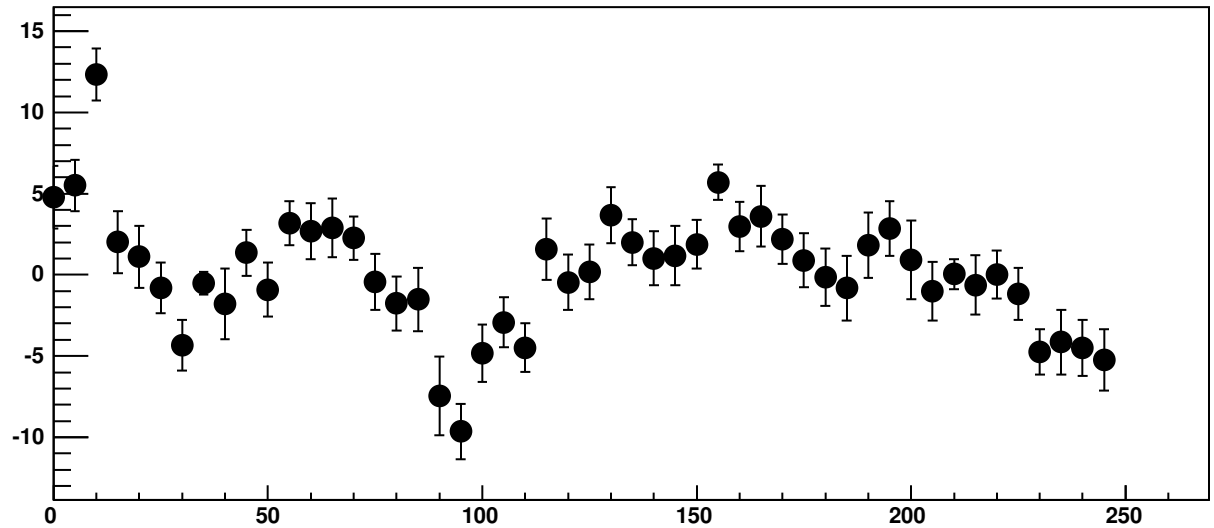
Chip 10, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold

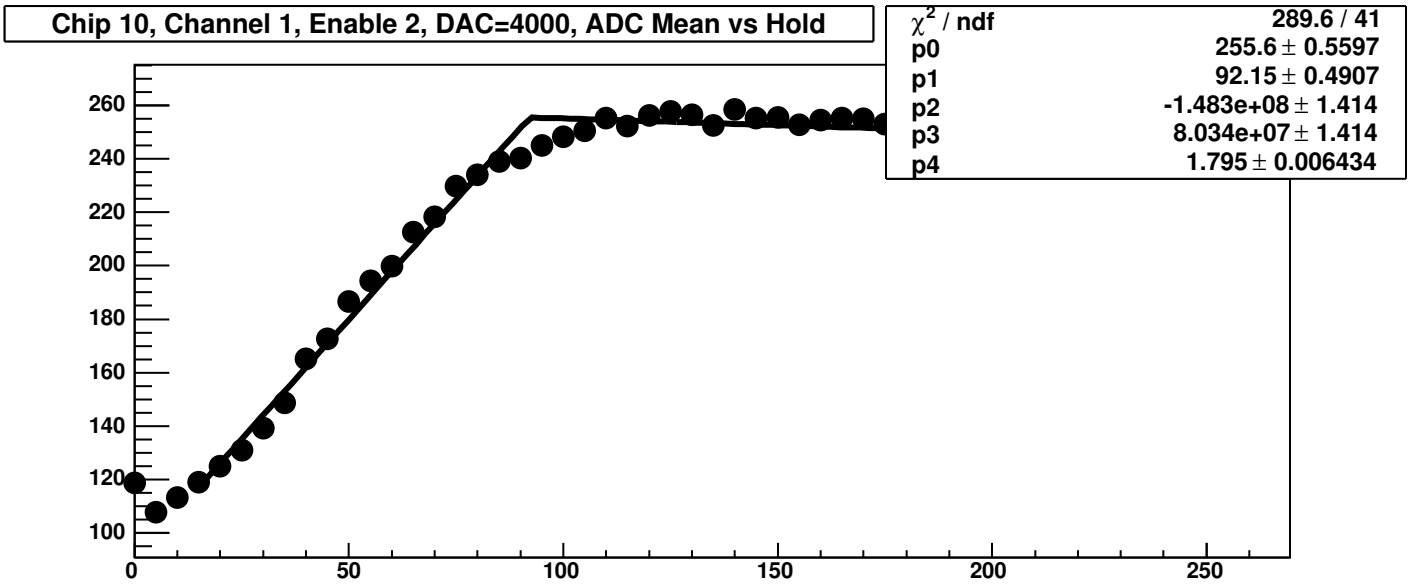


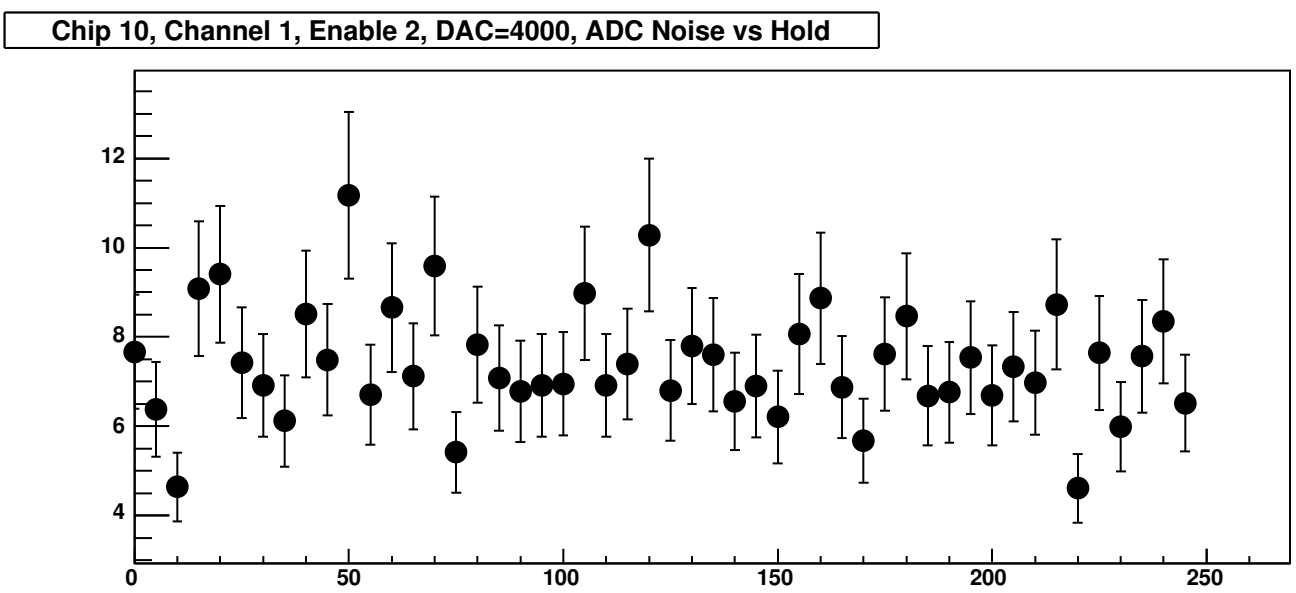
Chip 10, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold

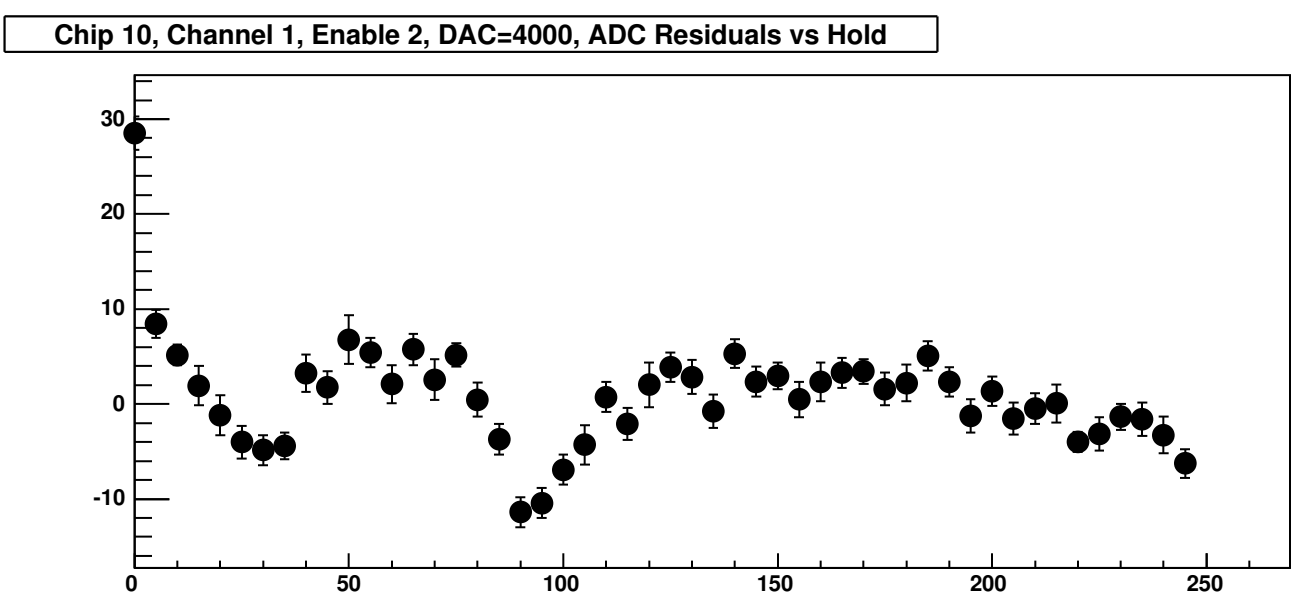


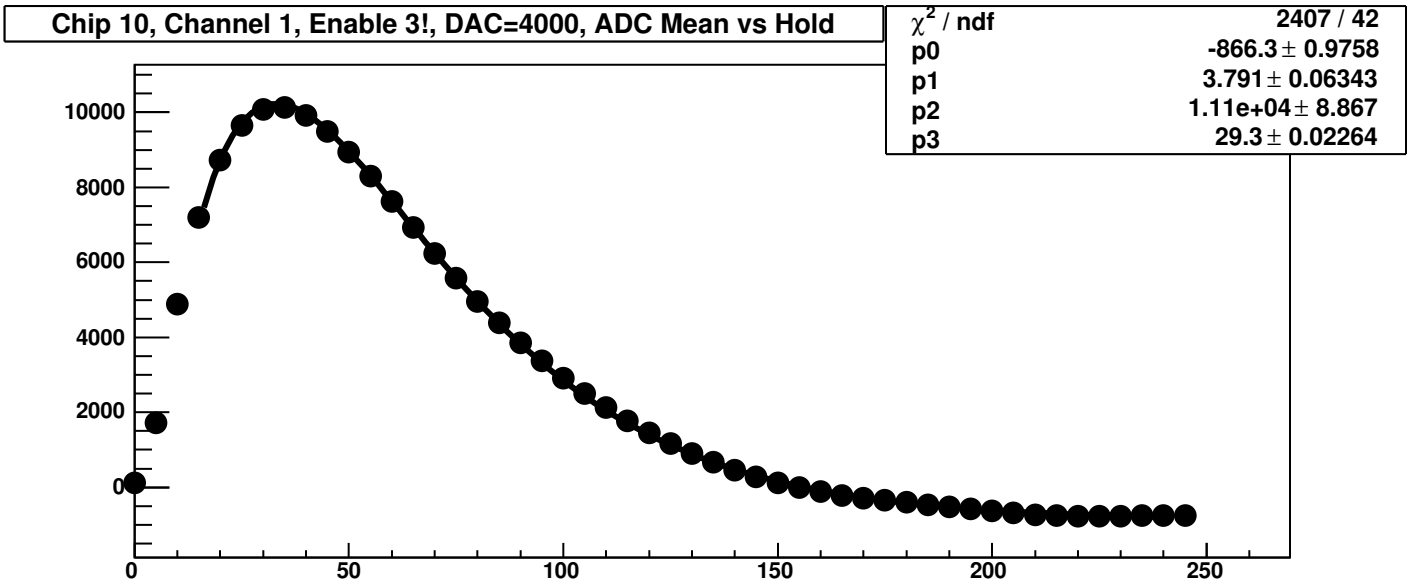
Chip 10, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

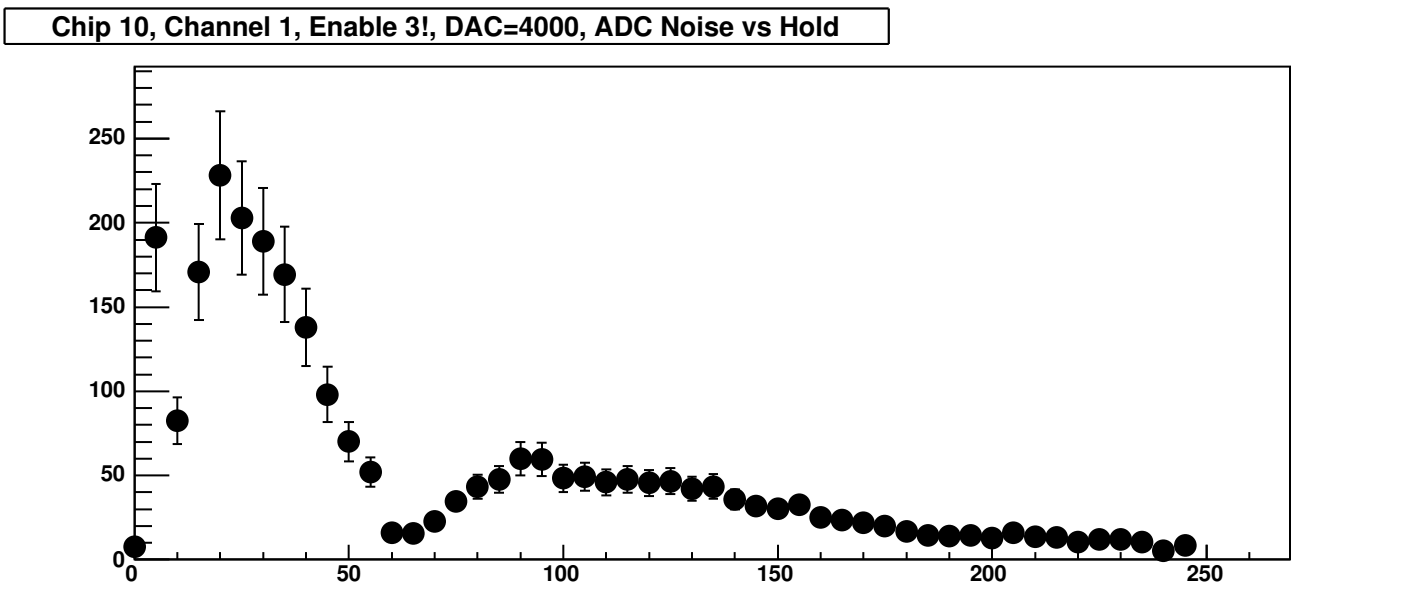


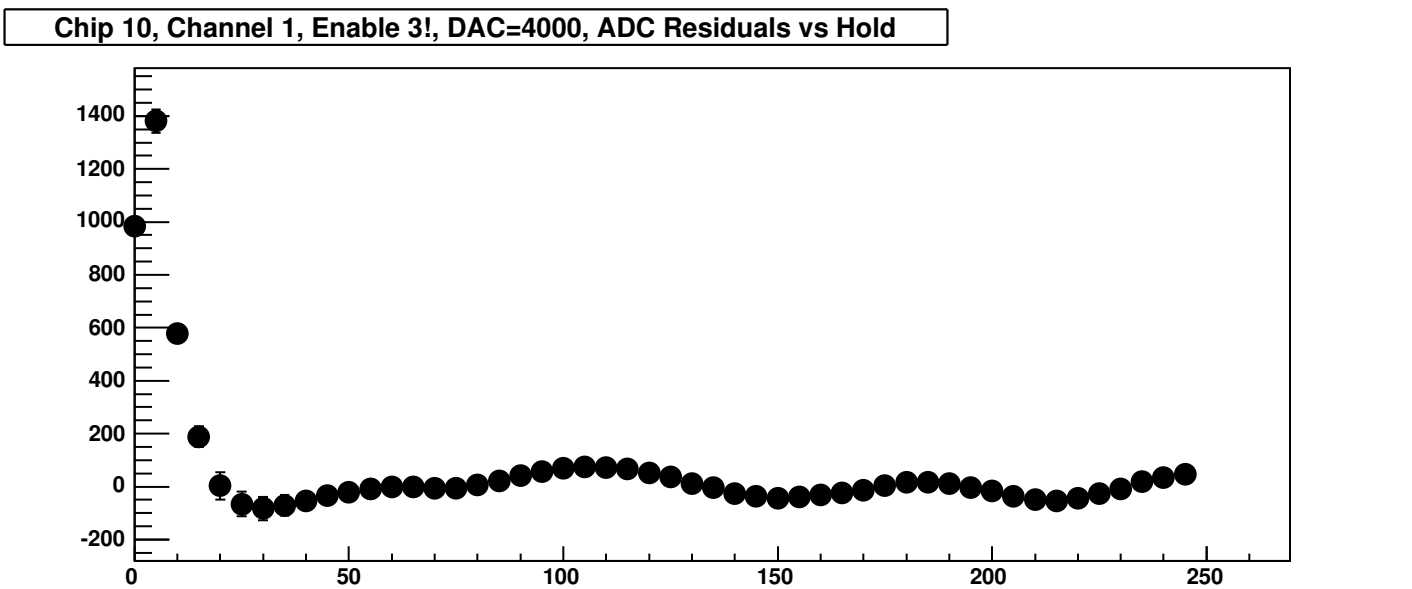

Chip 10, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 289.6 / 41
p0 255.6 ± 0.5597
p1 92.15 ± 0.4907
p2 -1.483e+08 ± 1.414
p3 8.034e+07 ± 1.414
p4 1.795 ± 0.006434
260
240
220
200
180
160
140
120
100
0
50
100
150
200
250


Chip 10, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

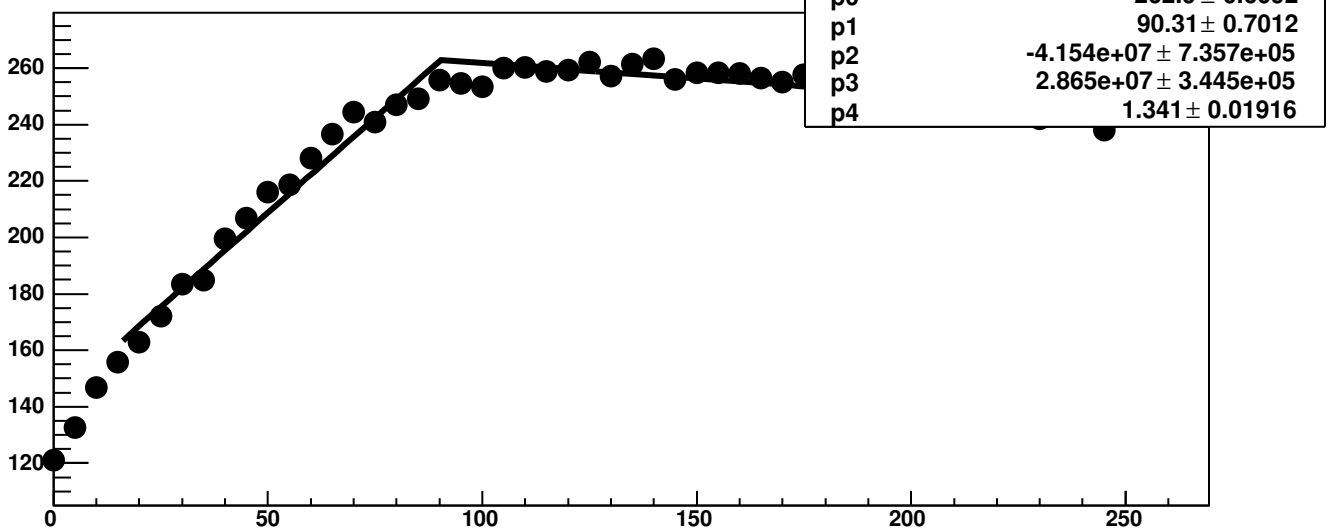

Chip 10, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
0
50
100
150
200
250


Chip 10, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 2407 / 42
p0 -866.3 ± 0.9758
p1 3.791 ± 0.06343
p2 1.11e+04 ± 8.867
p3 29.3 ± 0.02264
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

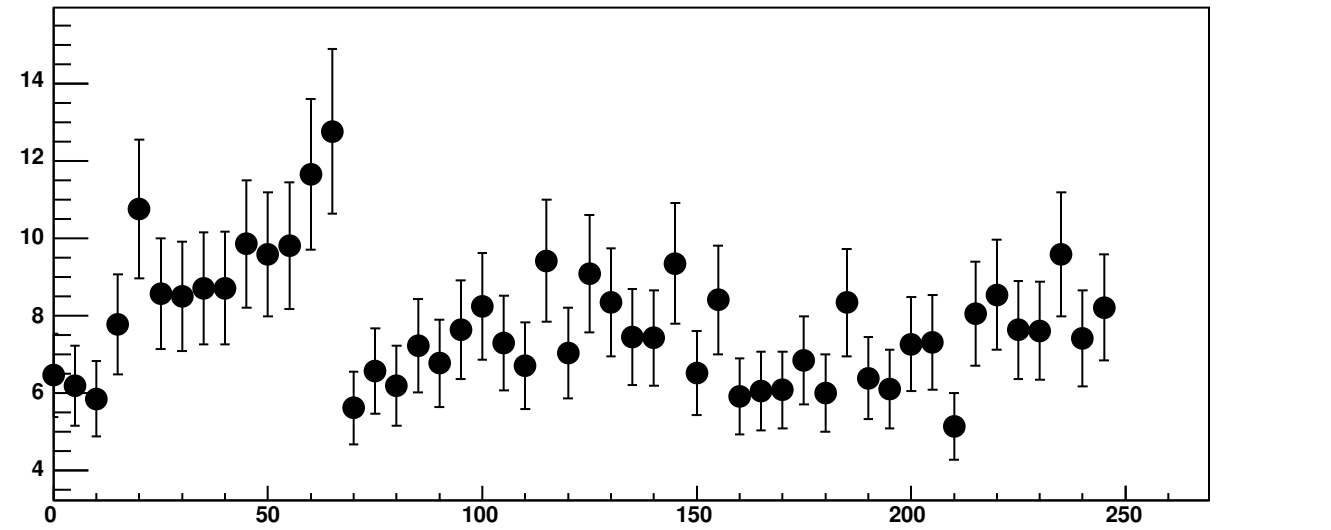

Chip 10, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250


Chip 10, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

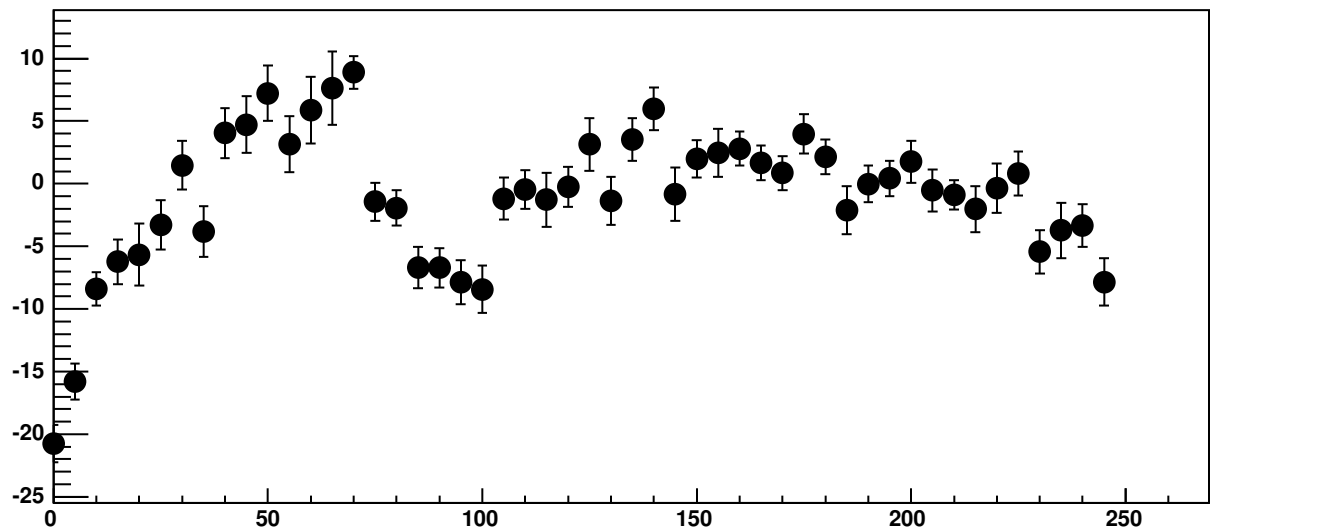
Chip 10, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

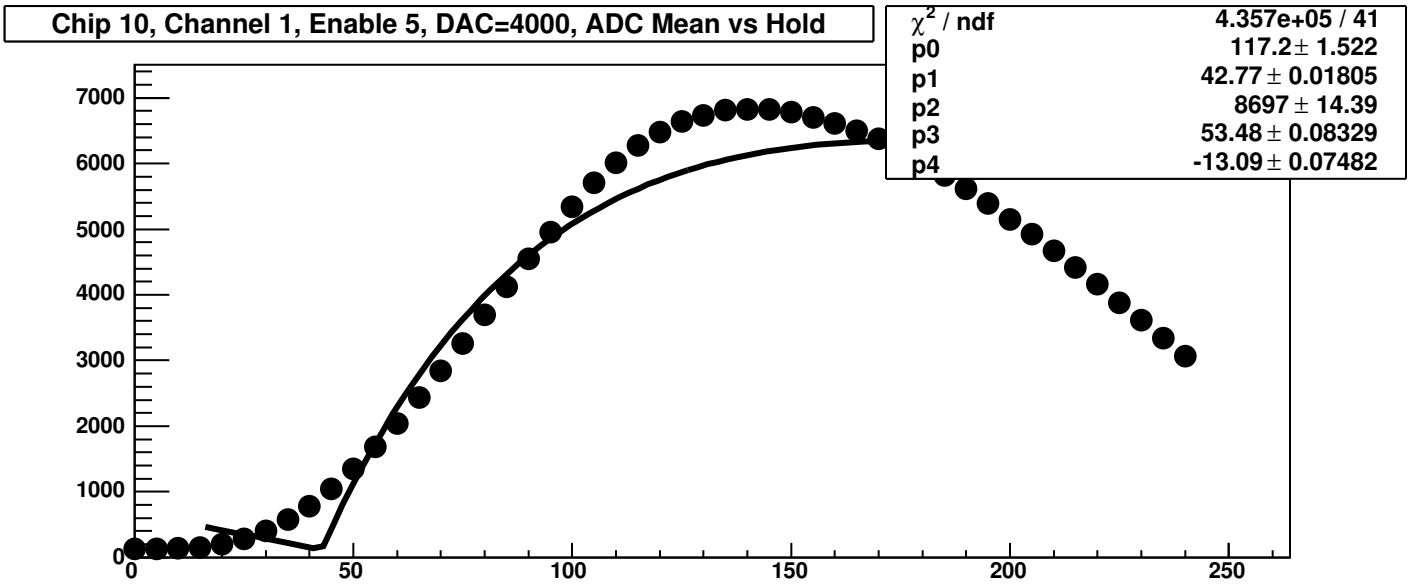


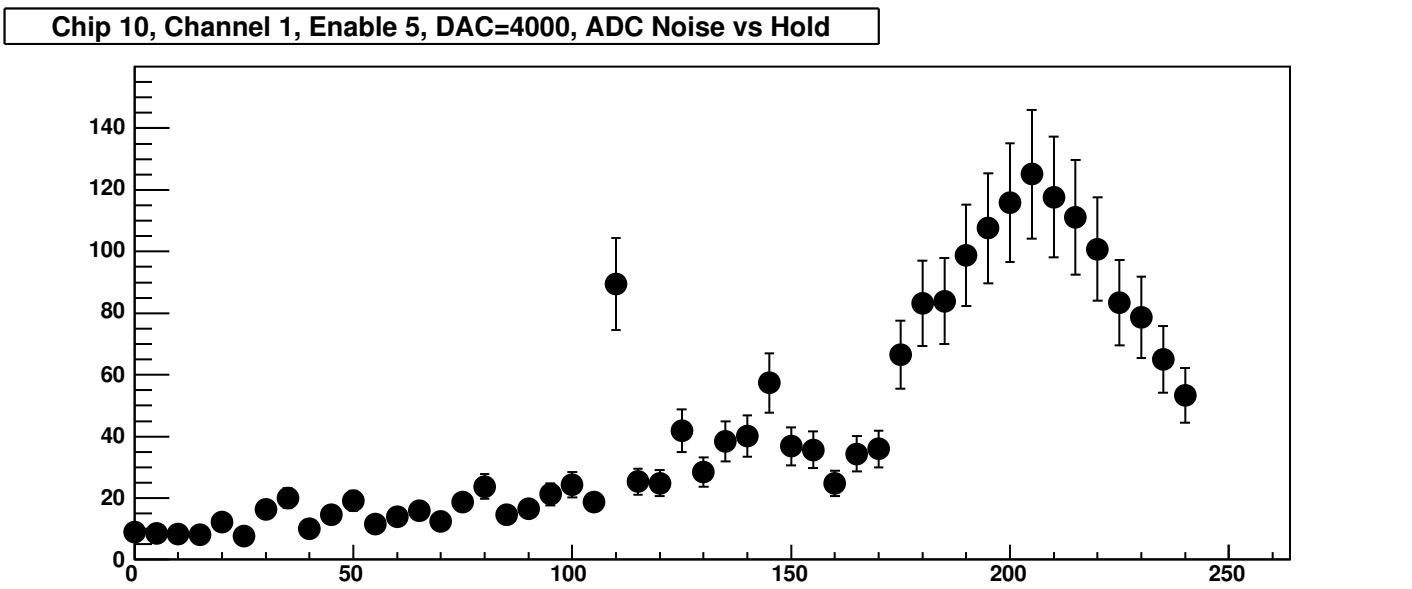
Chip 10, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold

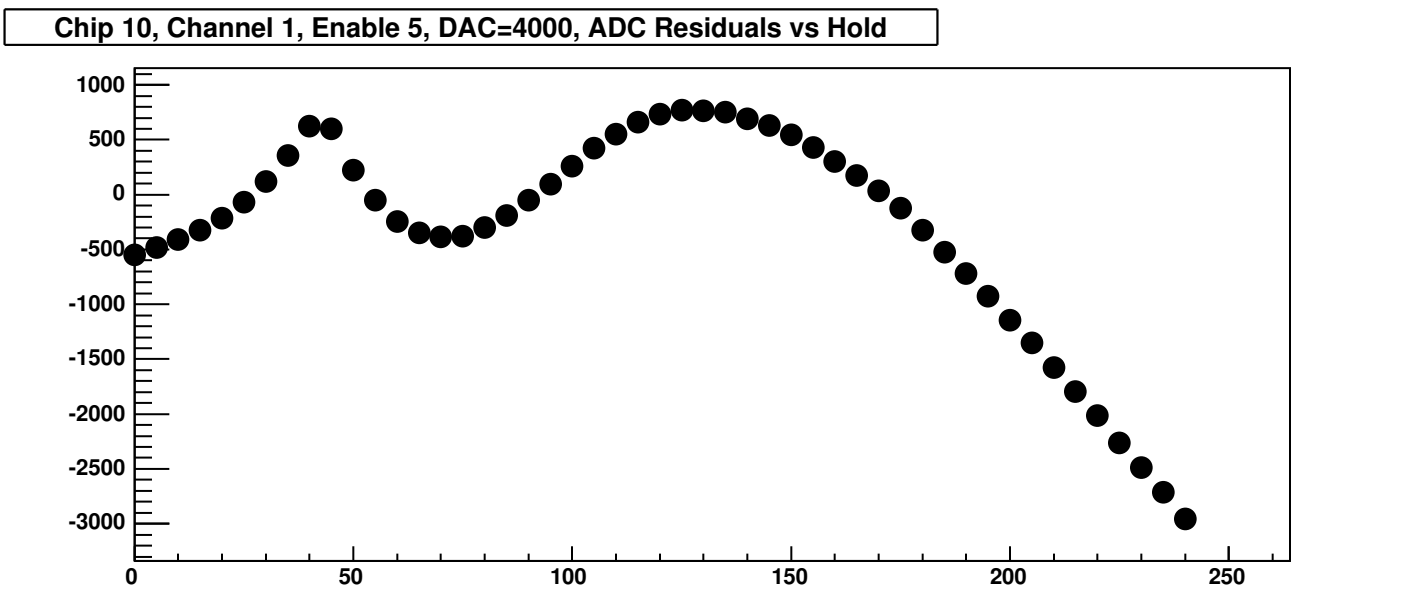


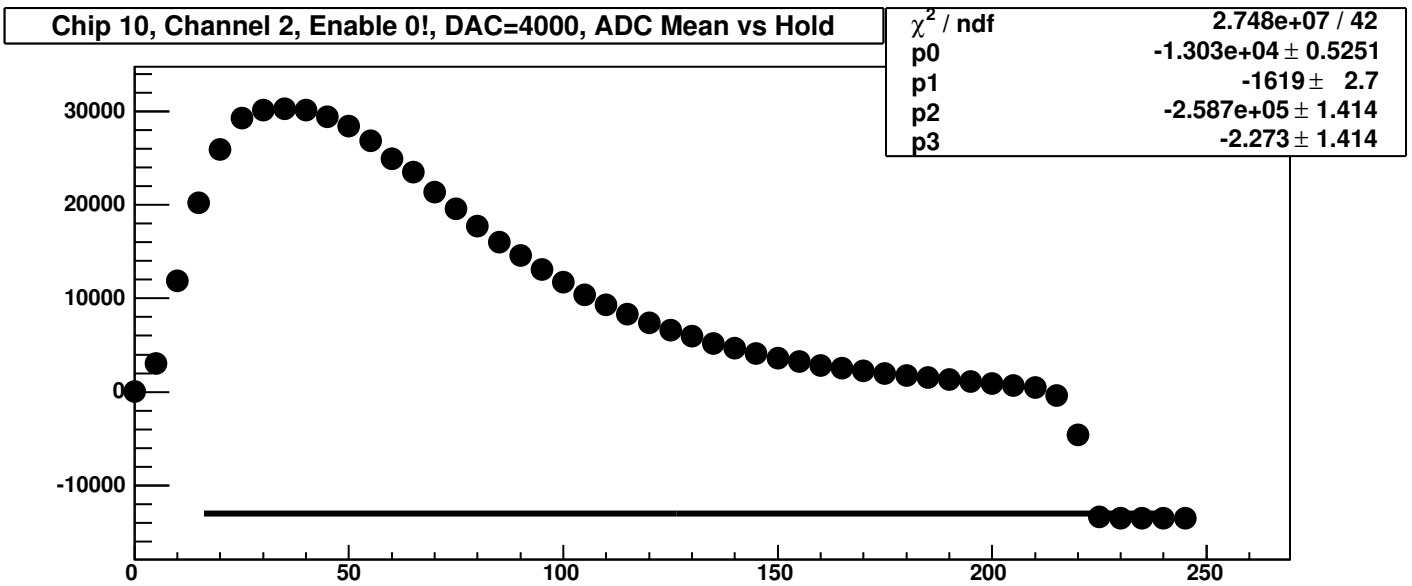
Chip 10, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

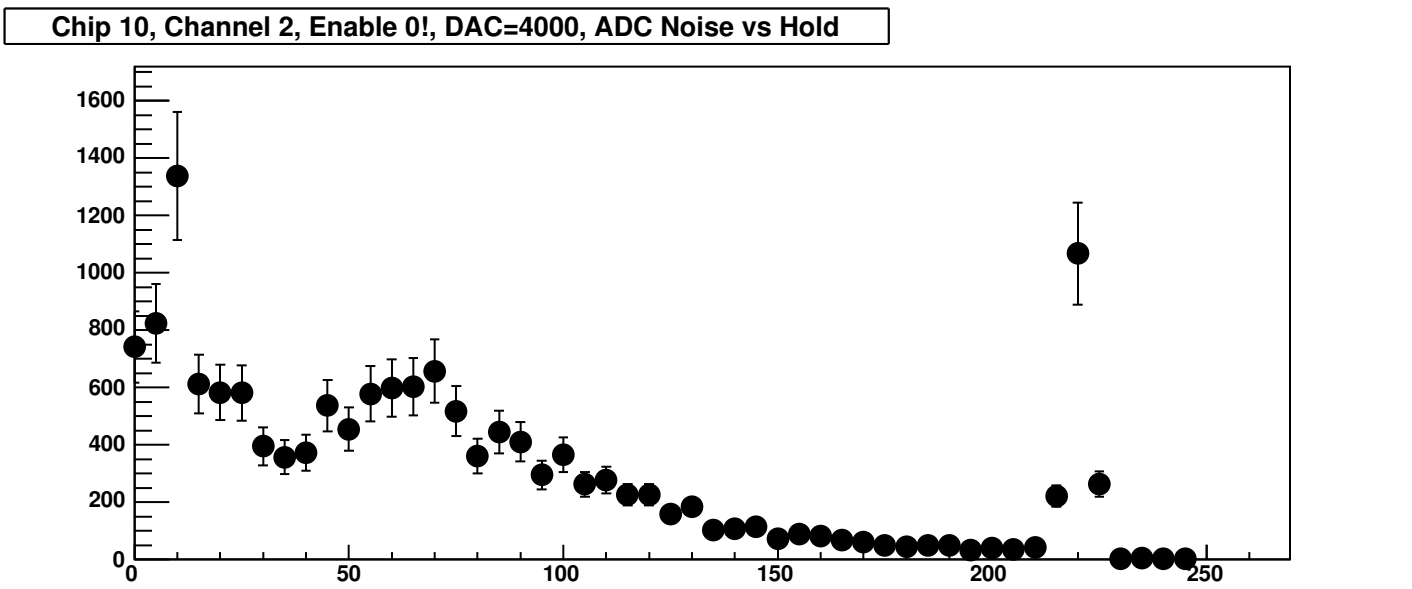


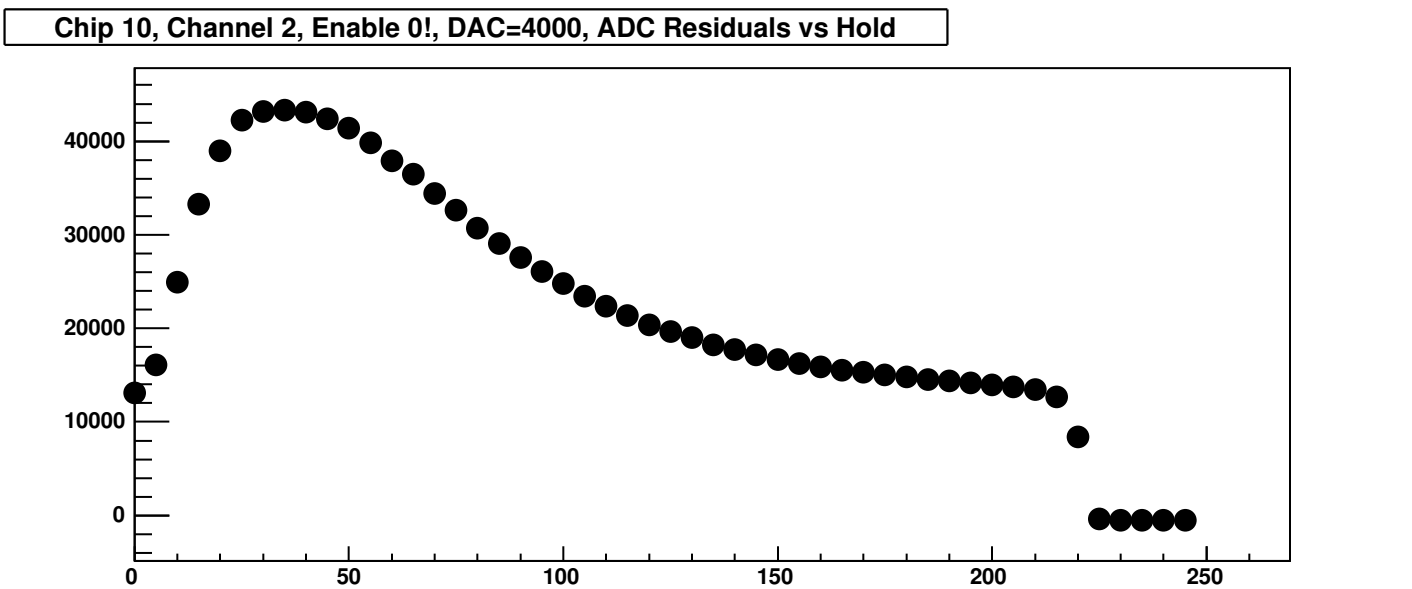

Chip 10, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.357e+05 / 41
p0 117.2 ± 1.522
p1 42.77 ± 0.01805
p2 8697 ± 14.39
p3 53.48 ± 0.08329
p4 -13.09 ± 0.07482
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 10, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250

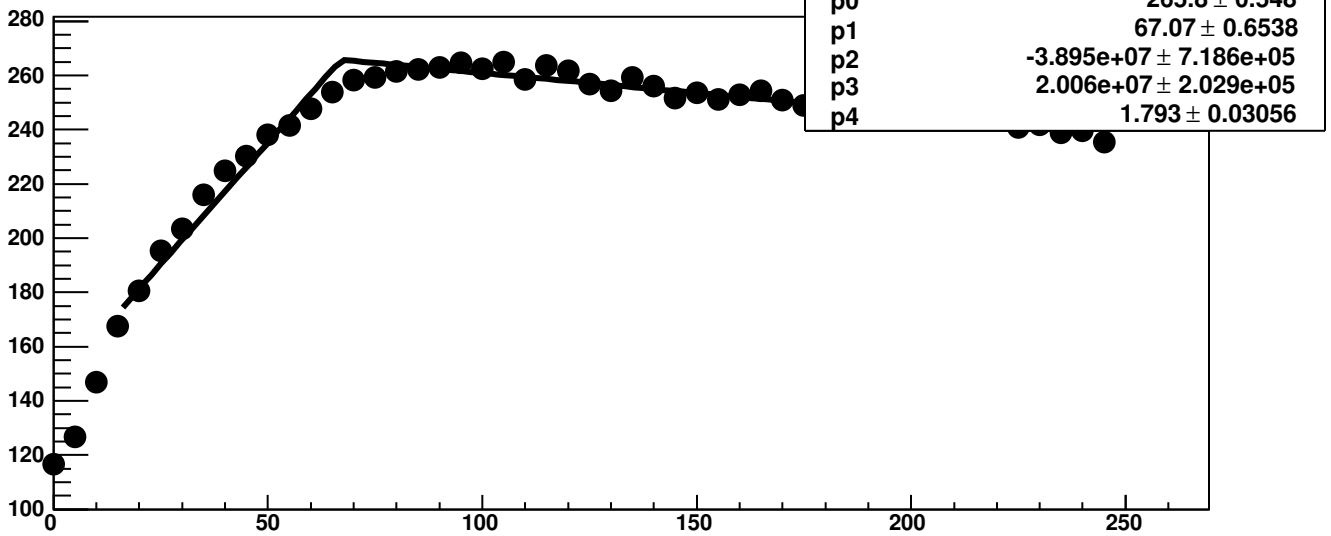

Chip 10, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250


Chip 10, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.748e+07 / 42
p0 -1.303e+04 ± 0.5251
p1 -1619 ± 2.7
p2 -2.587e+05 ± 1.414
p3 -2.273 ± 1.414
30000
20000
10000
0
-10000
0
50
100
150
200
250

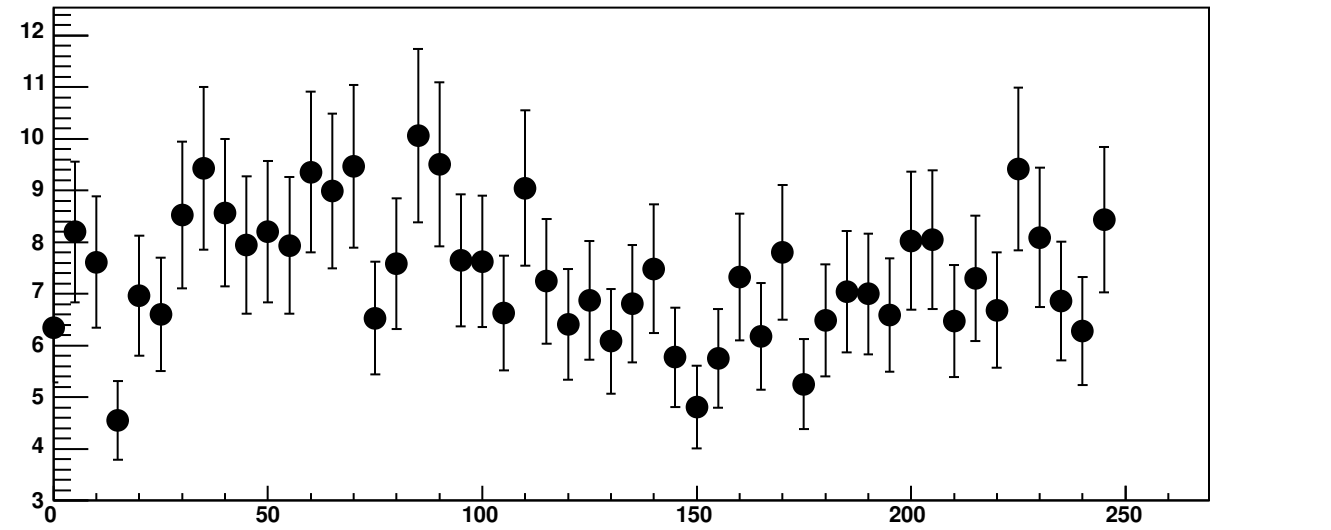

Chip 10, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 10, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold
40000
30000
20000
10000
0
0
50
100
150
200
250

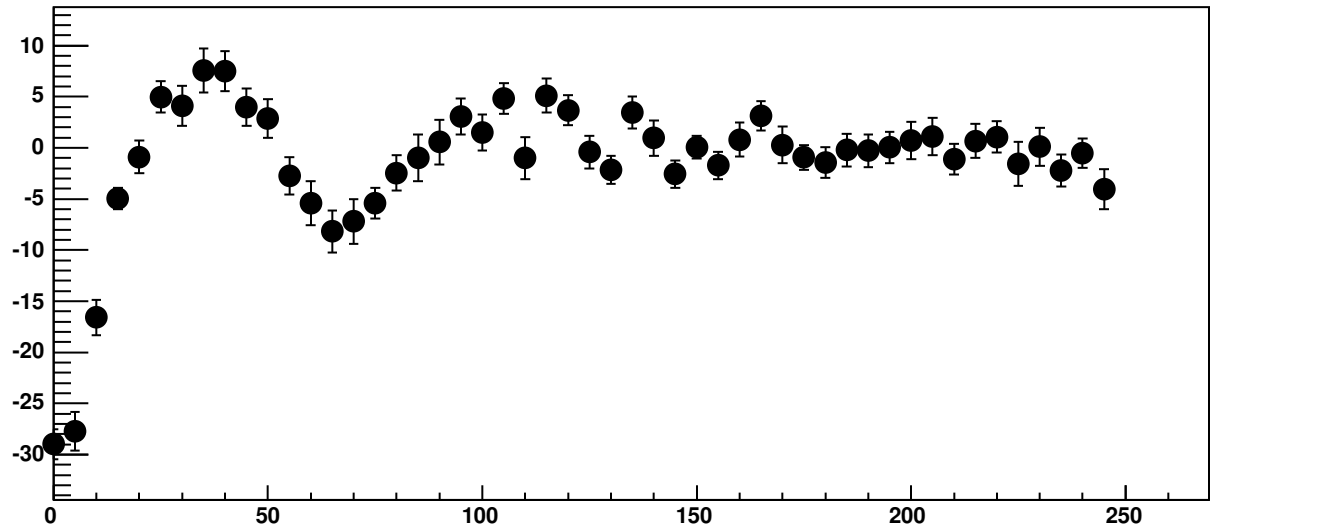
Chip 10, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold



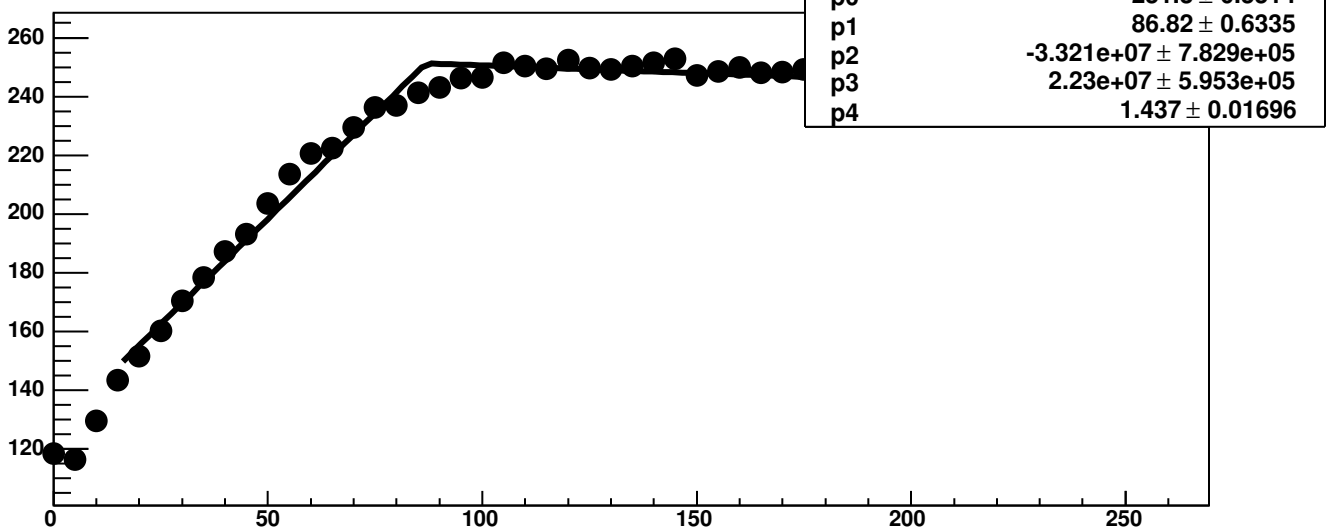
Chip 10, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



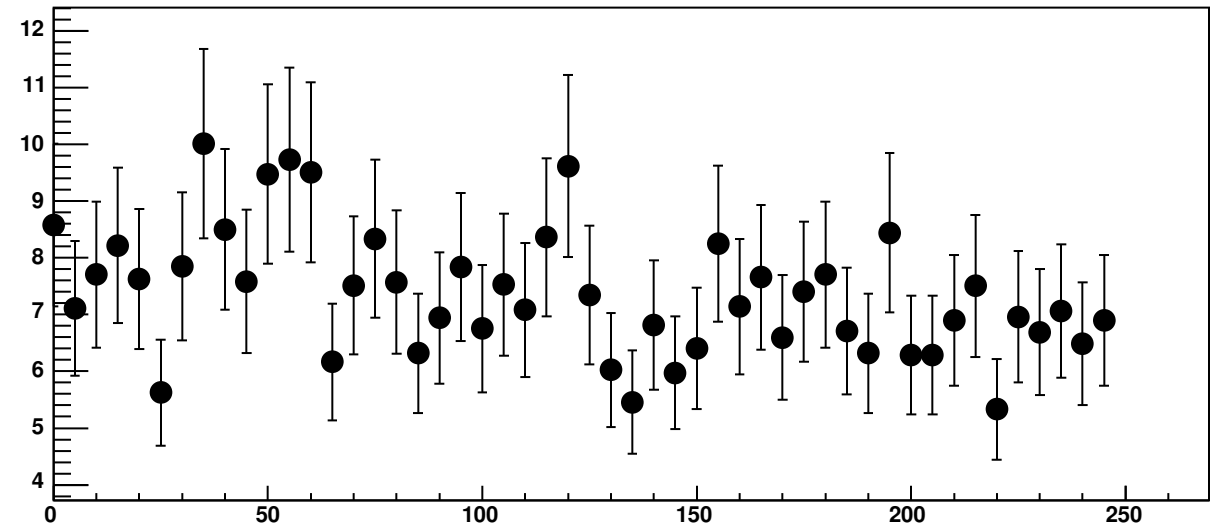
Chip 10, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold



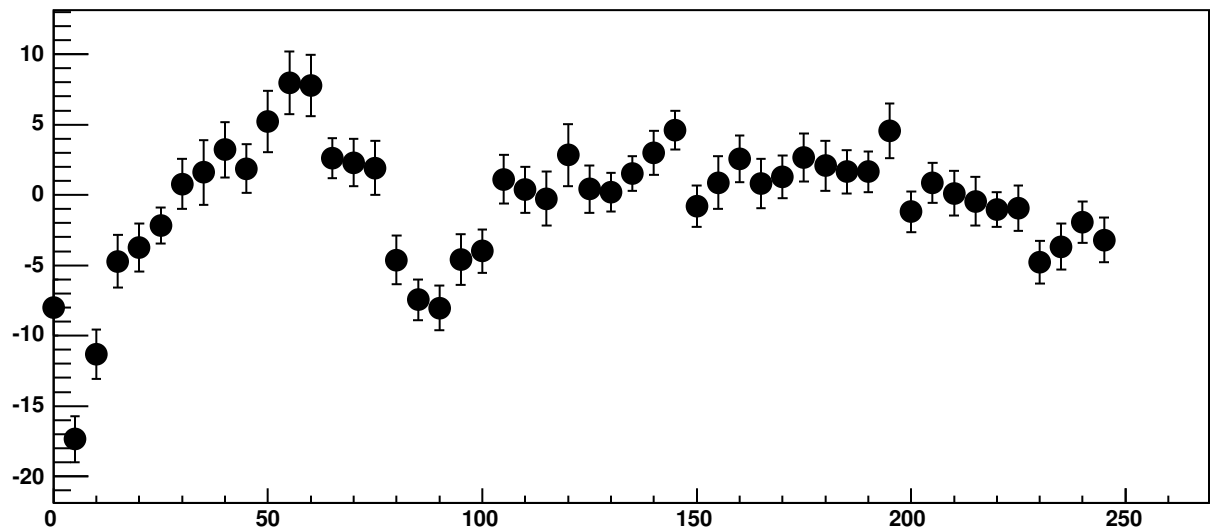
Chip 10, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold



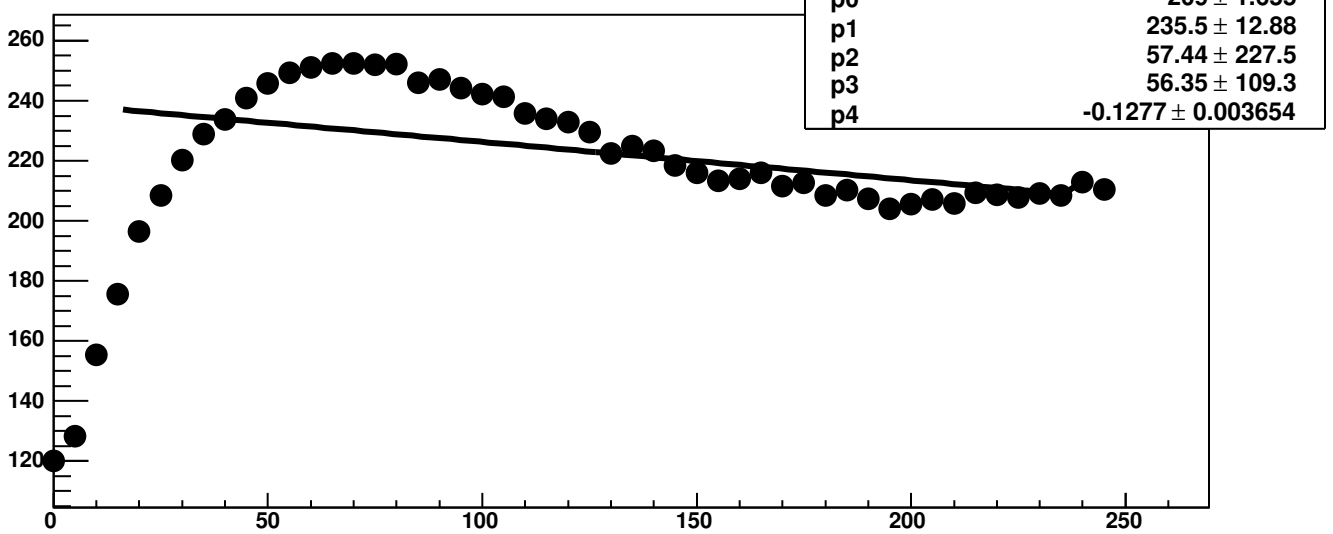
Chip 10, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



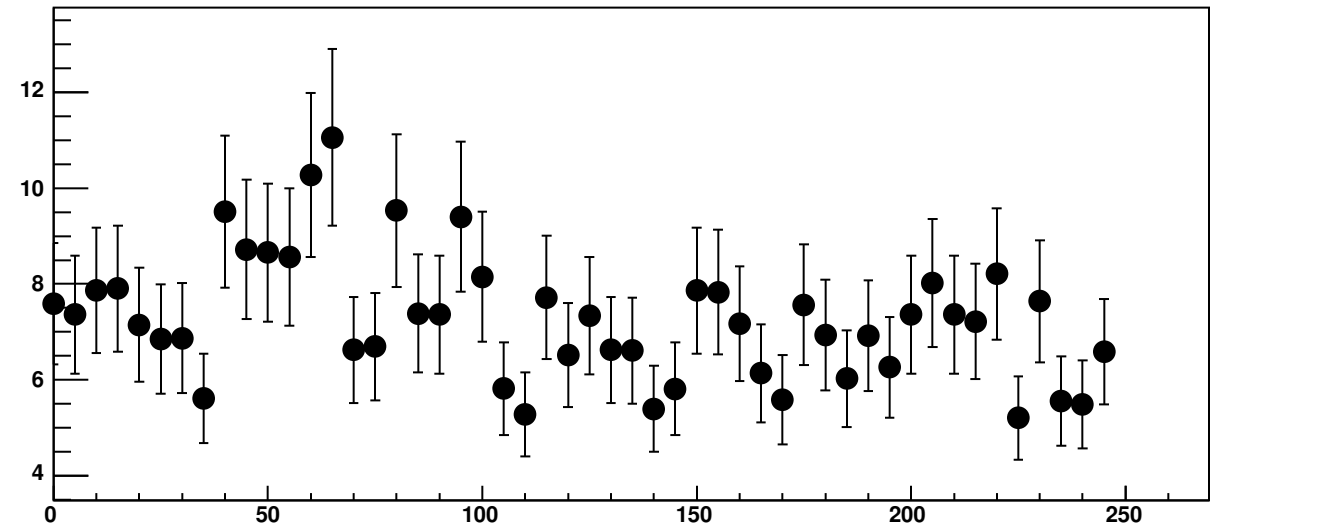
Chip 10, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold



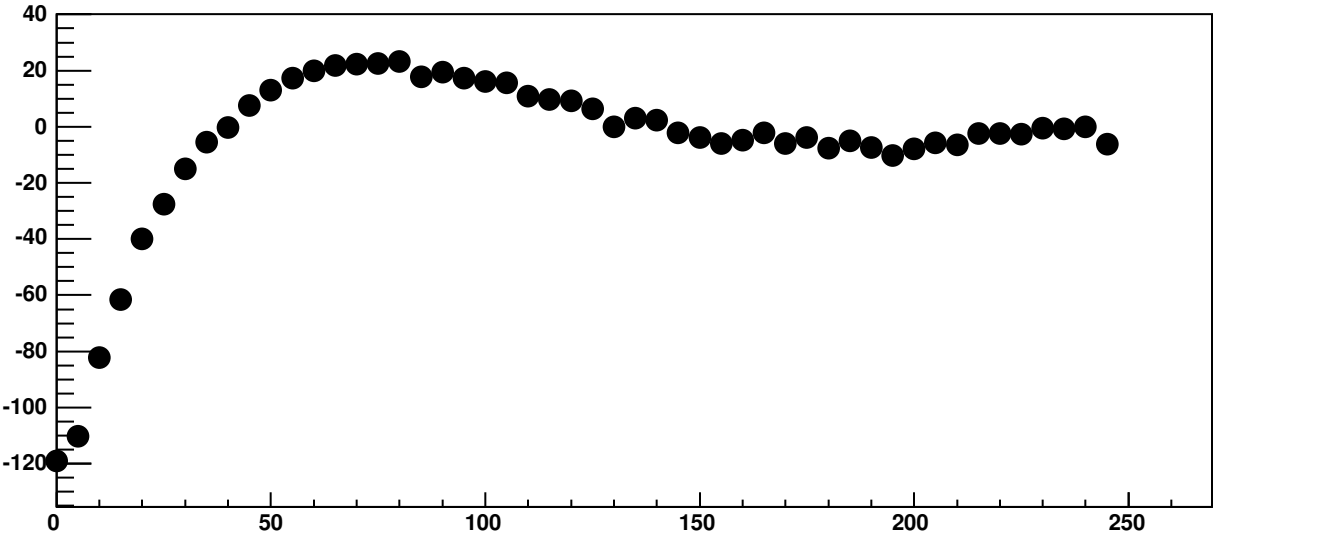
Chip 10, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold

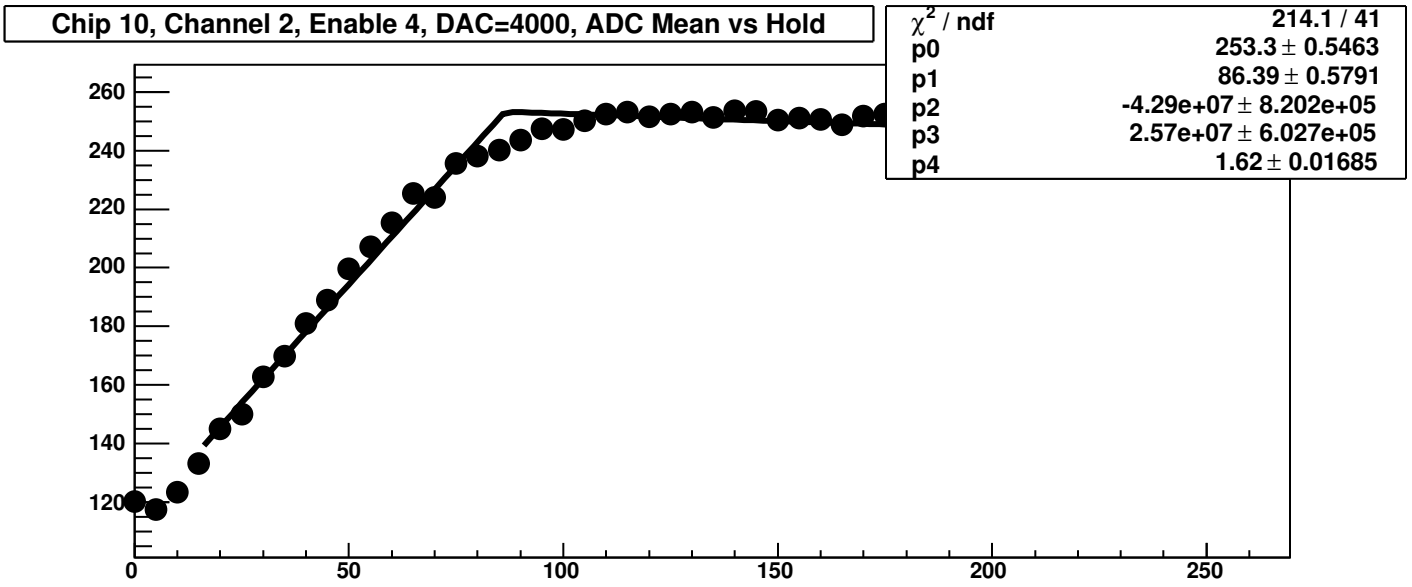


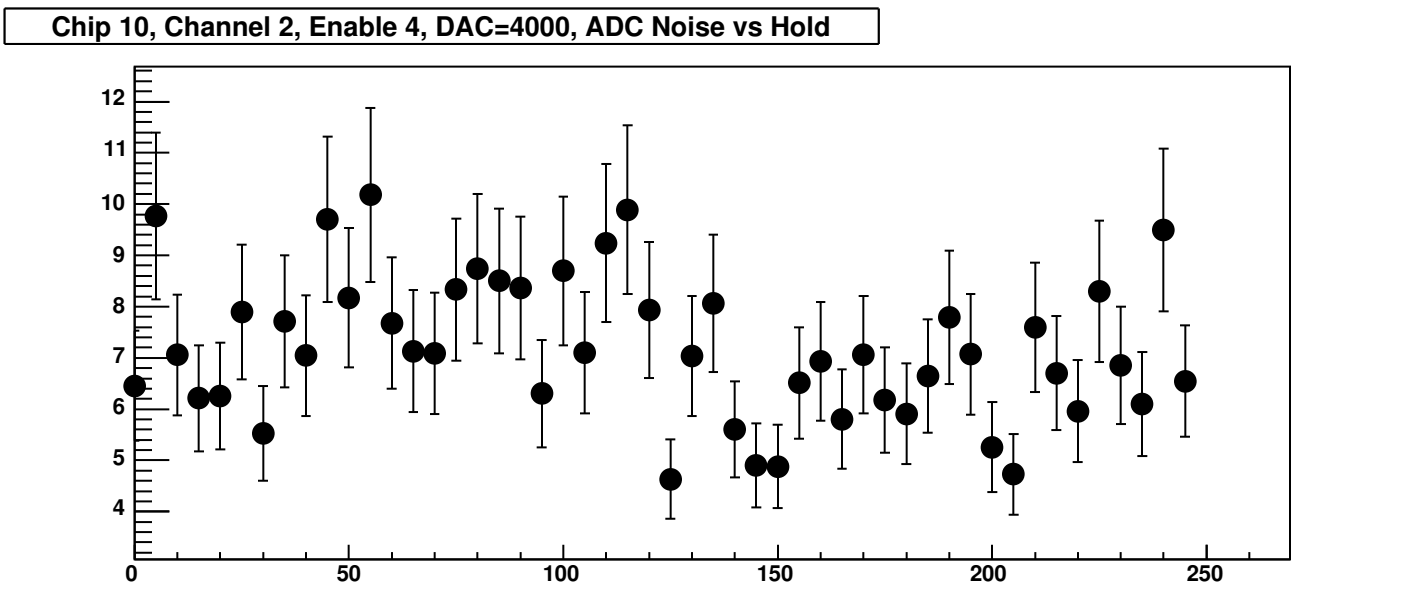
Chip 10, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold

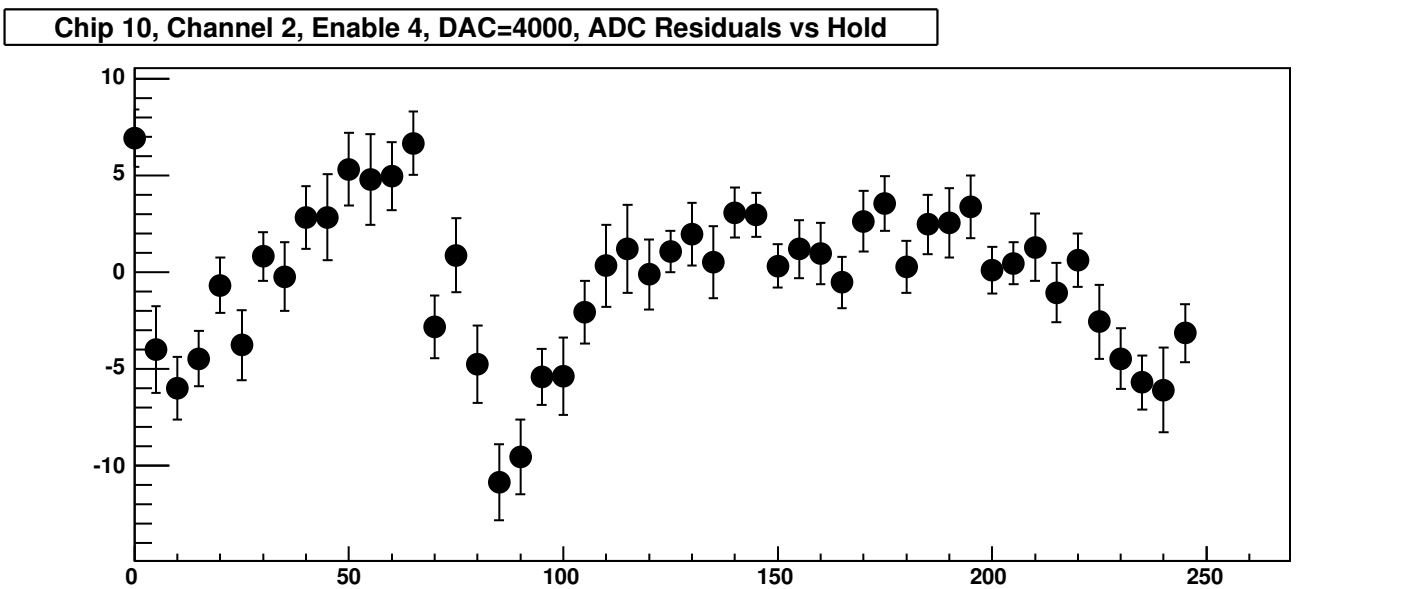


Chip 10, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold

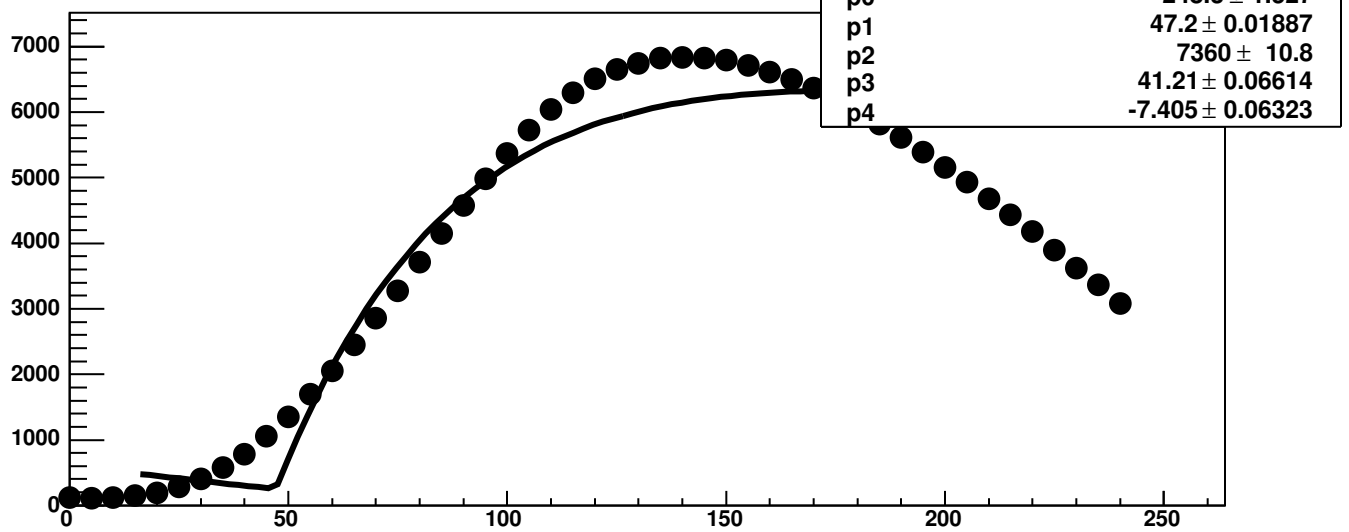



Chip 10, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 214.1 / 41
p0 253.3 ± 0.5463
p1 86.39 ± 0.5791
p2 -4.29e+07 ± 8.202e+05
p3 2.57e+07 ± 6.027e+05
p4 1.62 ± 0.01685
260
240
220
200
180
160
140
120
0
50
100
150
200
250

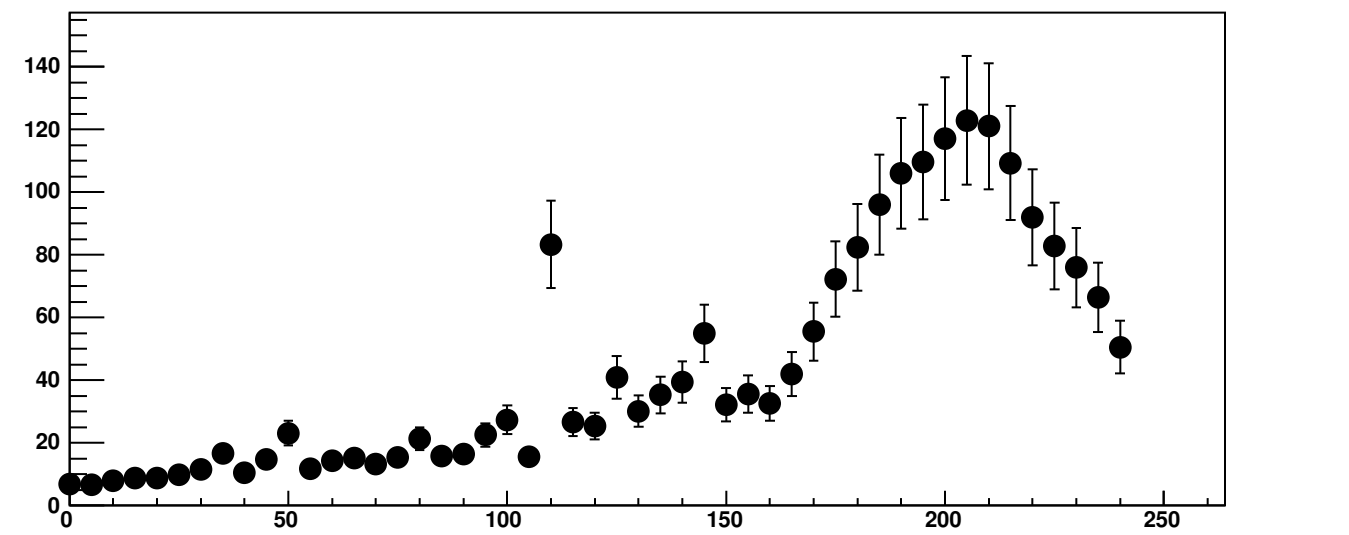

Chip 10, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 10, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250

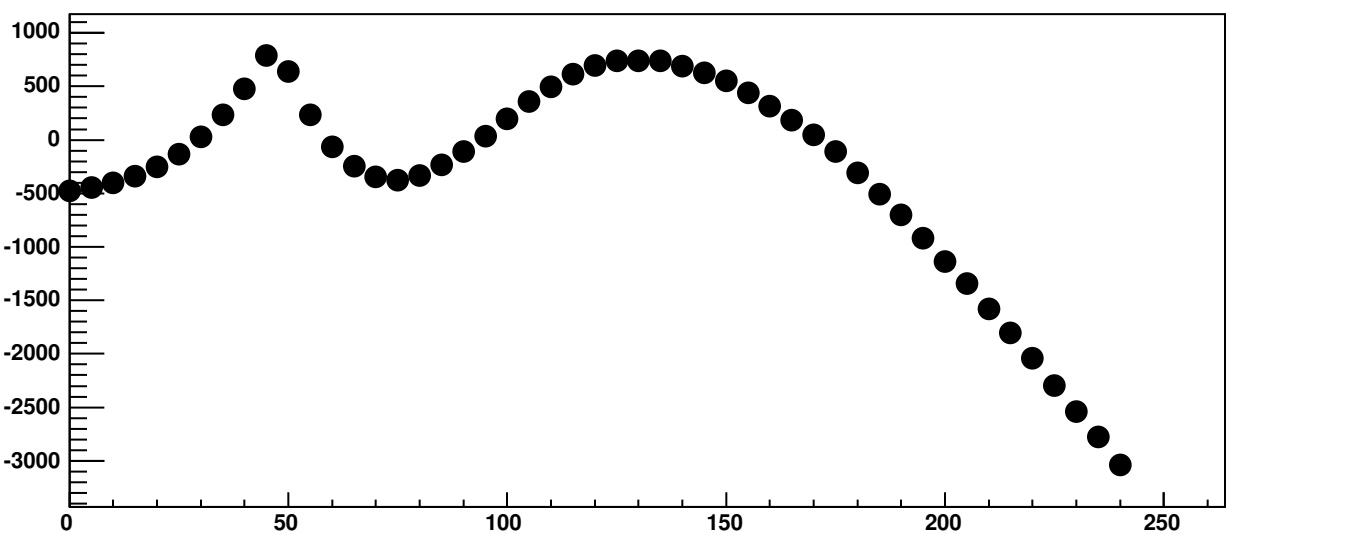
Chip 10, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

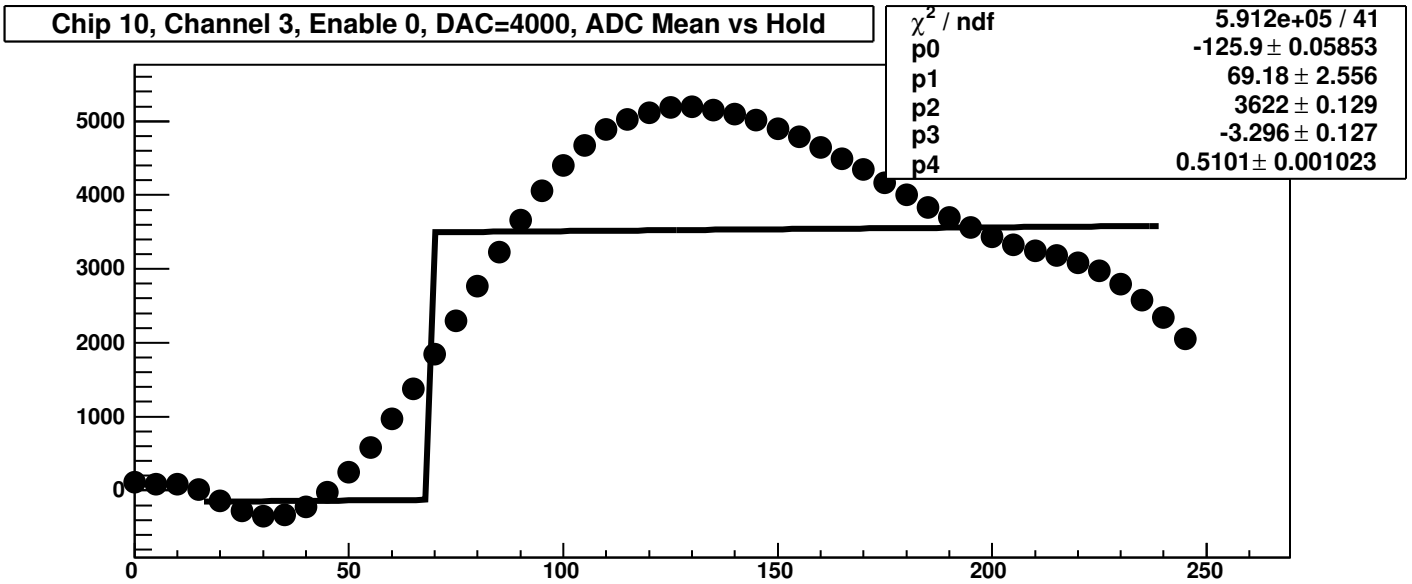


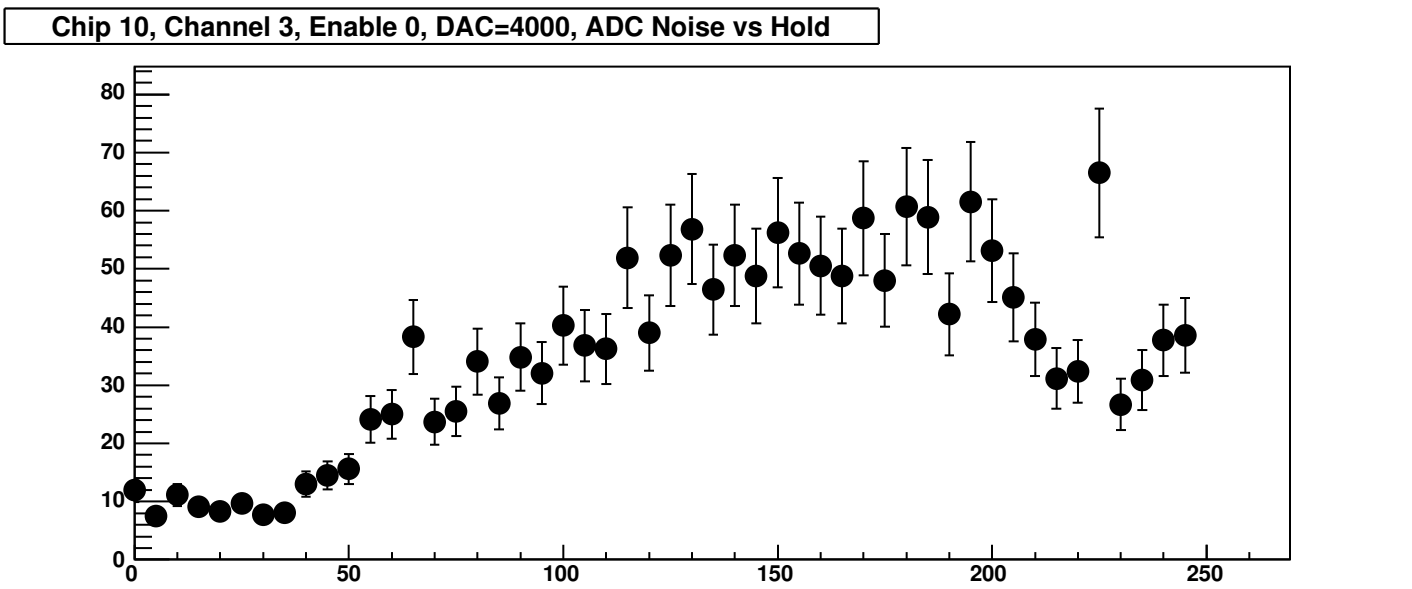
Chip 10, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold

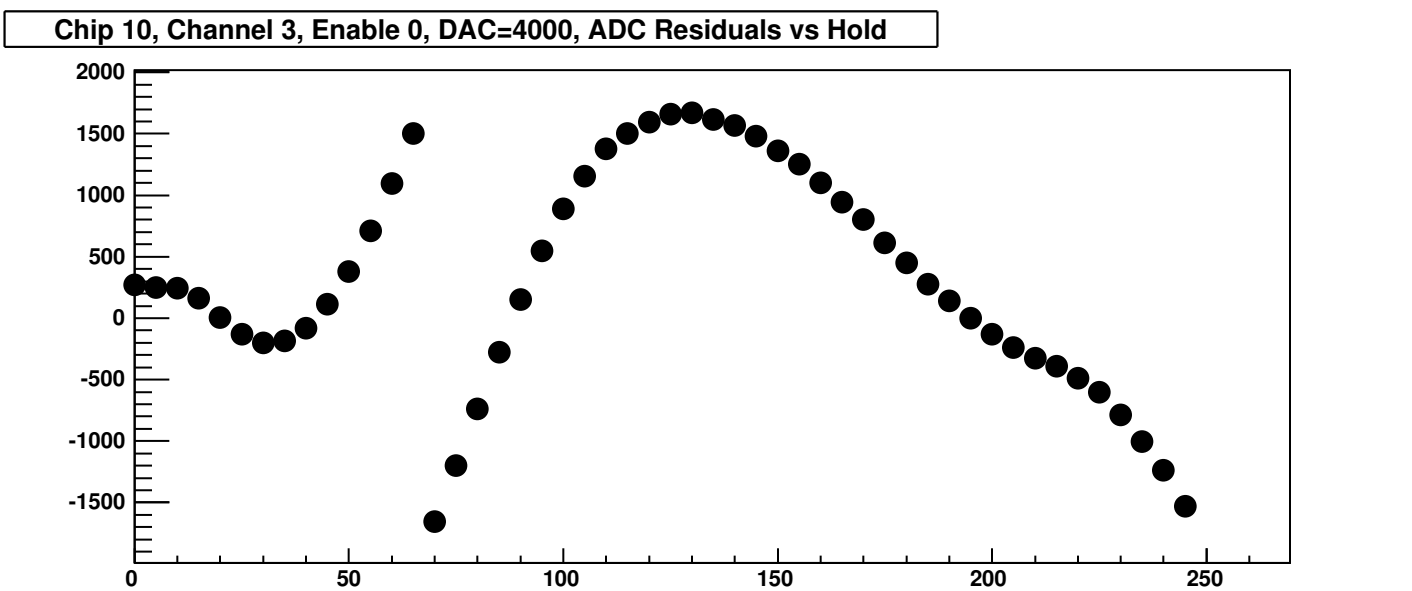


Chip 10, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold



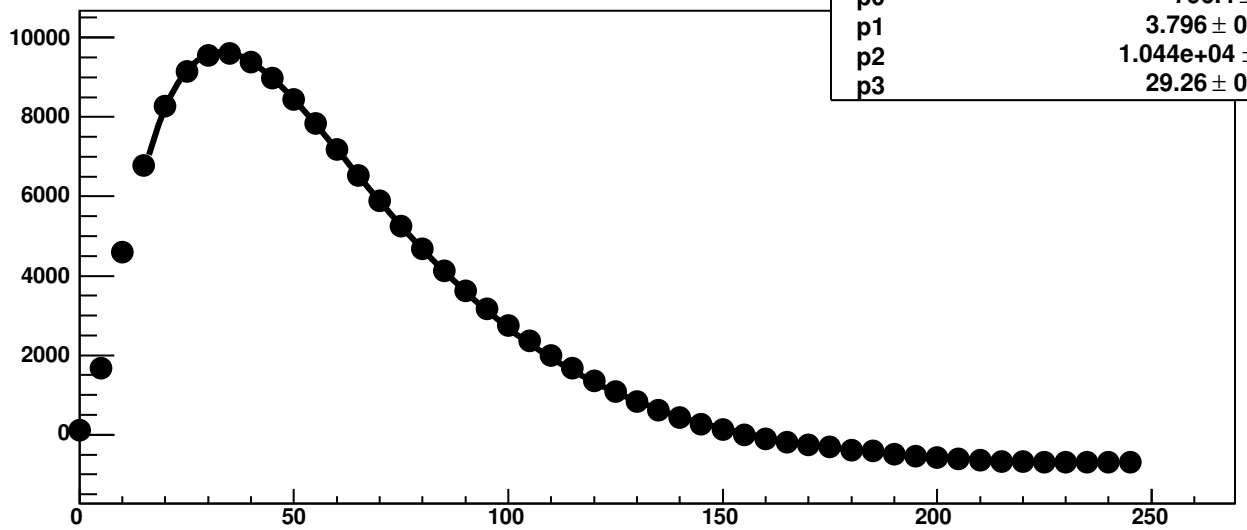

Chip 10, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.912e+05 / 41
p0 -125.9 ± 0.05853
p1 69.18 ± 2.556
p2 3622 ± 0.129
p3 -3.296 ± 0.127
p4 0.5101 ± 0.001023
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 10, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250

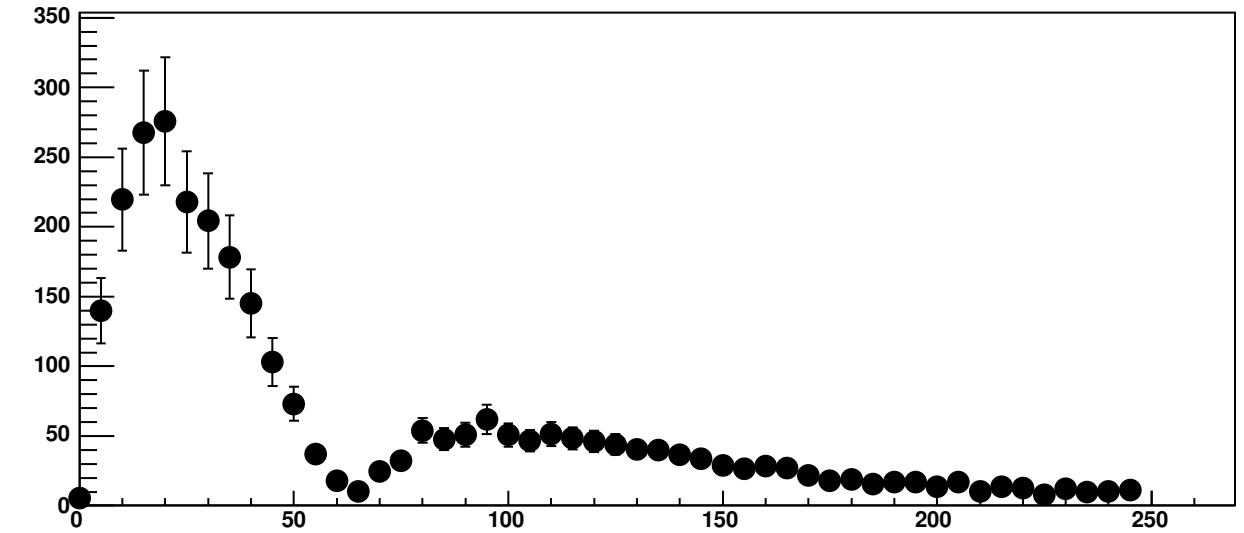

Chip 10, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

Chip 10, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold

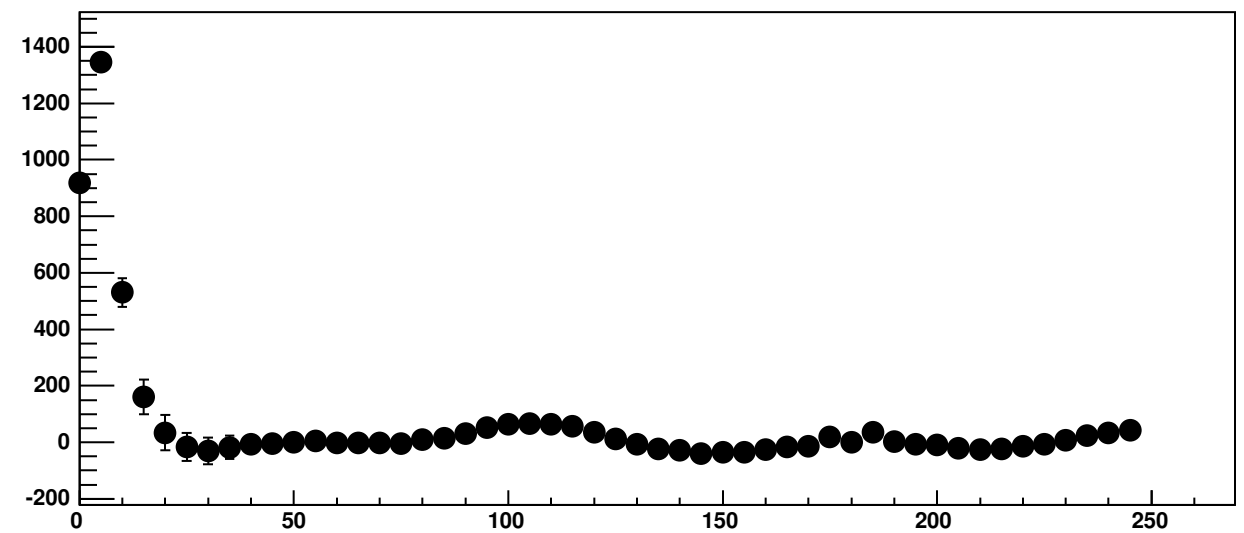
χ^2 / ndf	1013 / 42
p0	-796.4 ± 1.109
p1	3.796 ± 0.08857
p2	1.044e+04 ± 11.17
p3	29.26 ± 0.02694



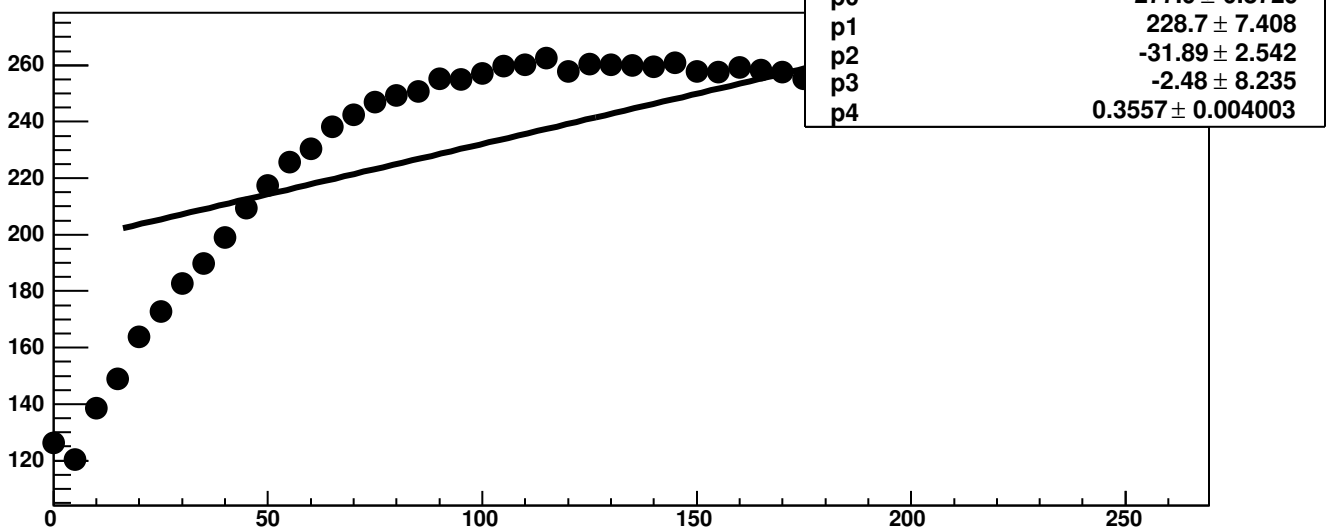
Chip 10, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold



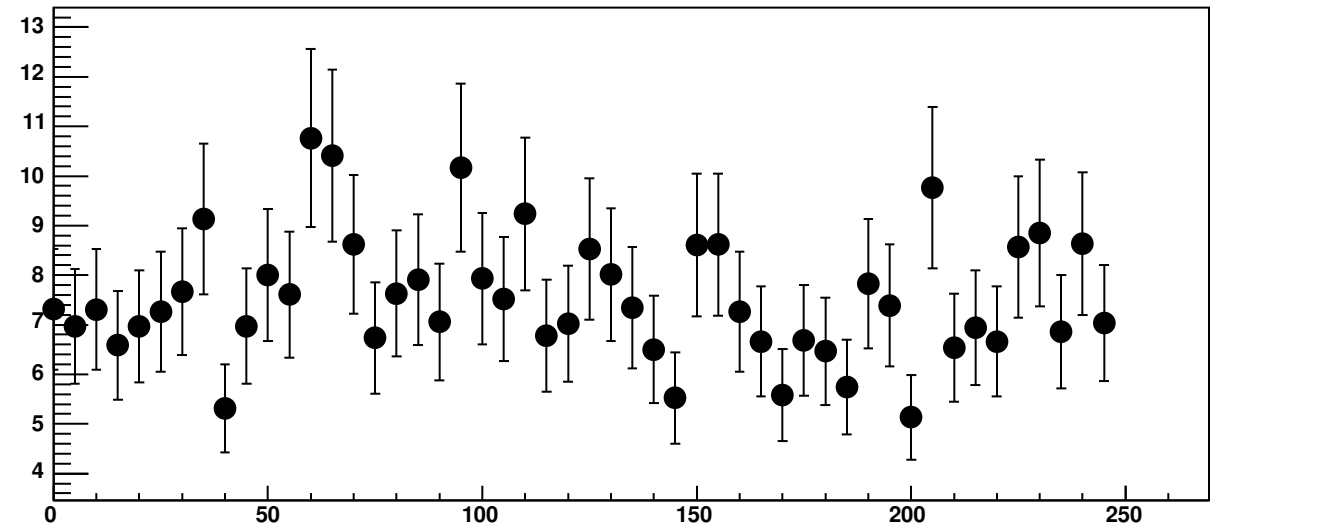
Chip 10, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold



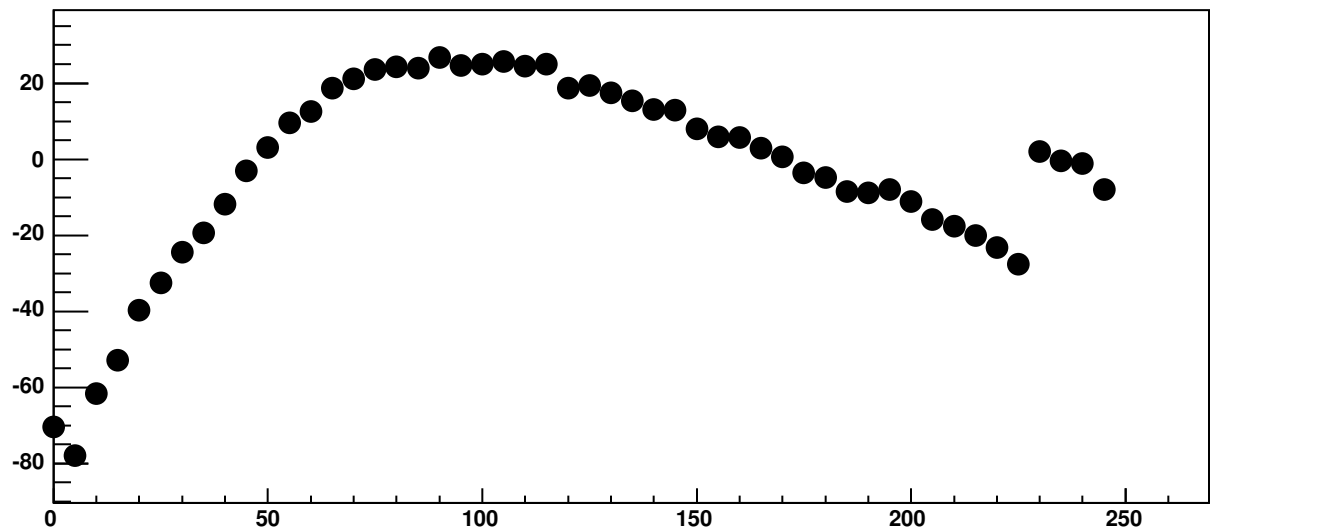
Chip 10, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold



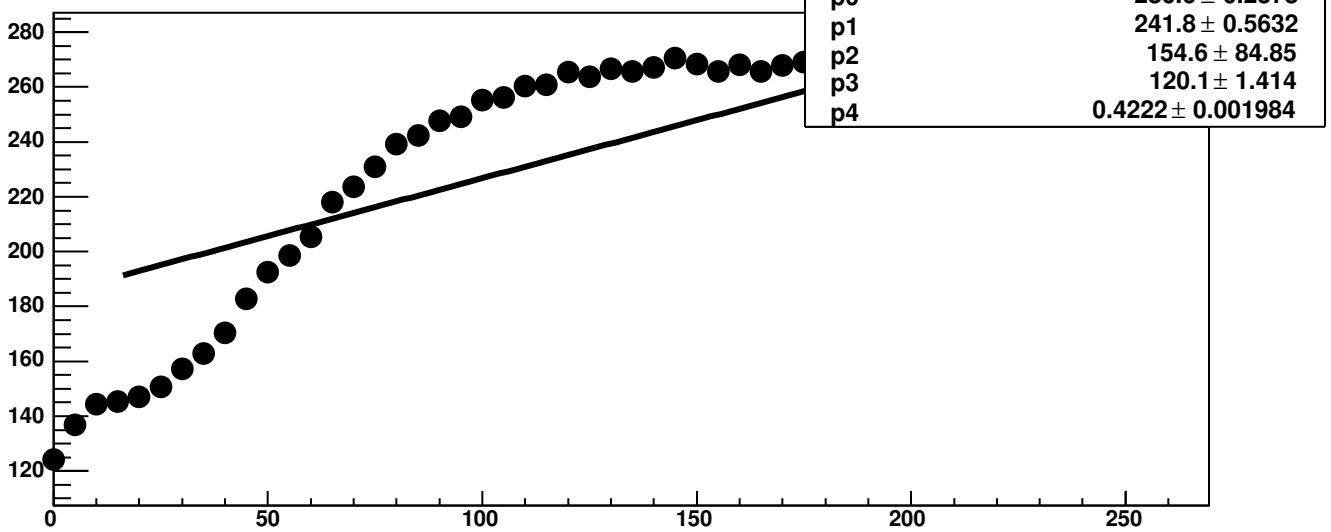
Chip 10, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



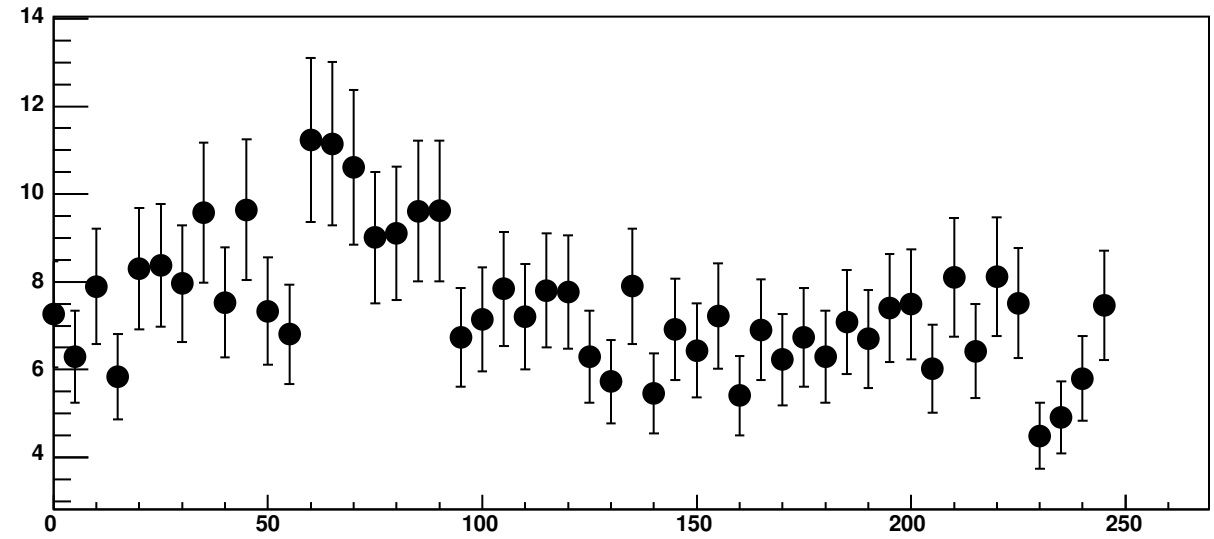
Chip 10, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold



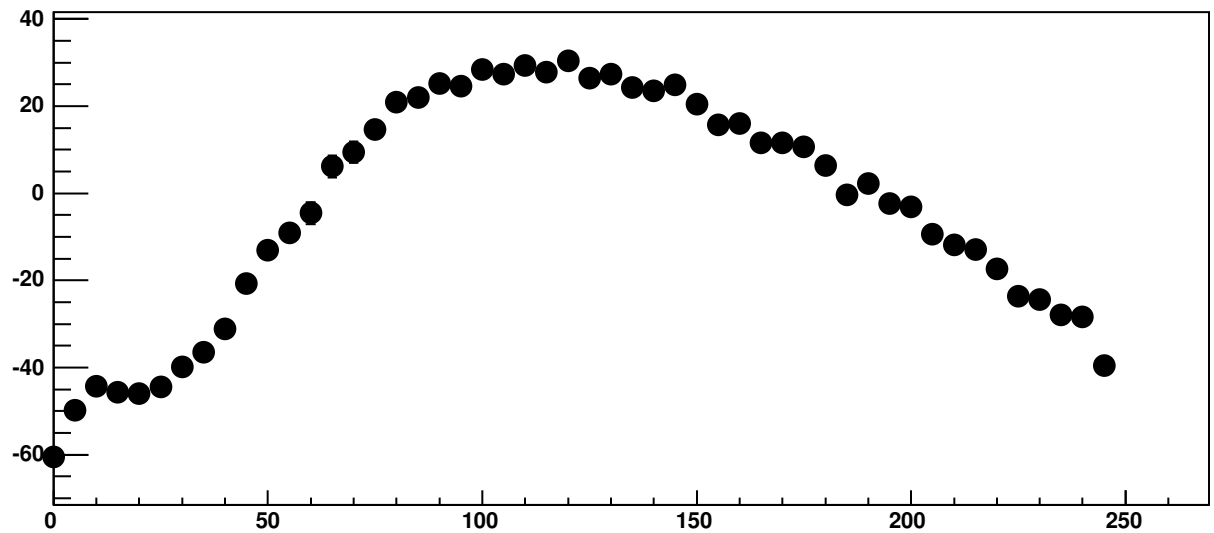
Chip 10, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

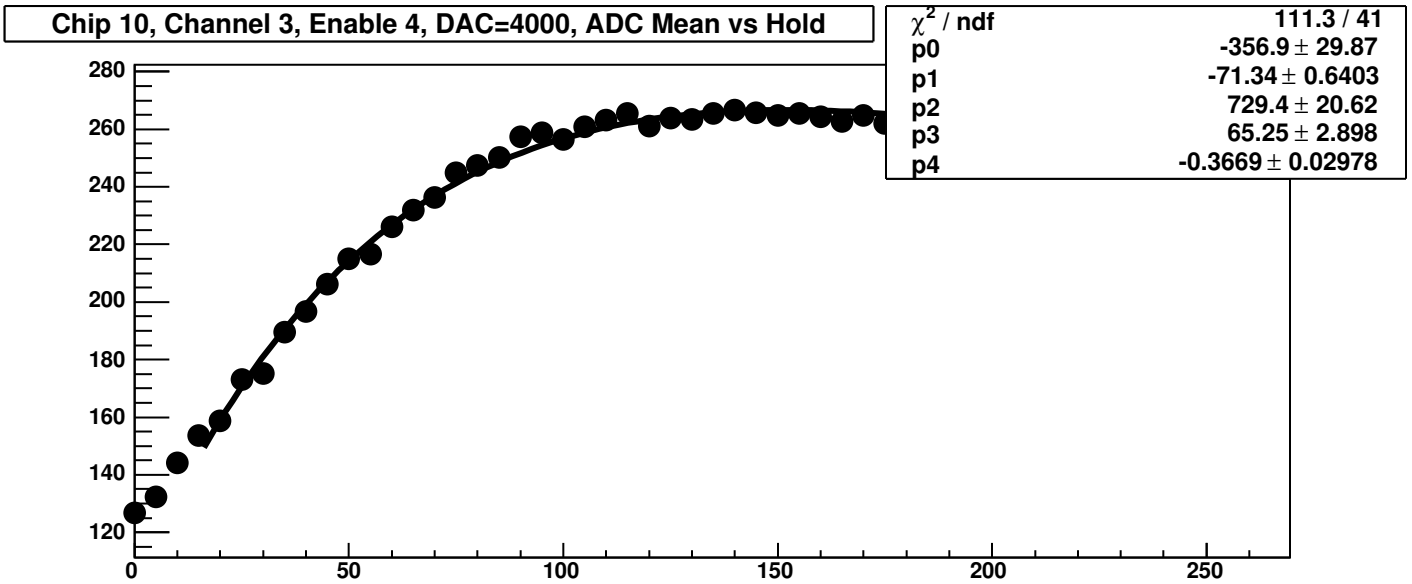


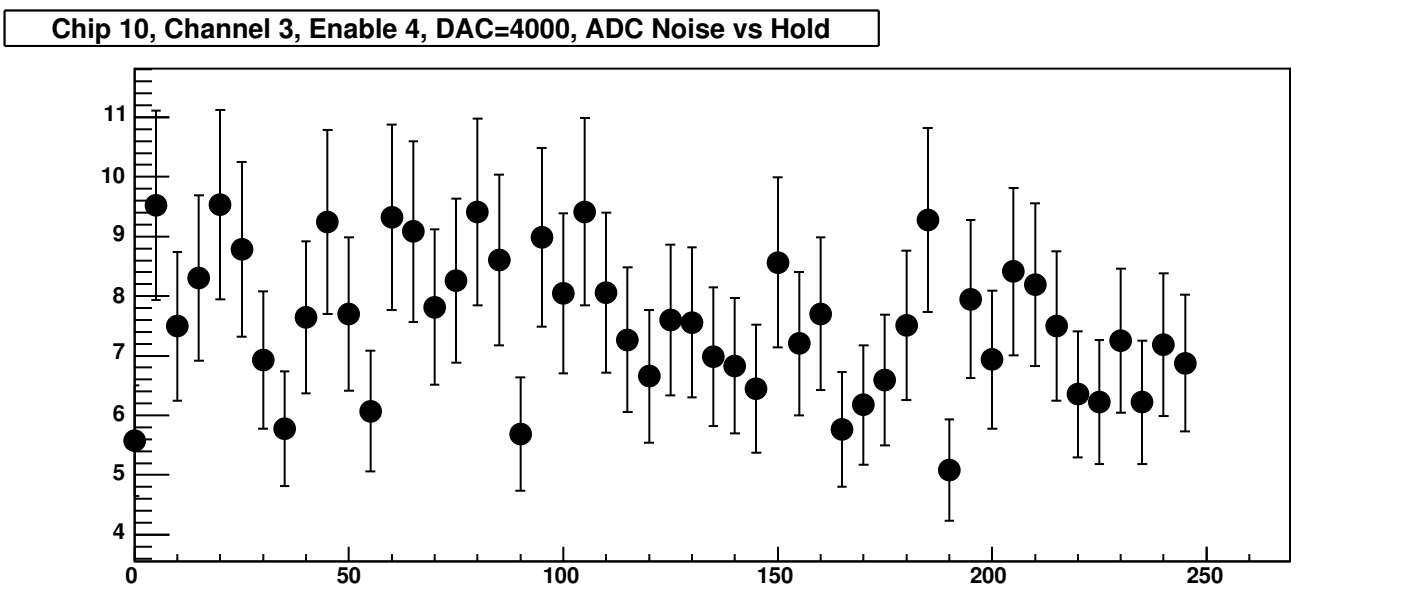
Chip 10, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold

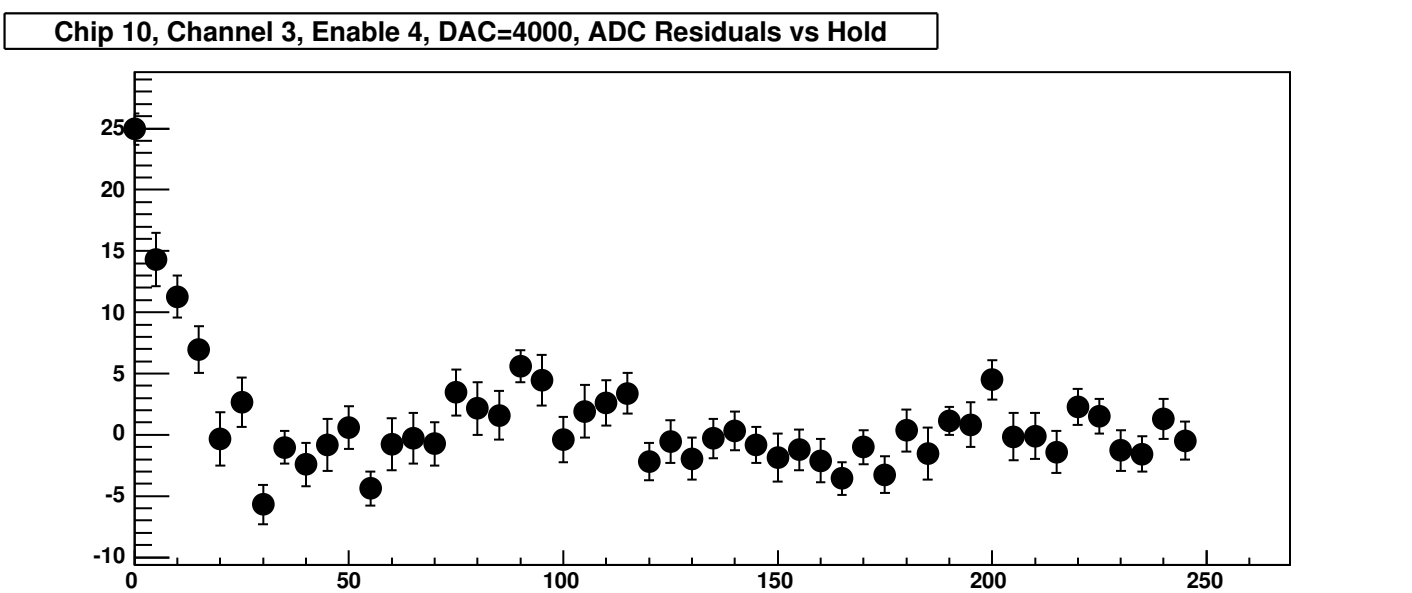


Chip 10, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

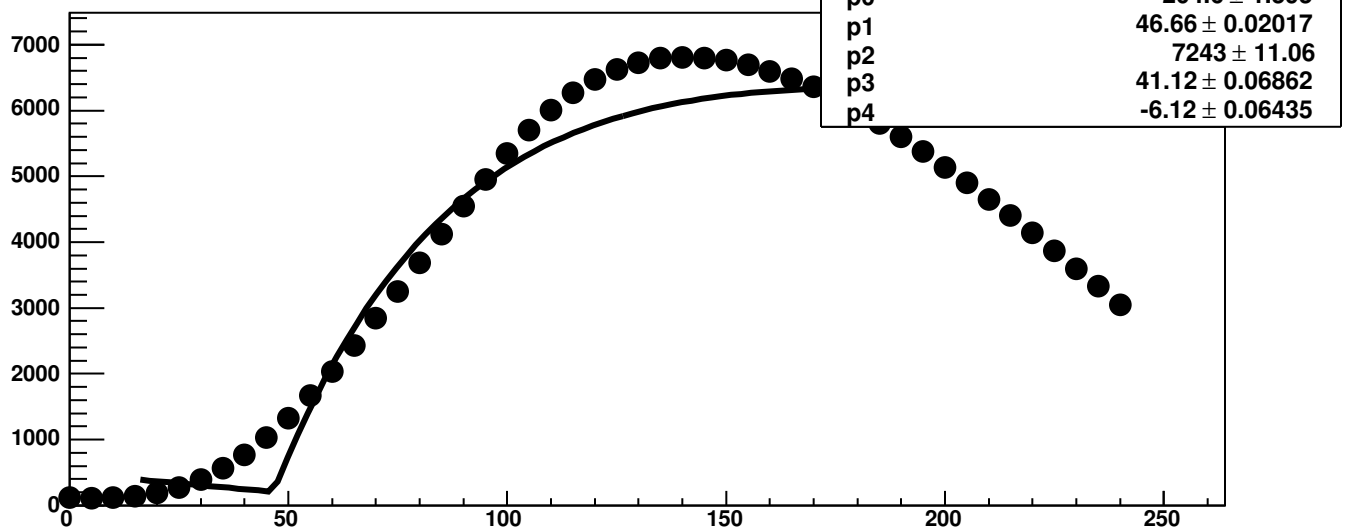



Chip 10, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 111.3 / 41
p0 -356.9 ± 29.87
p1 -71.34 ± 0.6403
p2 729.4 ± 20.62
p3 65.25 ± 2.898
p4 -0.3669 ± 0.02978
280
260
240
220
200
180
160
140
120
0
50
100
150
200
250

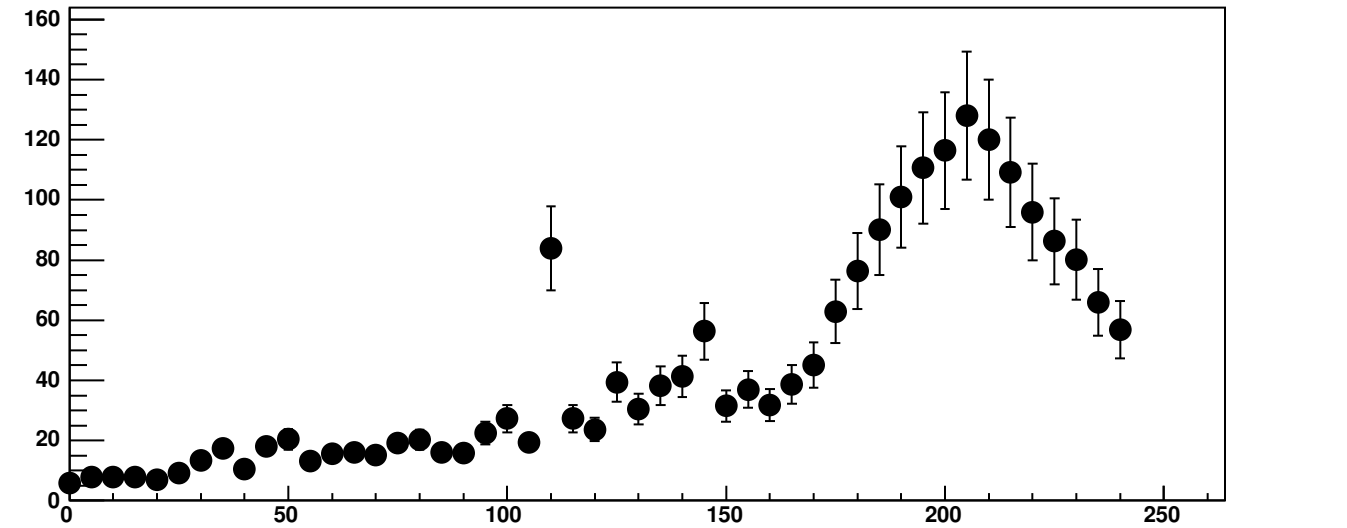

Chip 10, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 10, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
0
50
100
150
200
250

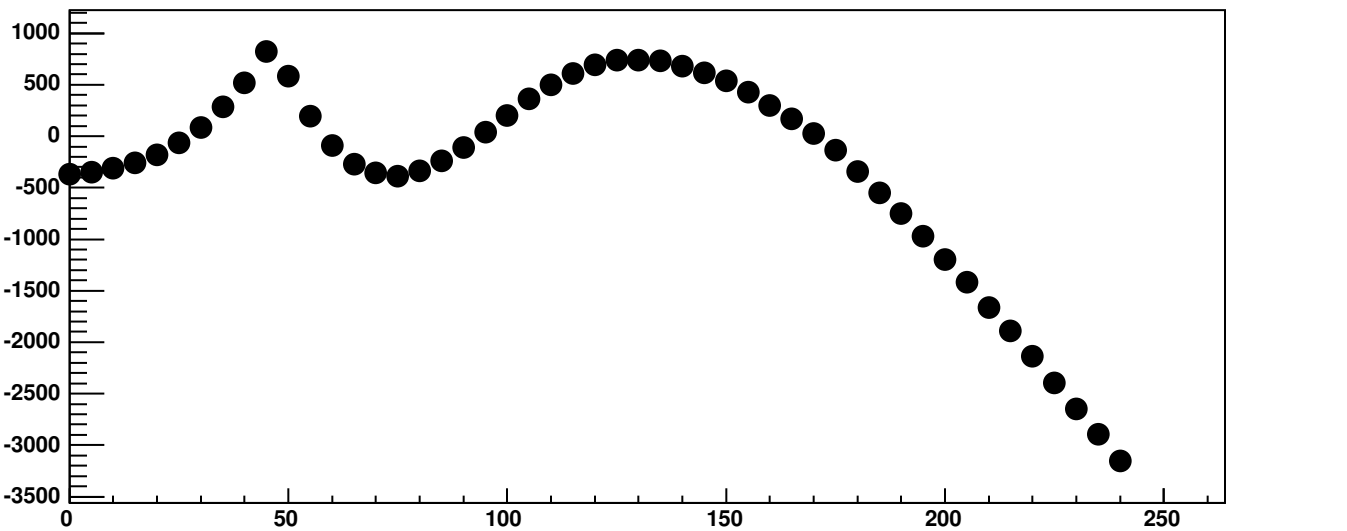
Chip 10, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold

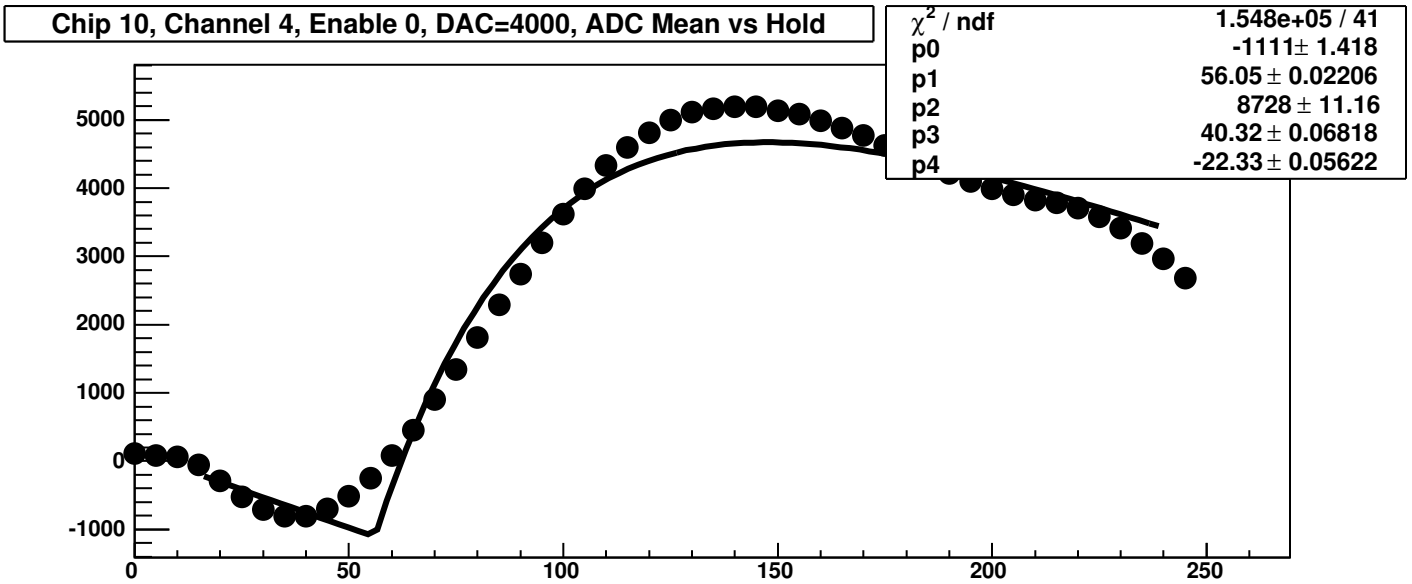


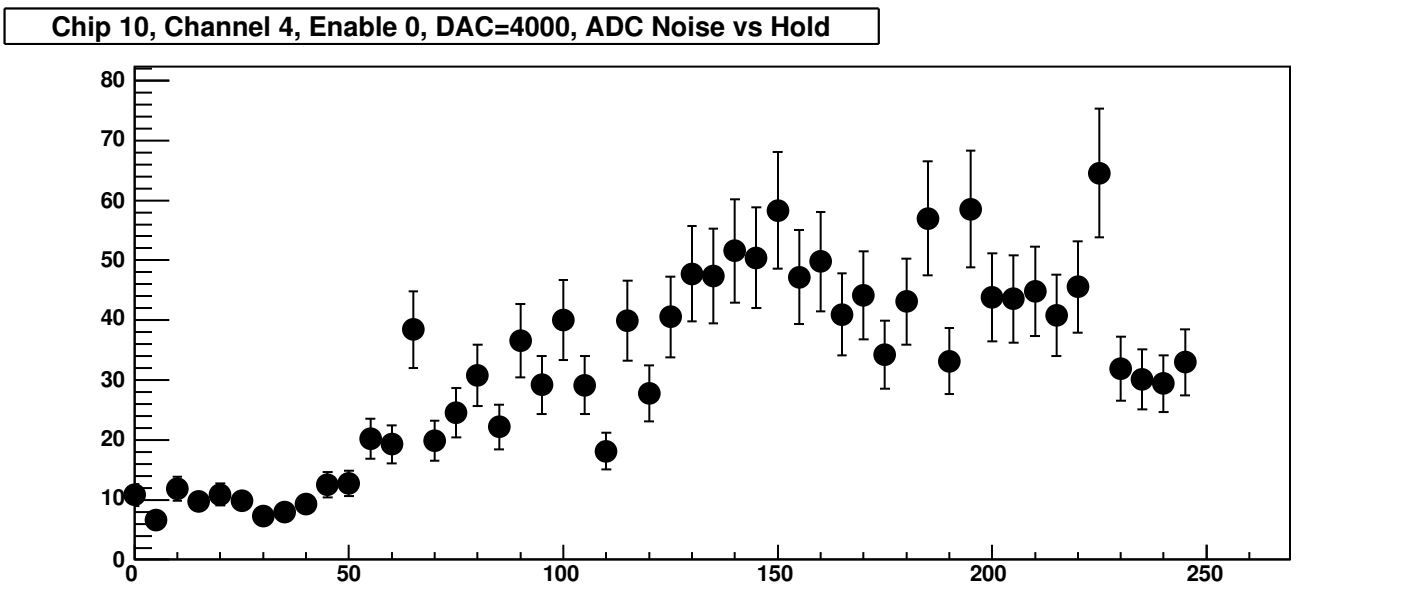
Chip 10, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold

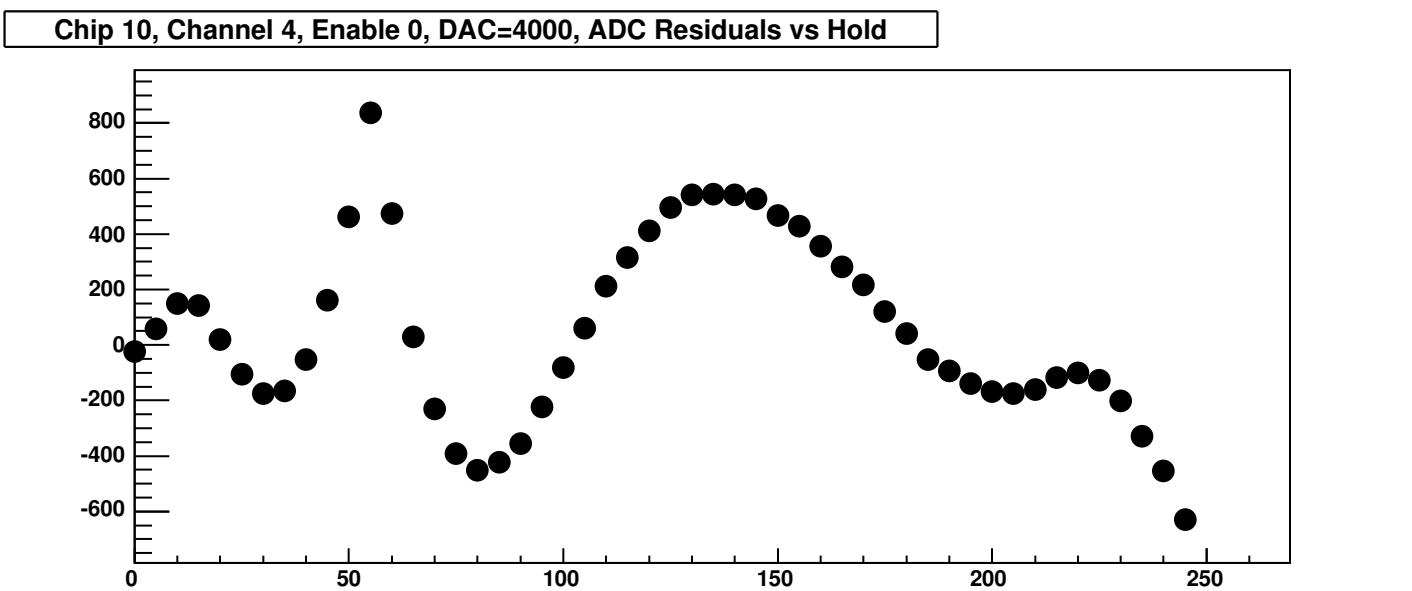


Chip 10, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold

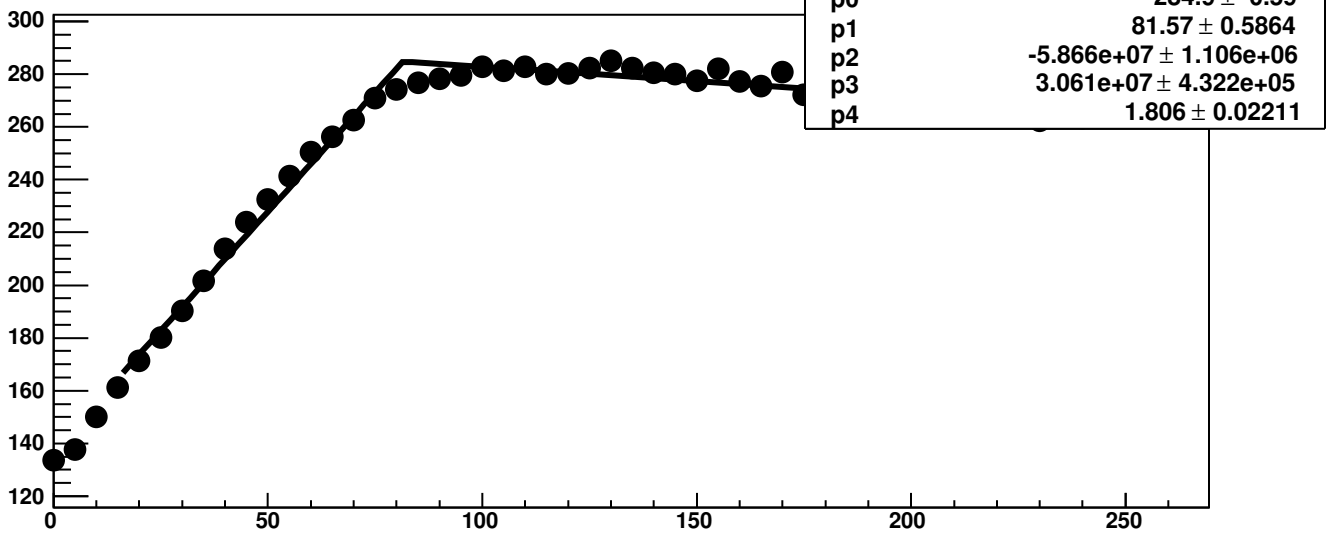



Chip 10, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.548e+05 / 41
p0 -1111 ± 1.418
p1 56.05 ± 0.02206
p2 8728 ± 11.16
p3 40.32 ± 0.06818
p4 -22.33 ± 0.05622
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

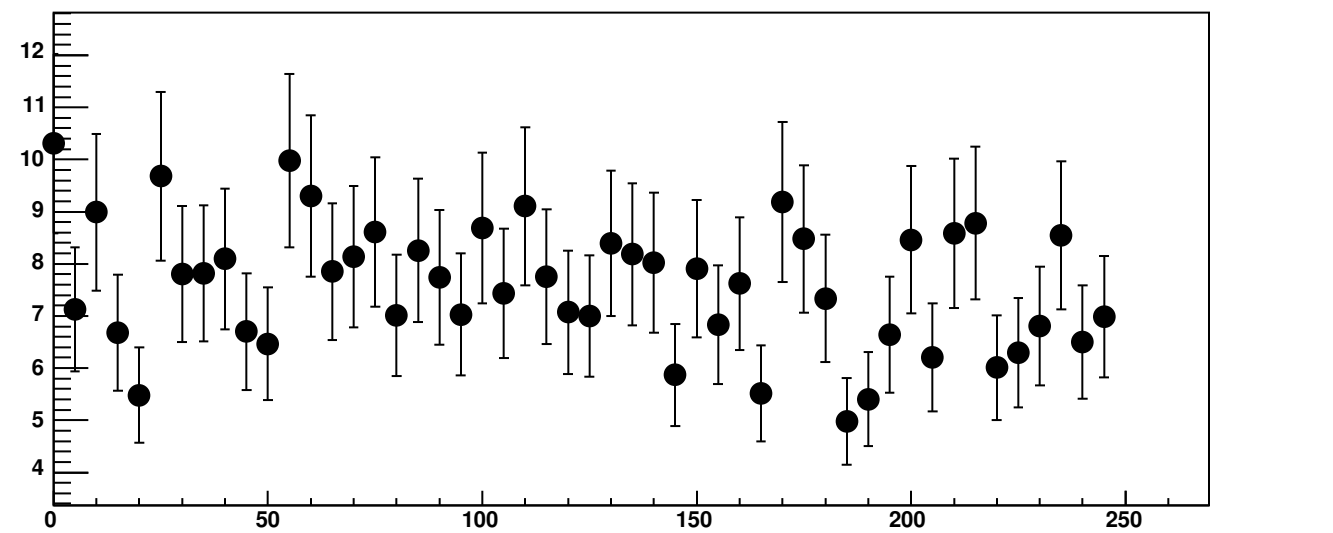

Chip 10, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250


Chip 10, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold
800
600
400
200
0
-200
-400
-600
0
50
100
150
200
250

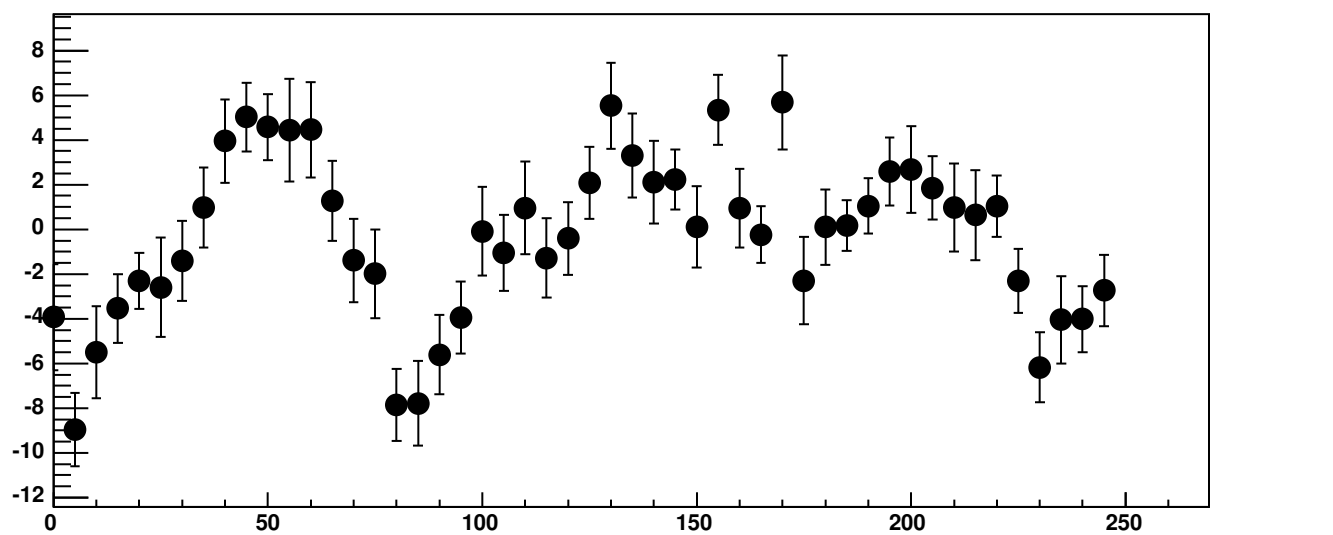
Chip 10, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold

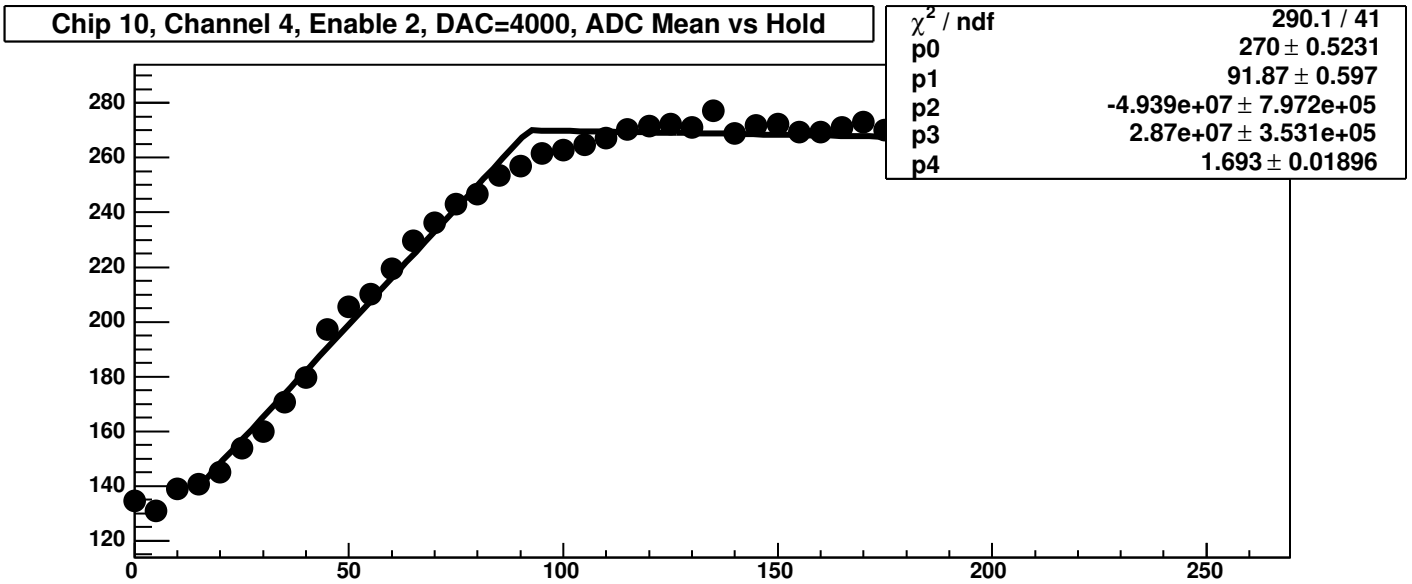


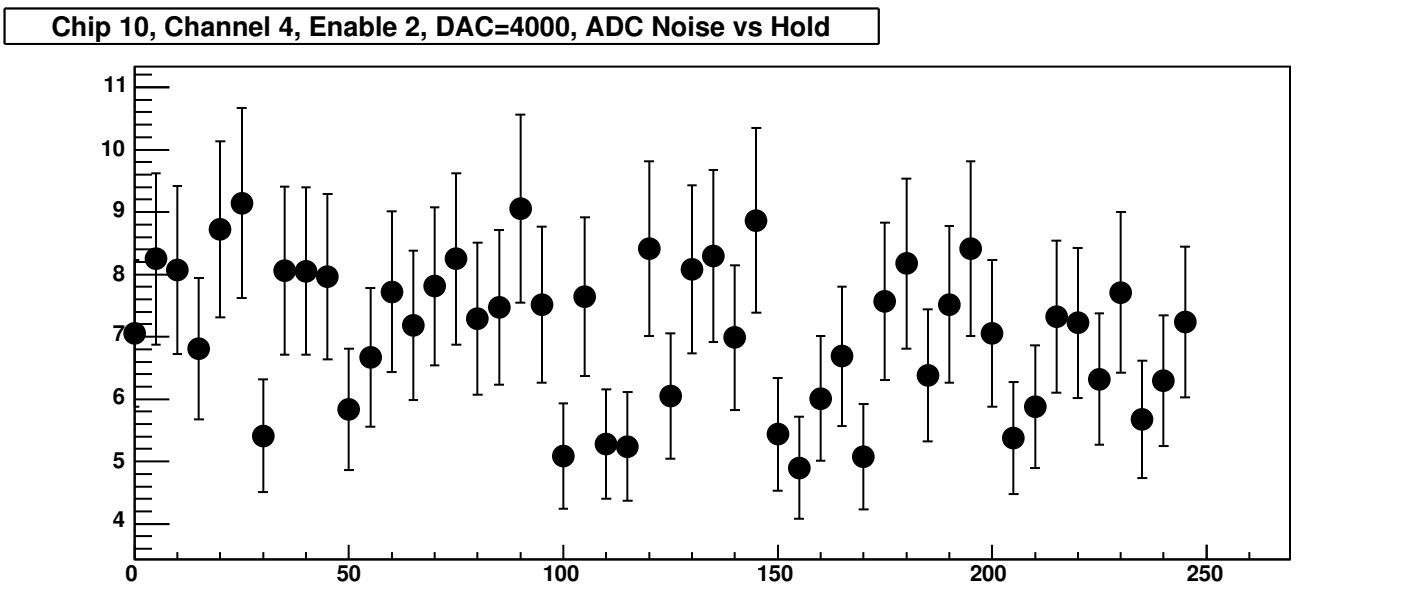
Chip 10, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold

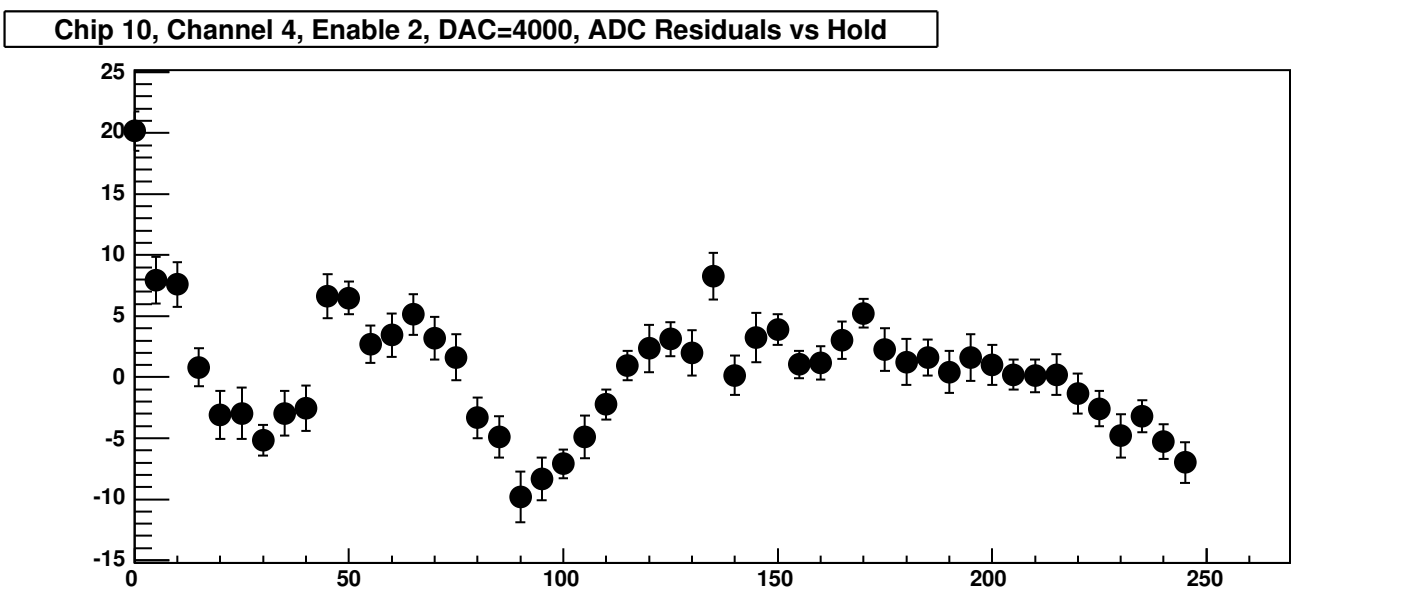


Chip 10, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold

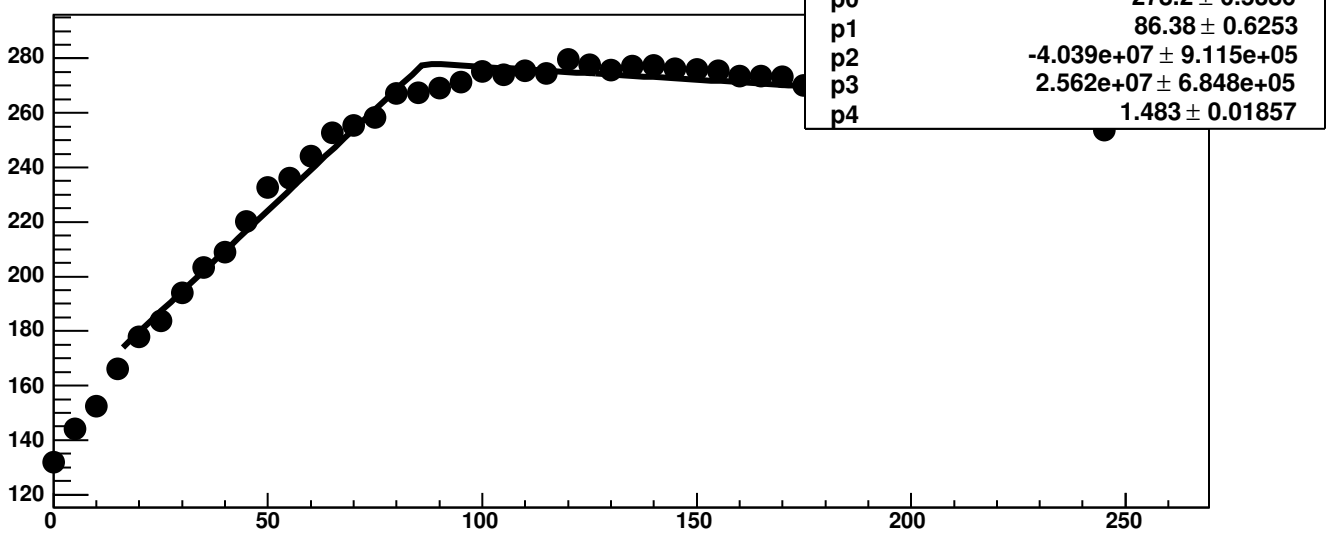



Chip 10, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 290.1 / 41
p0 270 ± 0.5231
p1 91.87 ± 0.597
p2 -4.939e+07 ± 7.972e+05
p3 2.87e+07 ± 3.531e+05
p4 1.693 ± 0.01896
280
260
240
220
200
180
160
140
120
0
50
100
150
200
250

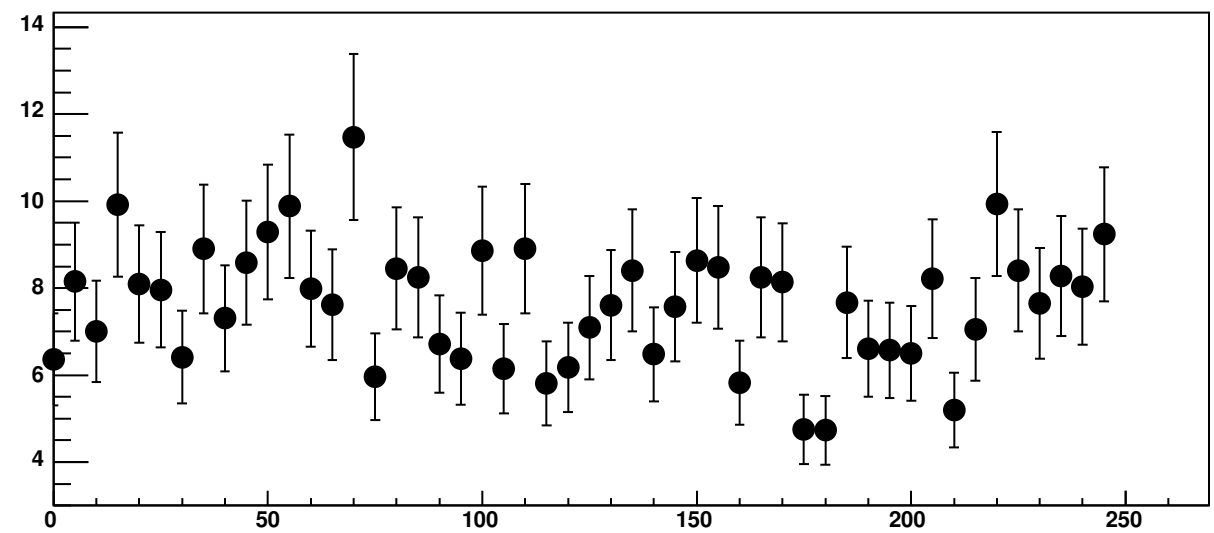

Chip 10, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 10, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

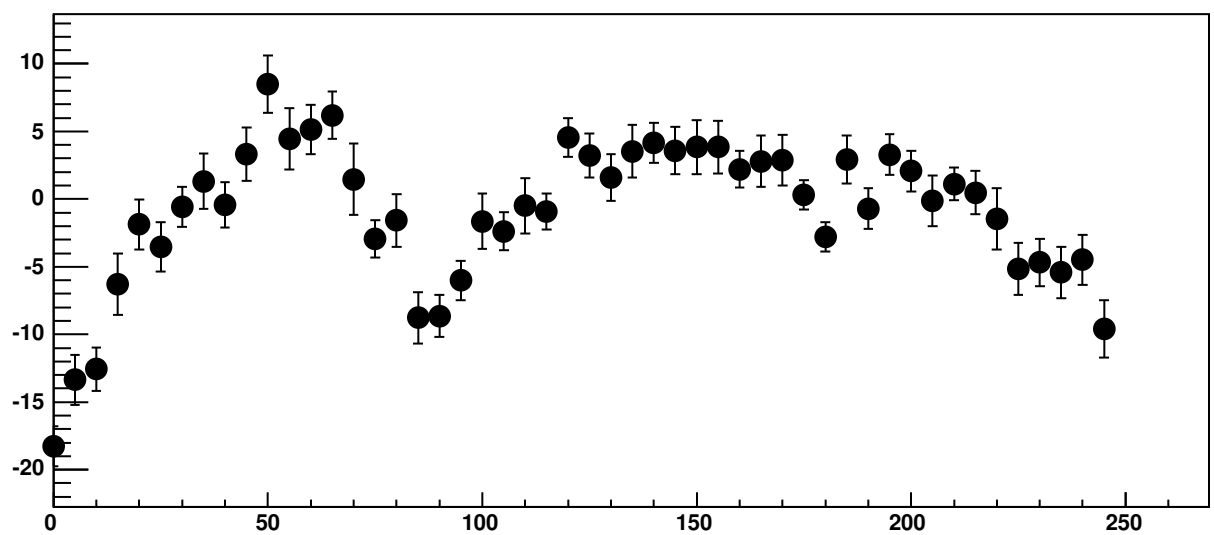
Chip 10, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold



Chip 10, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold

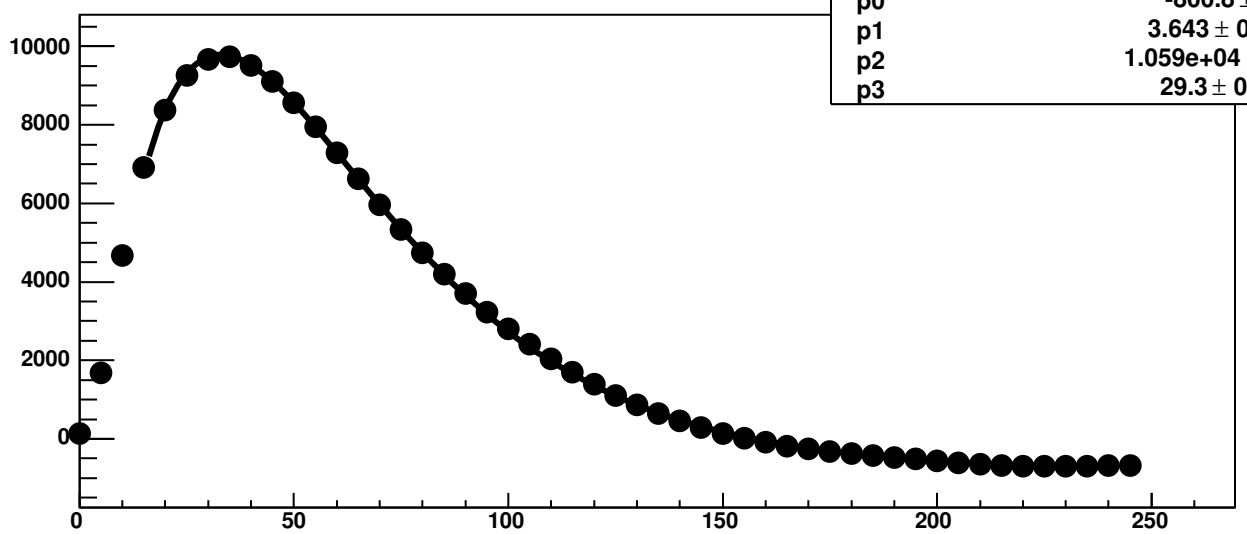


Chip 10, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

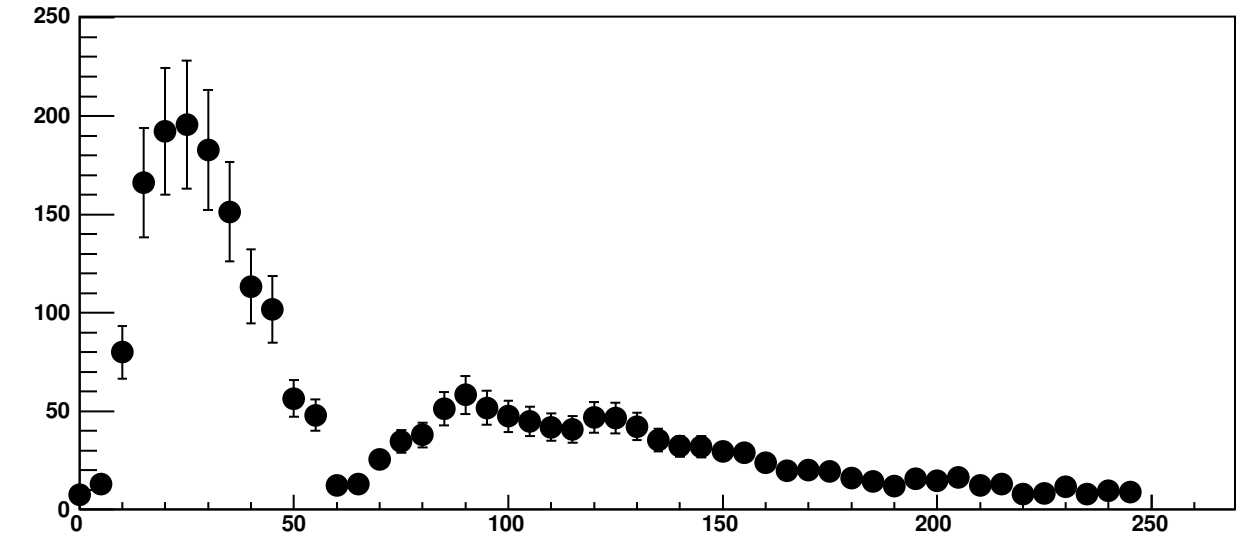


Chip 10, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold

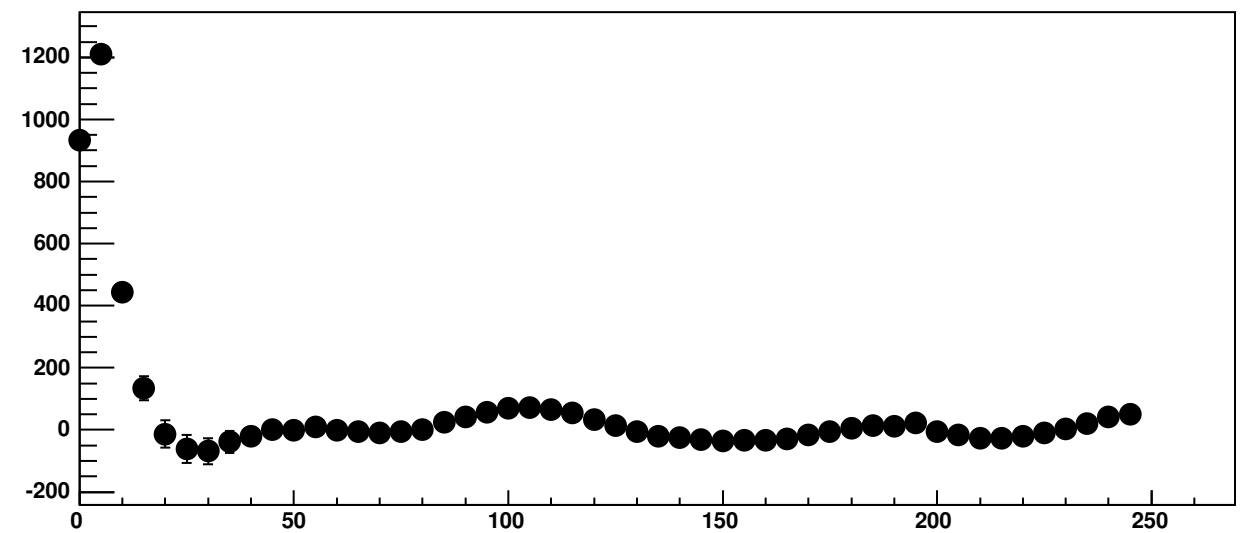
χ^2 / ndf	1337 / 42
p0	-800.8 ± 1.003
p1	3.643 ± 0.06421
p2	1.059e+04 ± 8.35
p3	29.3 ± 0.02289



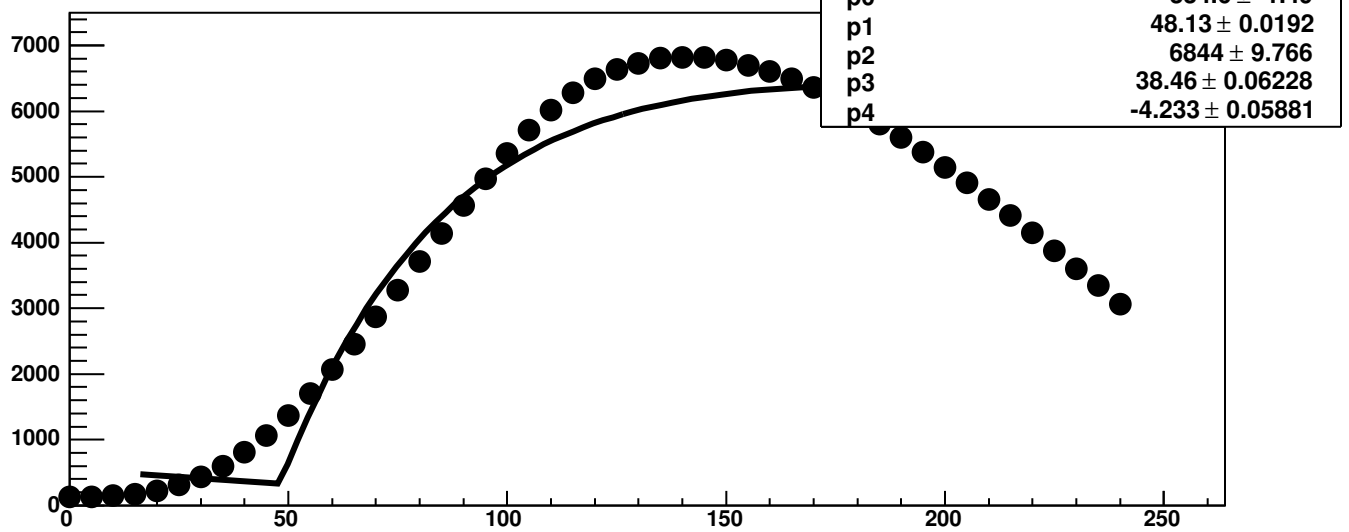
Chip 10, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold



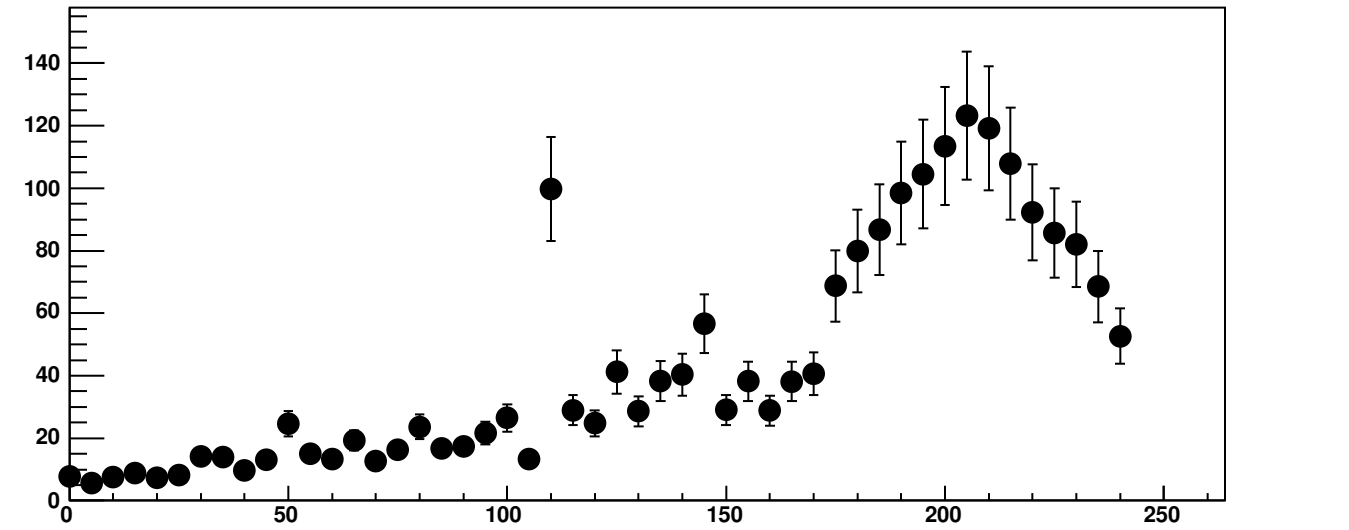
Chip 10, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold



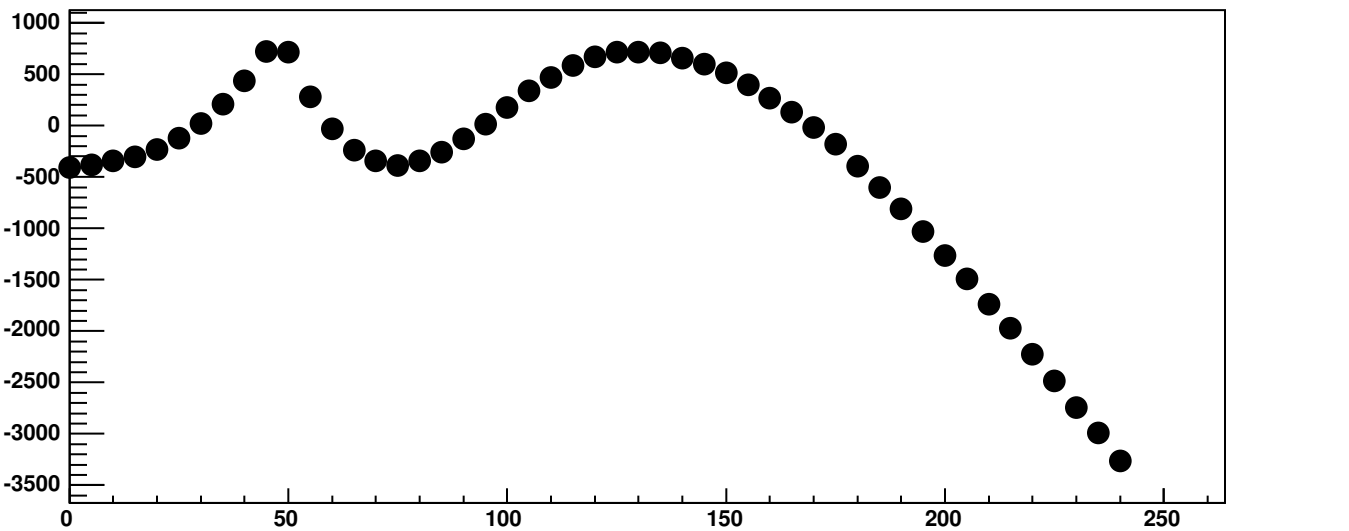
Chip 10, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold



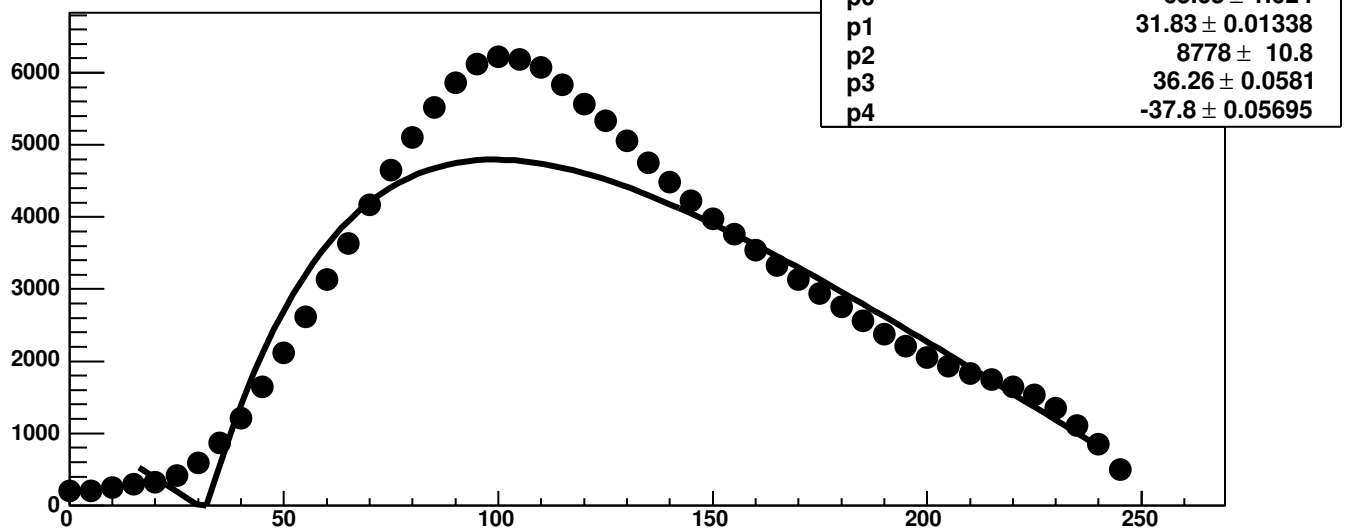
Chip 10, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold



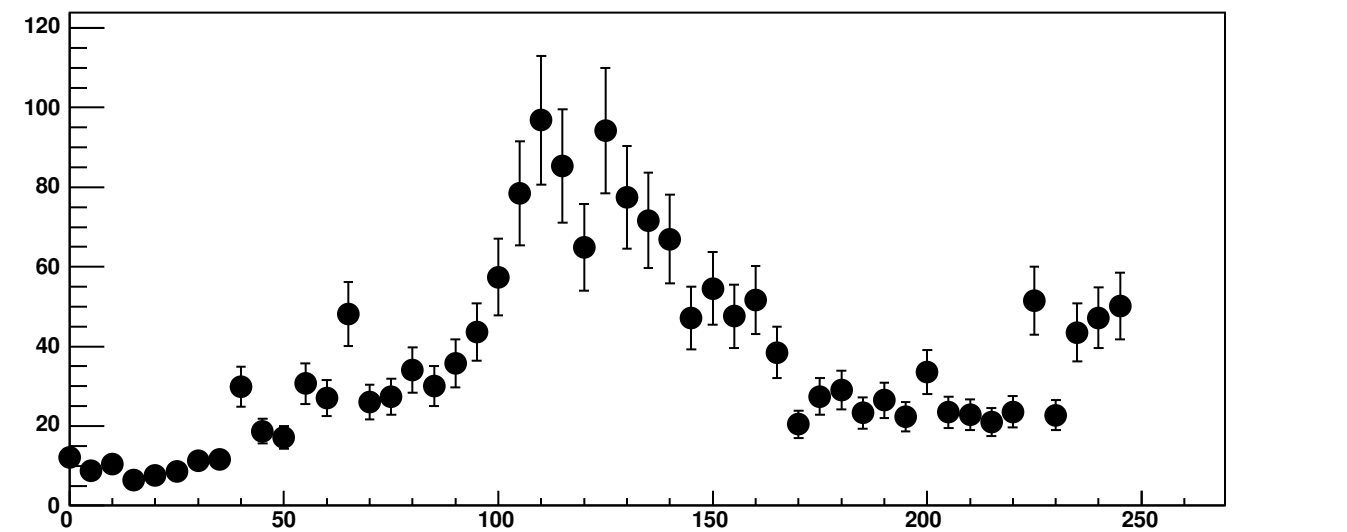
Chip 10, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold



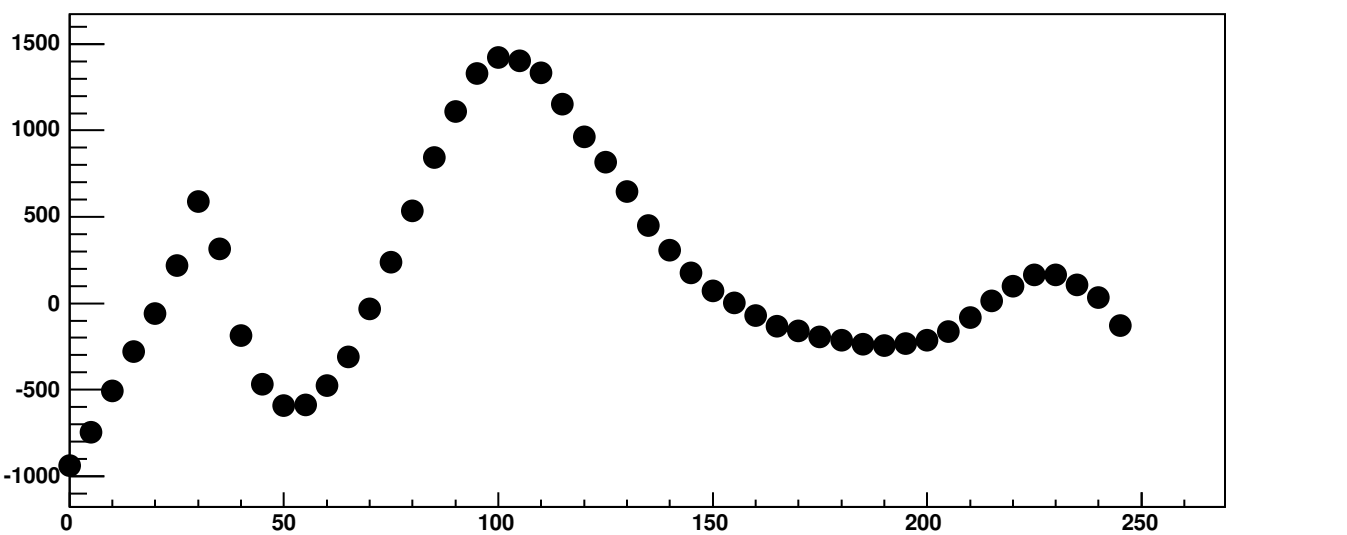
Chip 10, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold

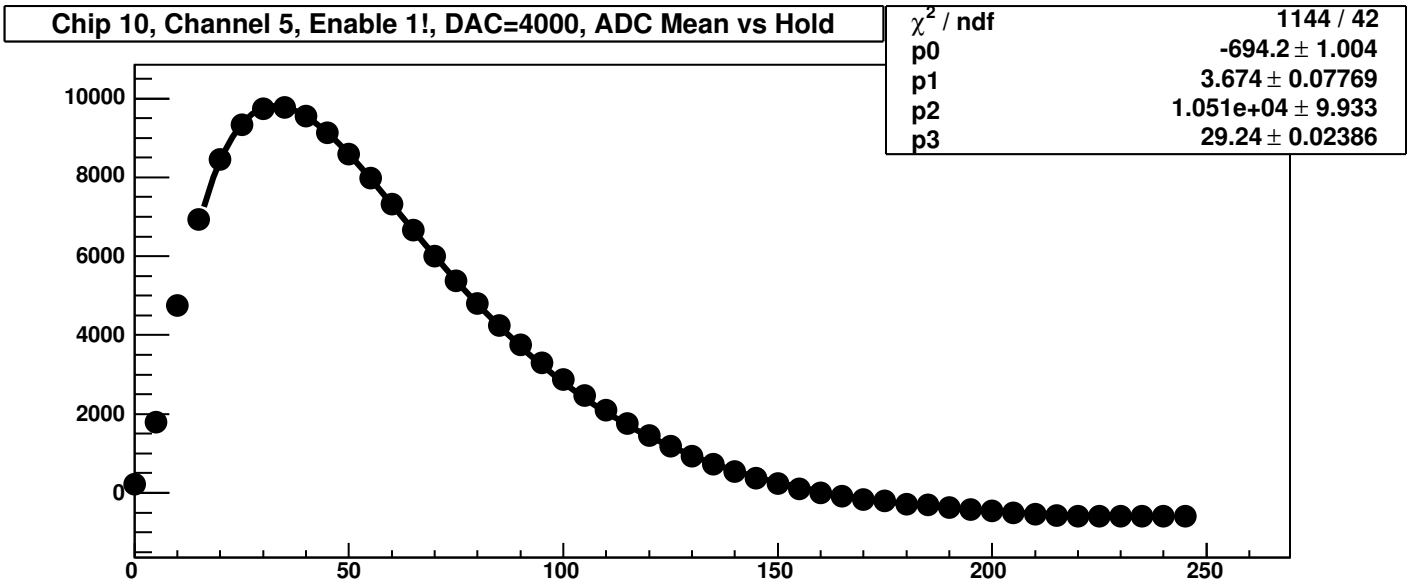


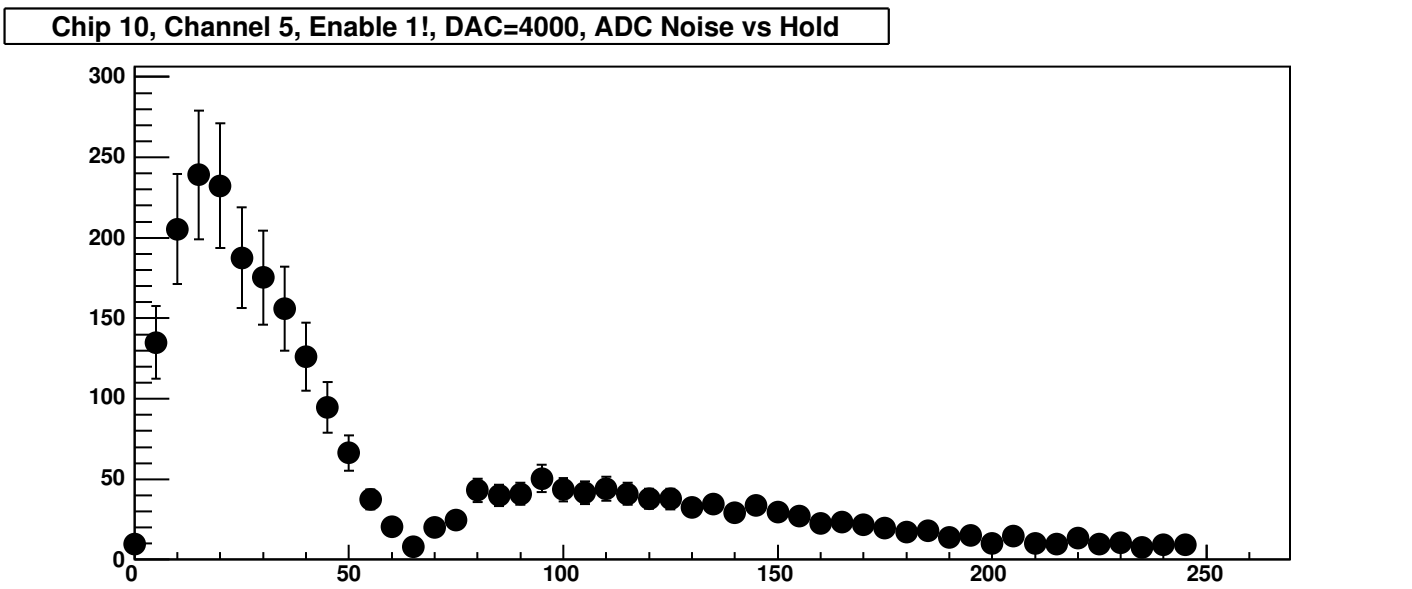
Chip 10, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold

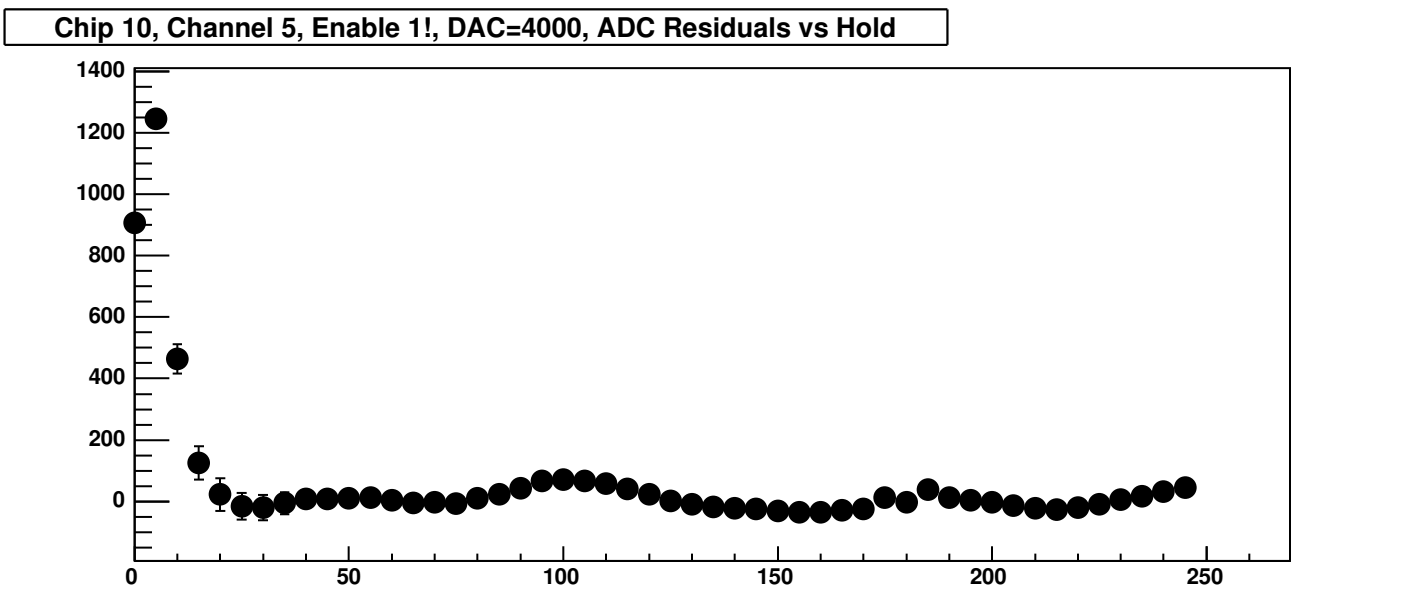


Chip 10, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold

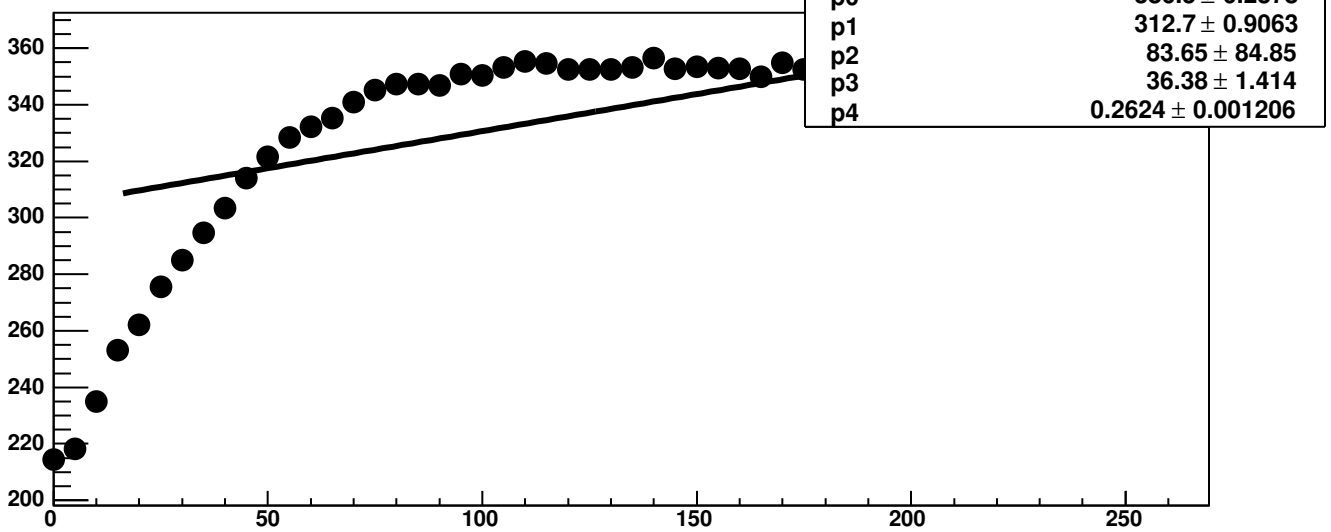



Chip 10, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1144 / 42
p0 -694.2 ± 1.004
p1 3.674 ± 0.07769
p2 1.051e+04 ± 9.933
p3 29.24 ± 0.02386
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

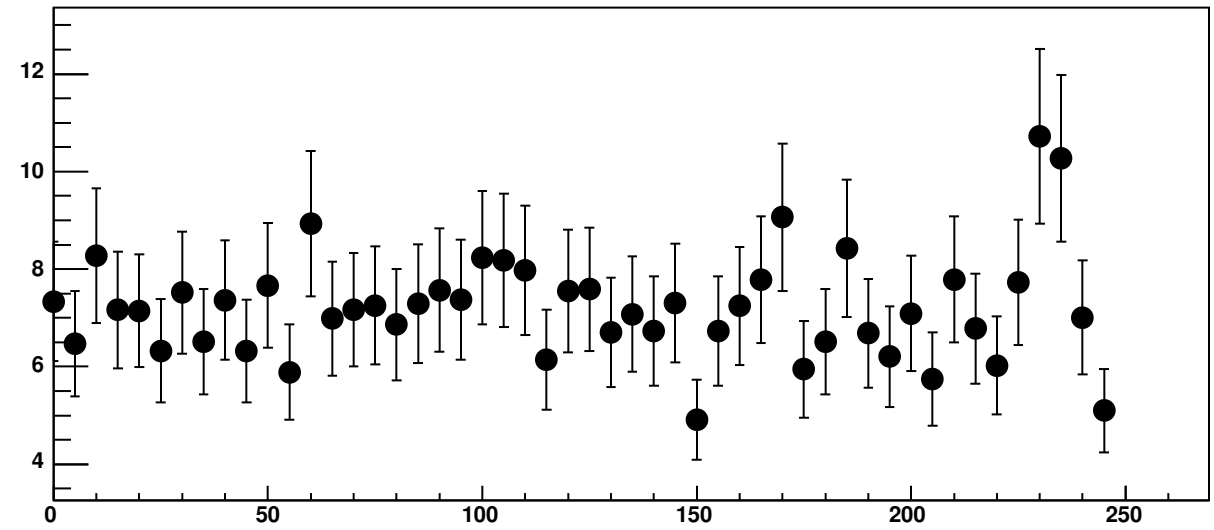

Chip 10, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold
300
250
200
150
100
50
0
0
50
100
150
200
250


Chip 10, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

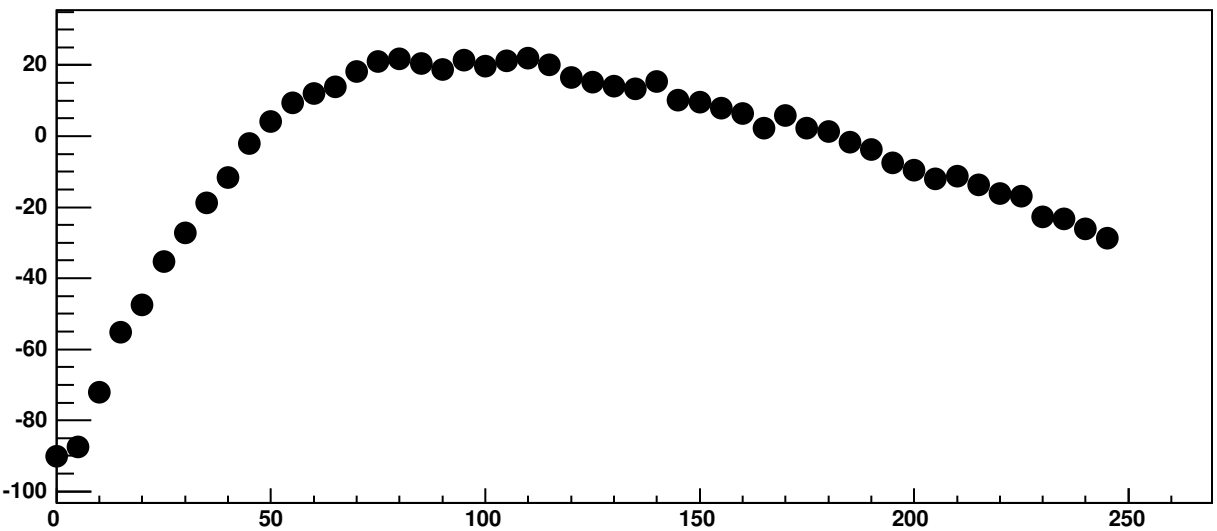
Chip 10, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold



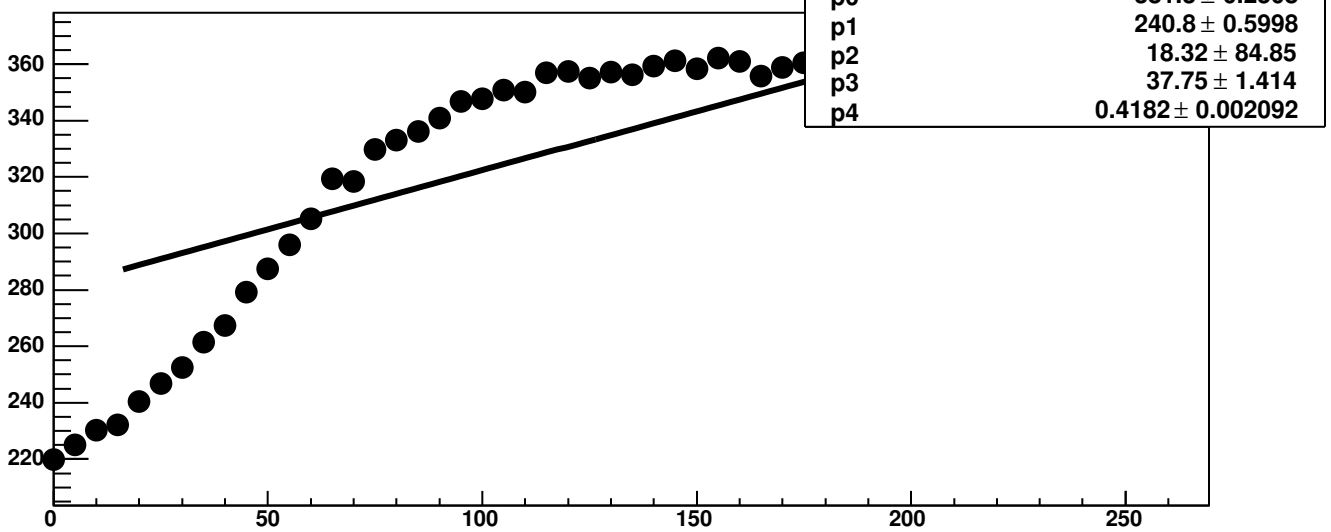
Chip 10, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



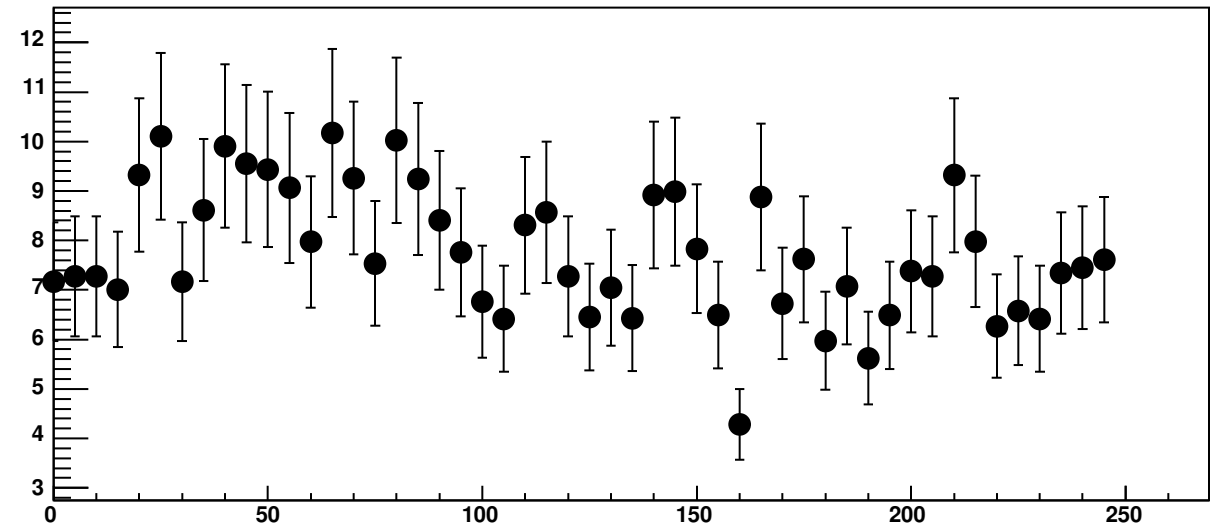
Chip 10, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold



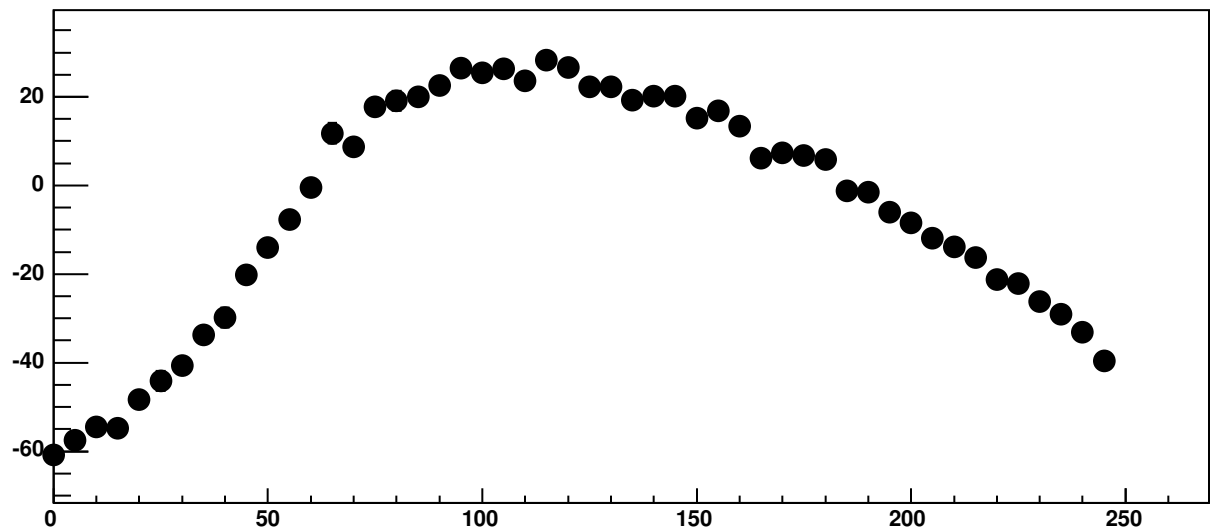
Chip 10, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold



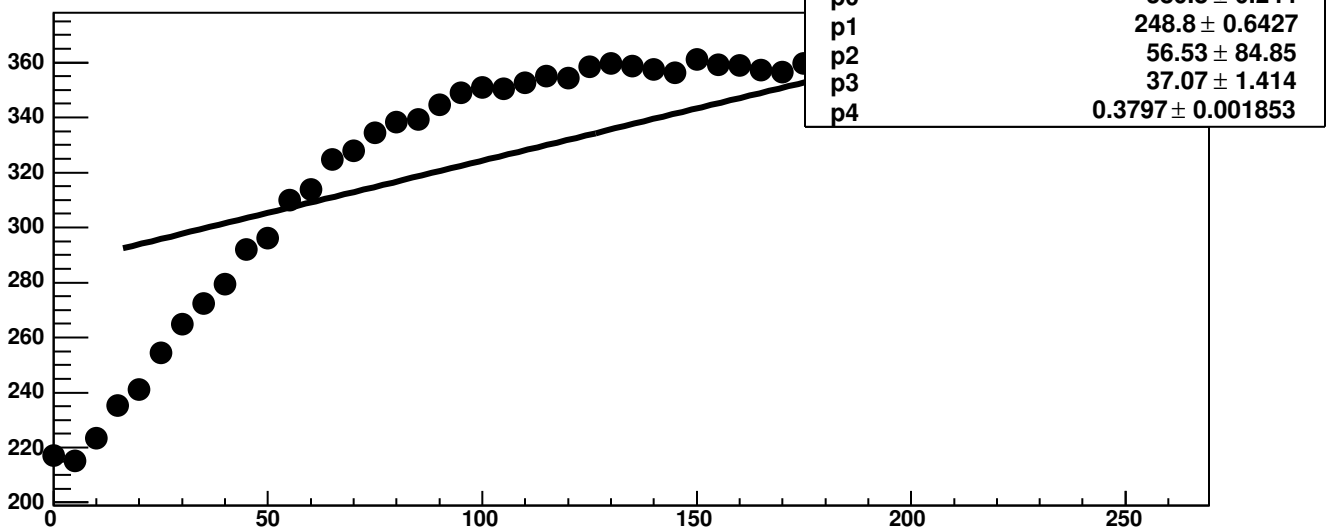
Chip 10, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



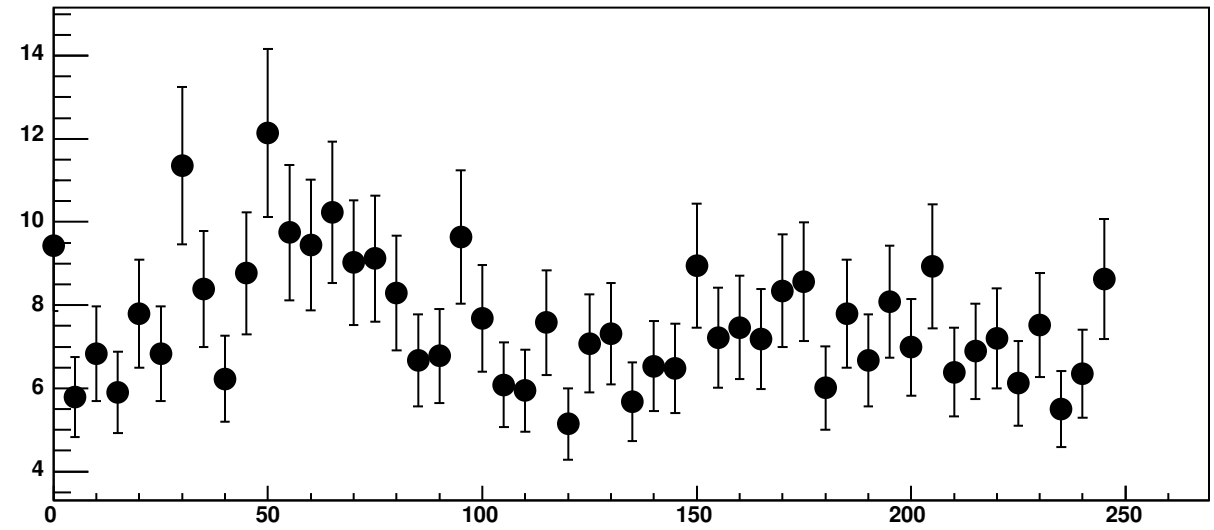
Chip 10, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold



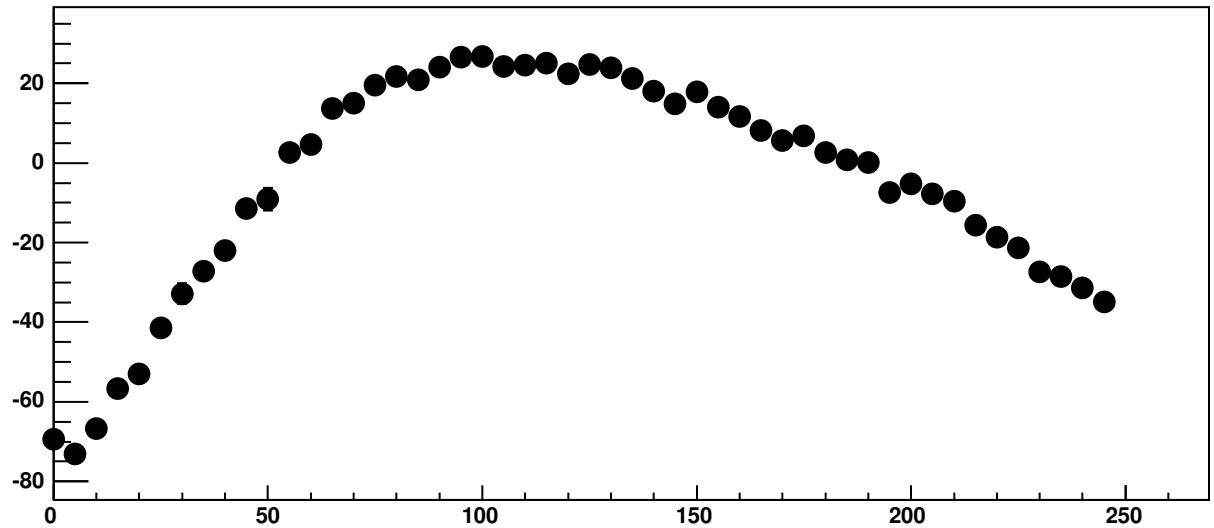
Chip 10, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold



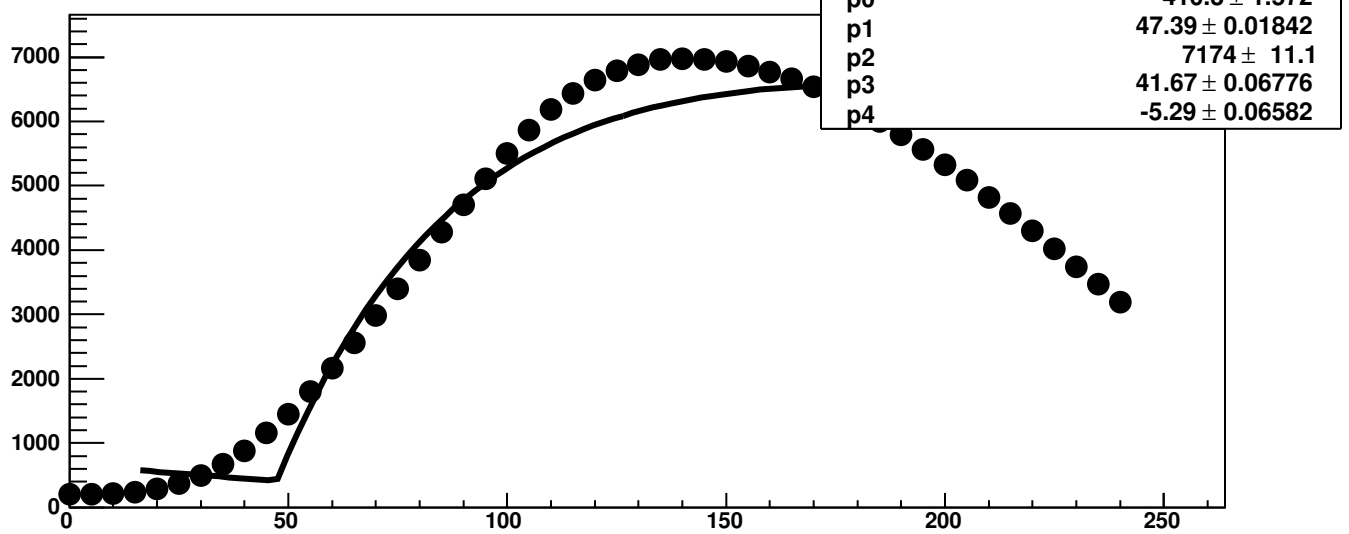
Chip 10, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold



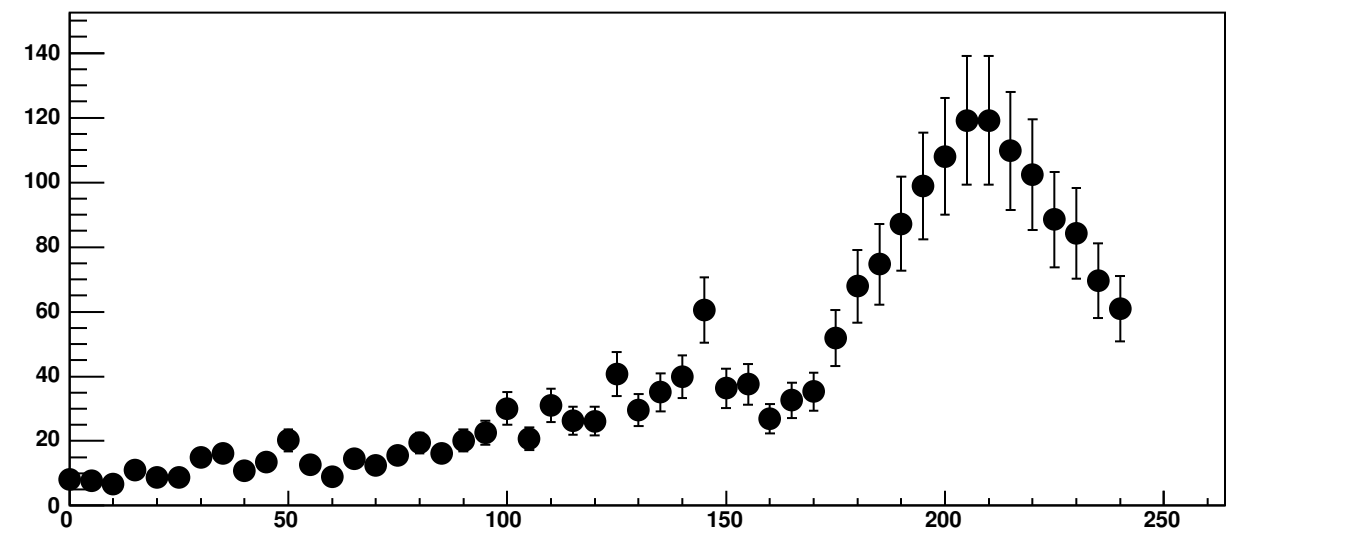
Chip 10, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold



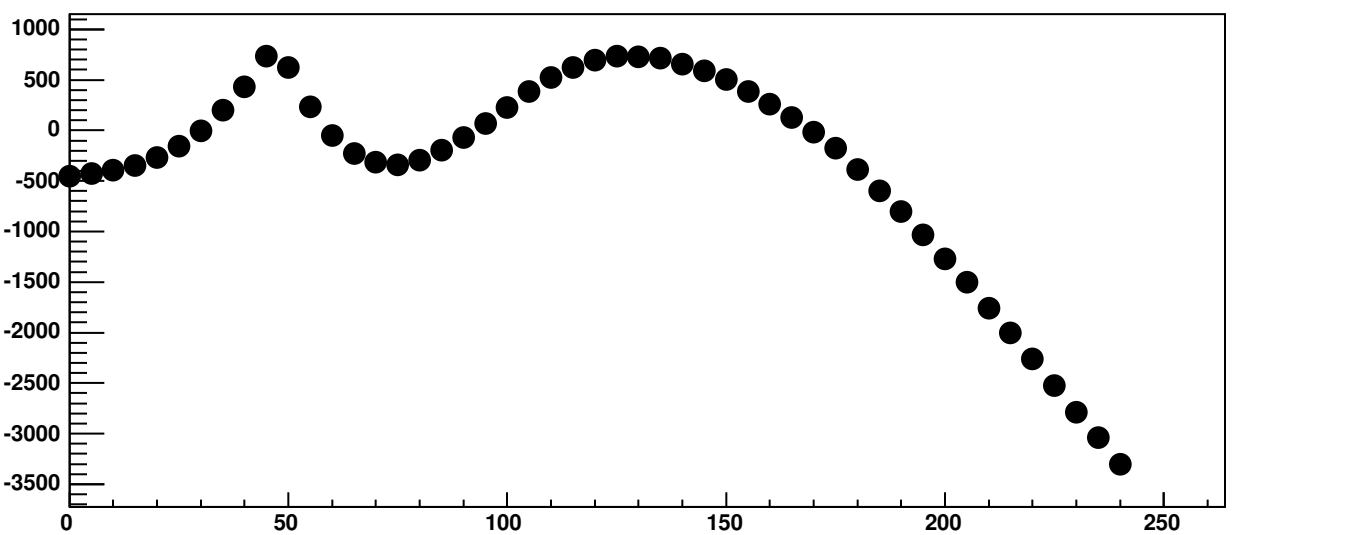
Chip 10, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold



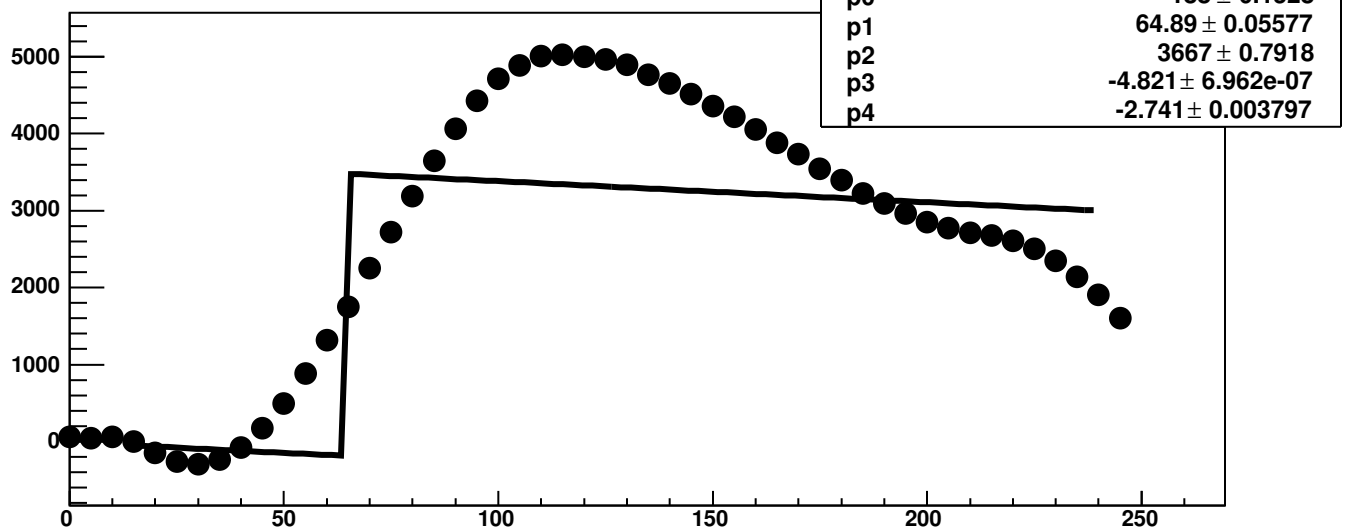
Chip 10, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



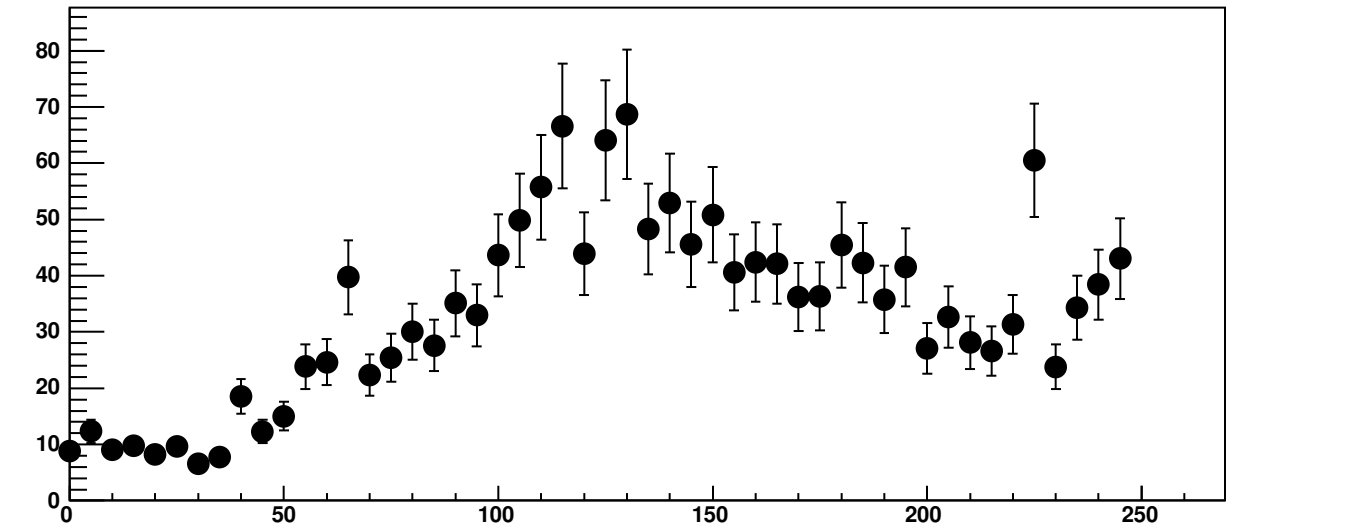
Chip 10, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold



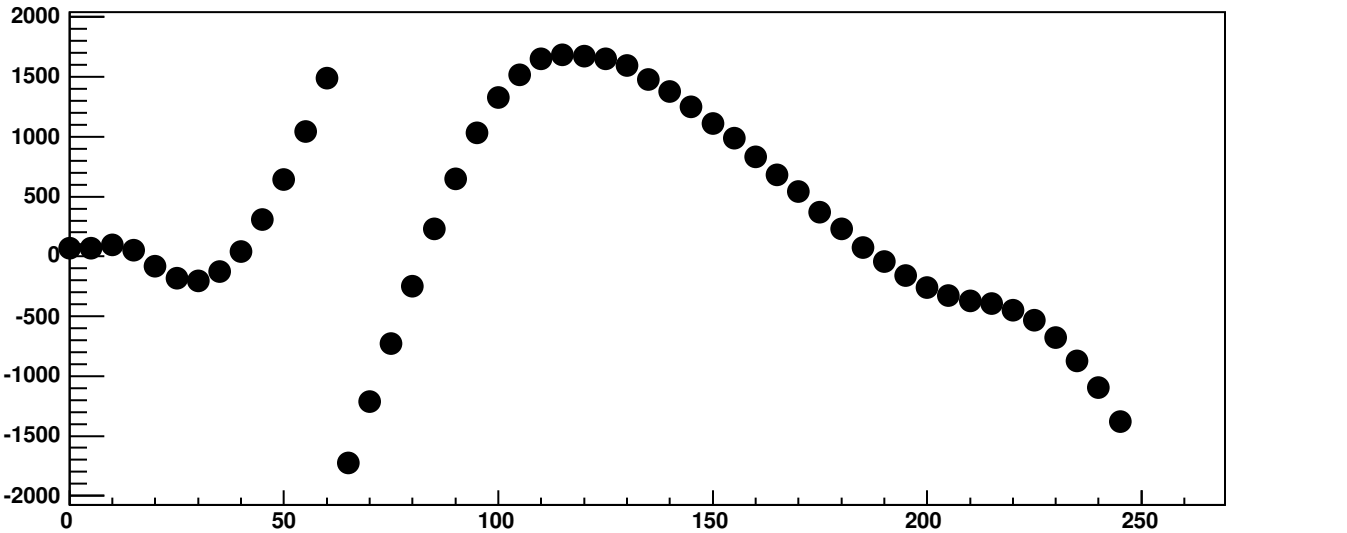
Chip 10, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold

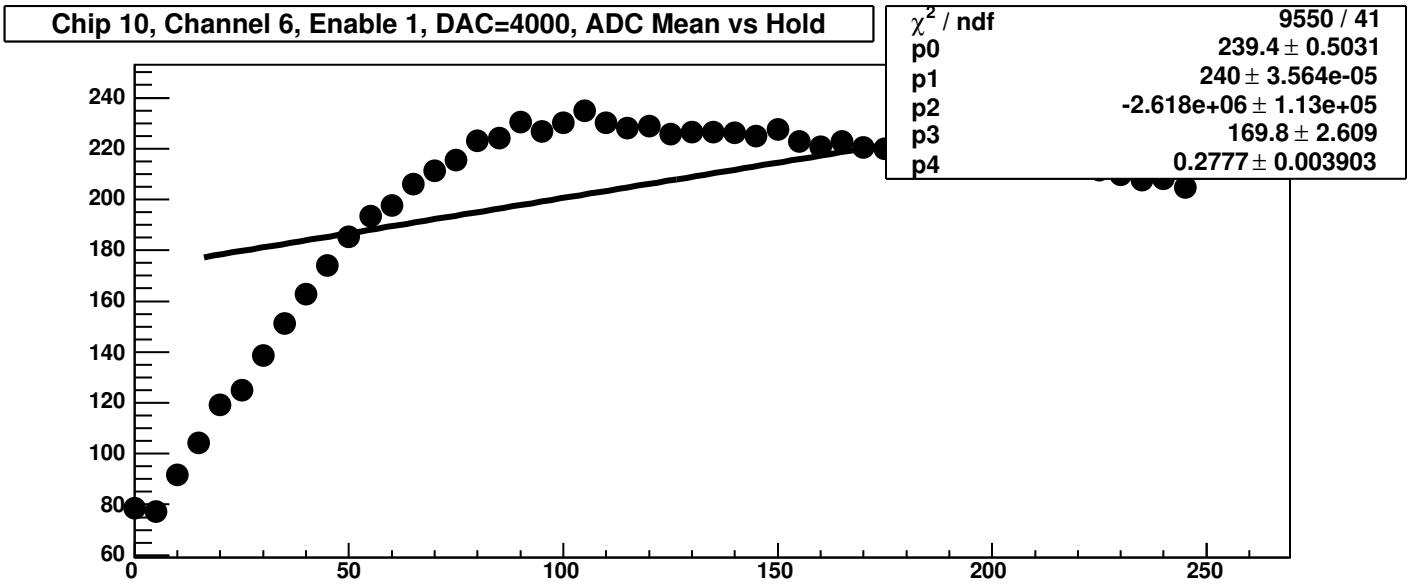


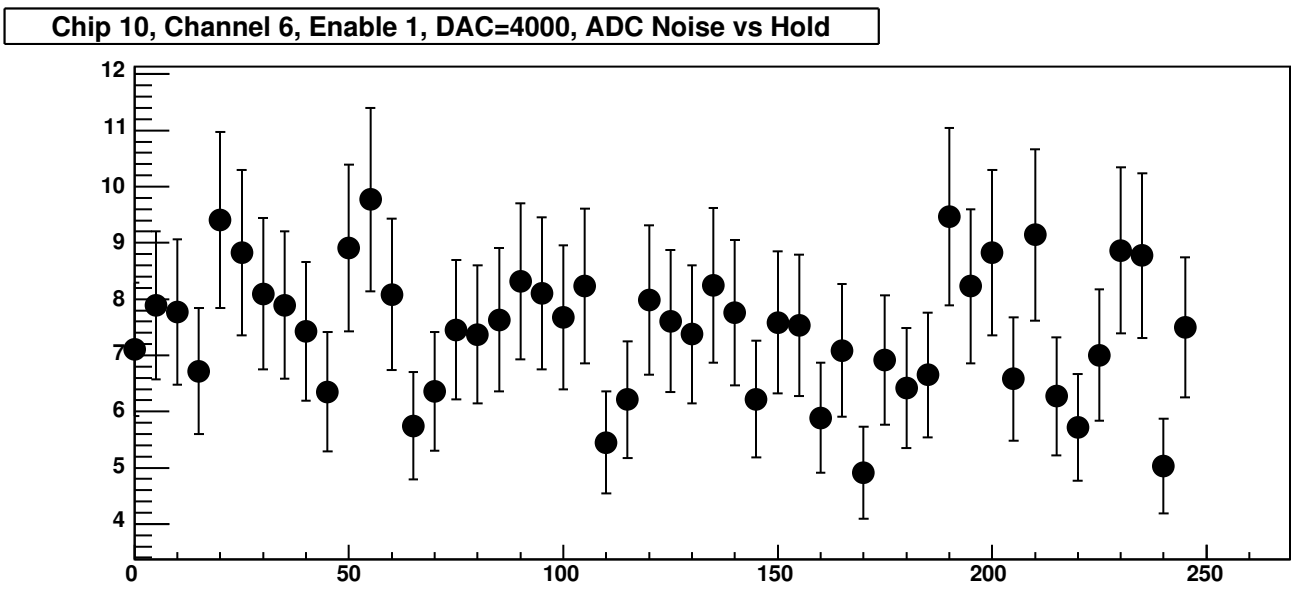
Chip 10, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold

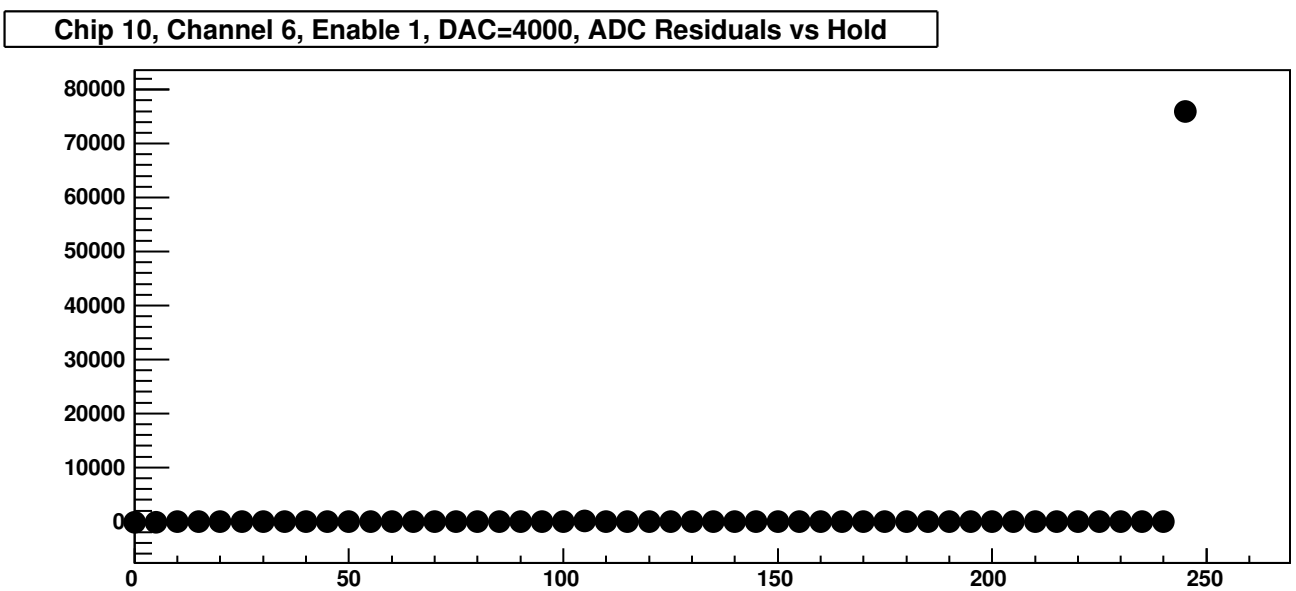


Chip 10, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold

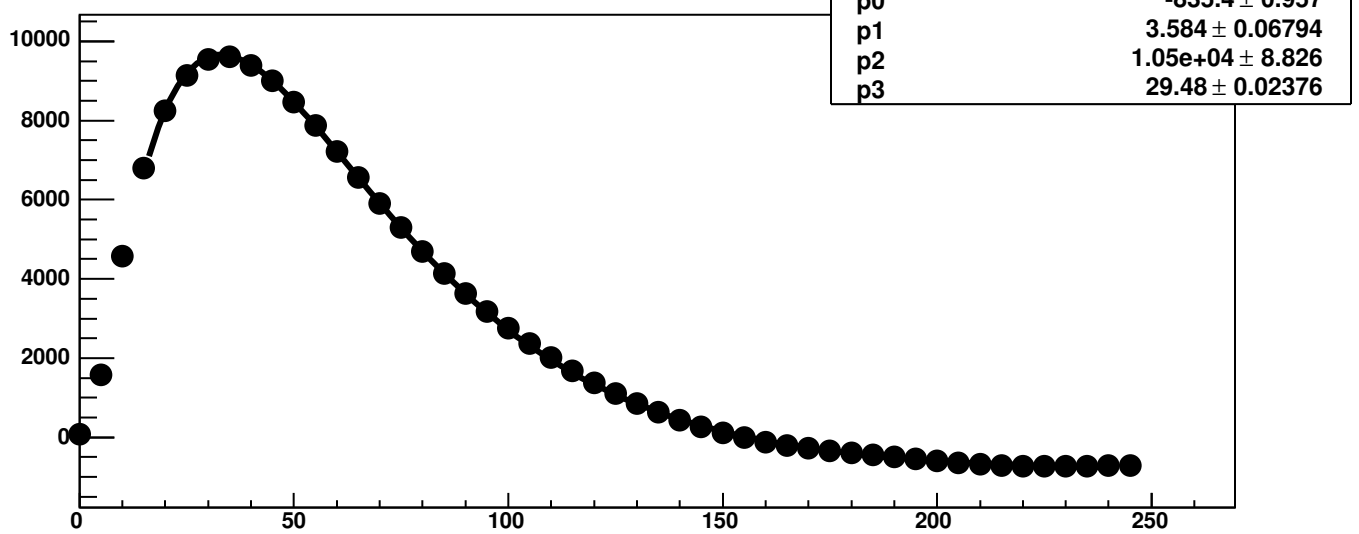



Chip 10, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 9550 / 41
p0 239.4 ± 0.5031
p1 240 ± 3.564e-05
p2 -2.618e+06 ± 1.13e+05
p3 169.8 ± 2.609
p4 0.2777 ± 0.003903
240
220
200
180
160
140
120
100
80
60
0
50
100
150
200
250

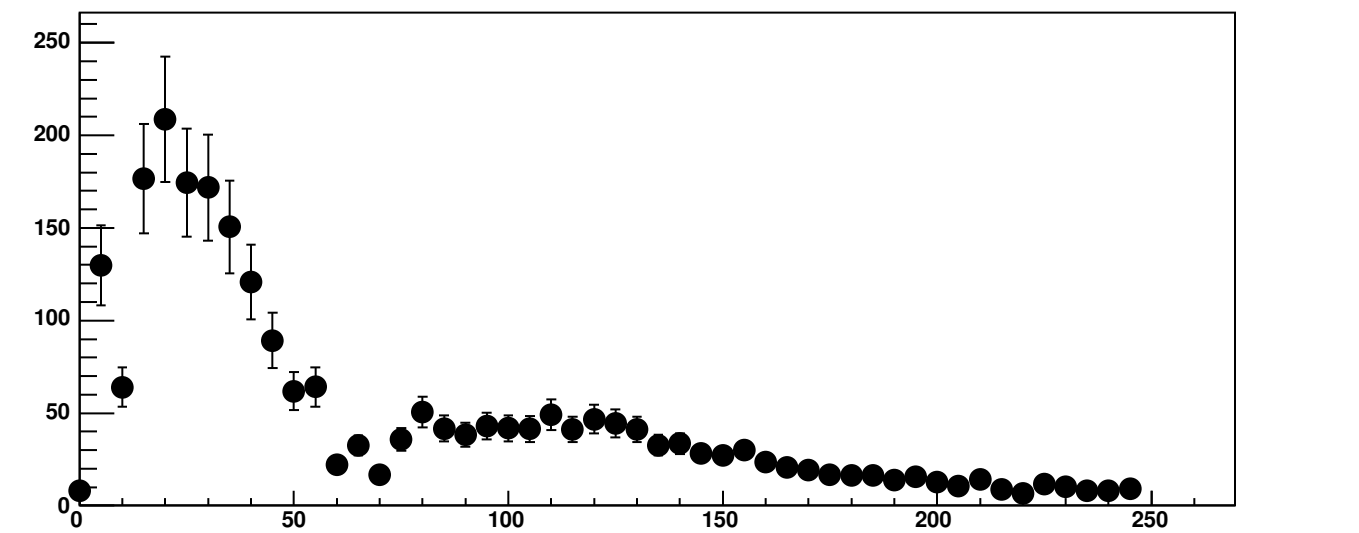

Chip 10, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 10, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold
80000
70000
60000
50000
40000
30000
20000
10000
0
0
50
100
150
200
250

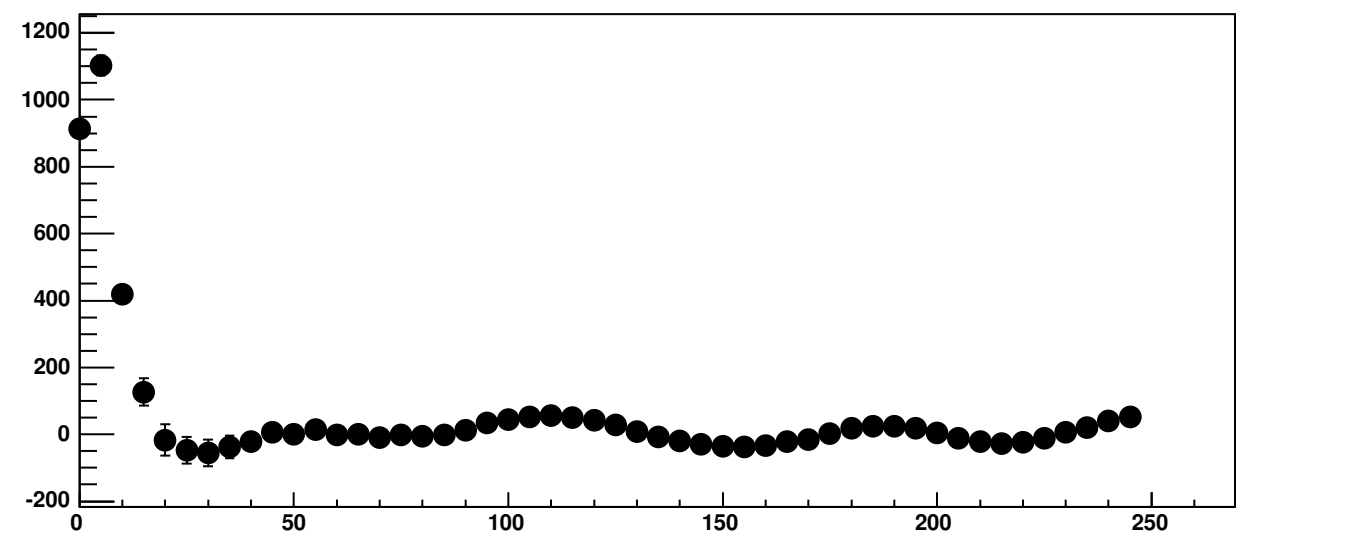
Chip 10, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold

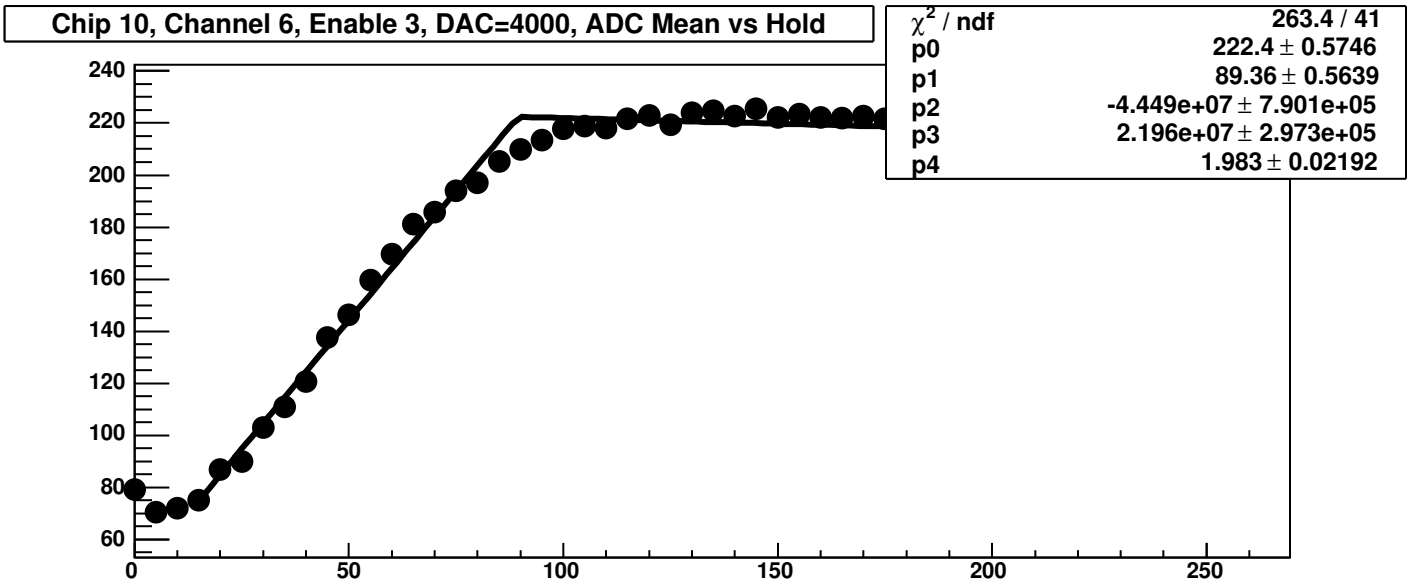


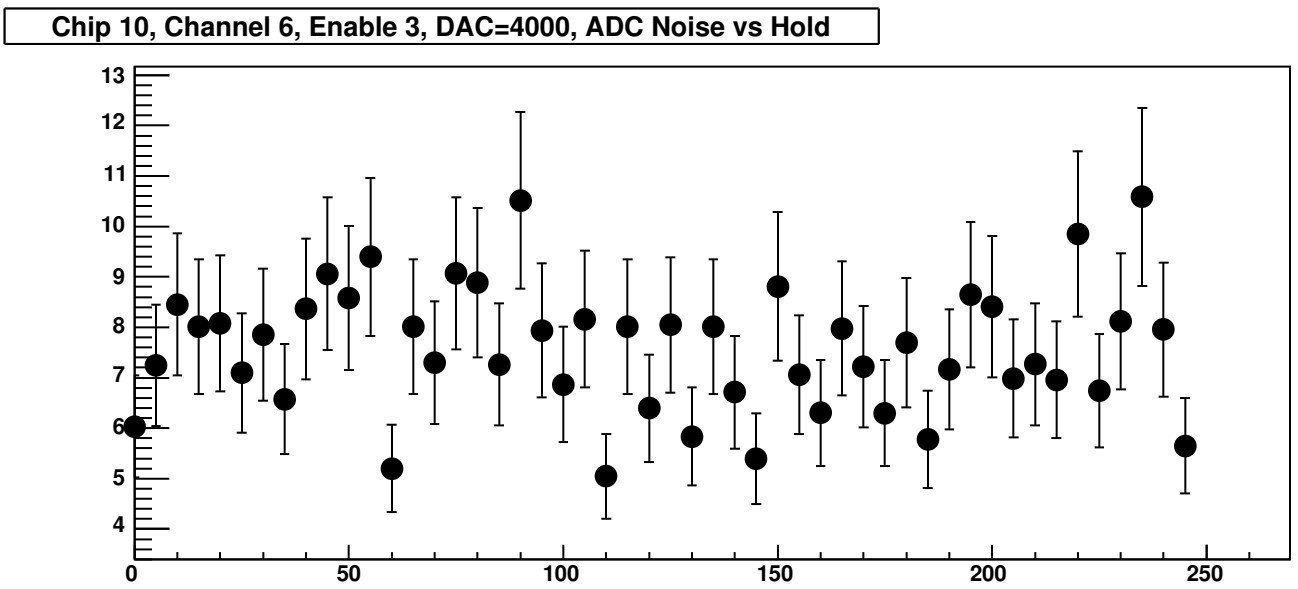
Chip 10, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold

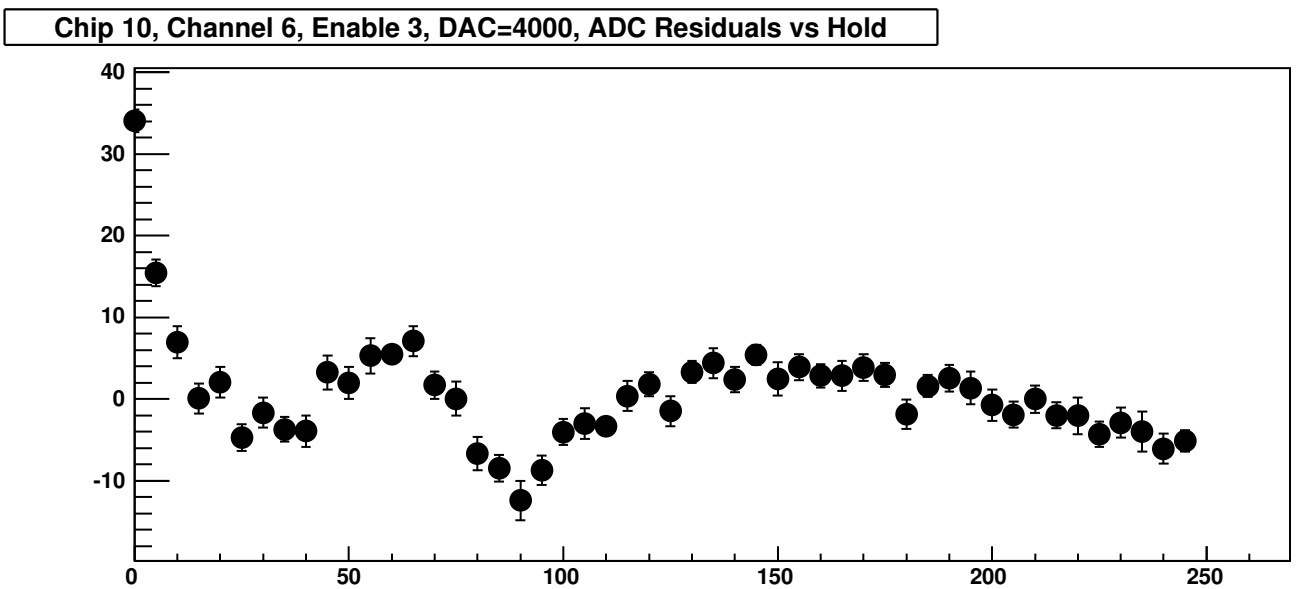


Chip 10, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold

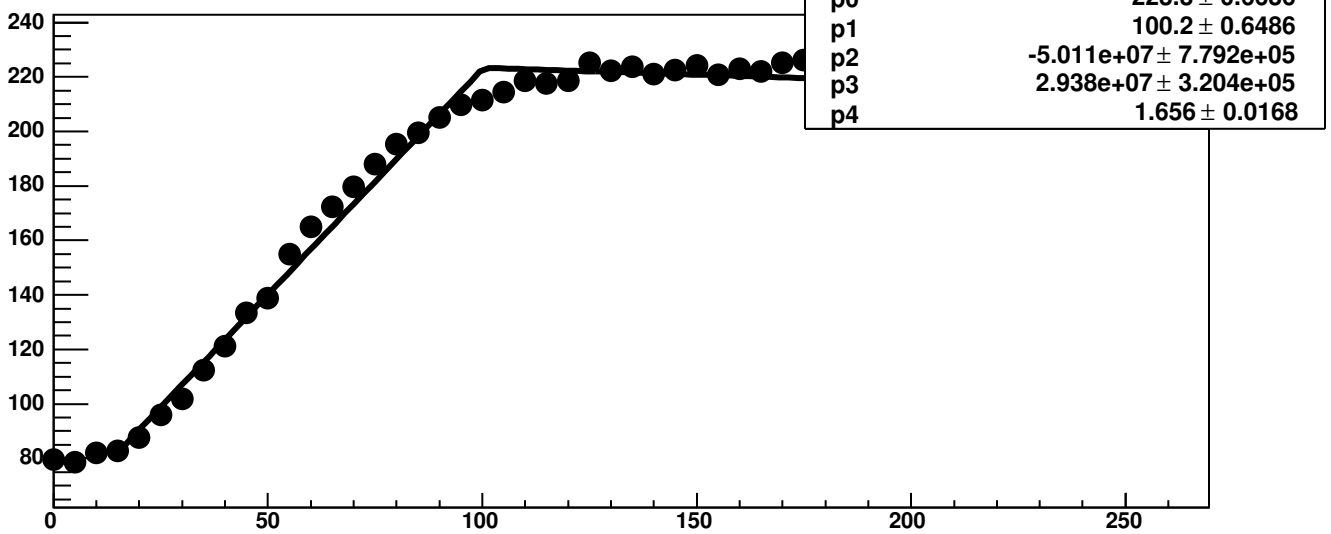



Chip 10, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 263.4 / 41
p0 222.4 ± 0.5746
p1 89.36 ± 0.5639
p2 -4.449e+07 ± 7.901e+05
p3 2.196e+07 ± 2.973e+05
p4 1.983 ± 0.02192

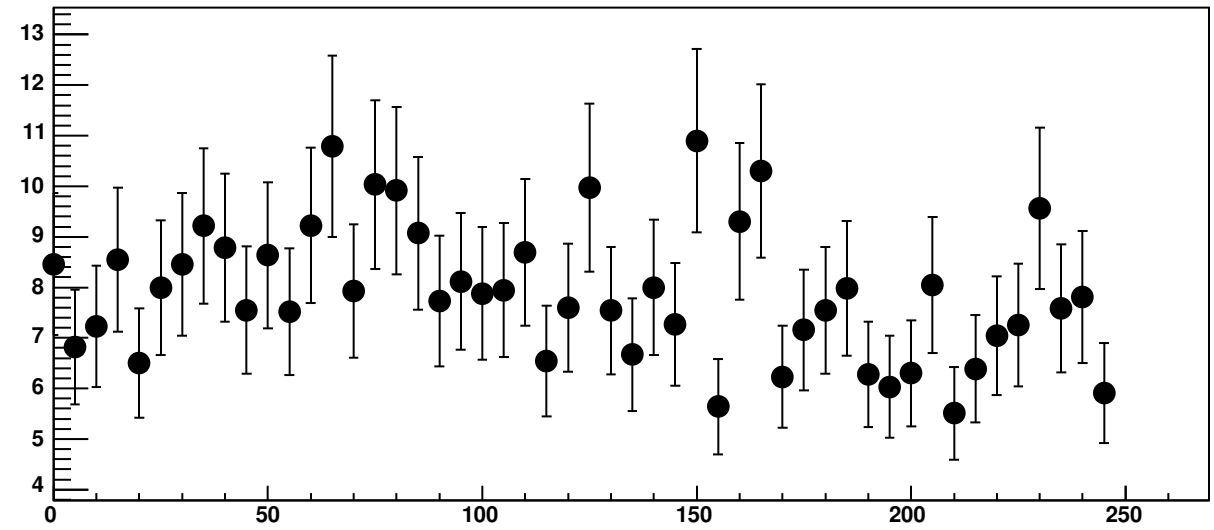

Chip 10, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold


Chip 10, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

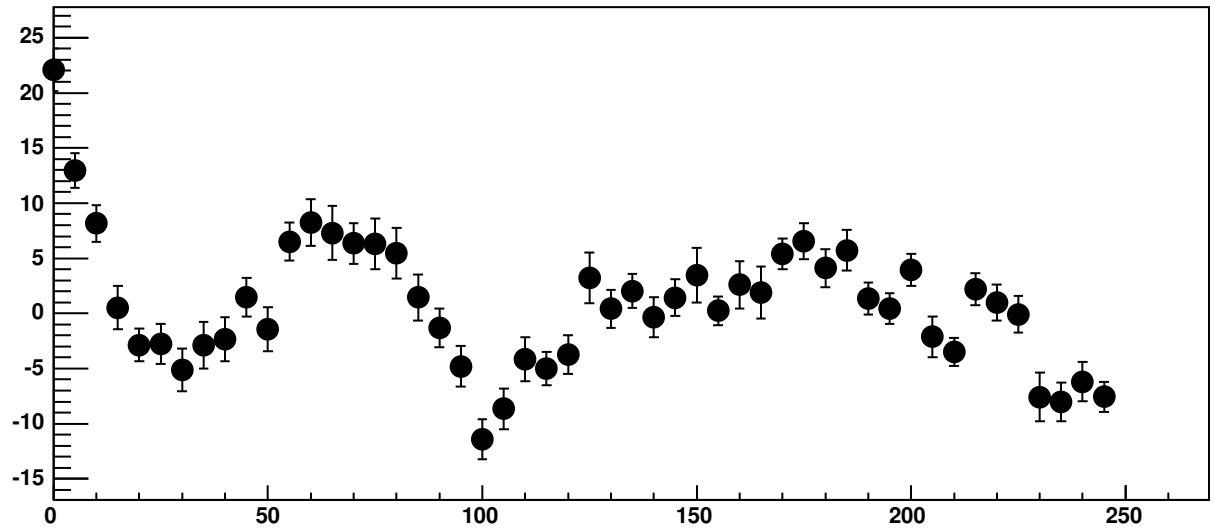
Chip 10, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold



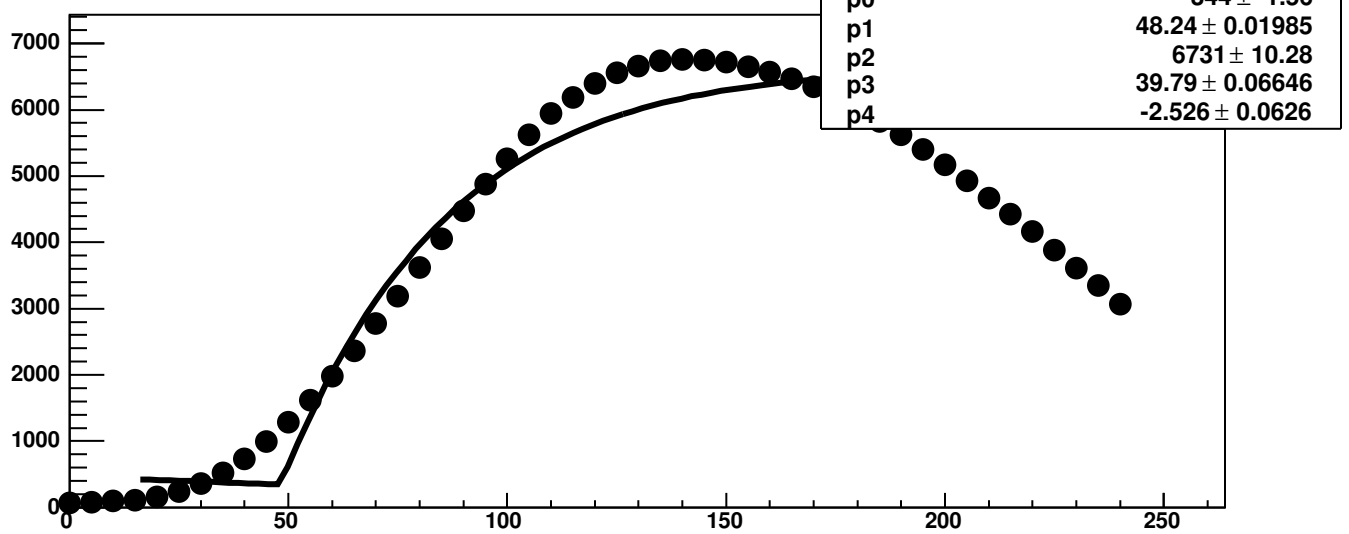
Chip 10, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



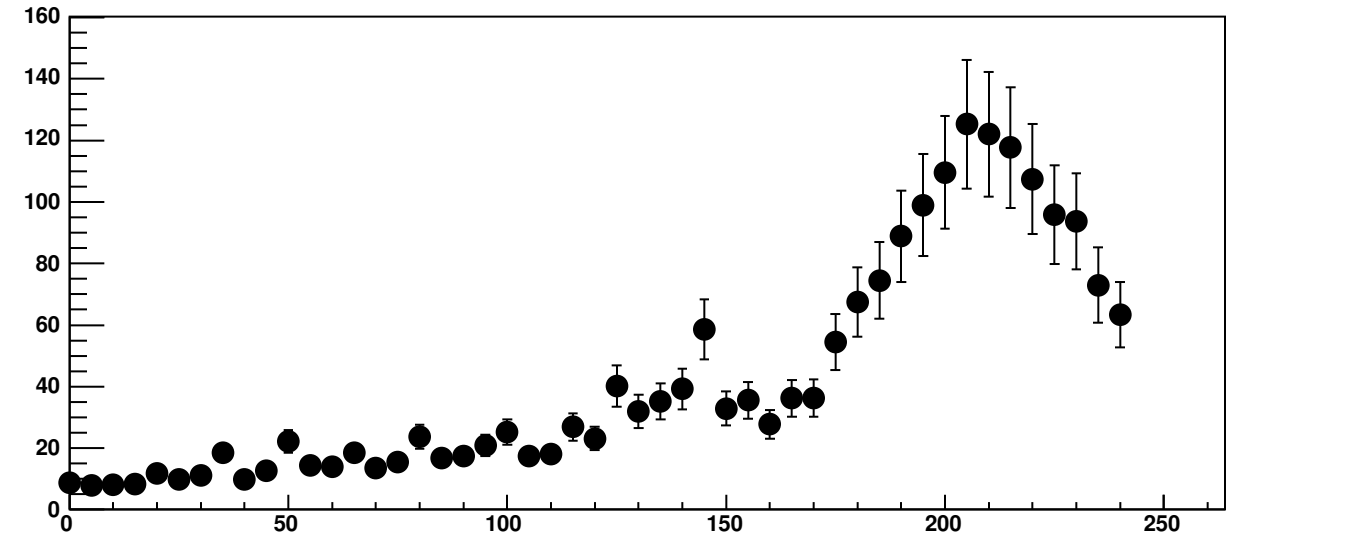
Chip 10, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold



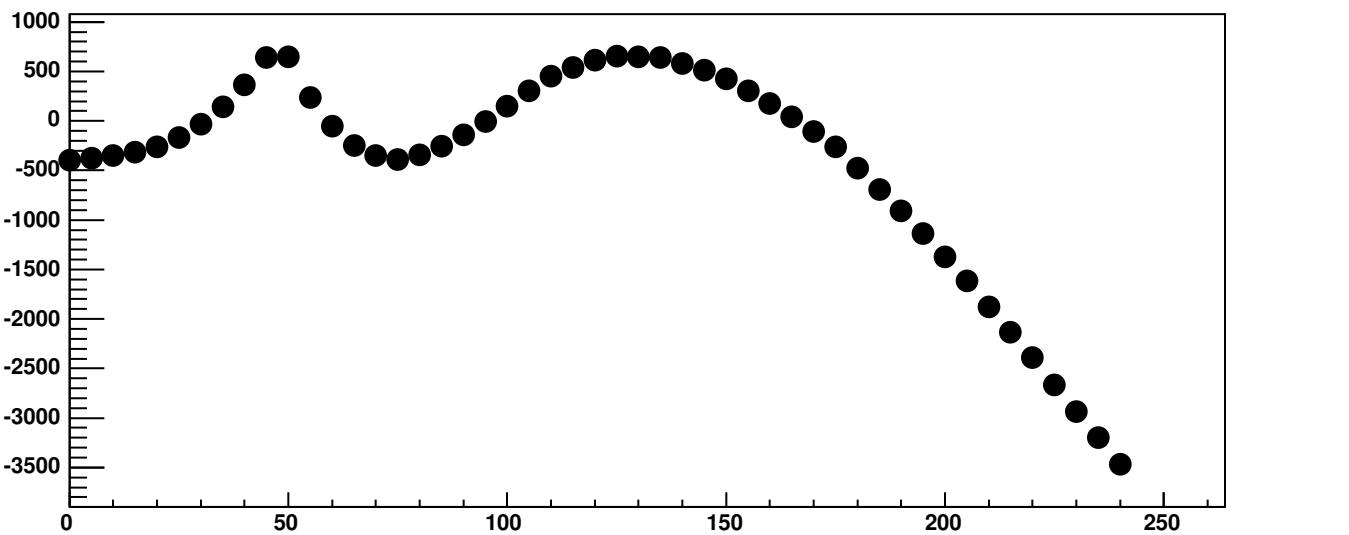
Chip 10, Channel 6, Enable 5, DAC=4000, ADC Mean vs Hold



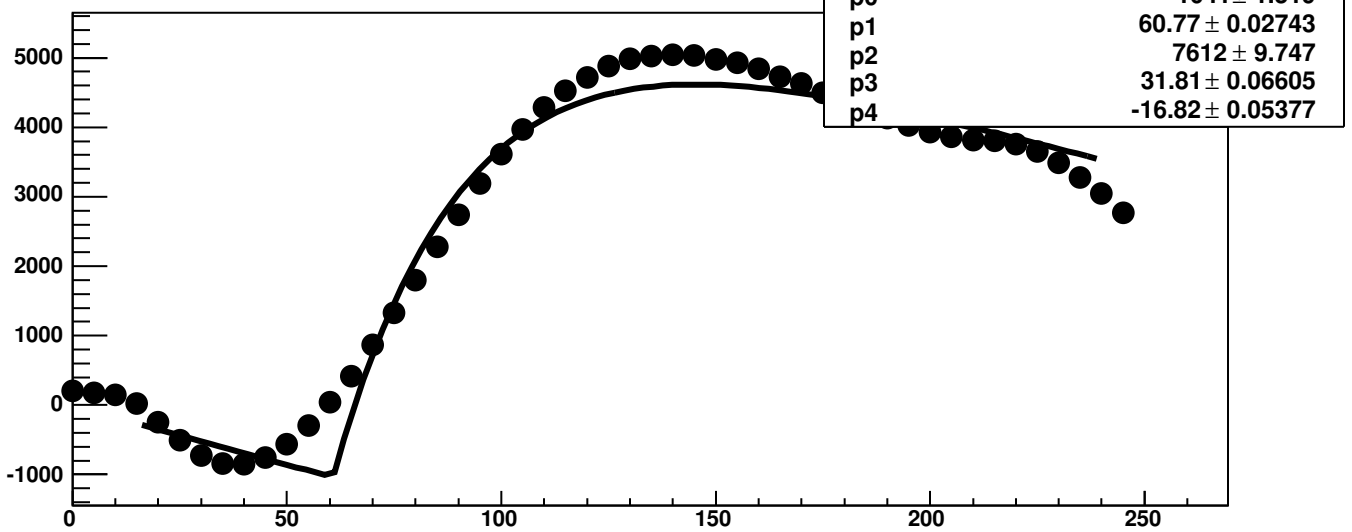
Chip 10, Channel 6, Enable 5, DAC=4000, ADC Noise vs Hold



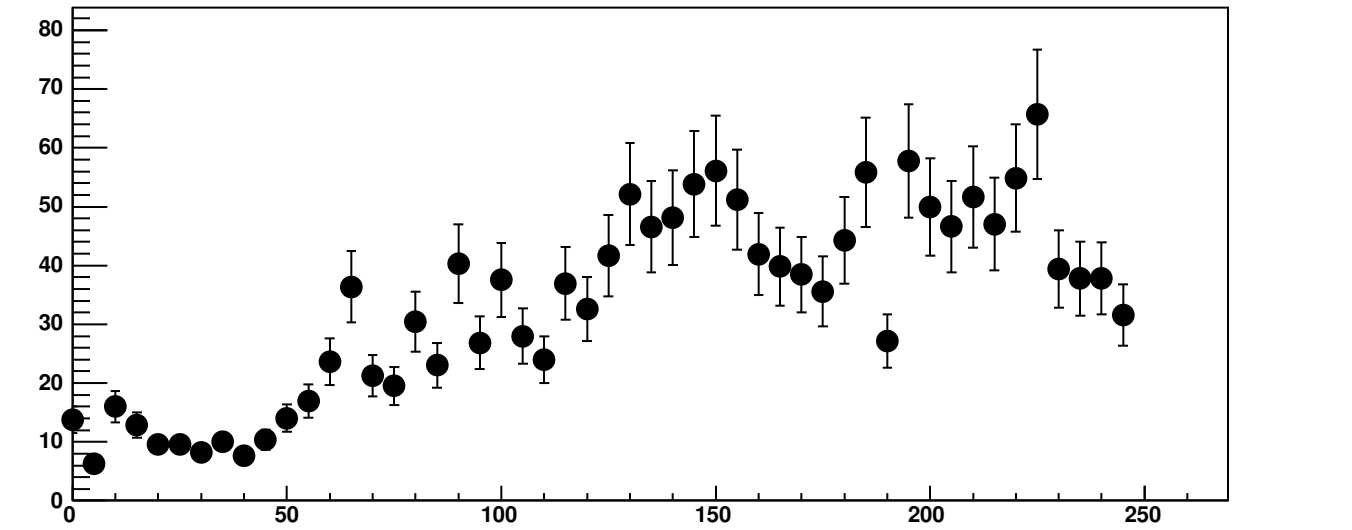
Chip 10, Channel 6, Enable 5, DAC=4000, ADC Residuals vs Hold



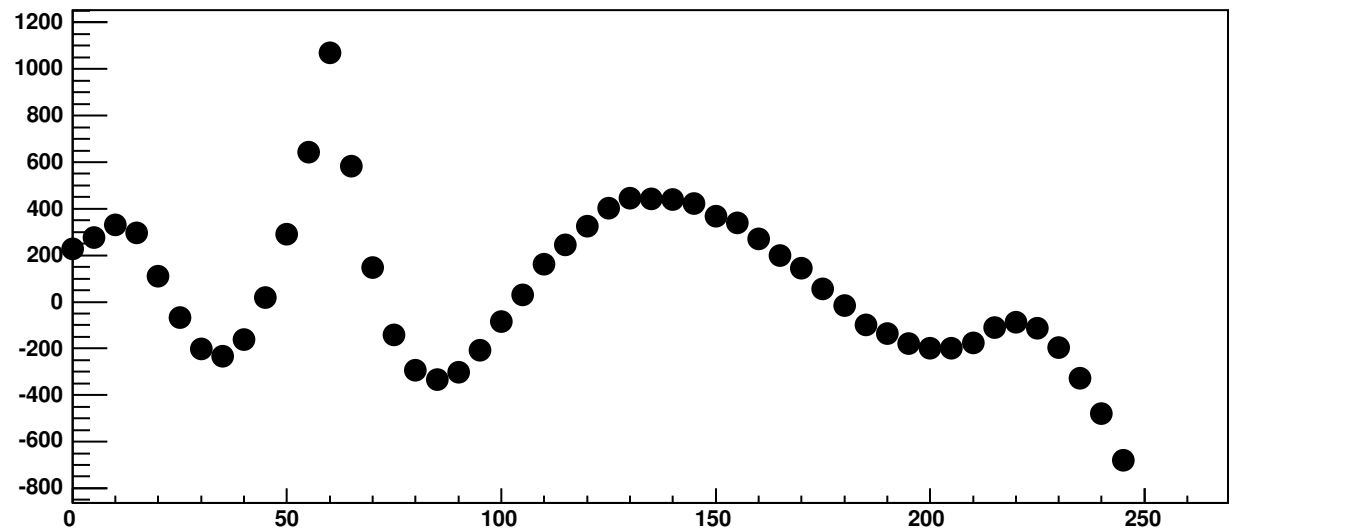
Chip 10, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold



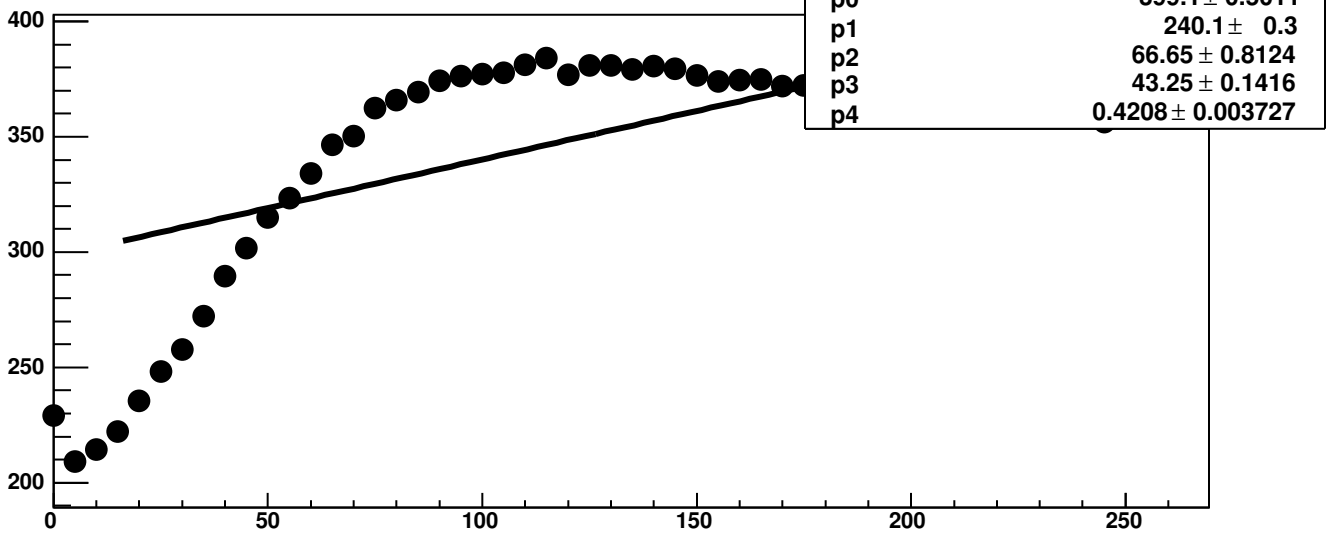
Chip 10, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



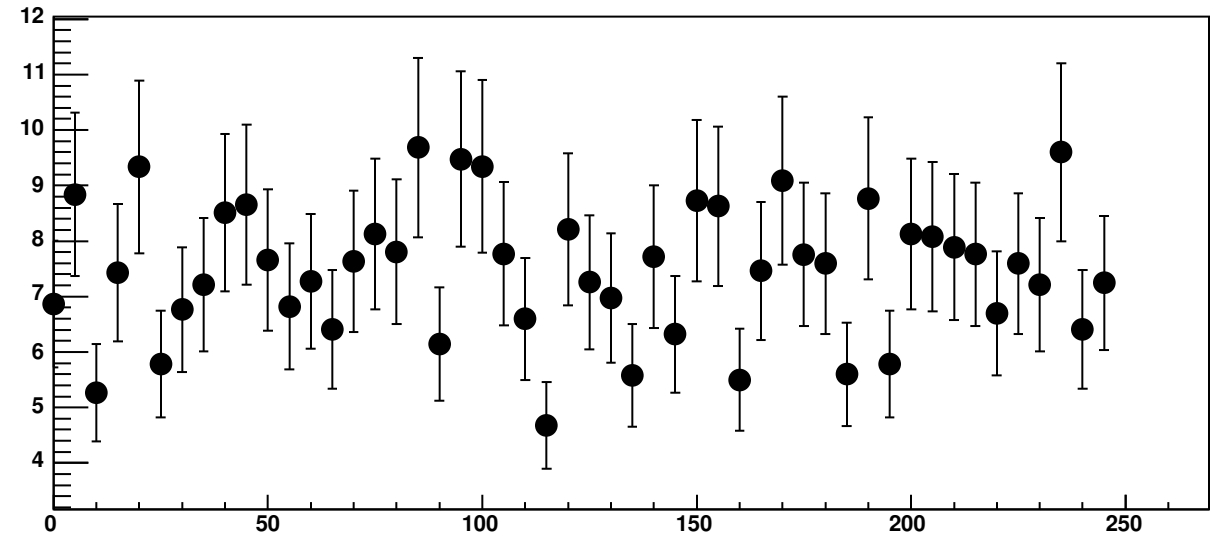
Chip 10, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold



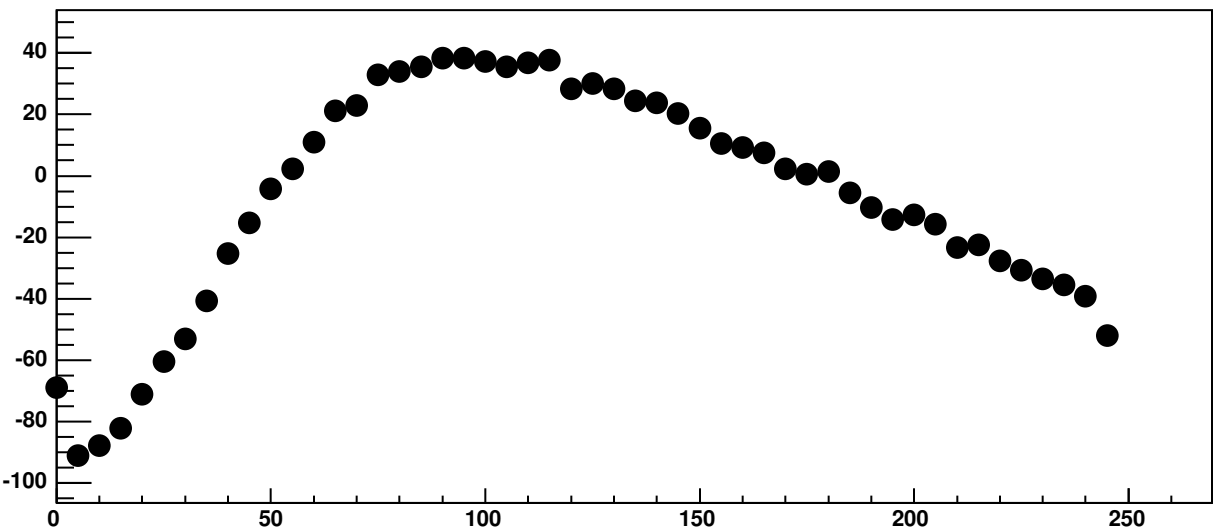
Chip 10, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold



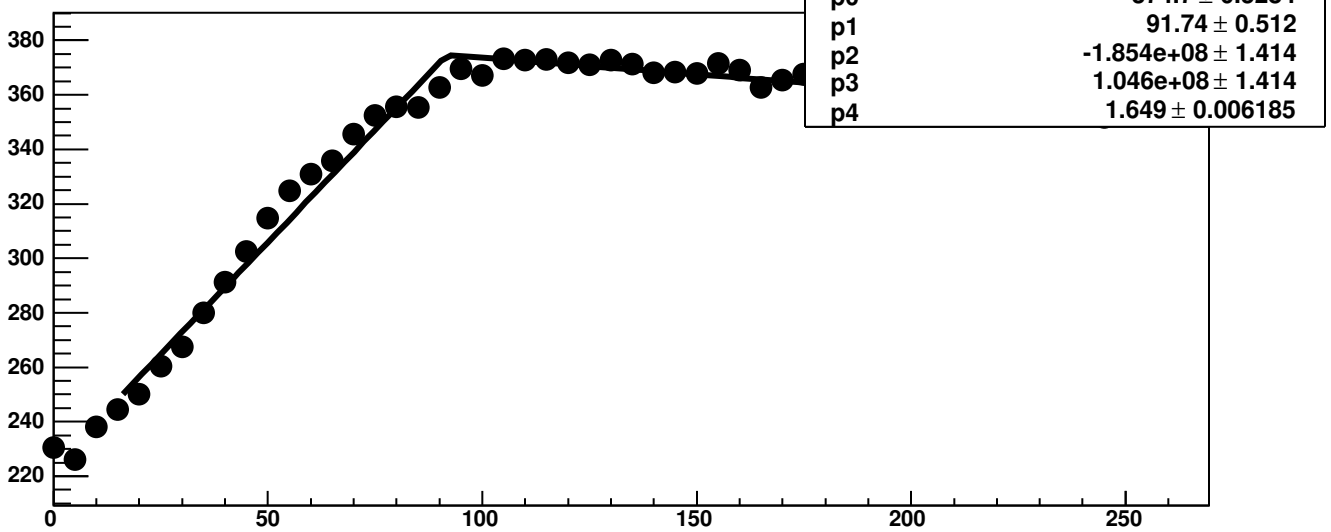
Chip 10, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



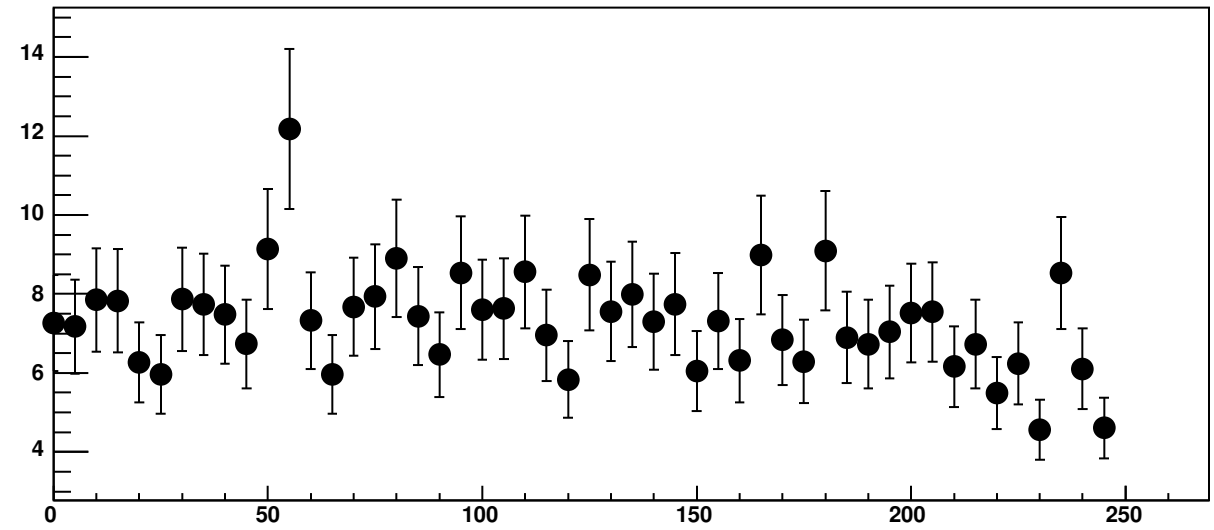
Chip 10, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold



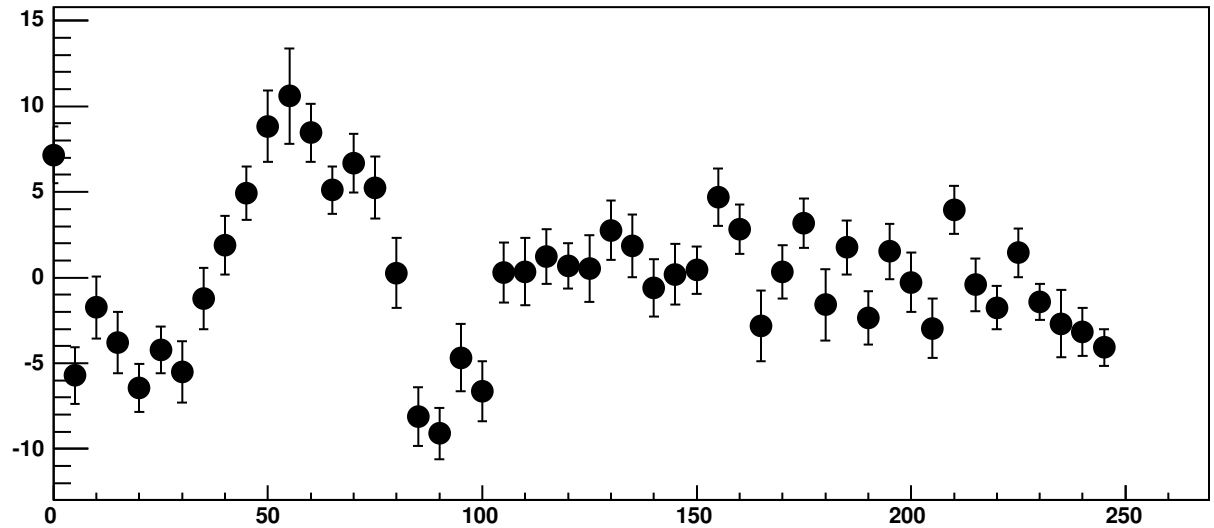
Chip 10, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold



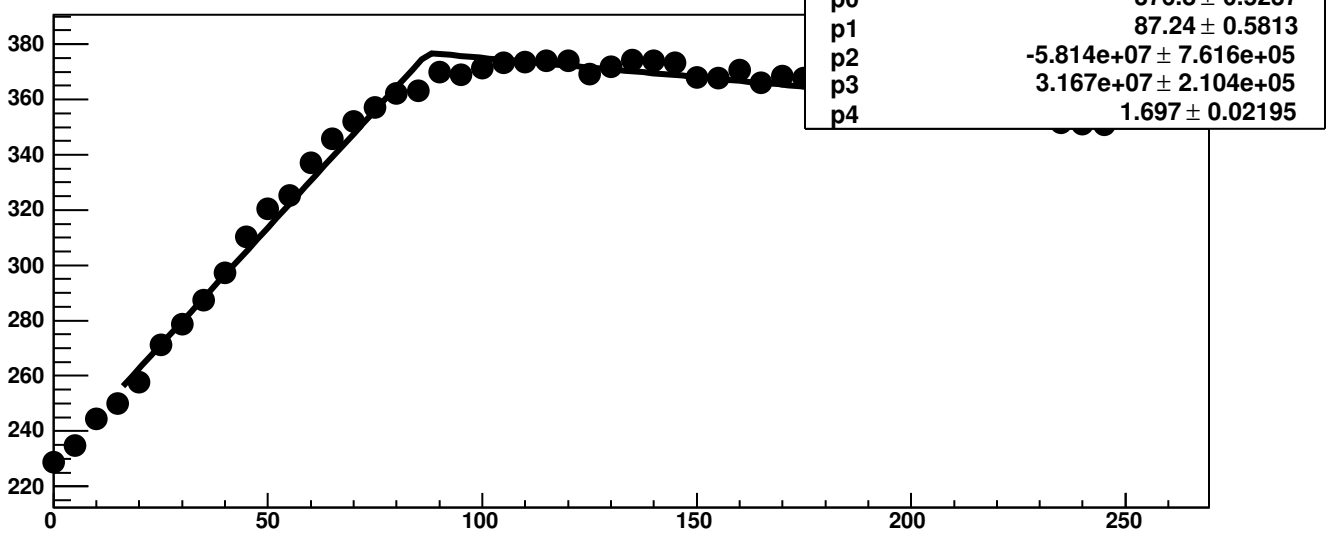
Chip 10, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold



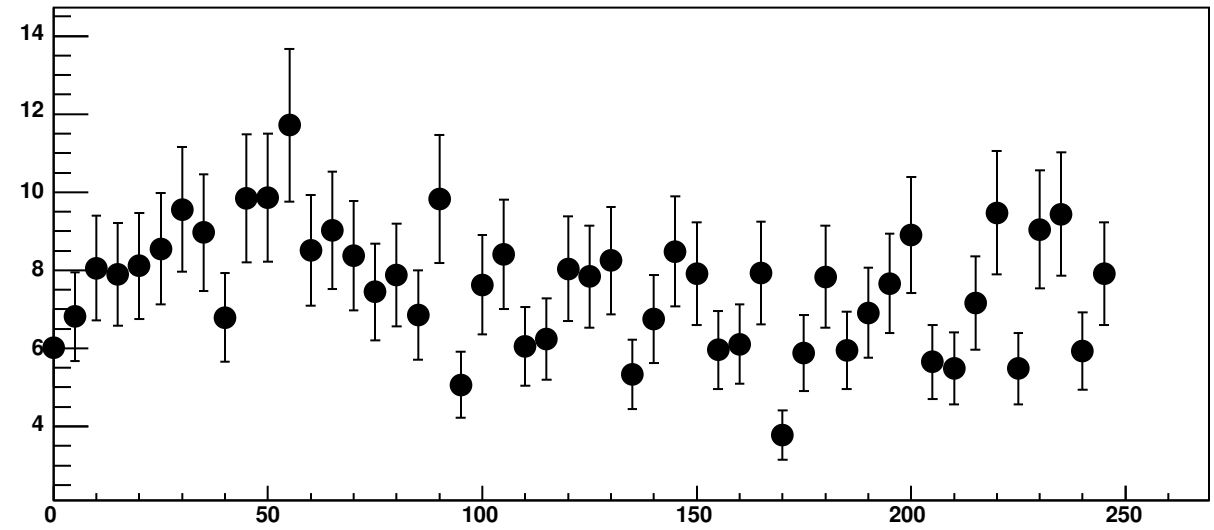
Chip 10, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold



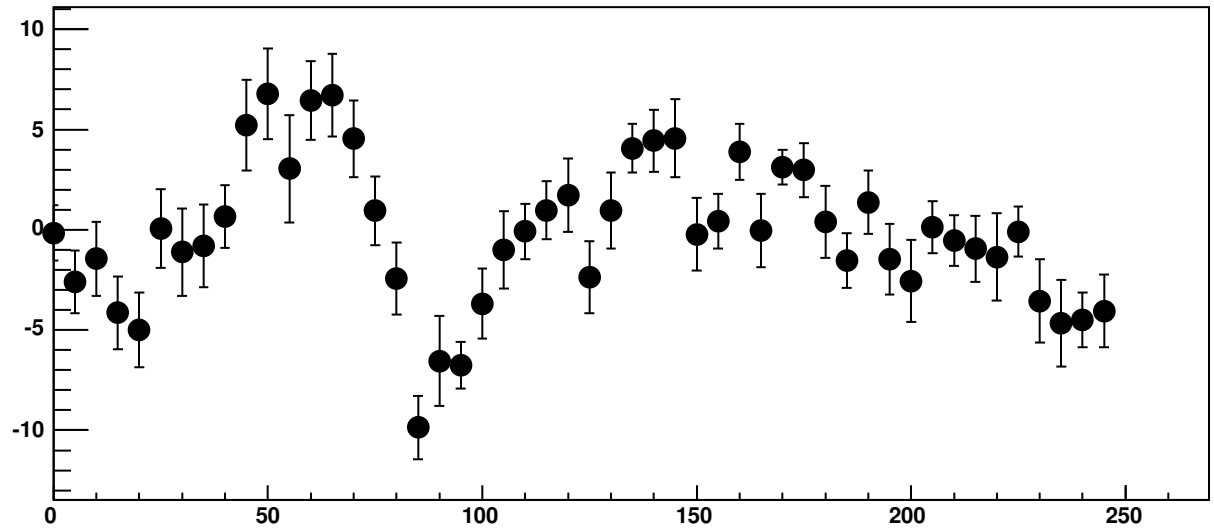
Chip 10, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold



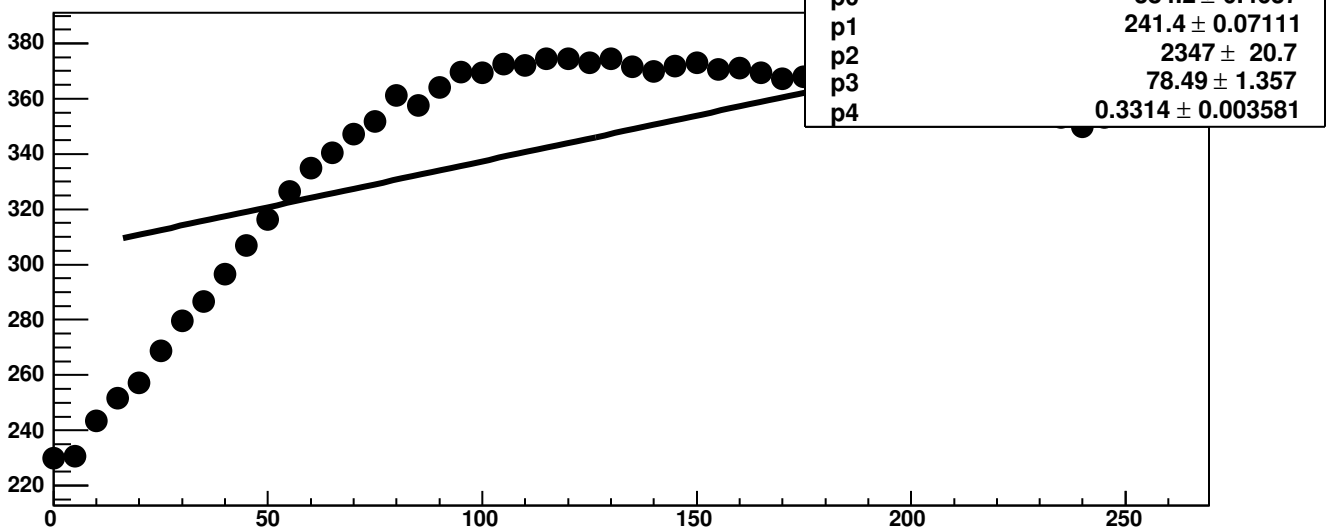
Chip 10, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



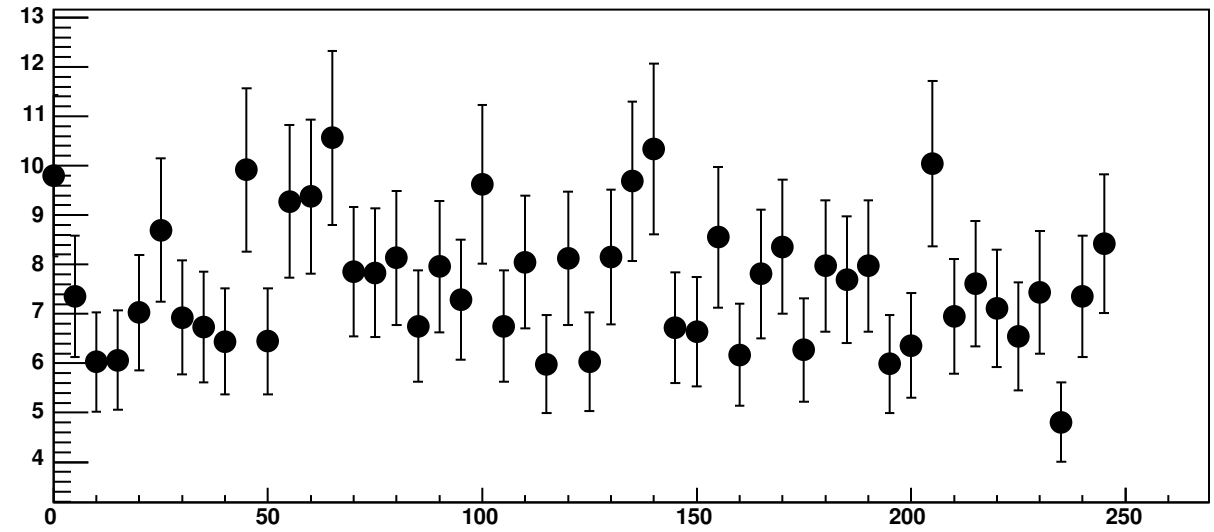
Chip 10, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold



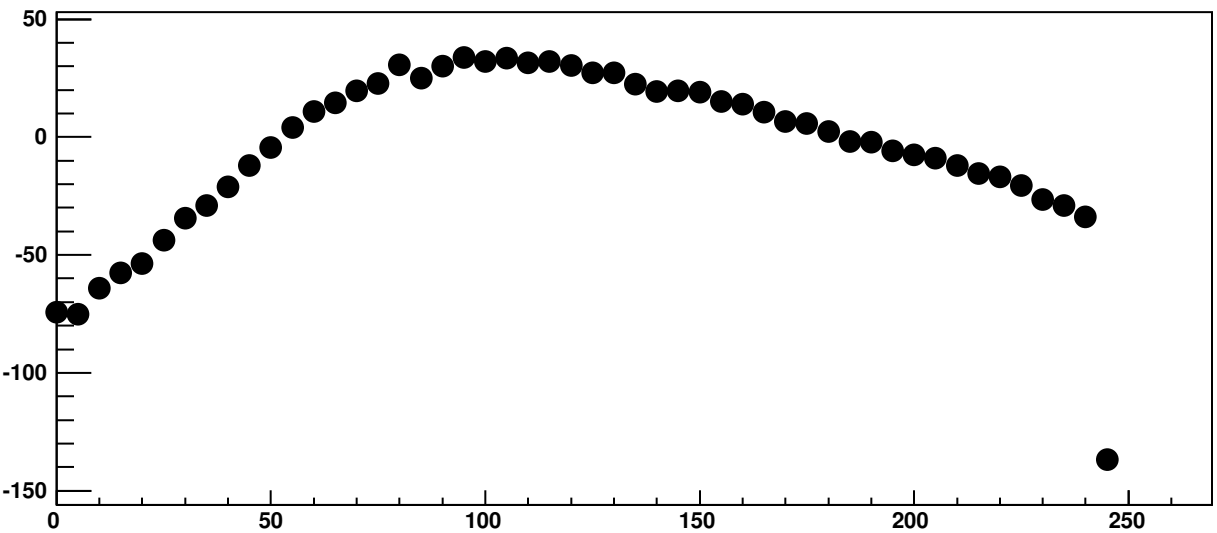
Chip 10, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold



Chip 10, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold

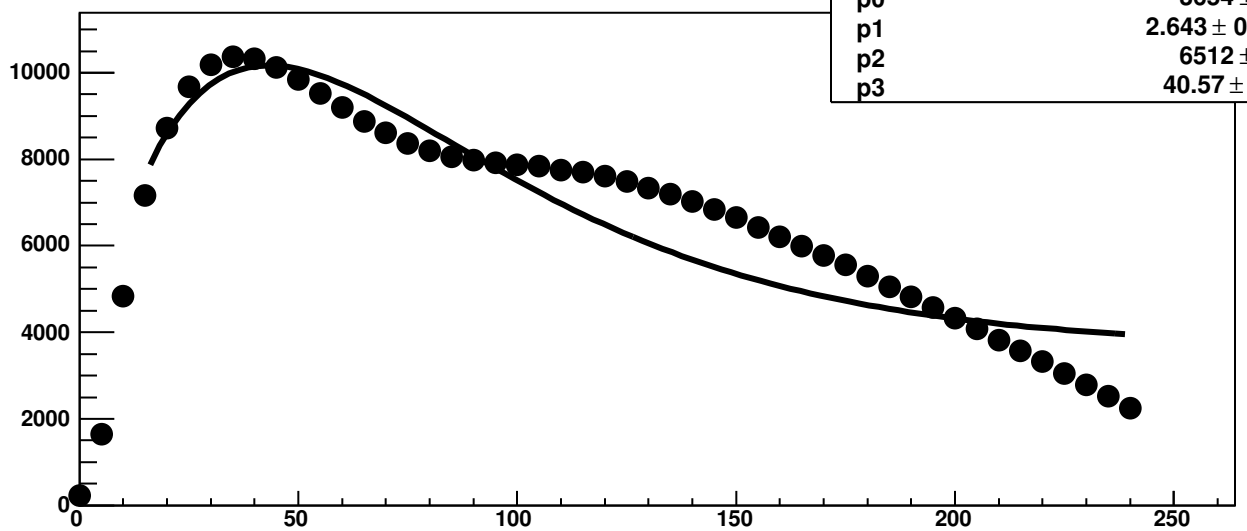


Chip 10, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

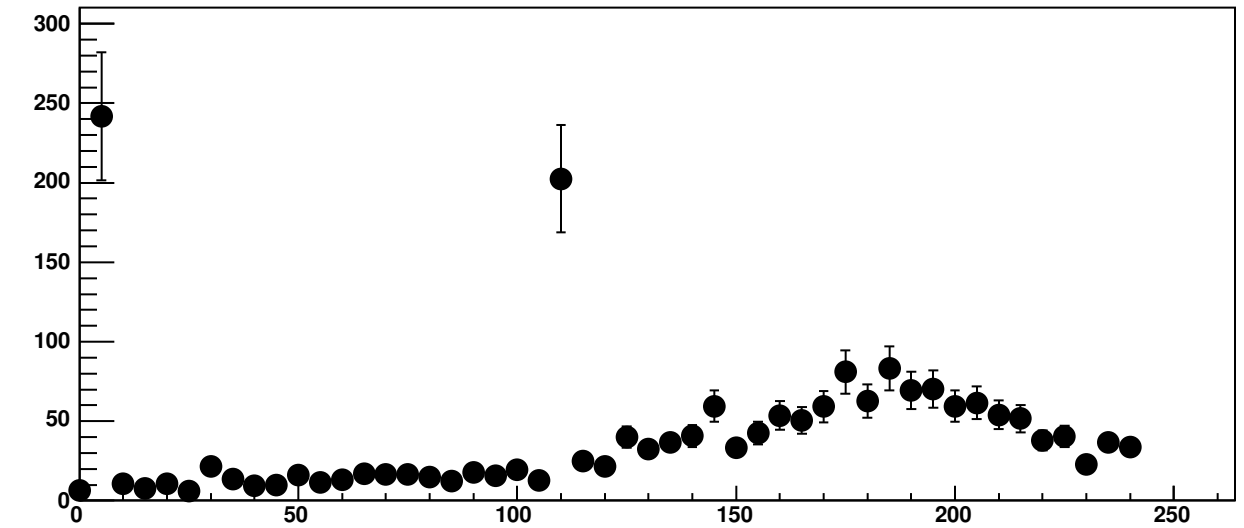


Chip 10, Channel 7, Enable 5!, DAC=4000, ADC Mean vs Hold

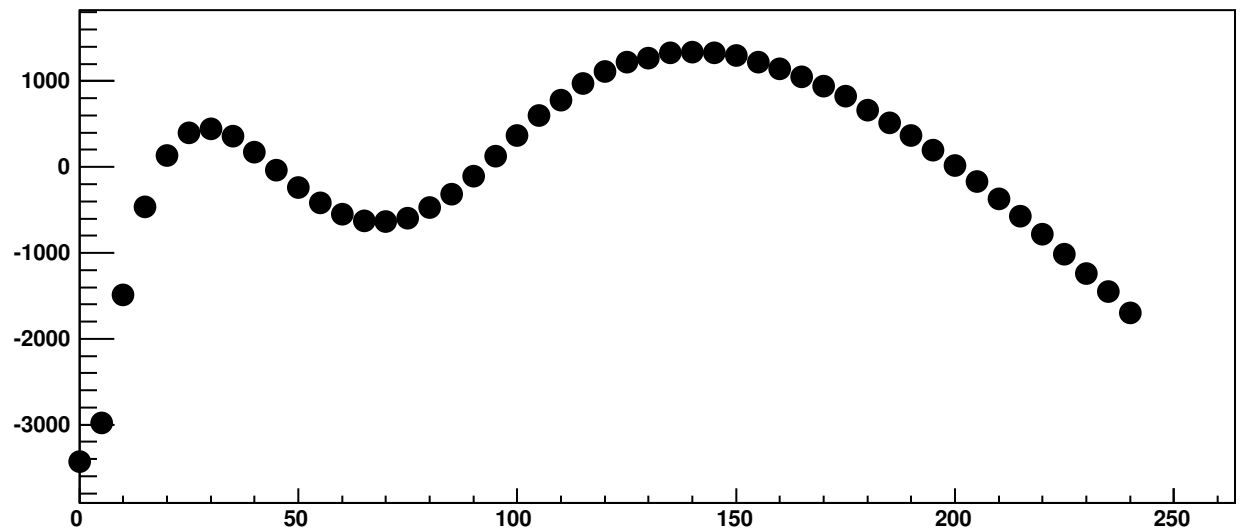
χ^2 / ndf	8.072e+05 / 42
p0	3654 ± 4.104
p1	2.643 ± 0.02143
p2	6512 ± 3.914
p3	40.57 ± 0.0291



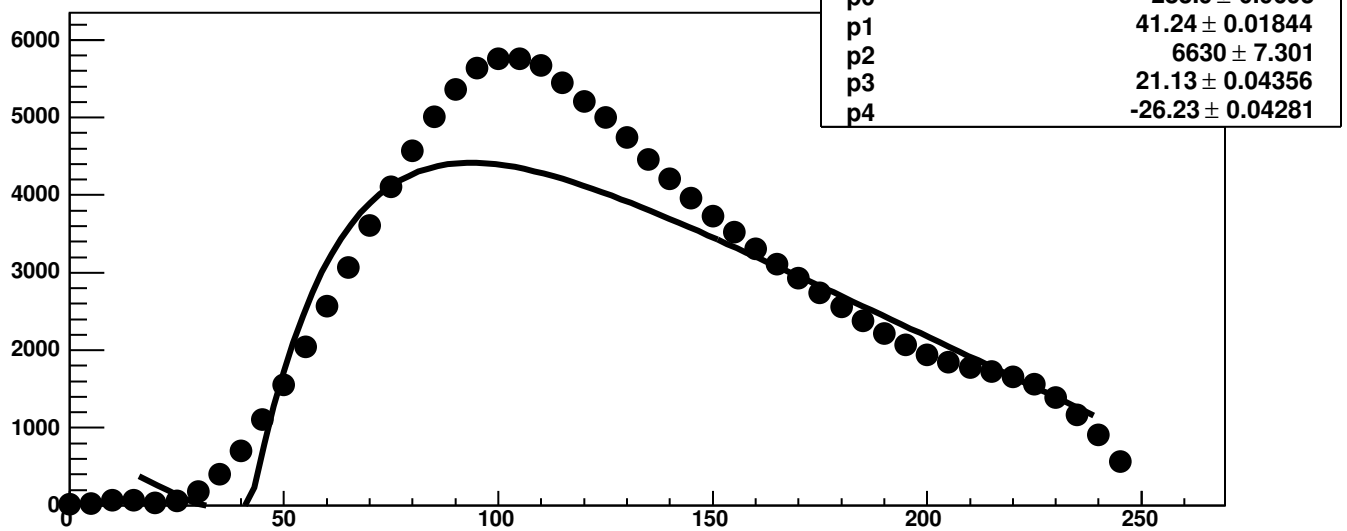
Chip 10, Channel 7, Enable 5!, DAC=4000, ADC Noise vs Hold



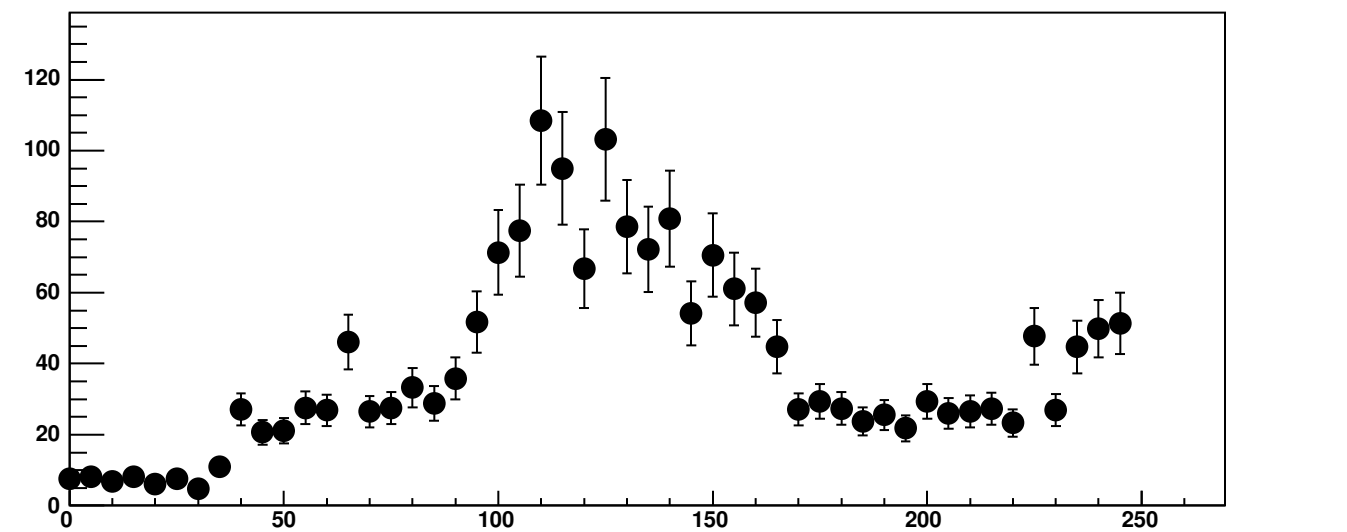
Chip 10, Channel 7, Enable 5!, DAC=4000, ADC Residuals vs Hold



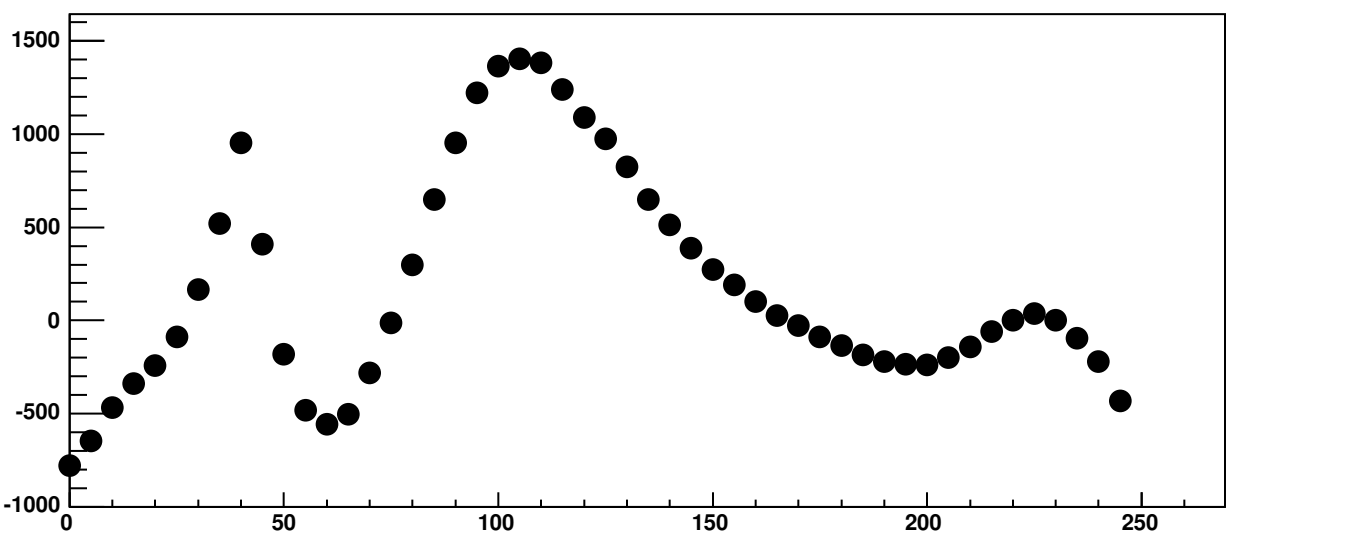
Chip 10, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold



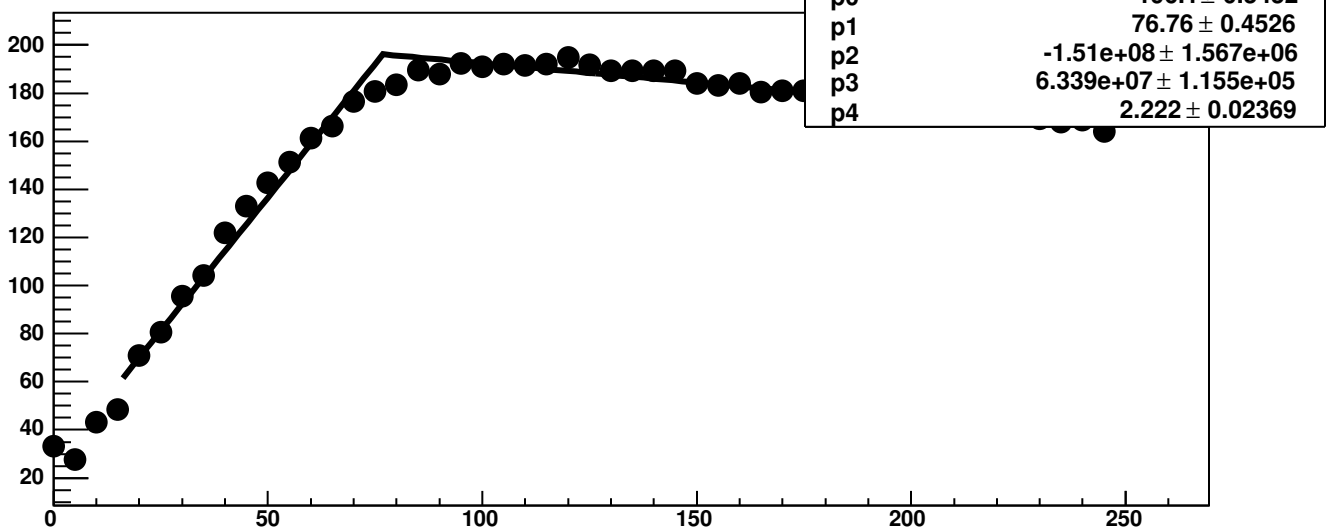
Chip 10, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



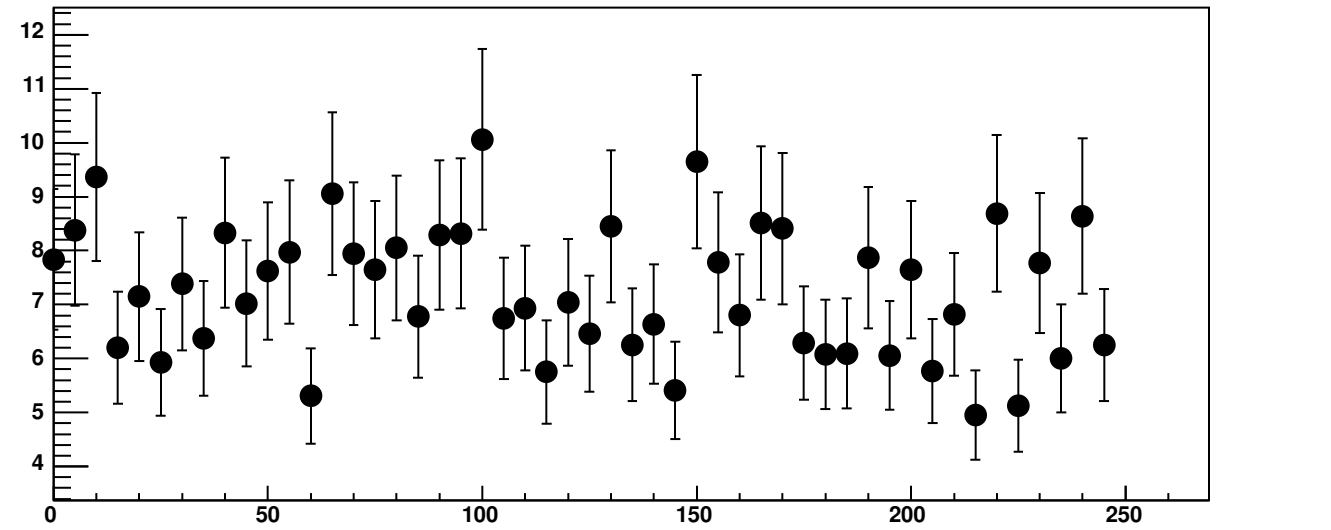
Chip 10, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold



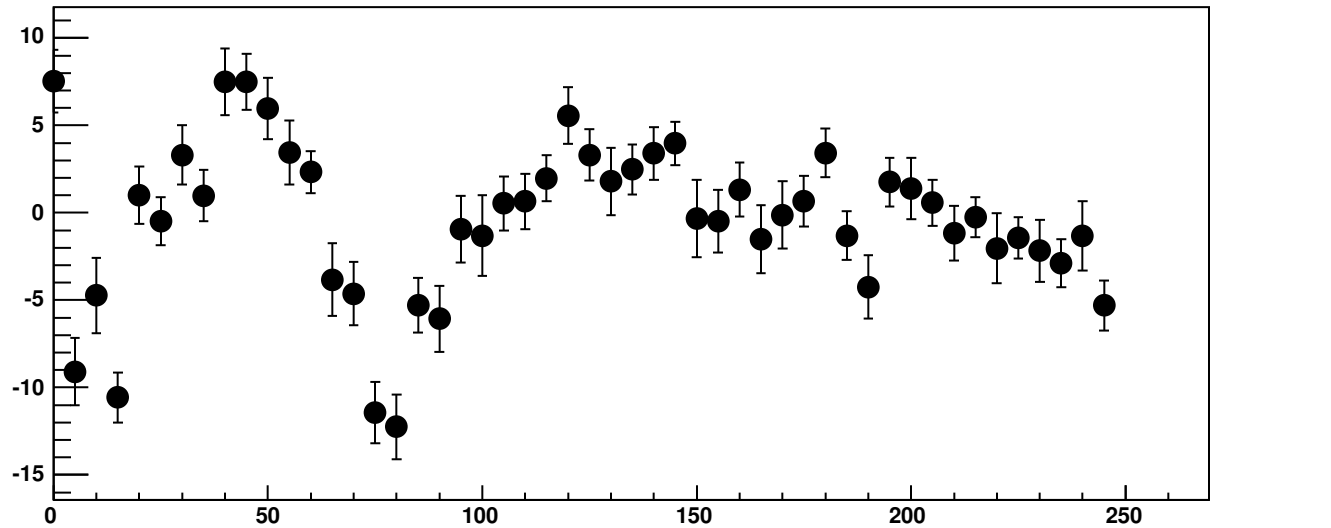
Chip 10, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

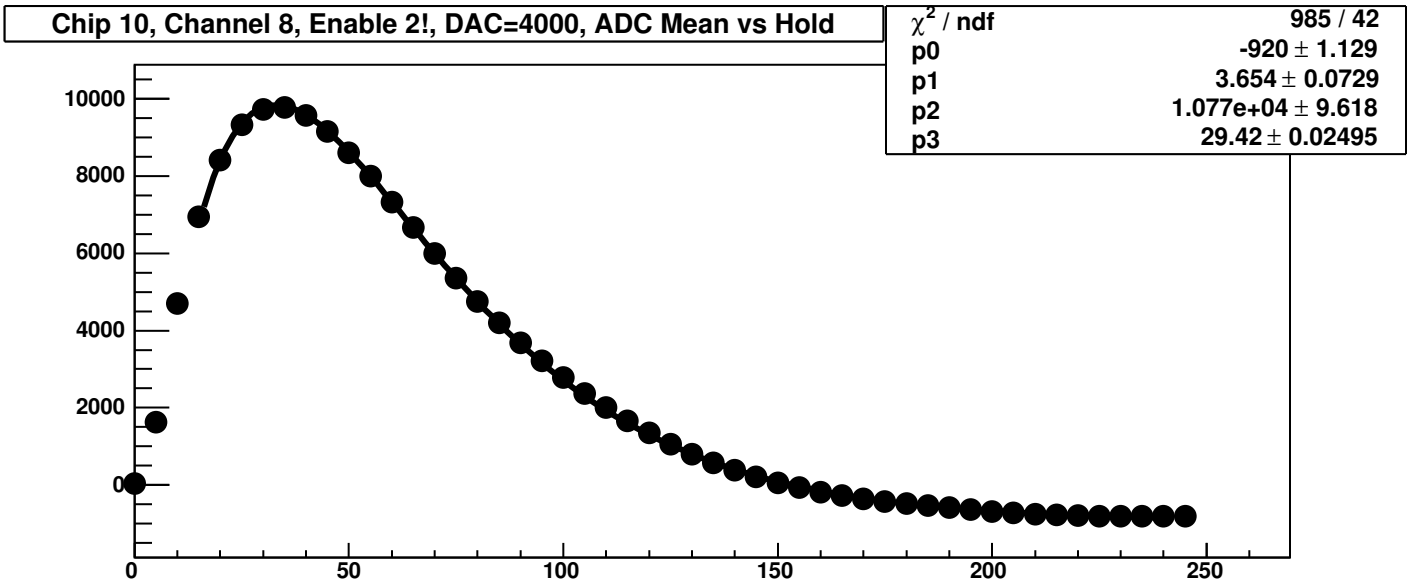


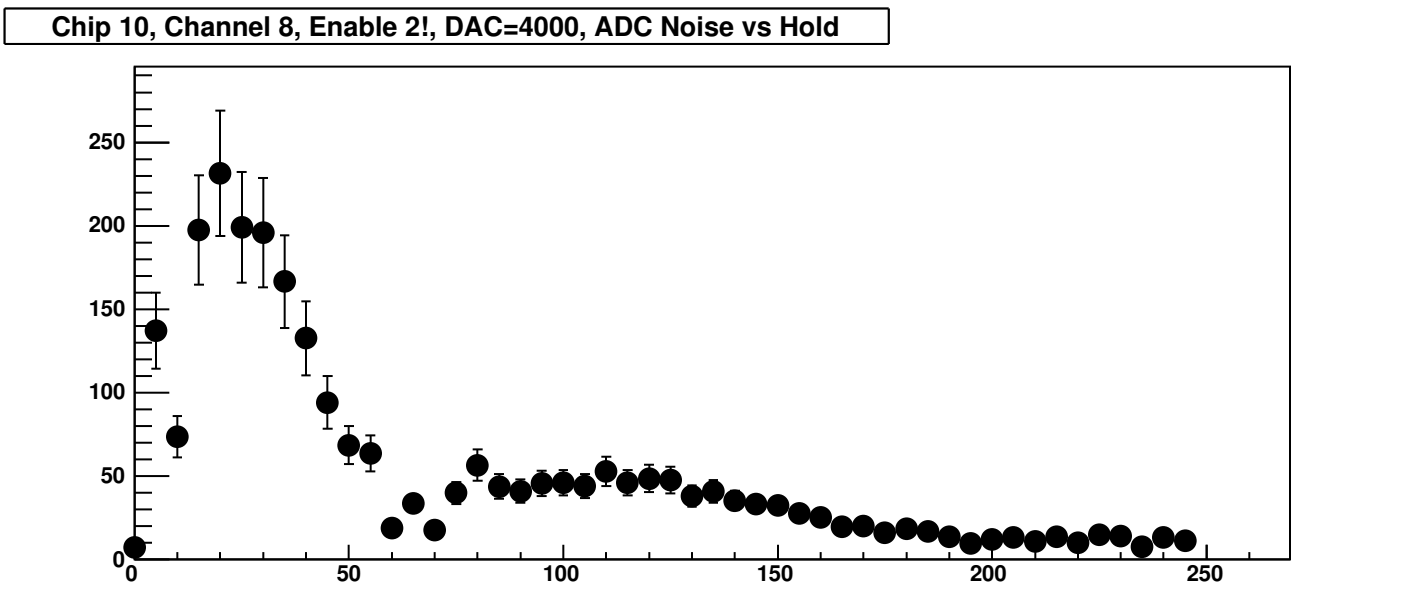
Chip 10, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold

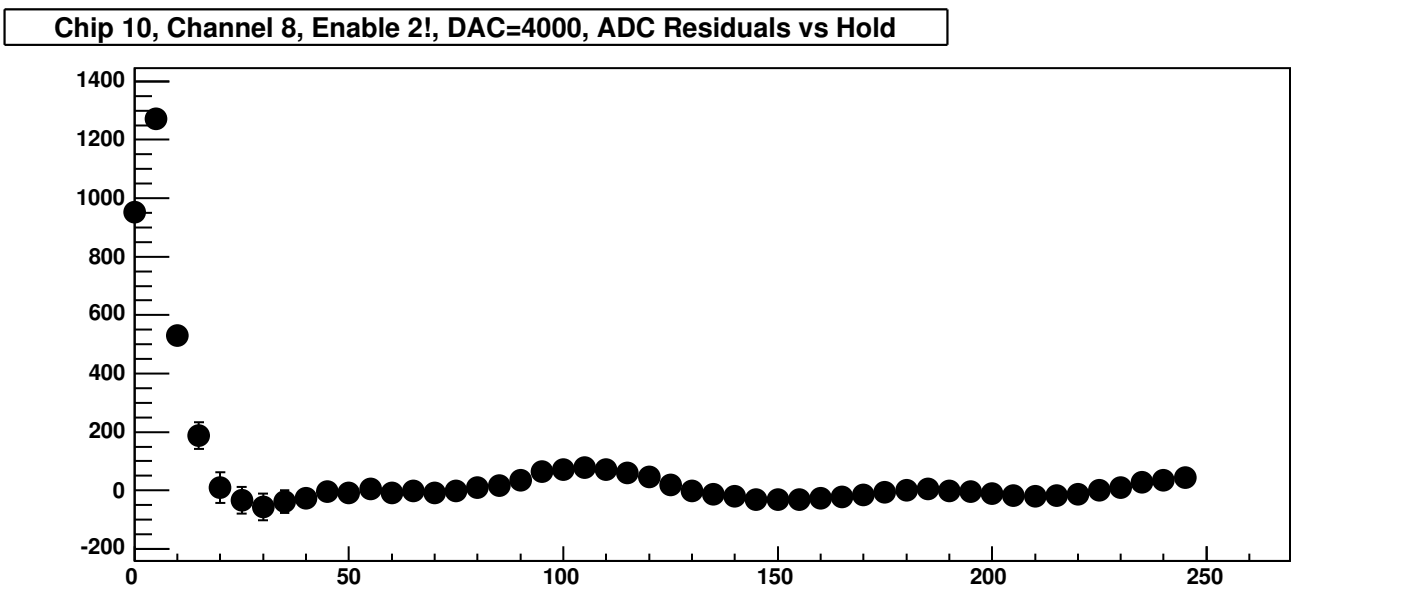


Chip 10, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

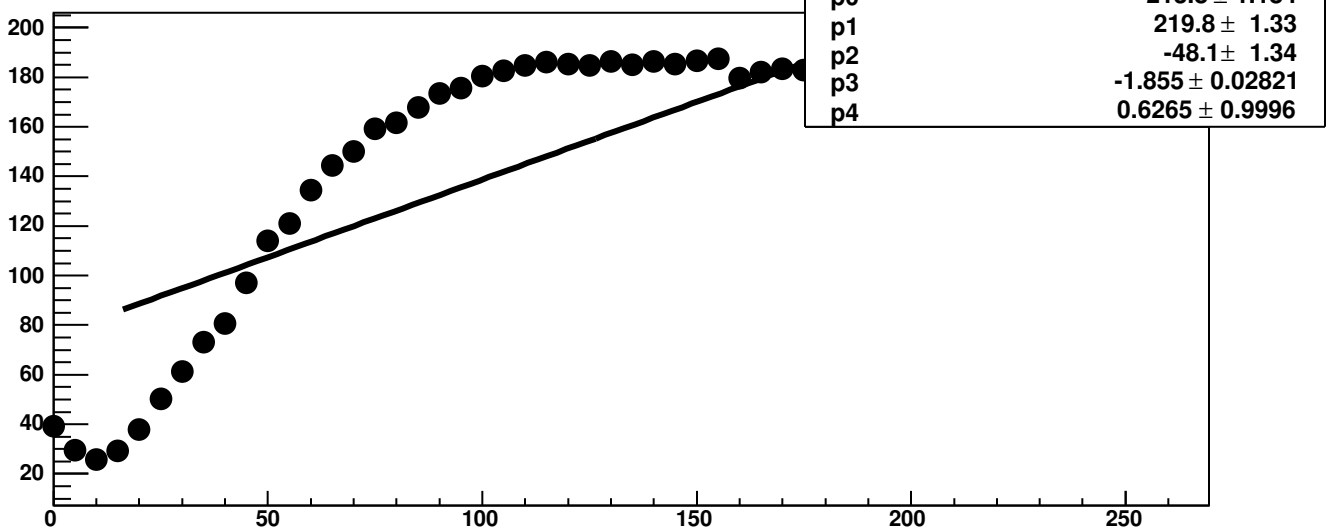



Chip 10, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 985 / 42
p0 -920 ± 1.129
p1 3.654 ± 0.0729
p2 1.077e+04 ± 9.618
p3 29.42 ± 0.02495
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

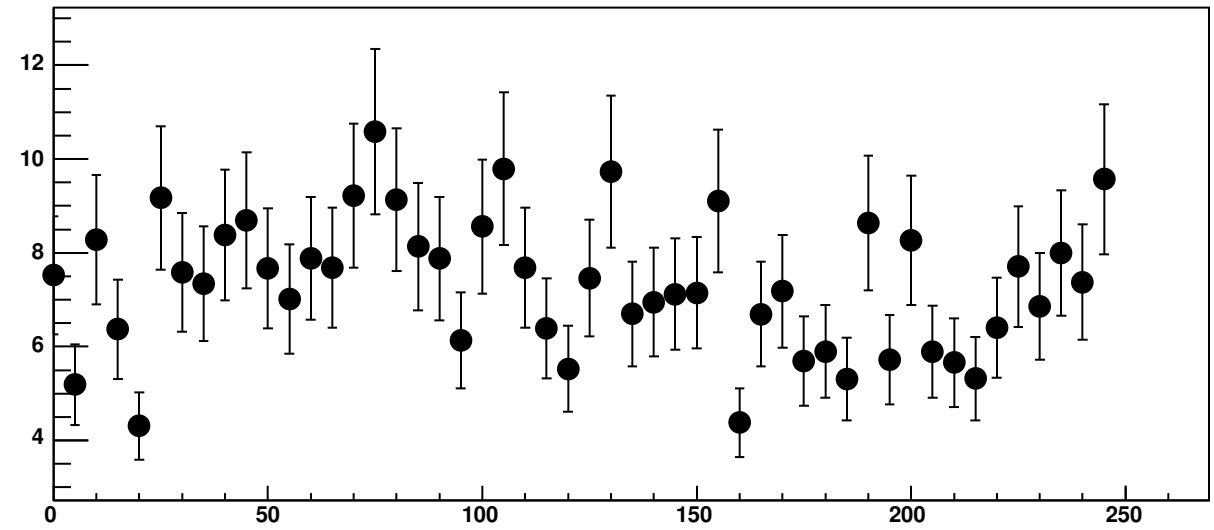

Chip 10, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250


Chip 10, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

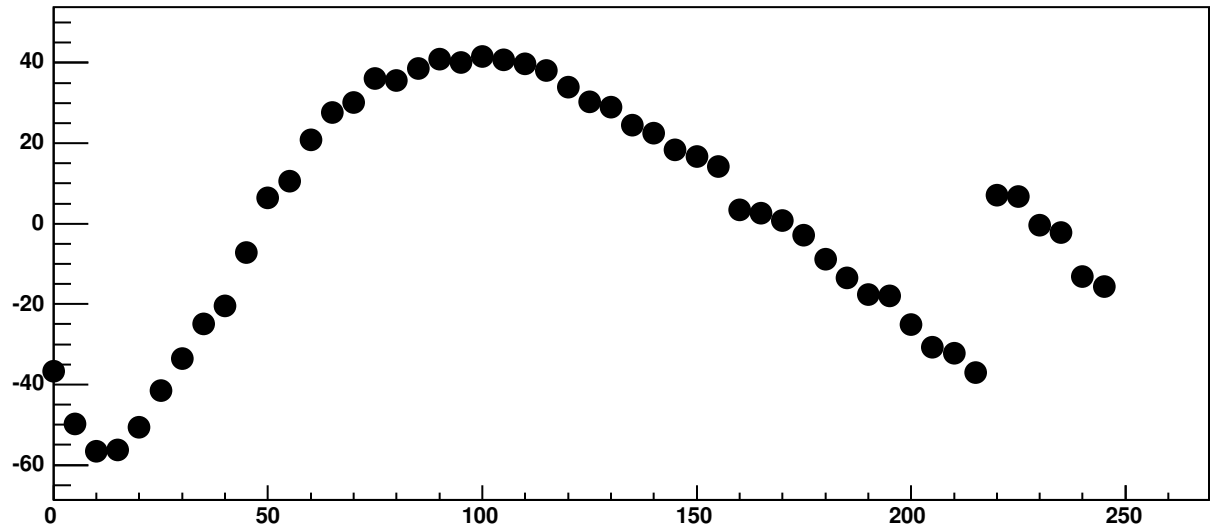
Chip 10, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold



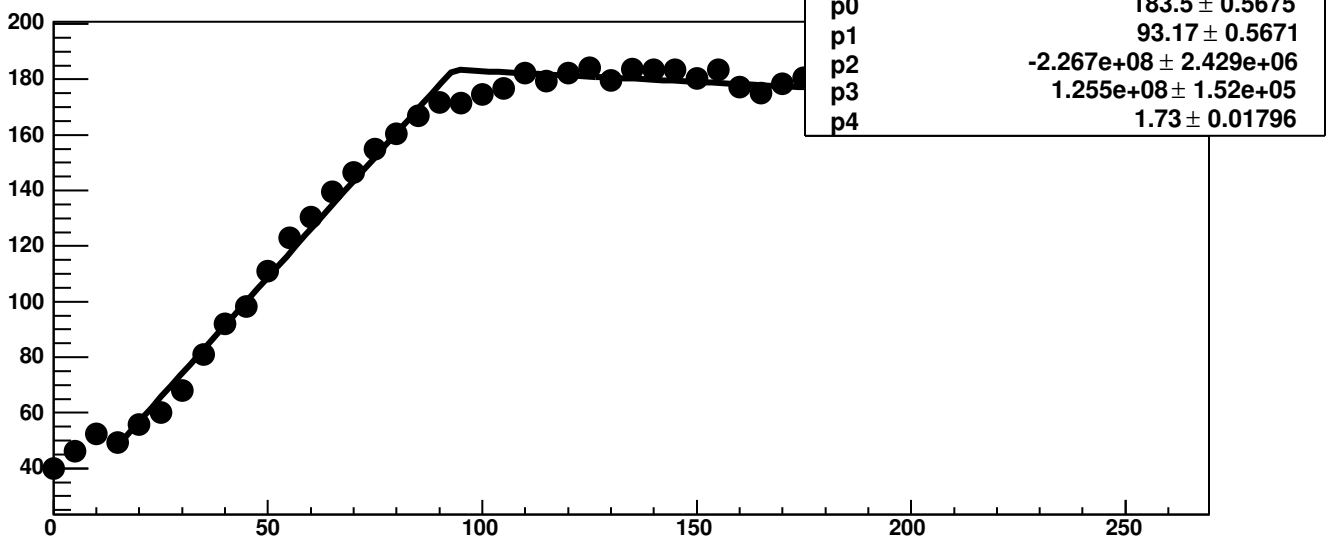
Chip 10, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



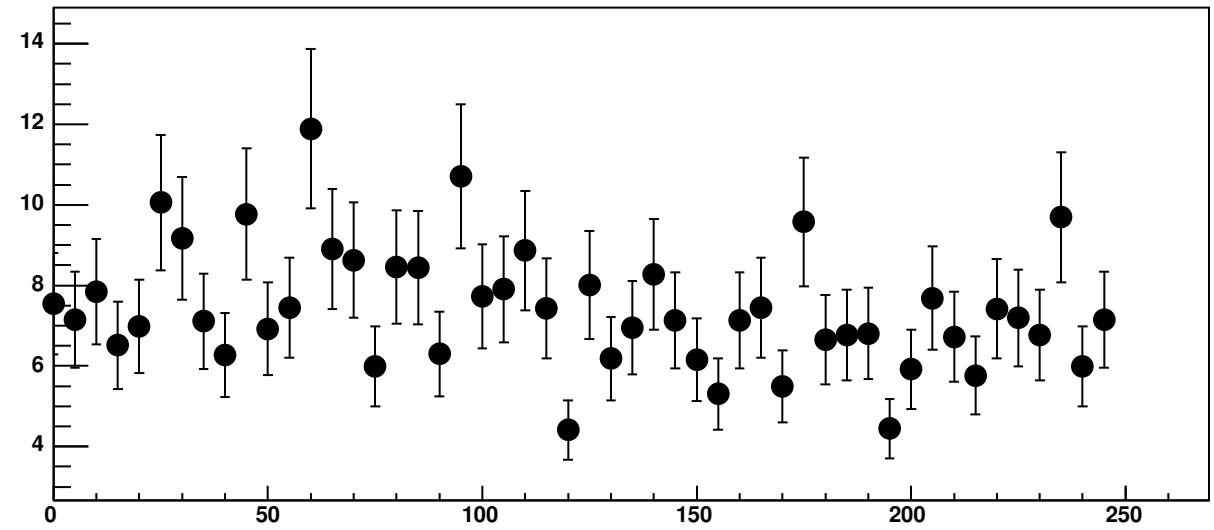
Chip 10, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold



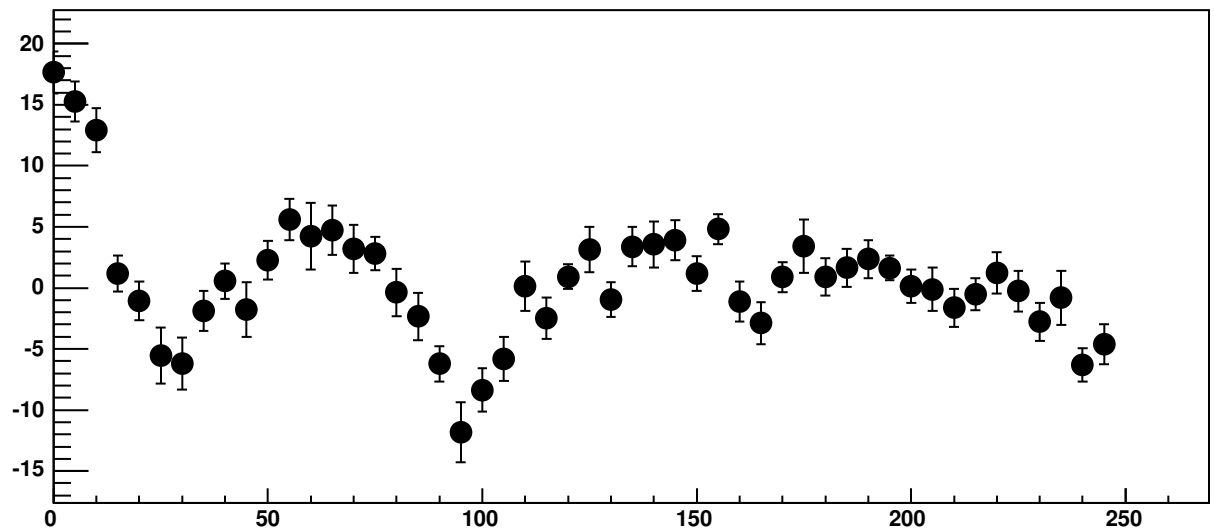
Chip 10, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

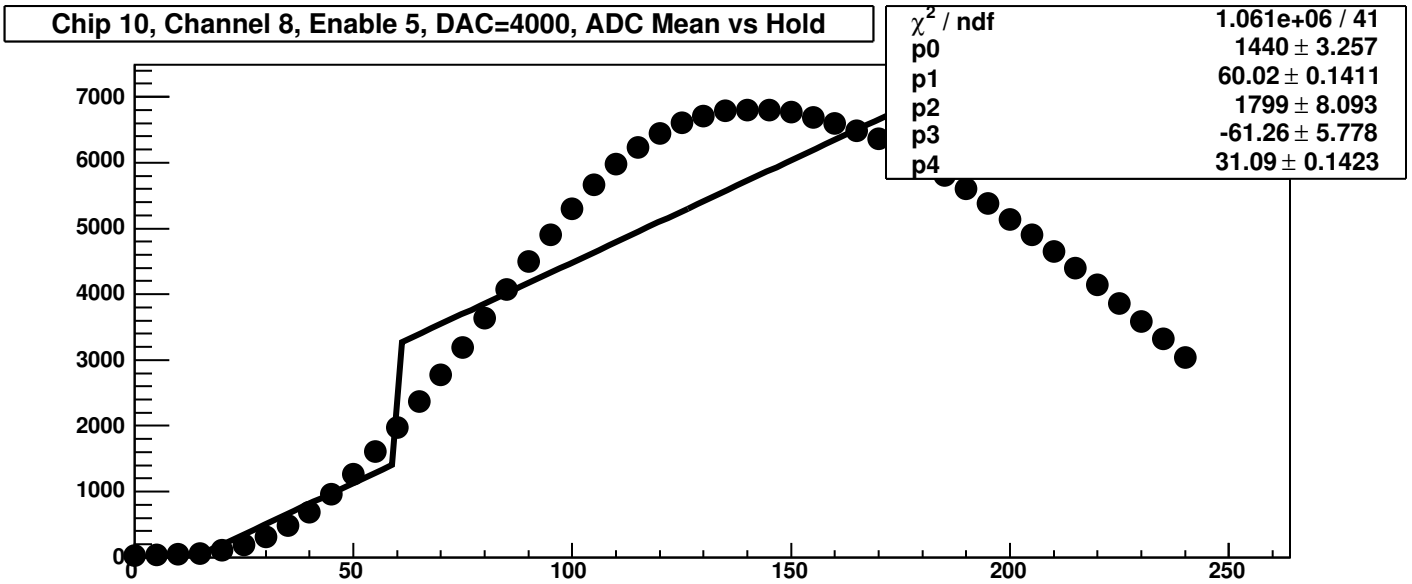


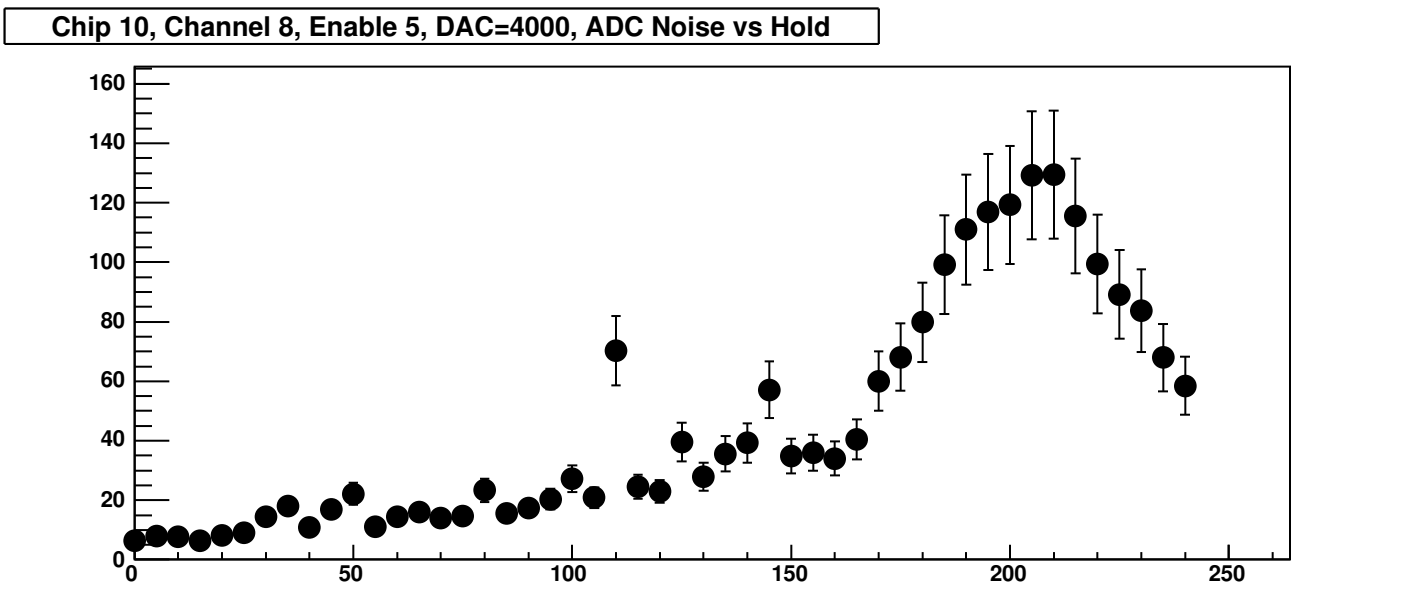
Chip 10, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold

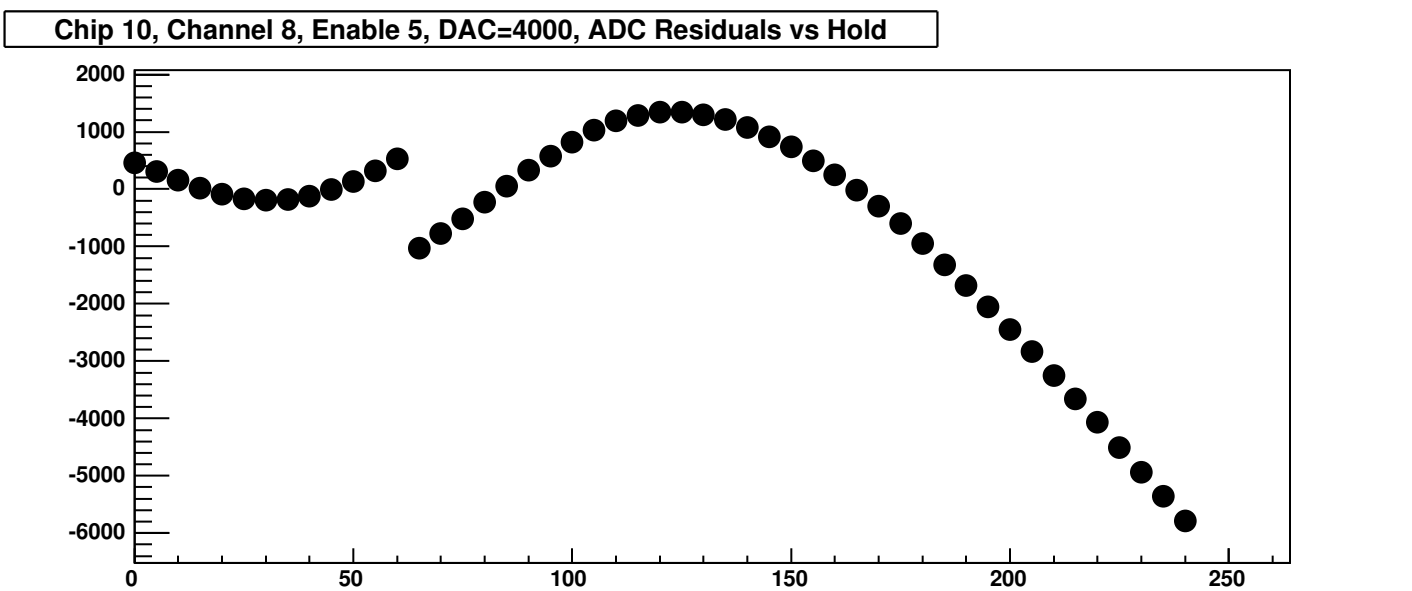


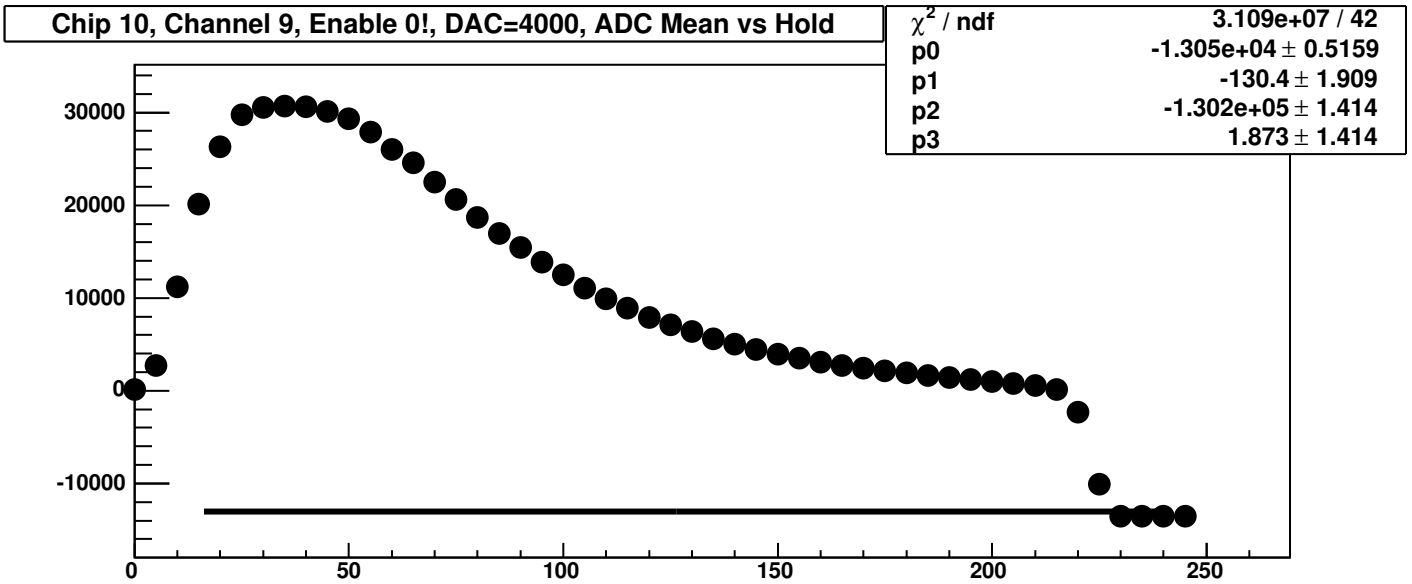
Chip 10, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

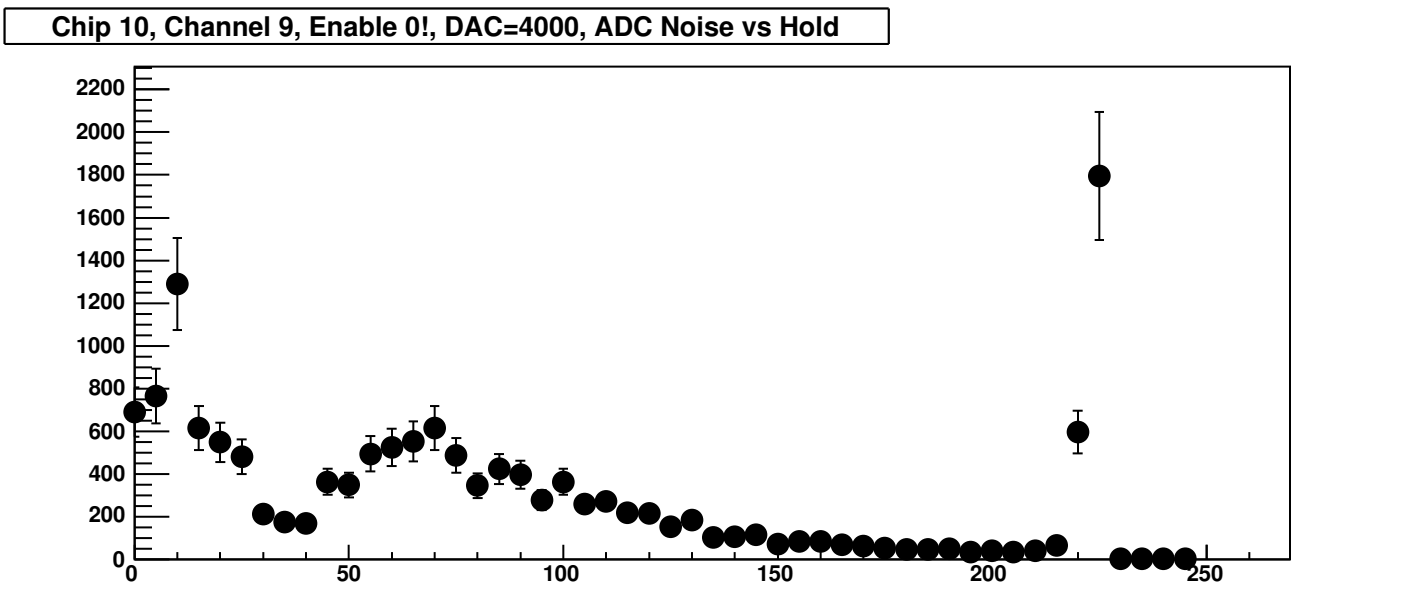


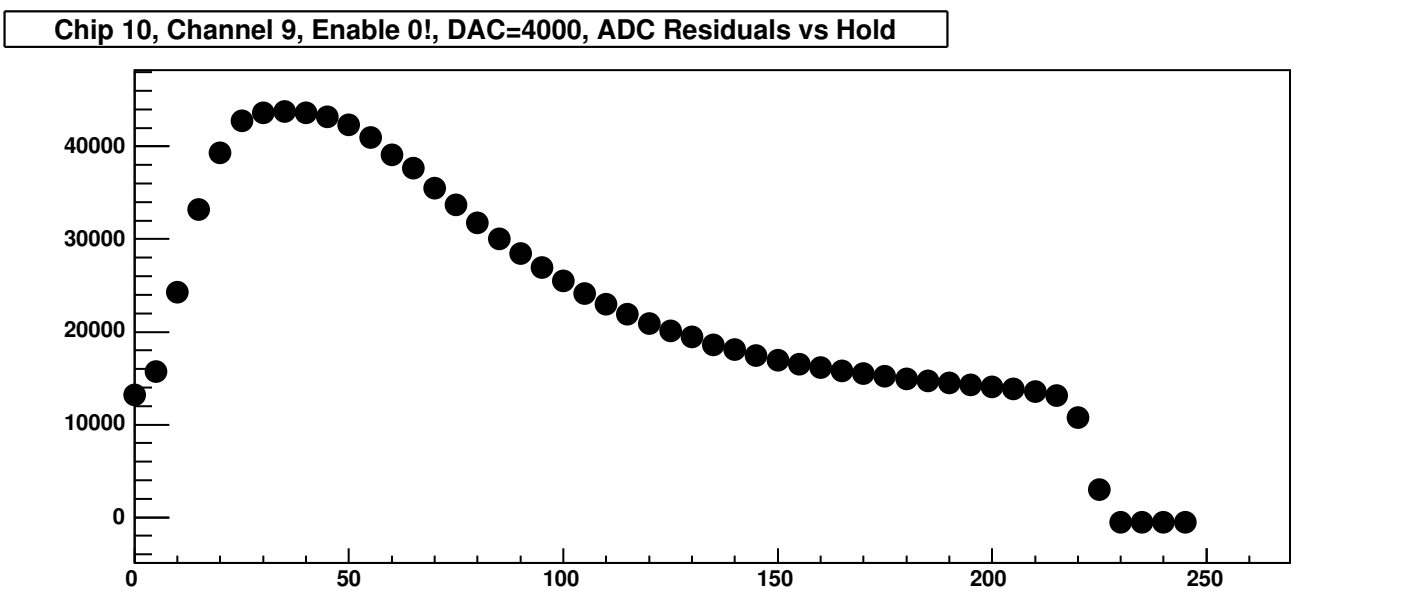

Chip 10, Channel 8, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.061e+06 / 41
p0 1440 ± 3.257
p1 60.02 ± 0.1411
p2 1799 ± 8.093
p3 -61.26 ± 5.778
p4 31.09 ± 0.1423
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

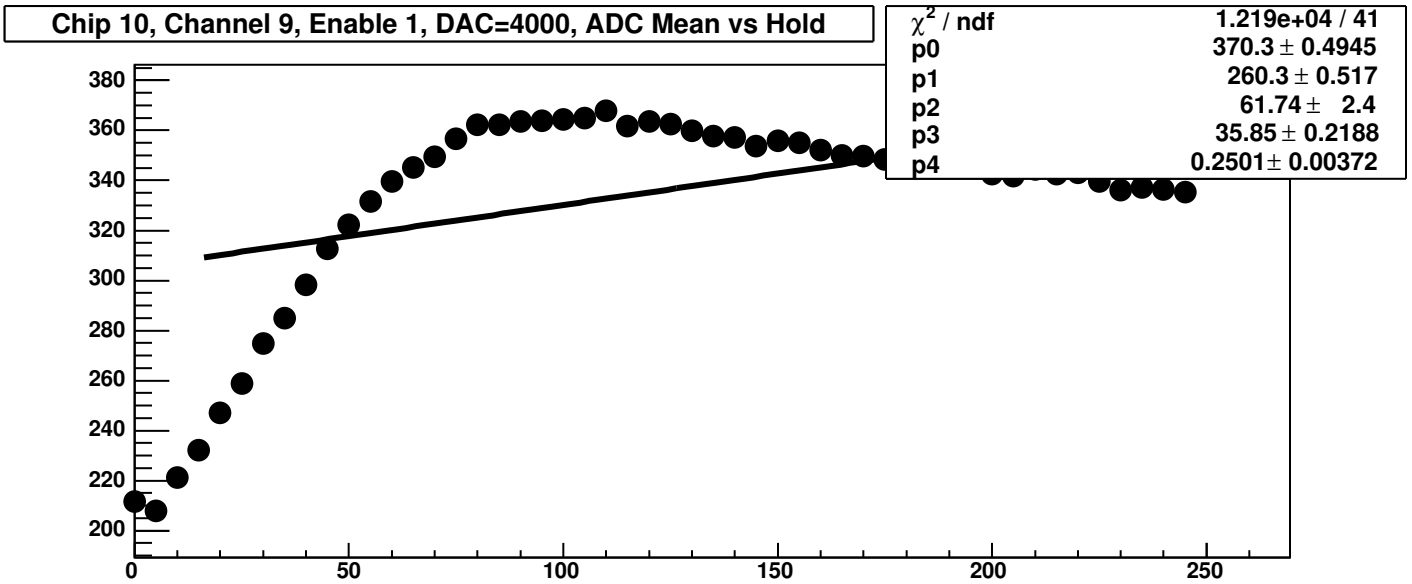

Chip 10, Channel 8, Enable 5, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

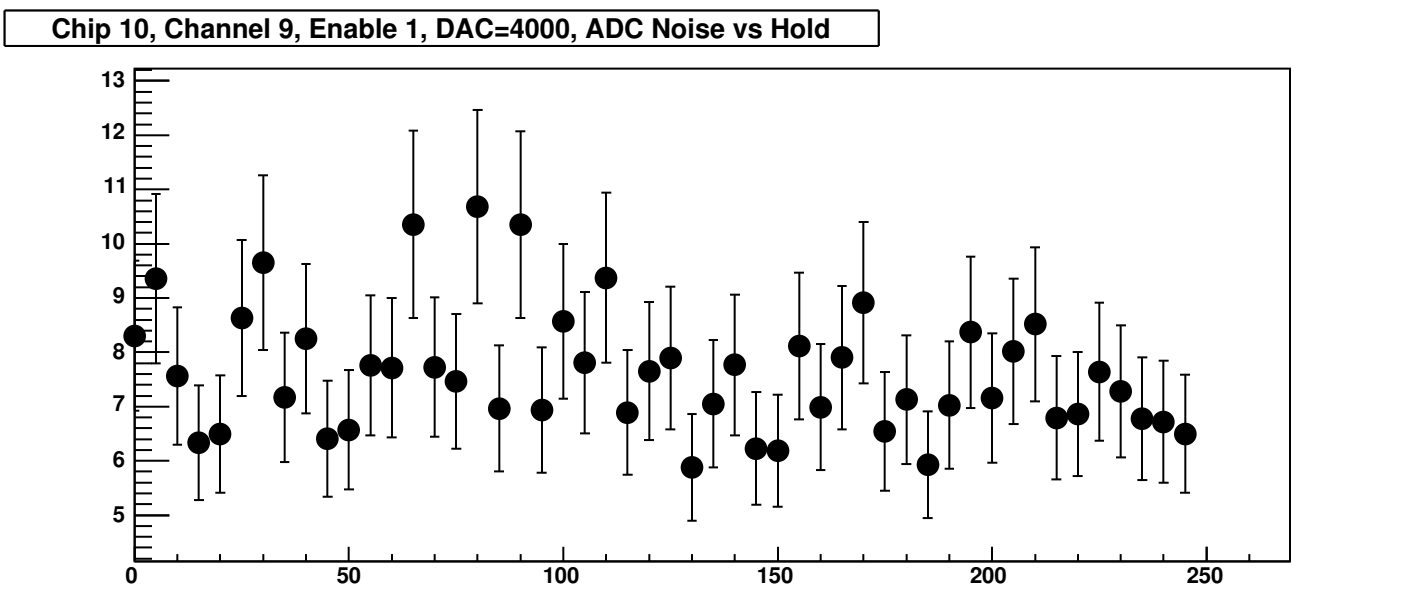

Chip 10, Channel 8, Enable 5, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
50
100
150
200
250

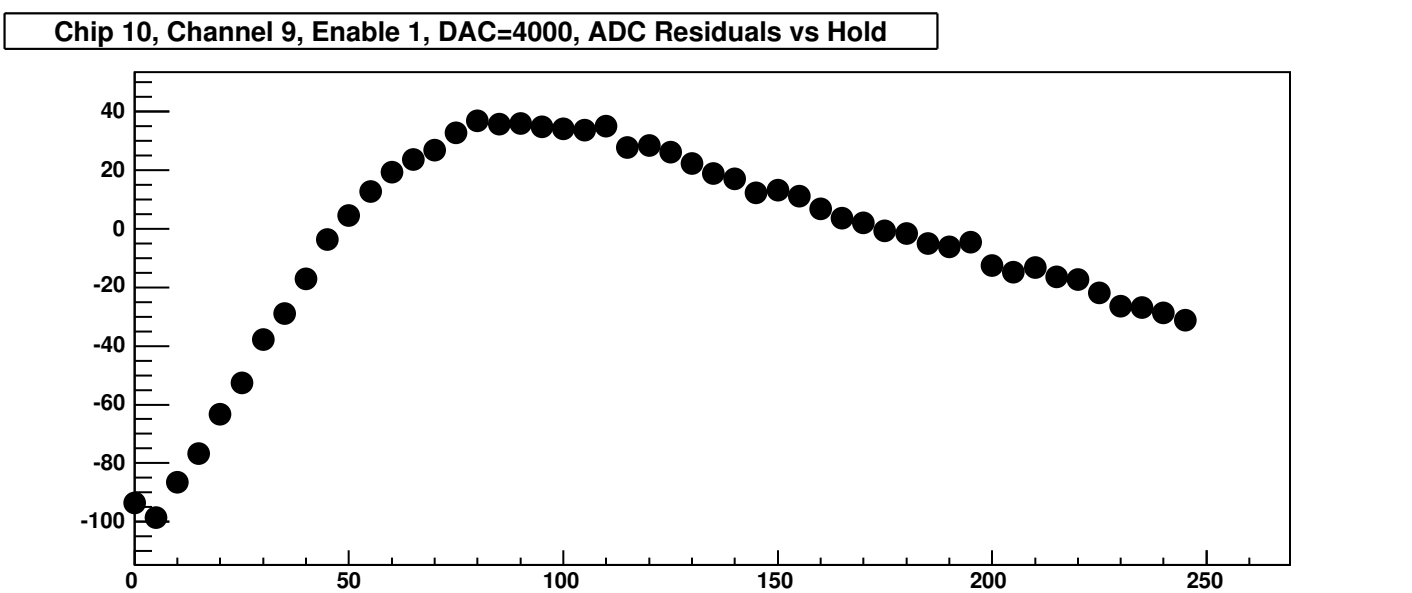

Chip 10, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.109e+07 / 42
p0 -1.305e+04 ± 0.5159
p1 -130.4 ± 1.909
p2 -1.302e+05 ± 1.414
p3 1.873 ± 1.414
30000
20000
10000
0
-10000
0
50
100
150
200
250


Chip 10, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold
2200
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

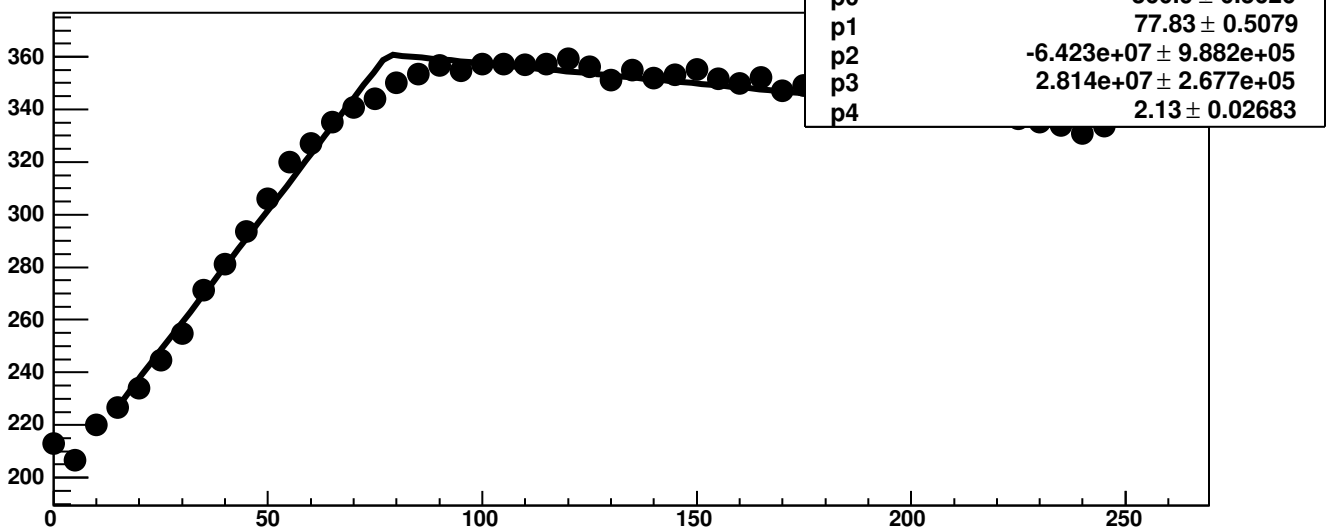

Chip 10, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold
40000
30000
20000
10000
0
0
50
100
150
200
250


Chip 10, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.219e+04 / 41
p0 370.3 ± 0.4945
p1 260.3 ± 0.517
p2 61.74 ± 2.4
p3 35.85 ± 0.2188
p4 0.2501 ± 0.00372
380
360
340
320
300
280
260
240
220
200
0
50
100
150
200
250

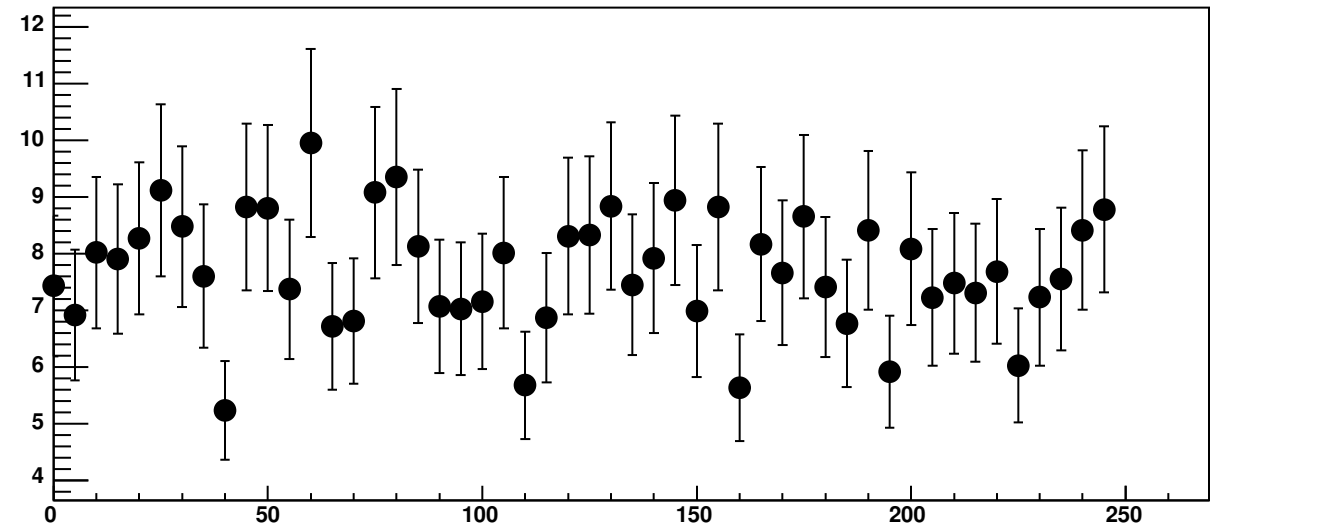

Chip 10, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
0
50
100
150
200
250


Chip 10, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
-100
0
50
100
150
200
250

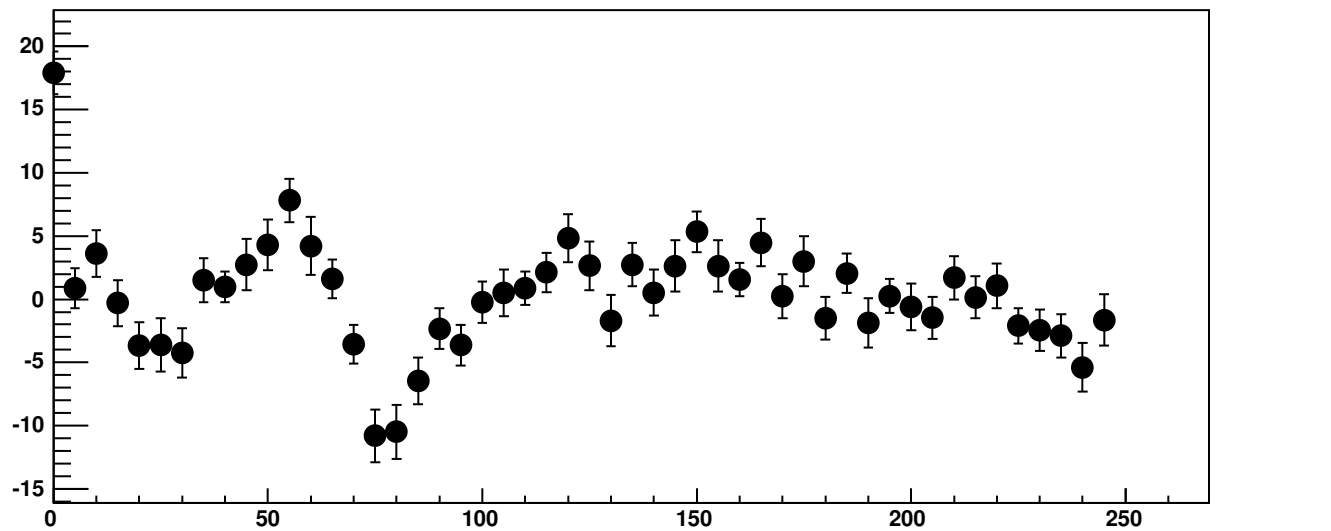
Chip 10, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold



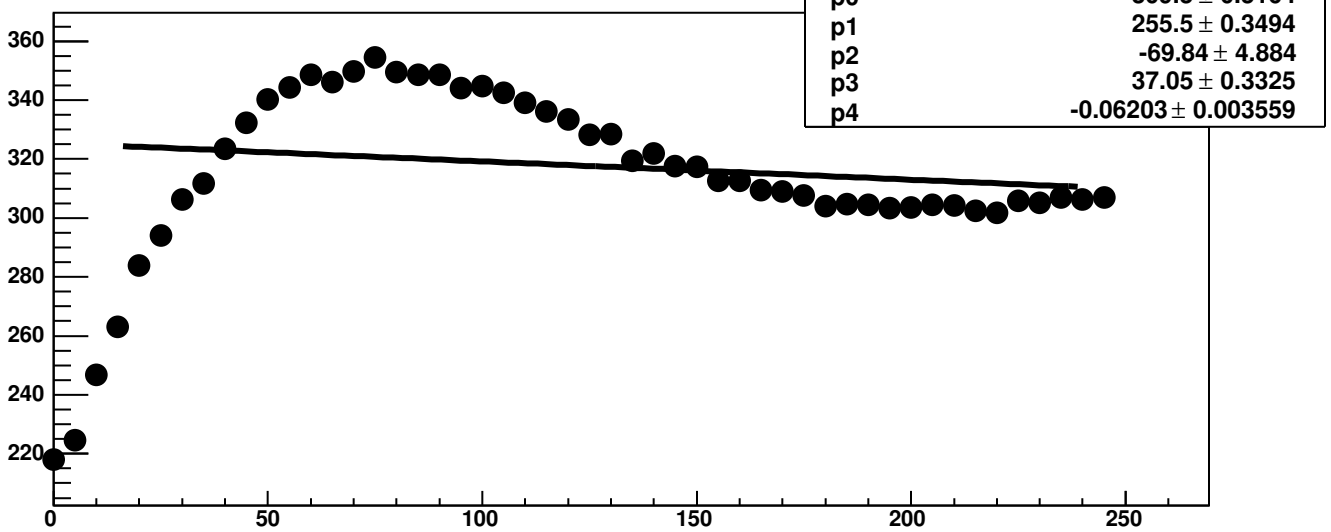
Chip 10, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



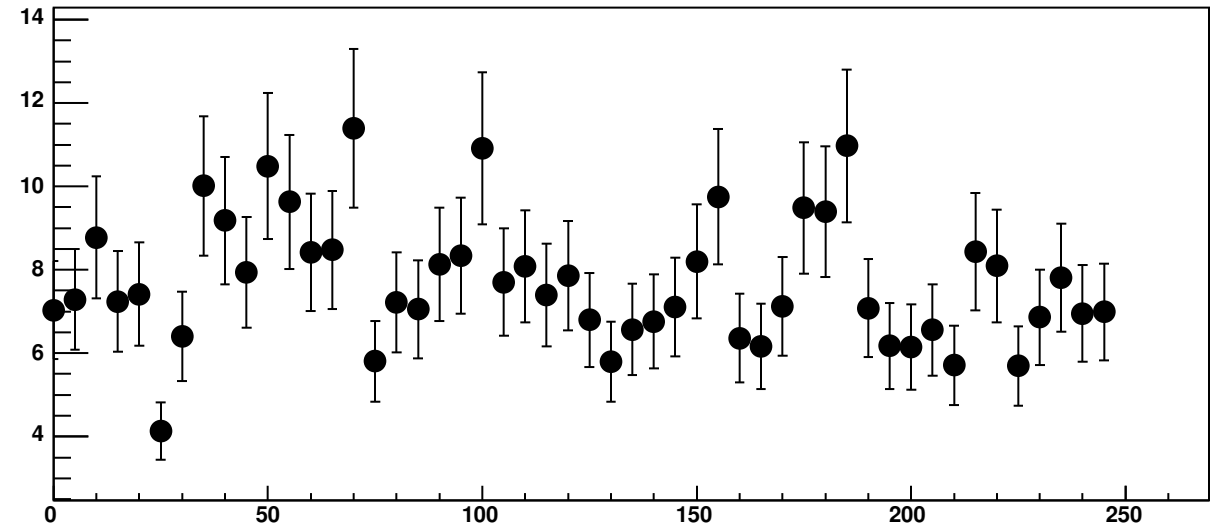
Chip 10, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold



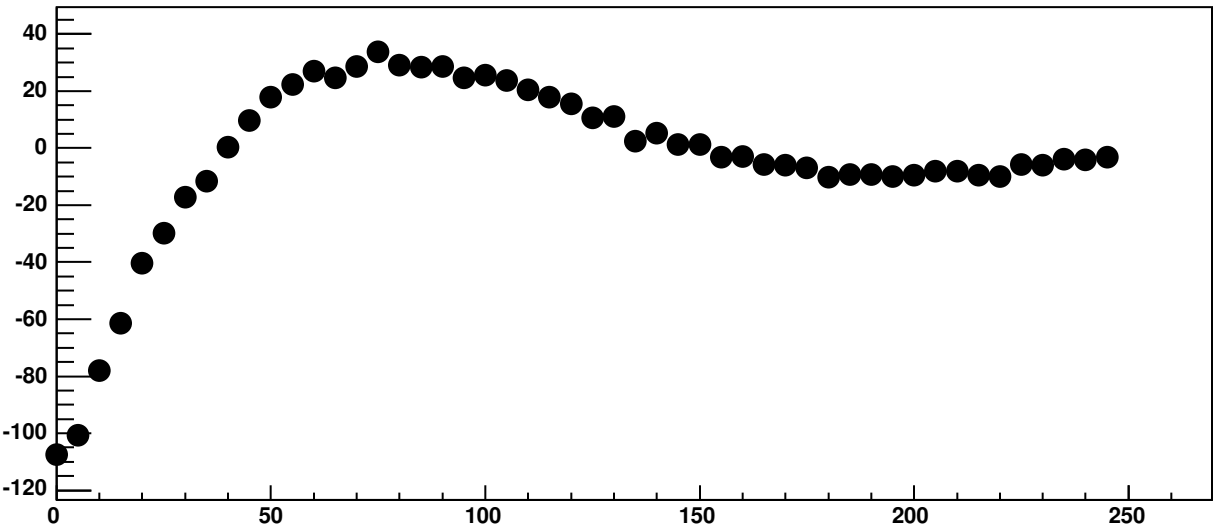
Chip 10, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold



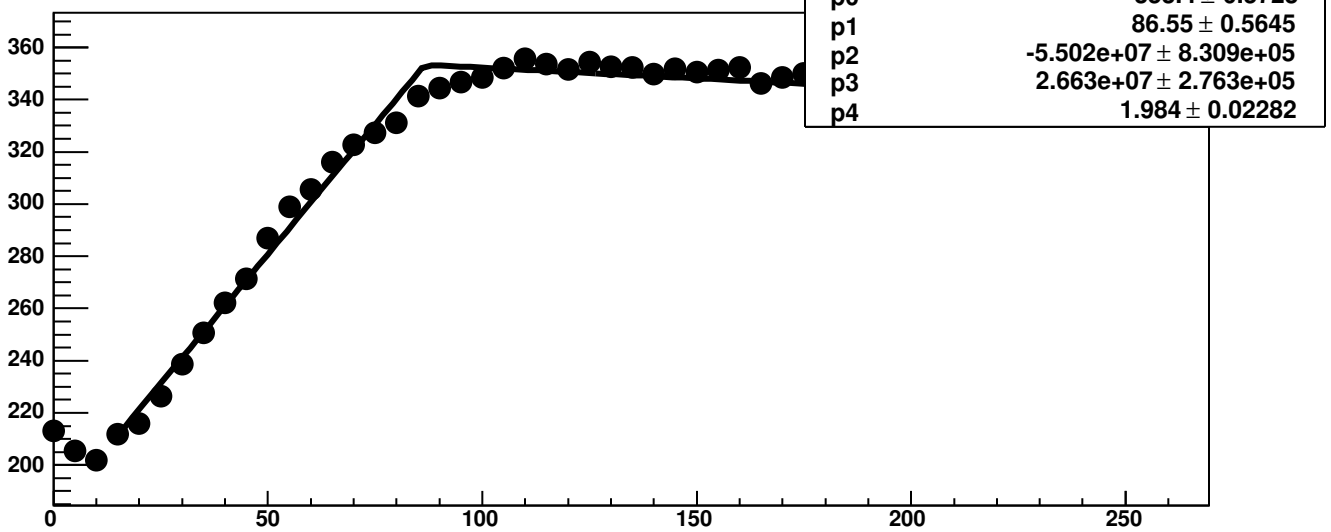
Chip 10, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold



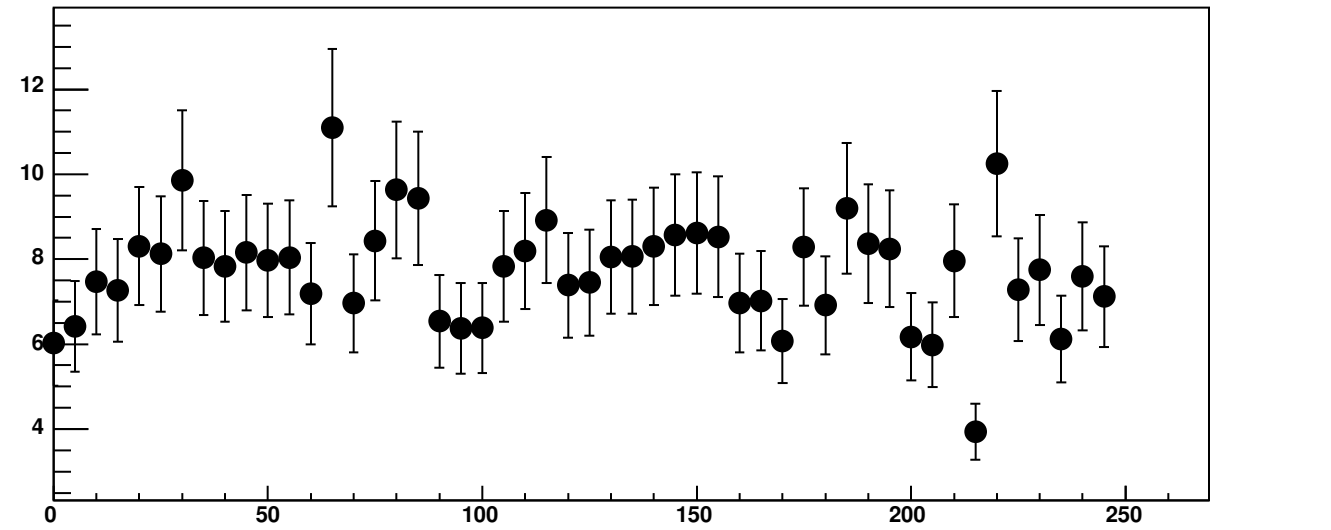
Chip 10, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold



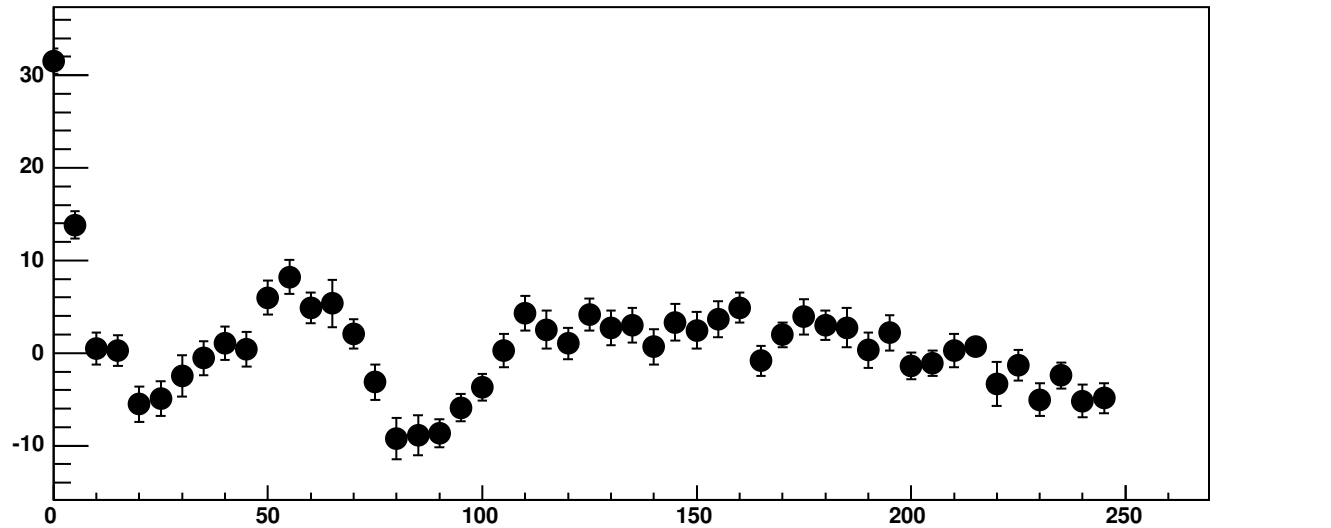
Chip 10, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold



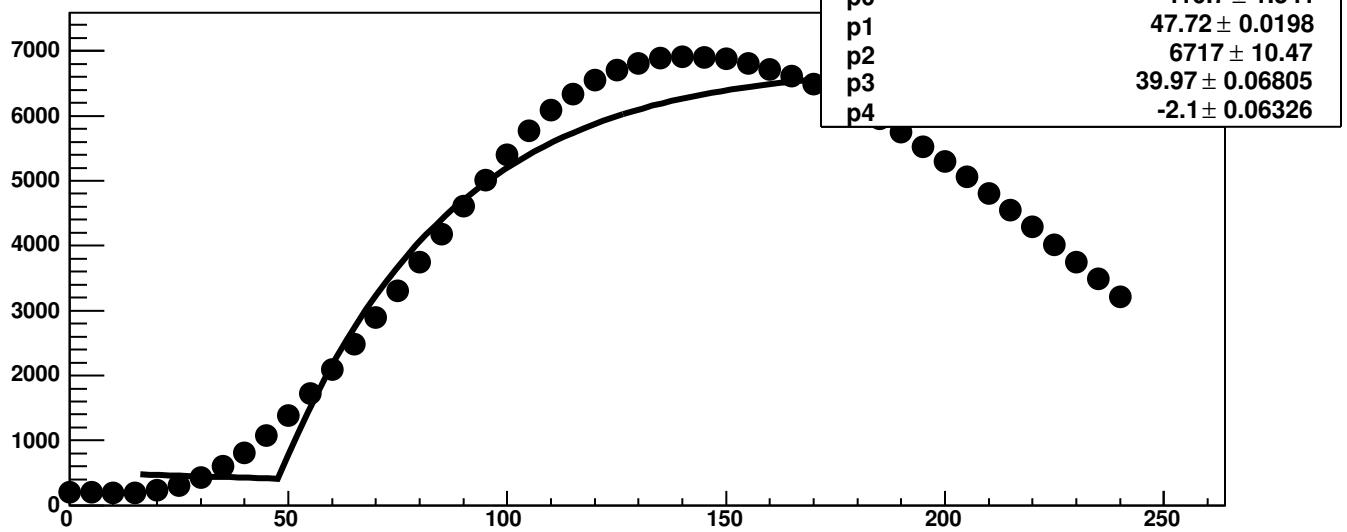
Chip 10, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



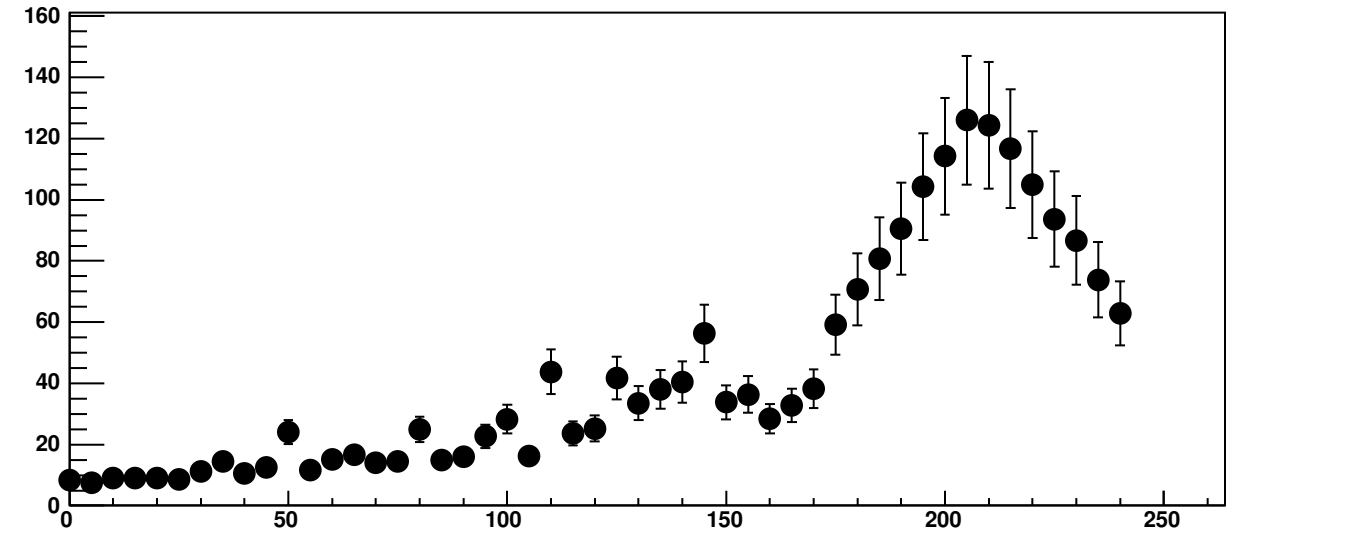
Chip 10, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold



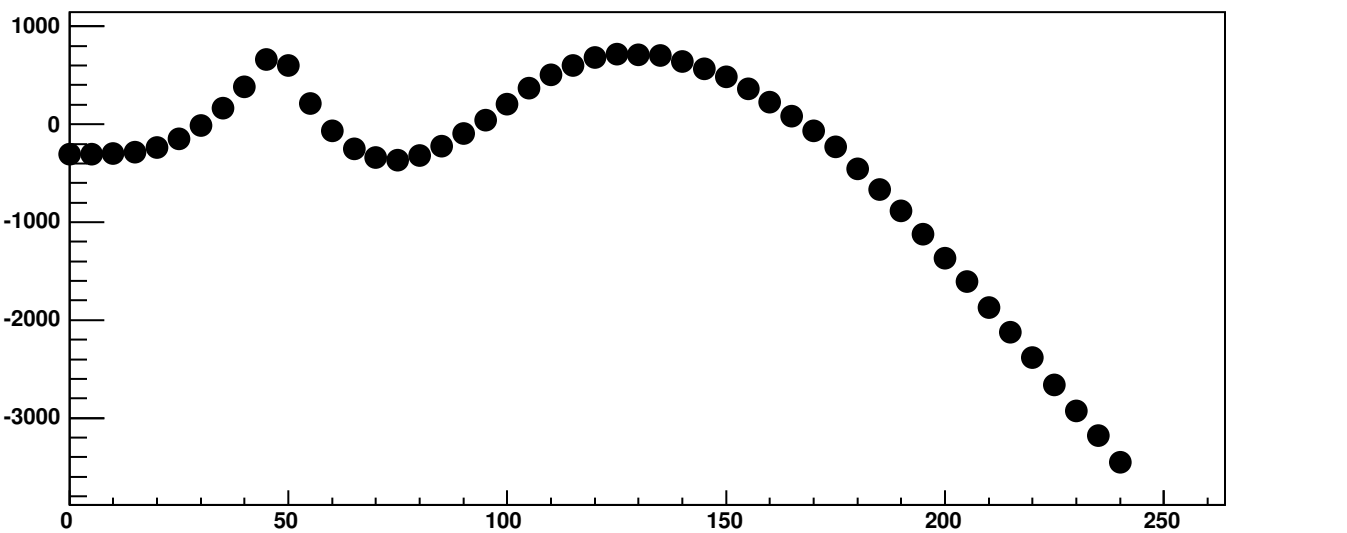
Chip 10, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold

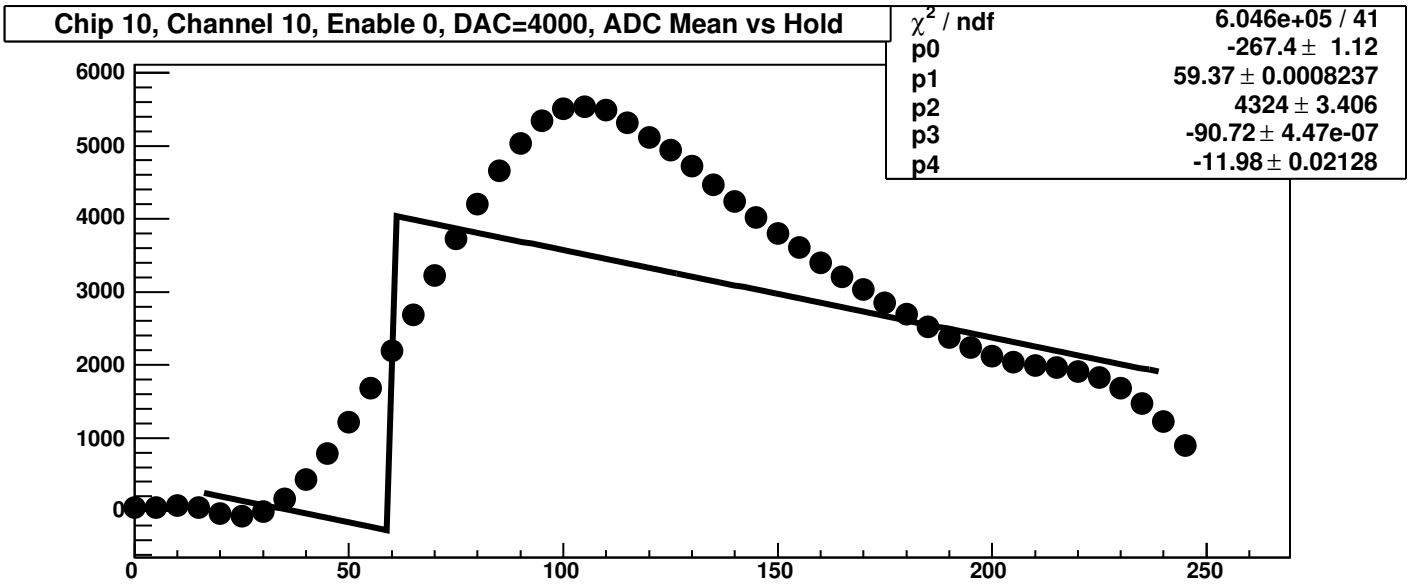


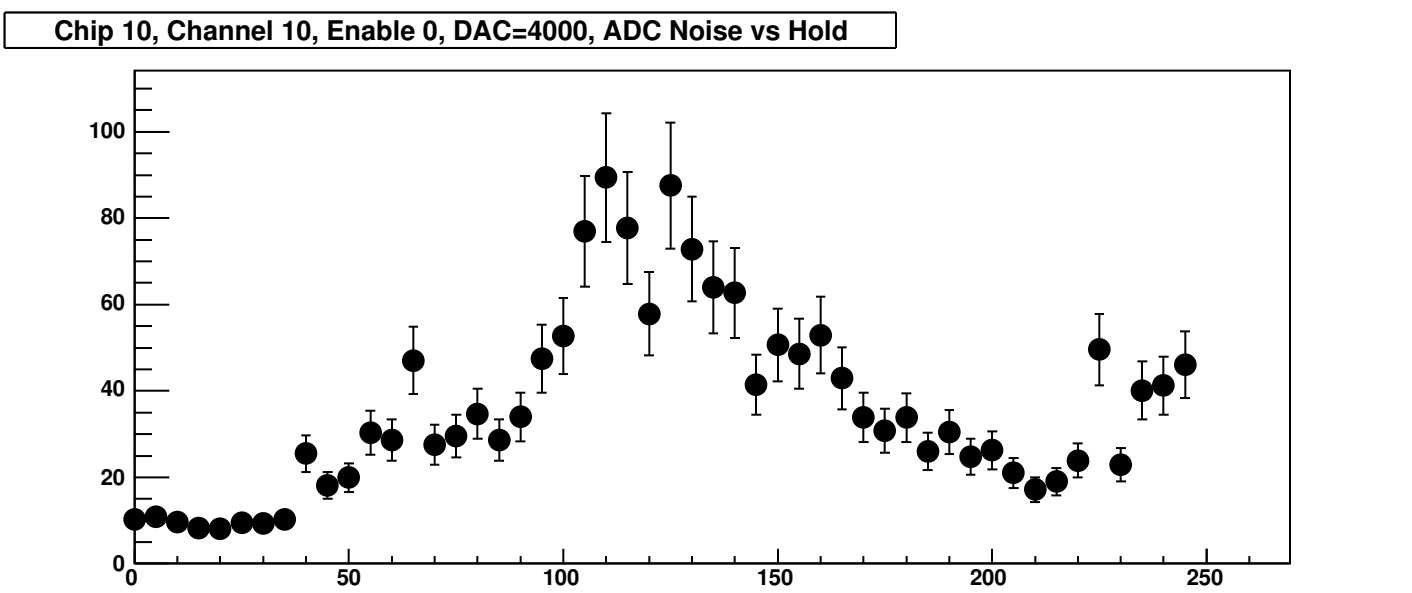
Chip 10, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold

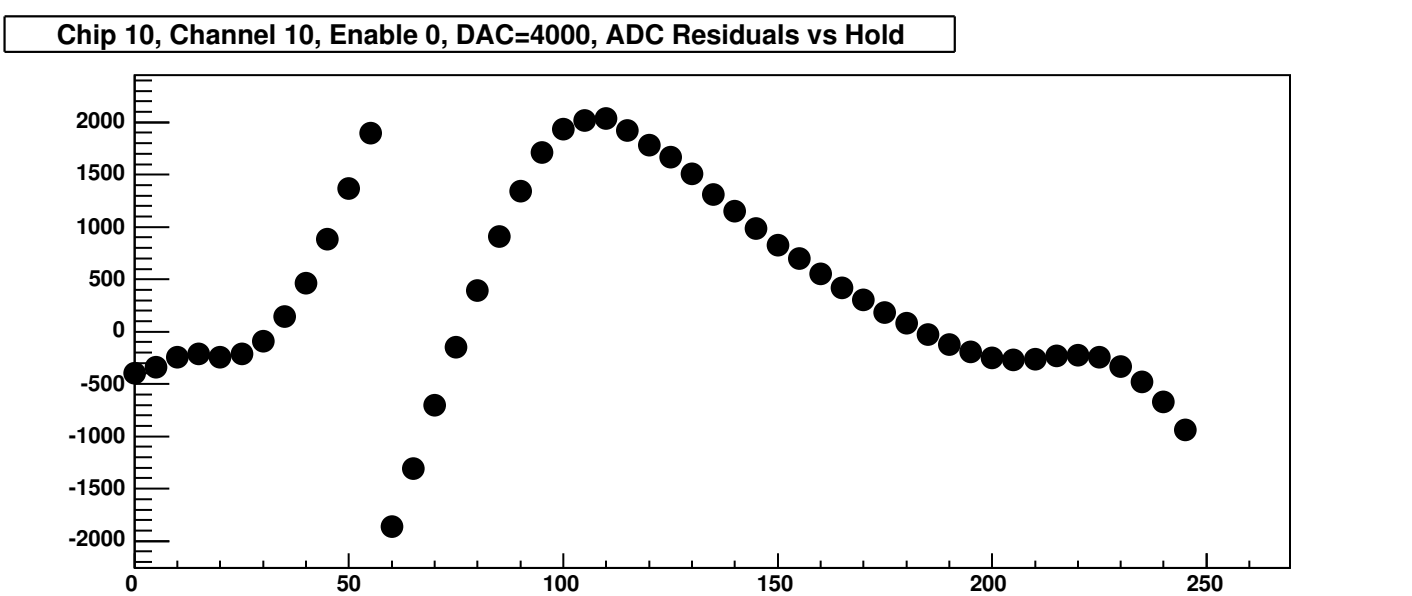


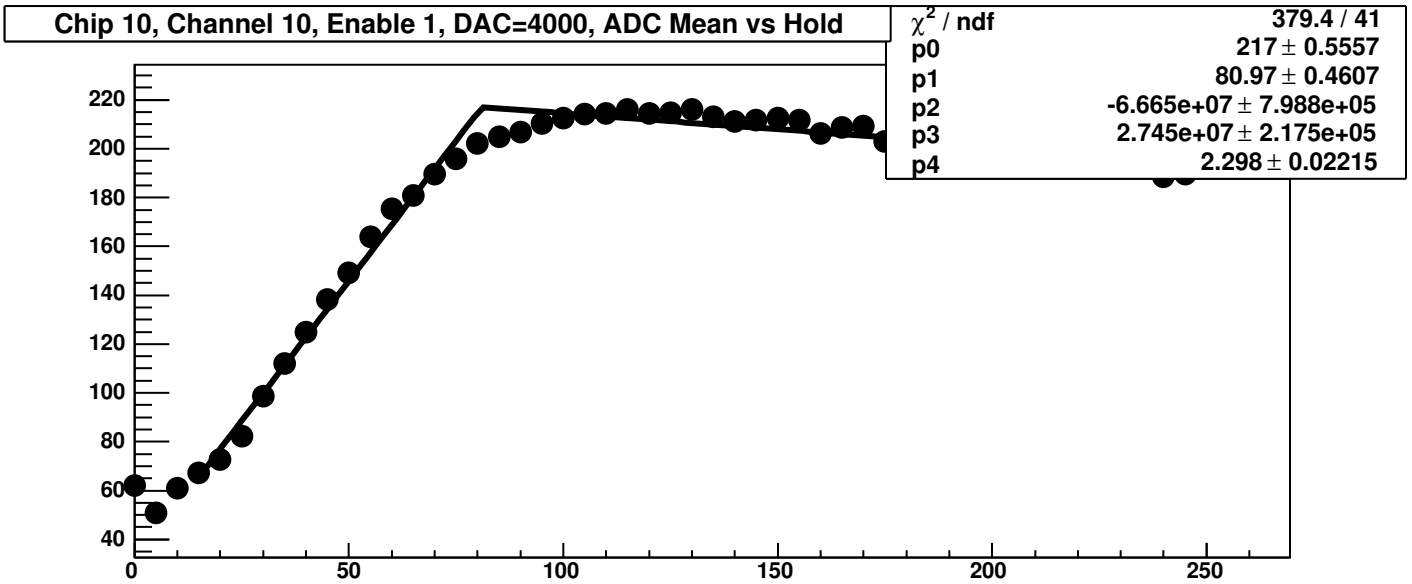
Chip 10, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold

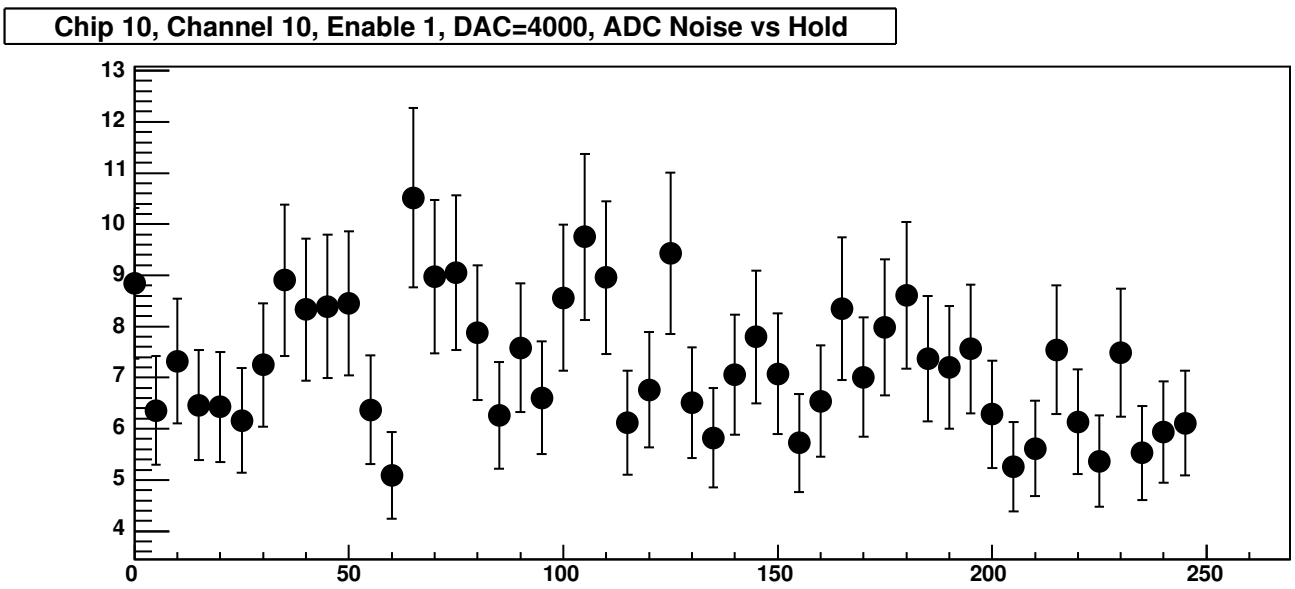


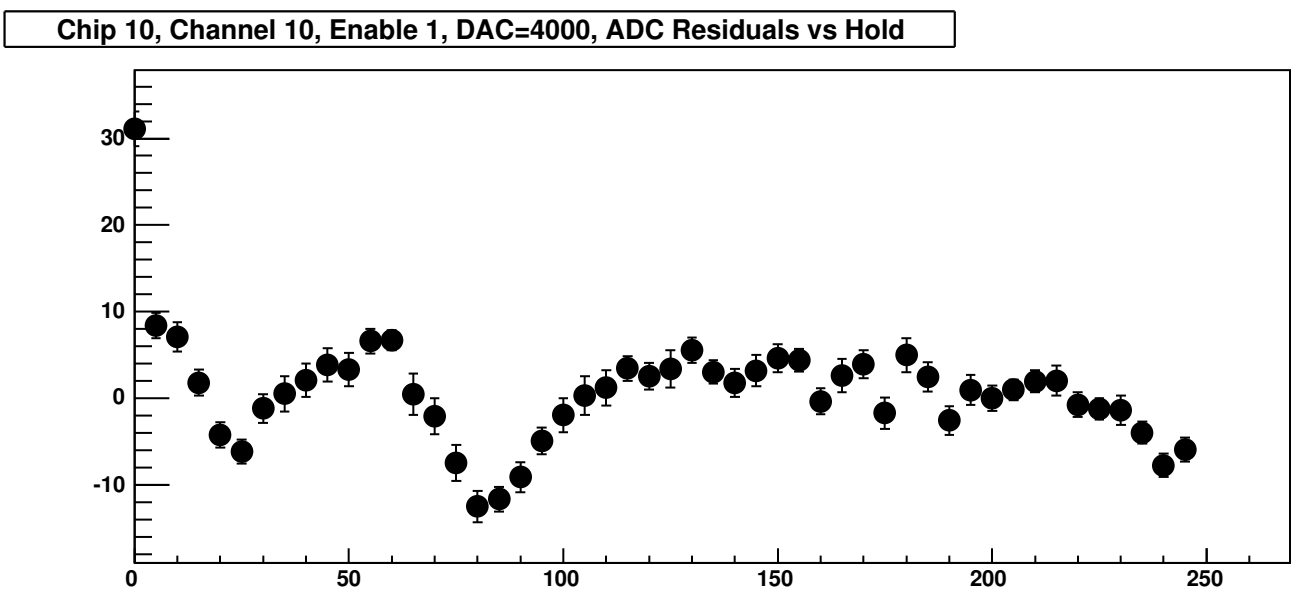

Chip 10, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 6.046e+05 / 41
p0 -267.4 ± 1.12
p1 59.37 ± 0.0008237
p2 4324 ± 3.406
p3 -90.72 ± 4.47e-07
p4 -11.98 ± 0.02128
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

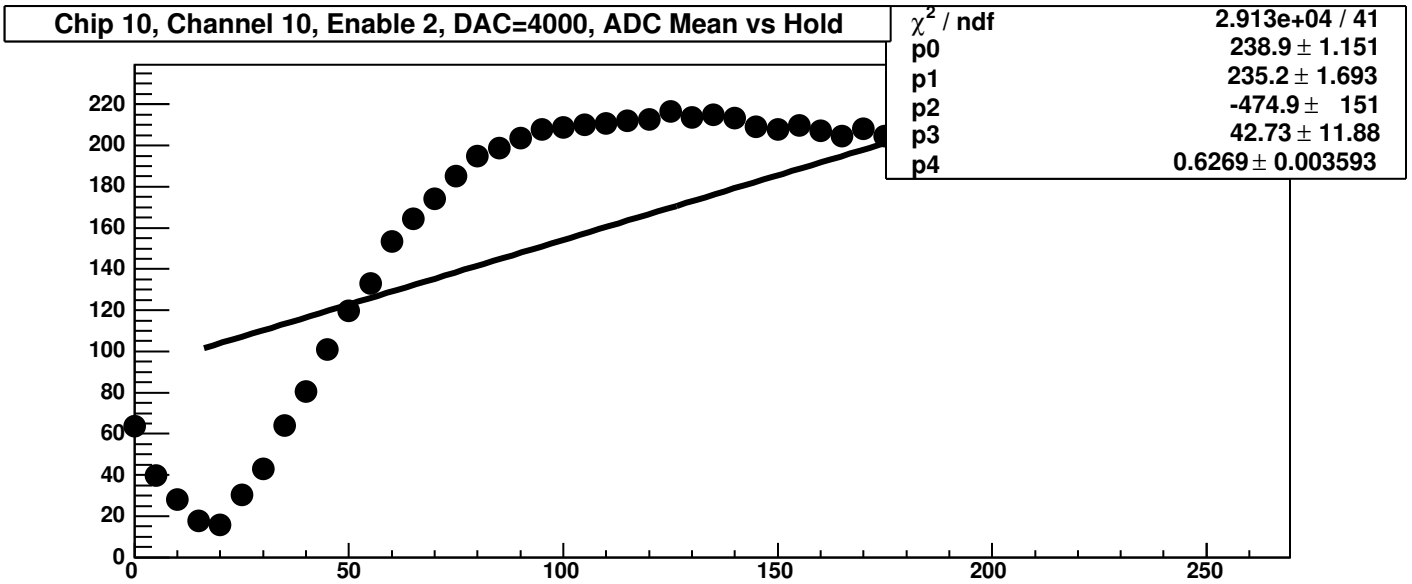

Chip 10, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250

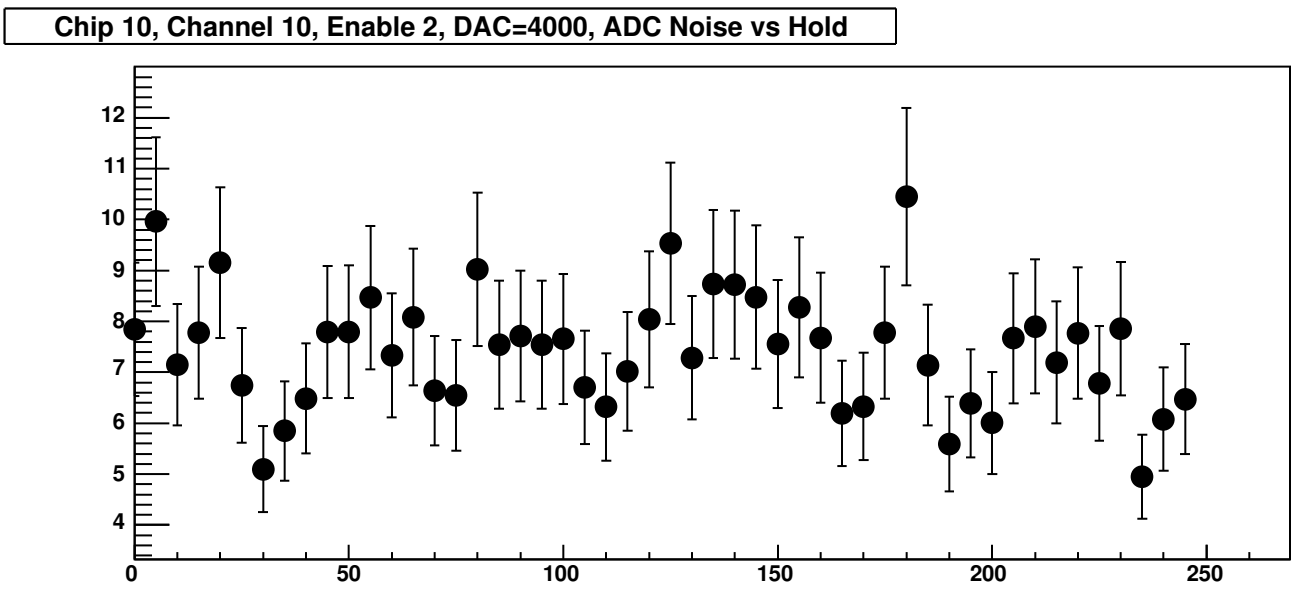

Chip 10, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

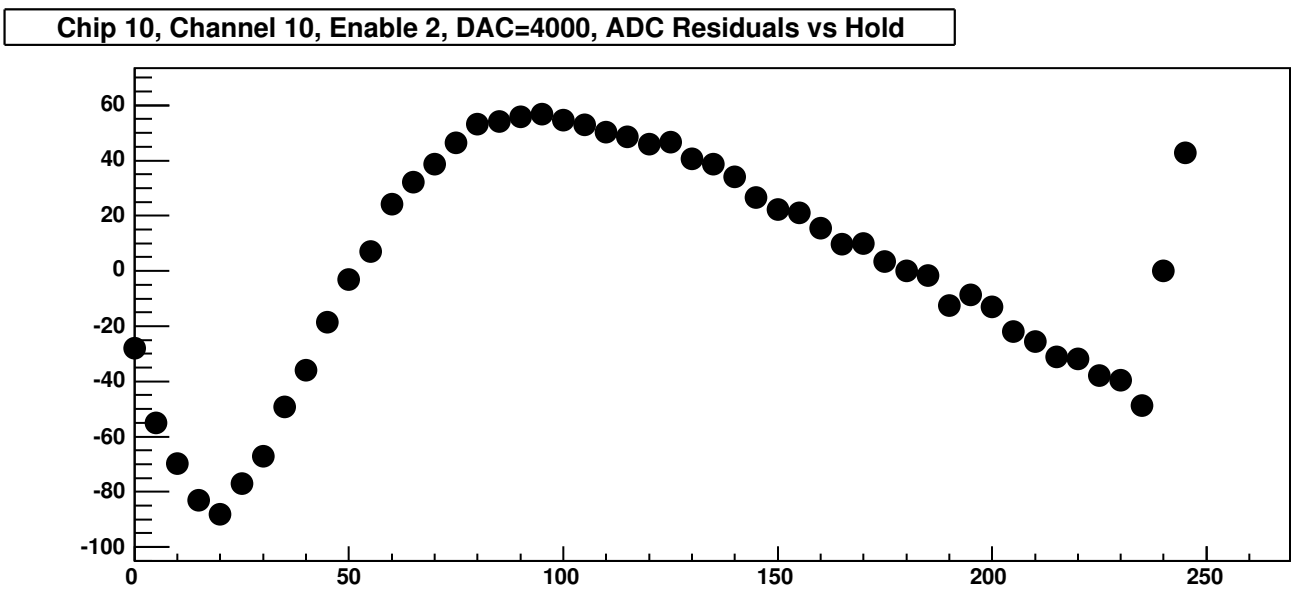

Chip 10, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 379.4 / 41
p0 217 ± 0.5557
p1 80.97 ± 0.4607
p2 -6.665e+07 ± 7.988e+05
p3 2.745e+07 ± 2.175e+05
p4 2.298 ± 0.02215

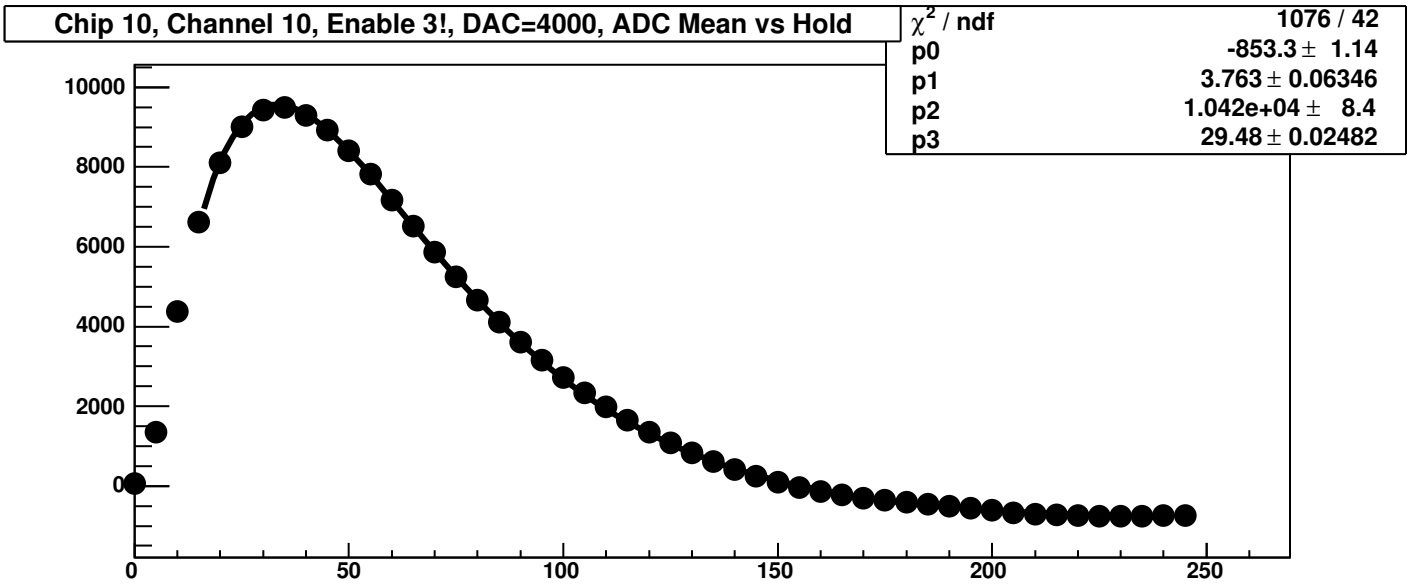

Chip 10, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold

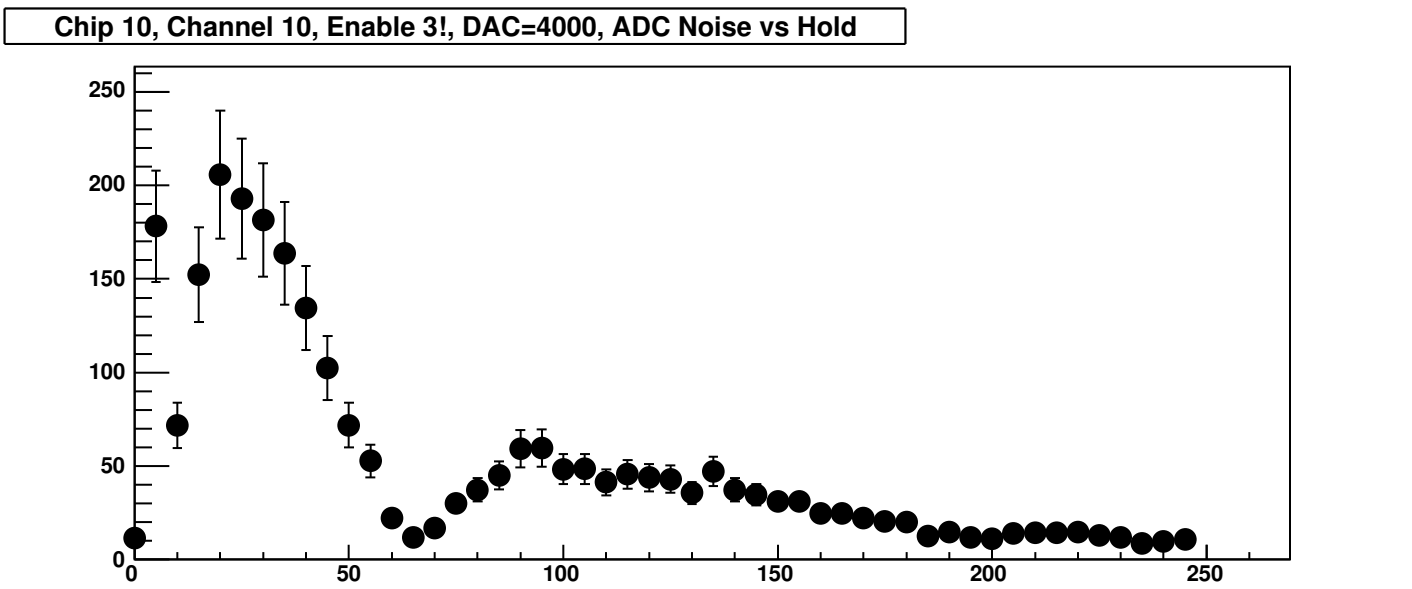

Chip 10, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

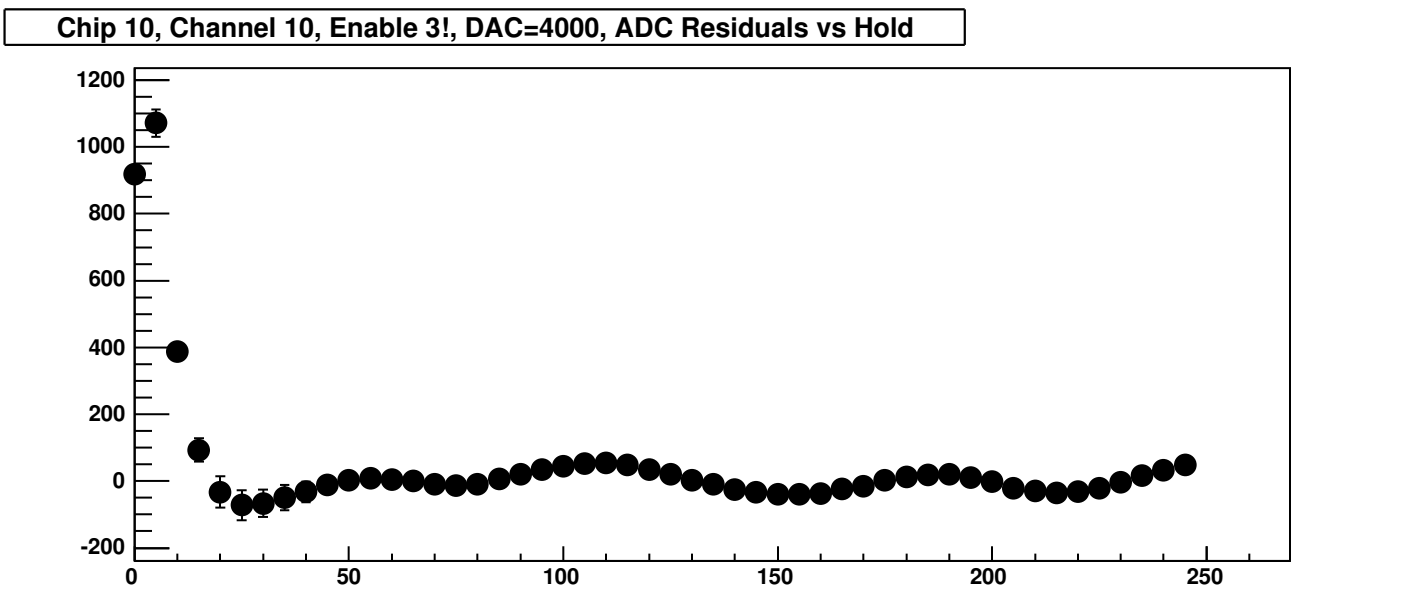

Chip 10, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.913e+04 / 41
p0 238.9 ± 1.151
p1 235.2 ± 1.693
p2 -474.9 ± 151
p3 42.73 ± 11.88
p4 0.6269 ± 0.003593

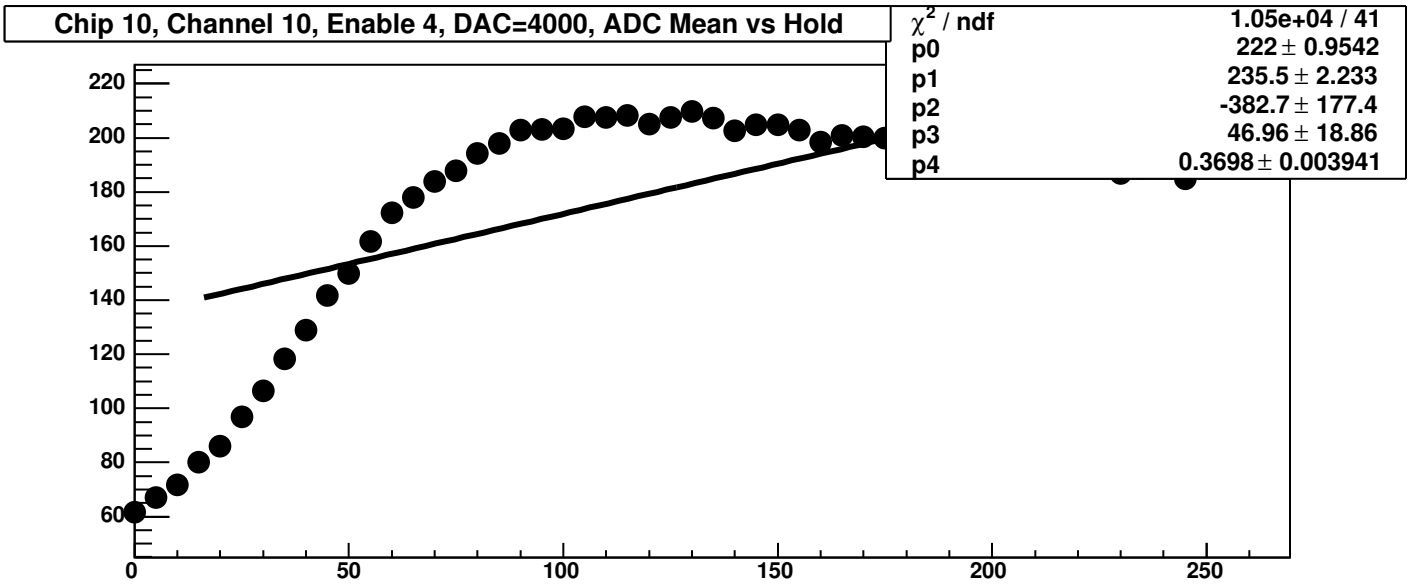

Chip 10, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold

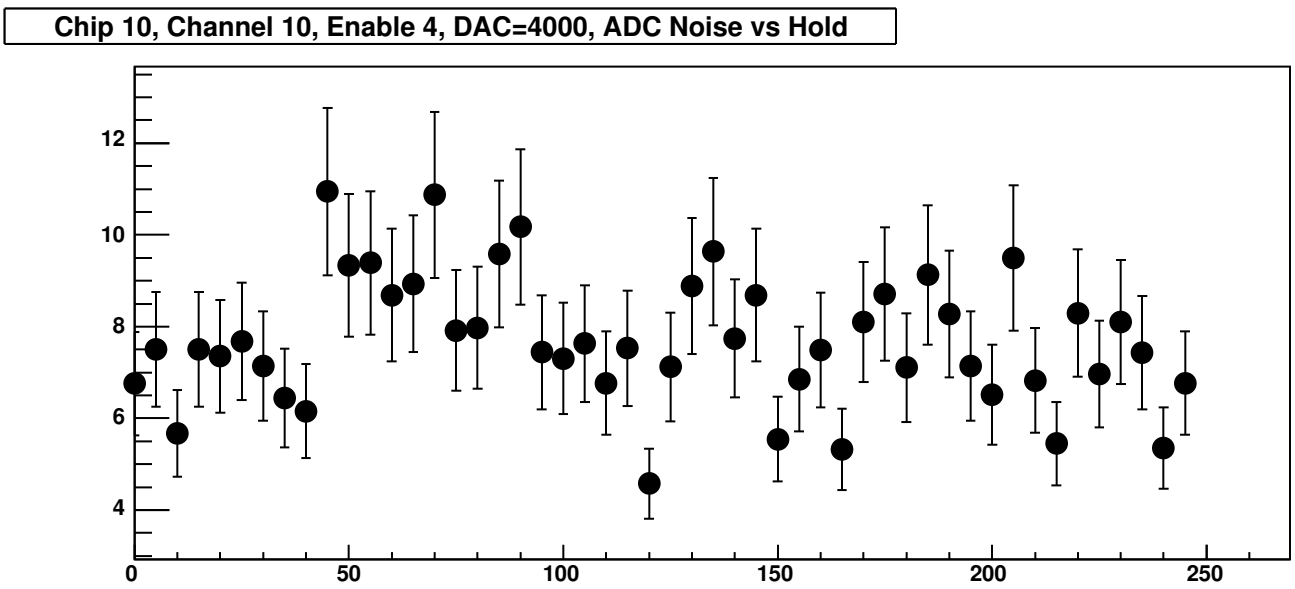

Chip 10, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

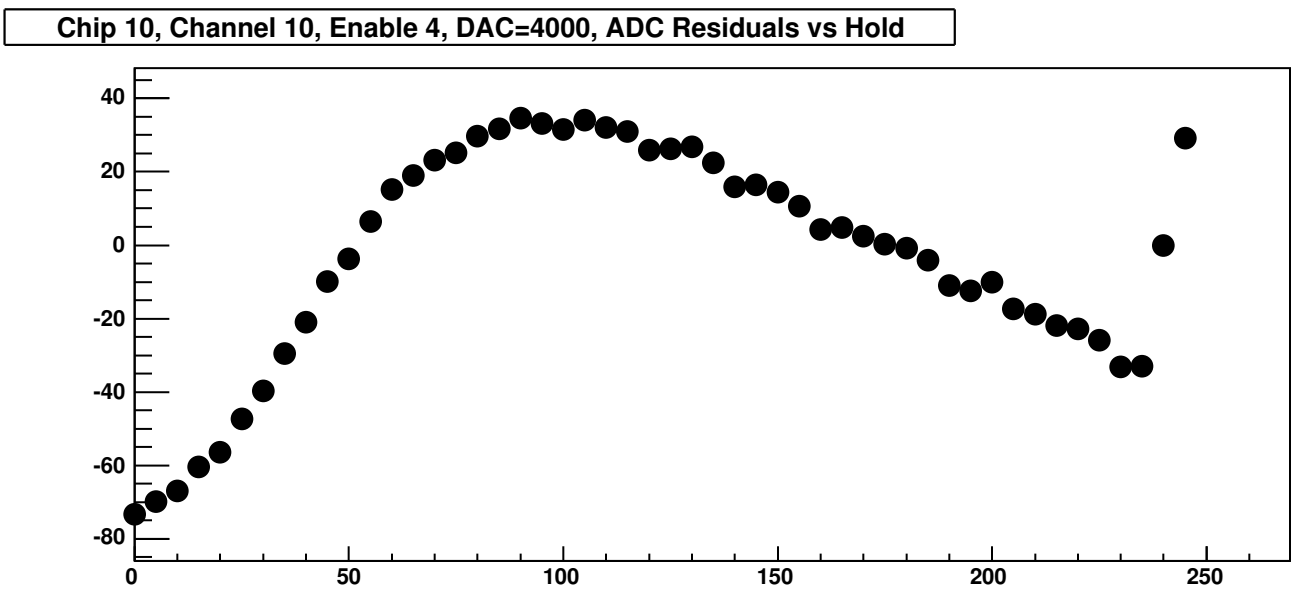

Chip 10, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1076 / 42
p0 -853.3 ± 1.14
p1 3.763 ± 0.06346
p2 1.042e+04 ± 8.4
p3 29.48 ± 0.02482
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

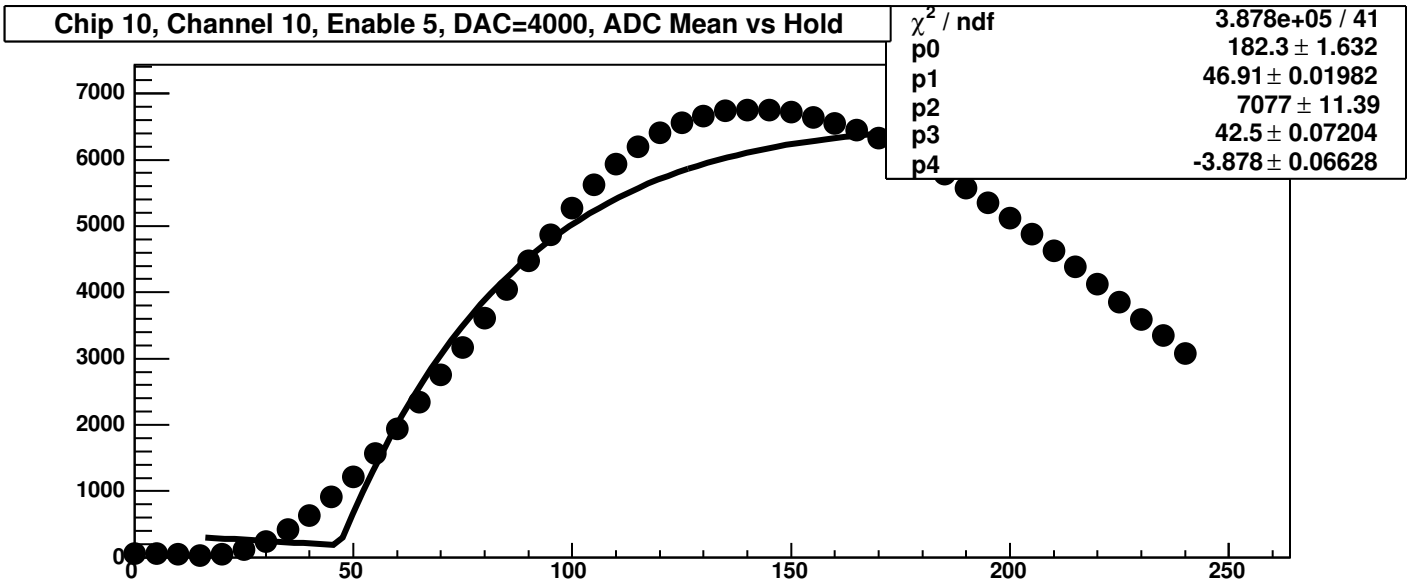

Chip 10, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250

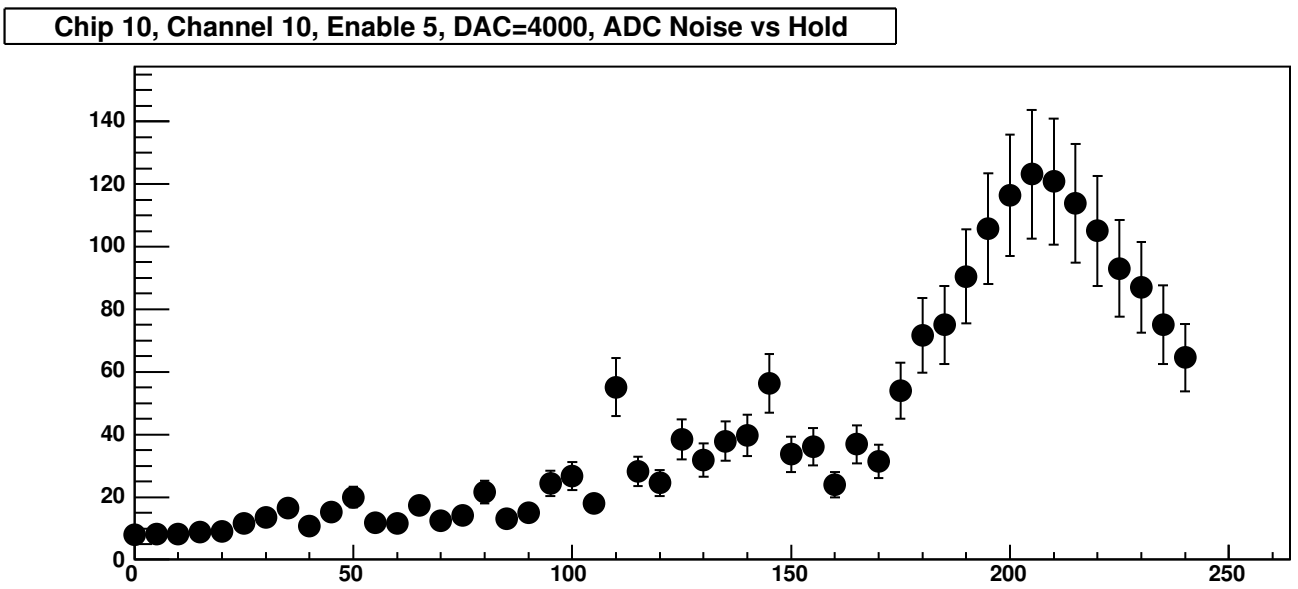

Chip 10, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

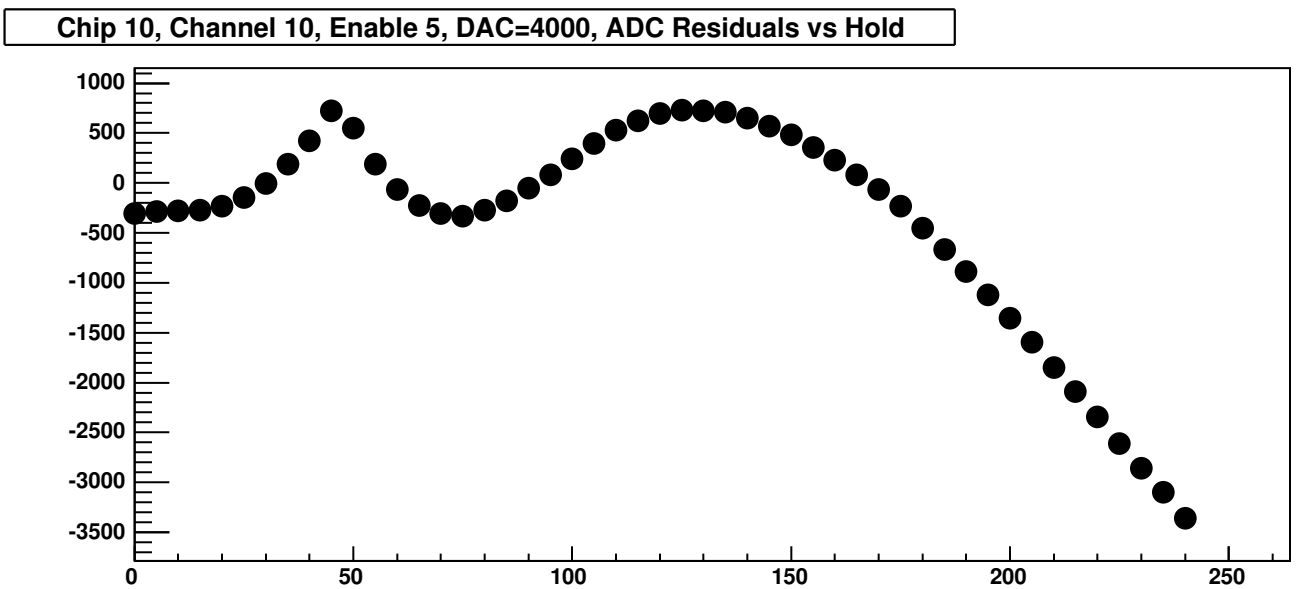

Chip 10, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.05e+04 / 41
p0 222 ± 0.9542
p1 235.5 ± 2.233
p2 -382.7 ± 177.4
p3 46.96 ± 18.86
p4 0.3698 ± 0.003941
220
200
180
160
140
120
100
80
60
0
50
100
150
200
250

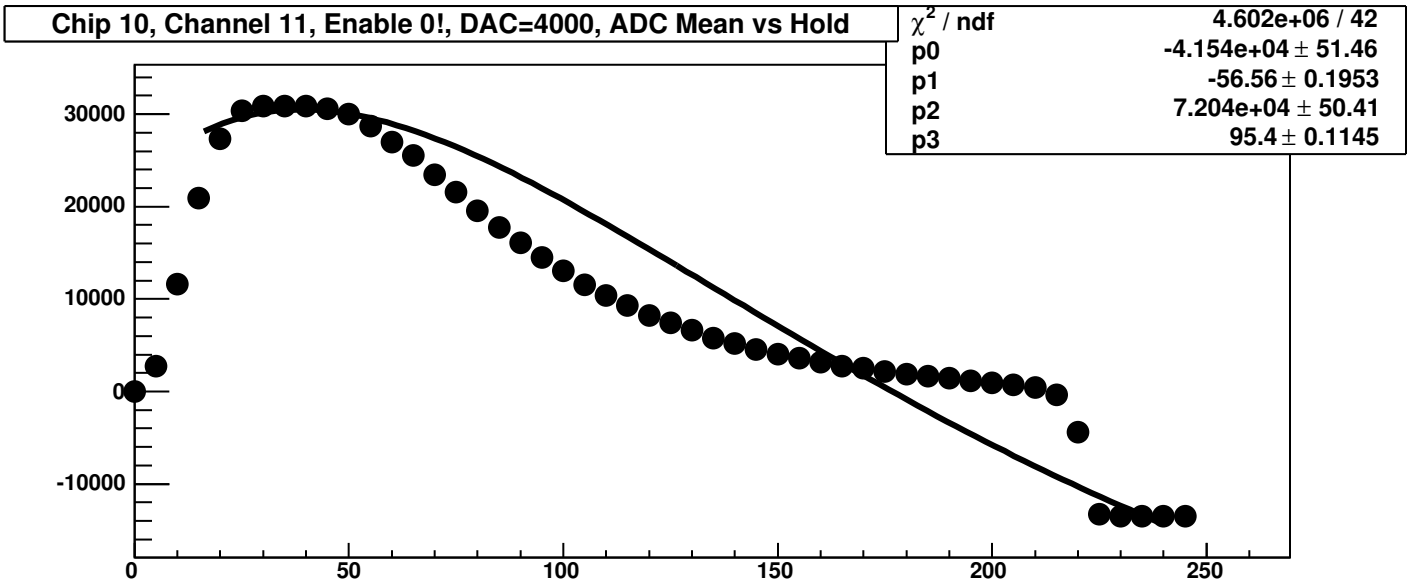

Chip 10, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

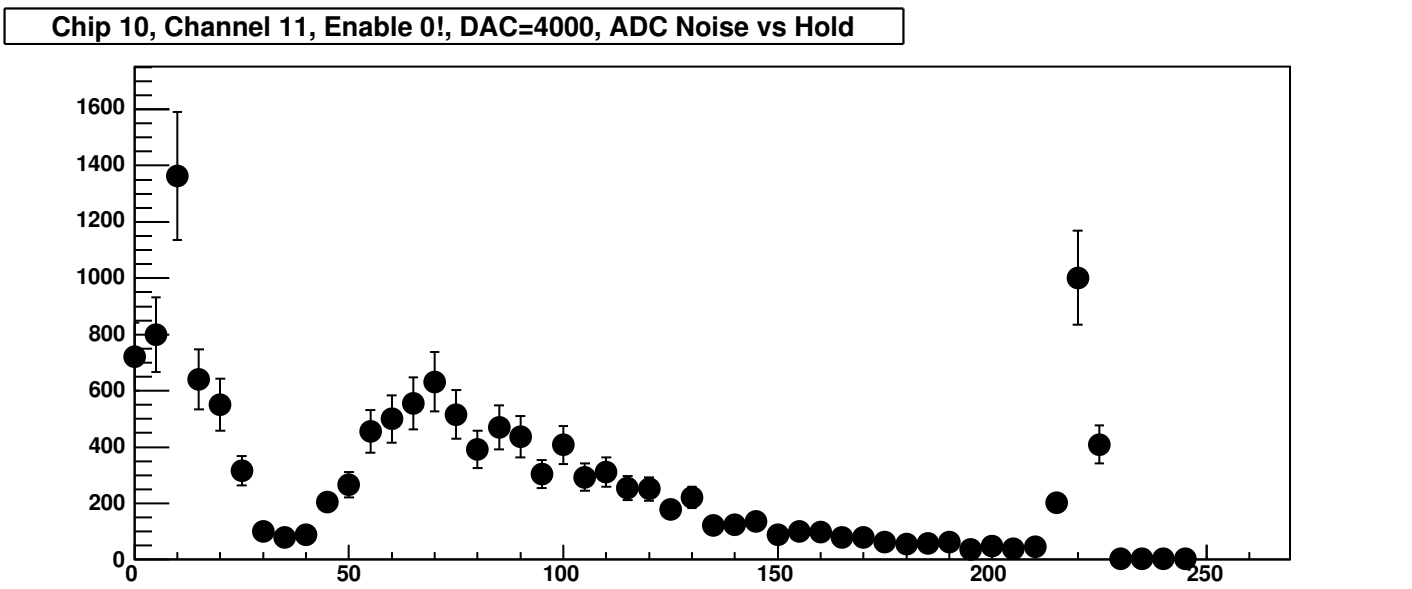

Chip 10, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
0
50
100
150
200
250

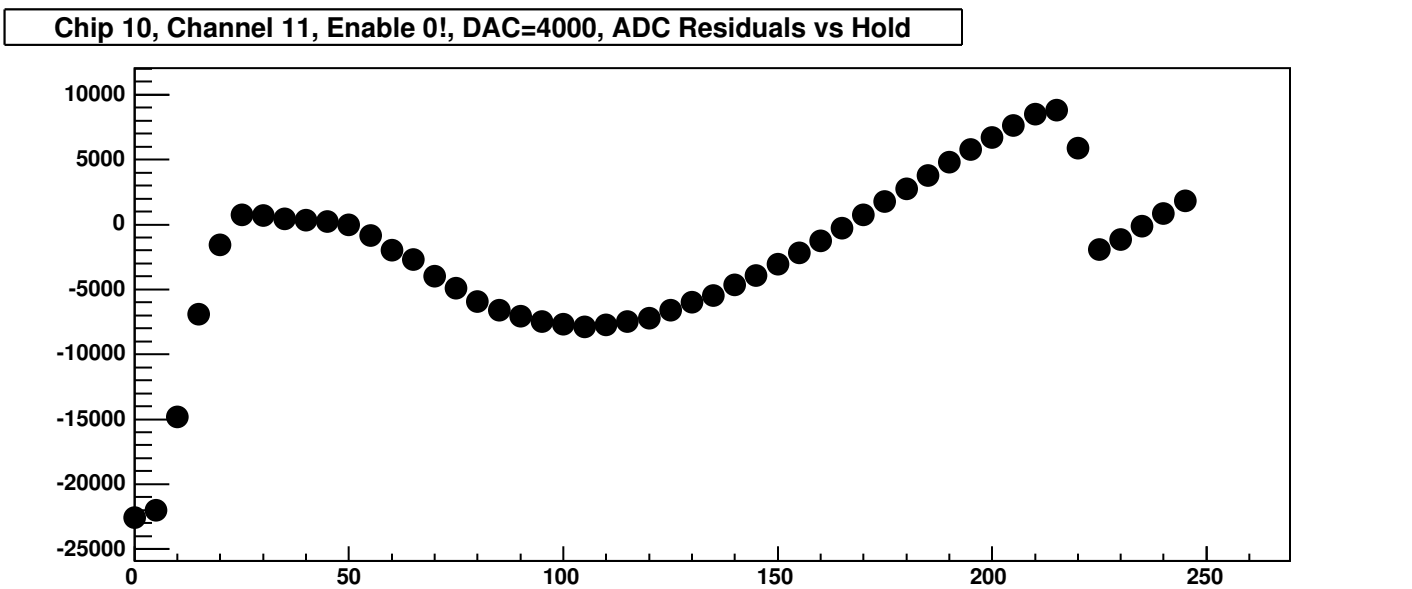

Chip 10, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.878e+05 / 41
p0 182.3 ± 1.632
p1 46.91 ± 0.01982
p2 7077 ± 11.39
p3 42.5 ± 0.07204
p4 -3.878 ± 0.06628
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

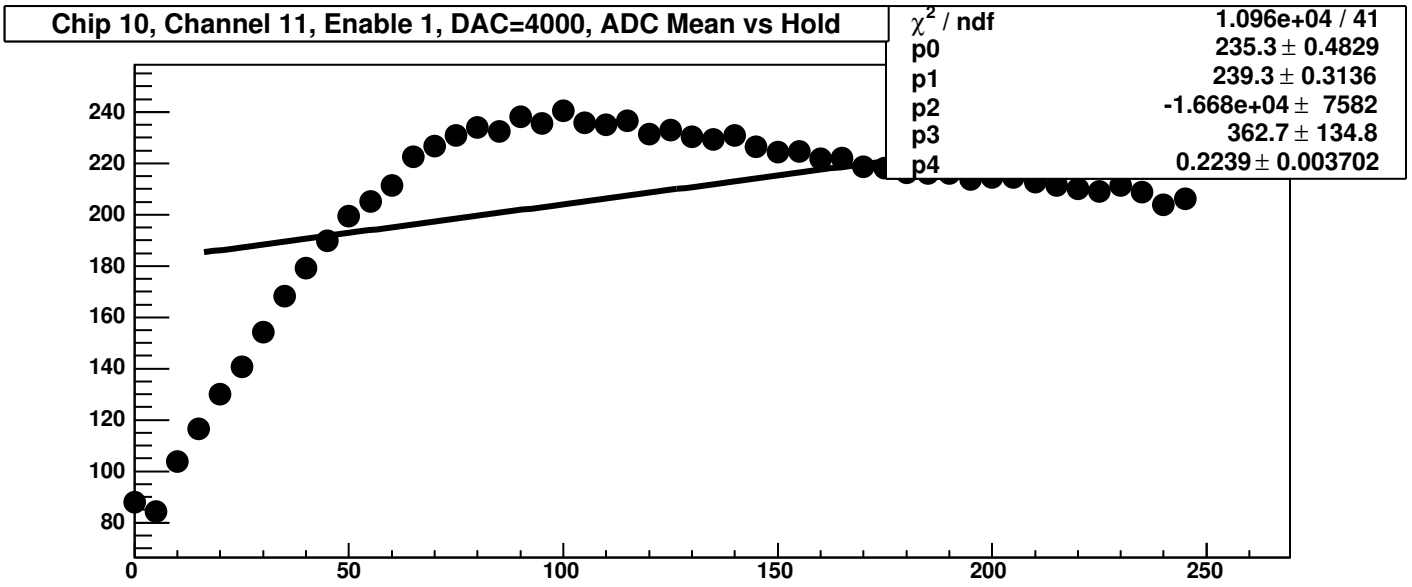

Chip 10, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250

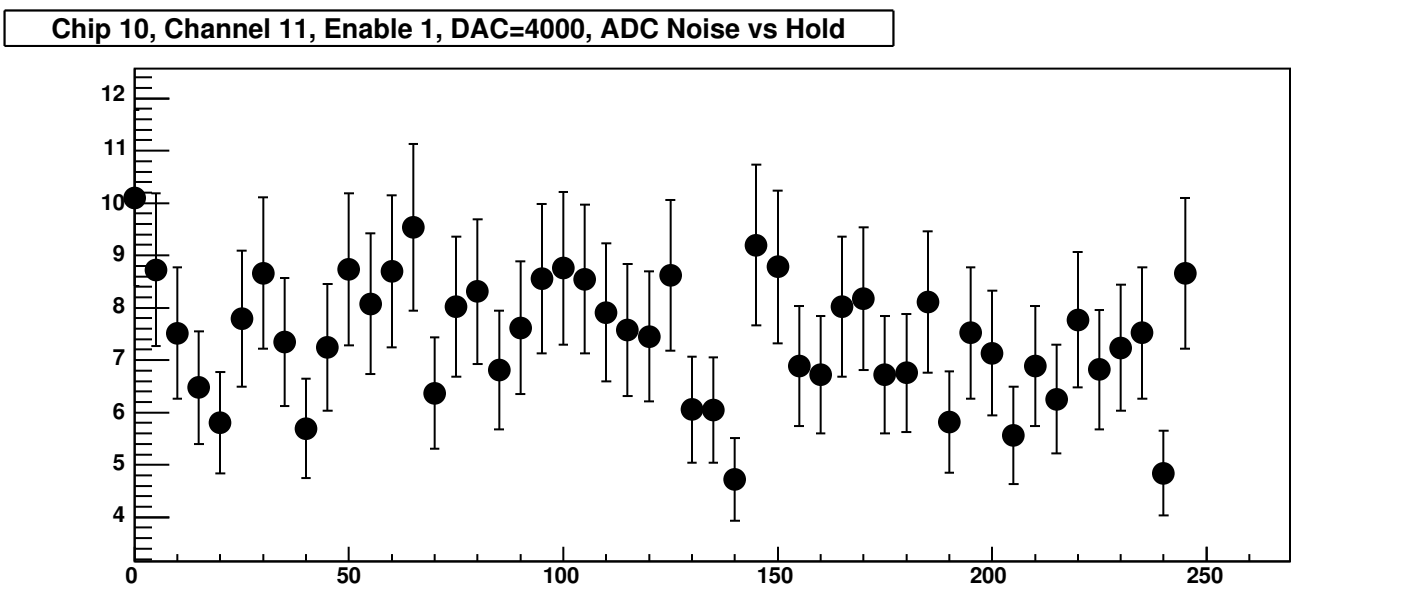

Chip 10, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
0
50
100
150
200
250

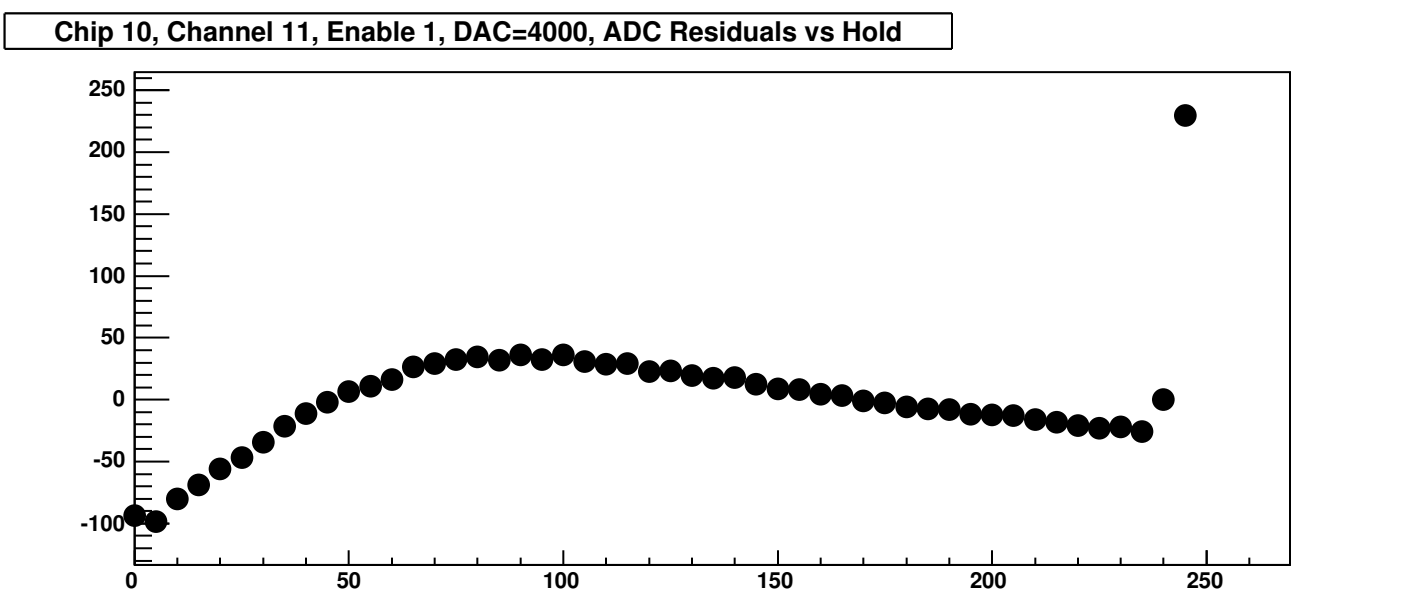

Chip 10, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.602e+06 / 42
p0 -4.154e+04 ± 51.46
p1 -56.56 ± 0.1953
p2 7.204e+04 ± 50.41
p3 95.4 ± 0.1145
30000
20000
10000
0
-10000
0
50
100
150
200
250

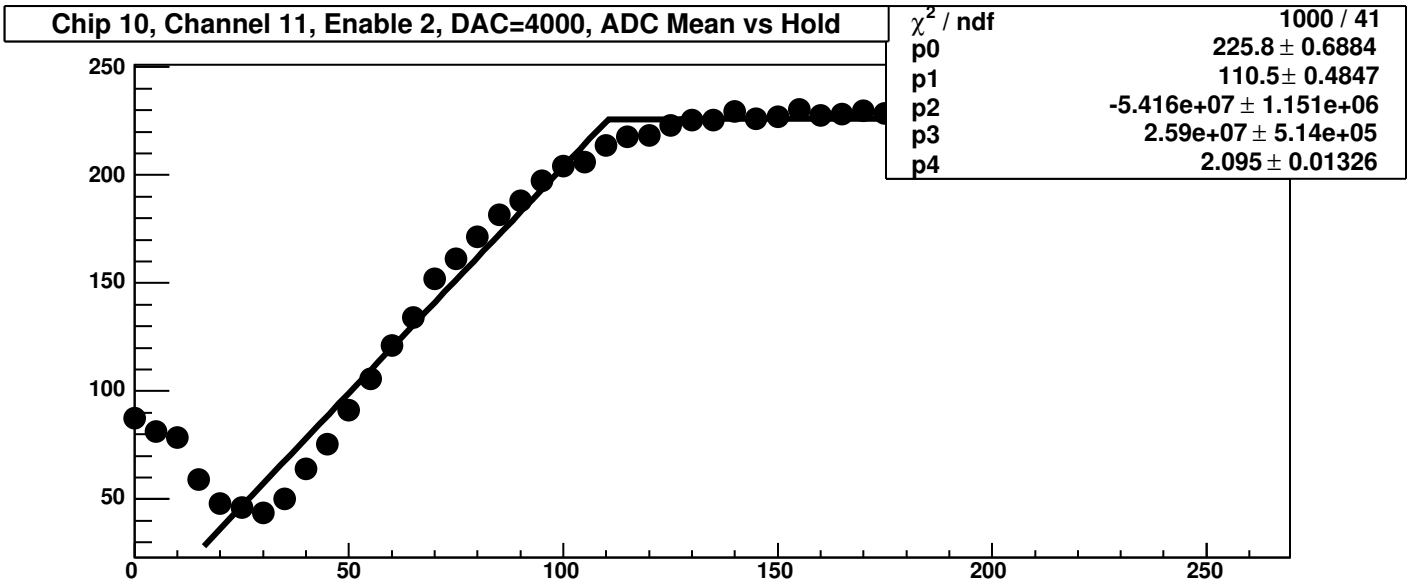

Chip 10, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

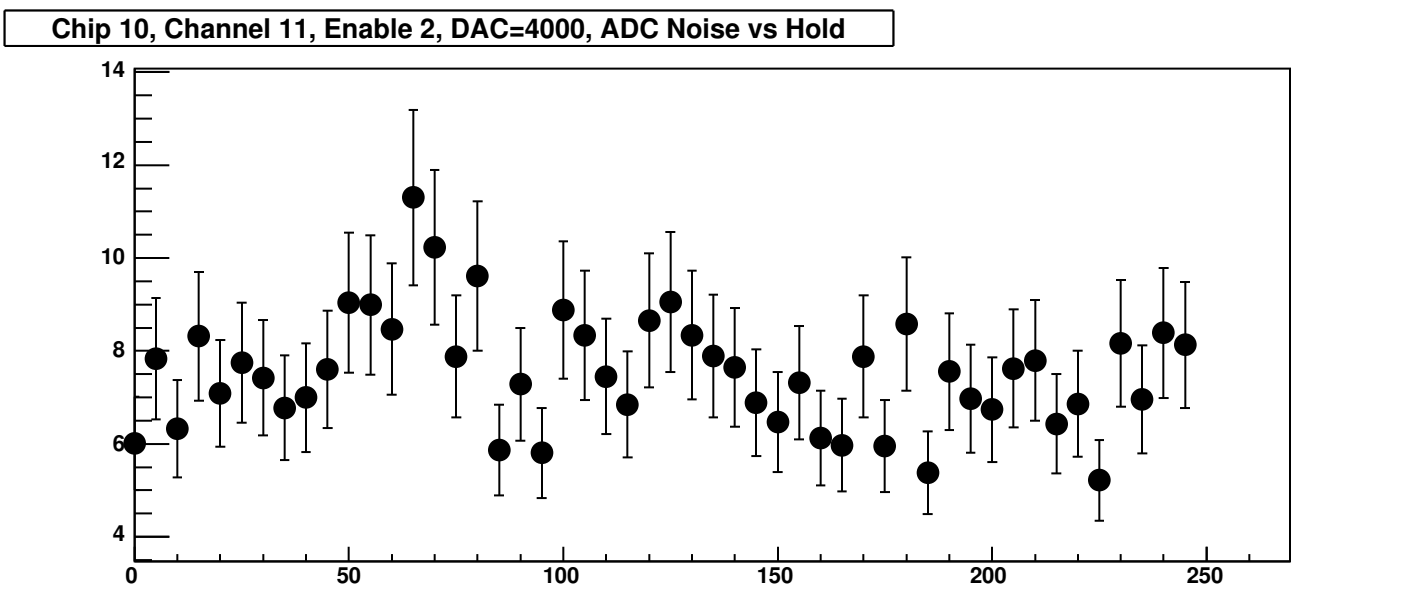

Chip 10, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold
10000
5000
0
-5000
-10000
-15000
-20000
-25000
0
50
100
150
200
250

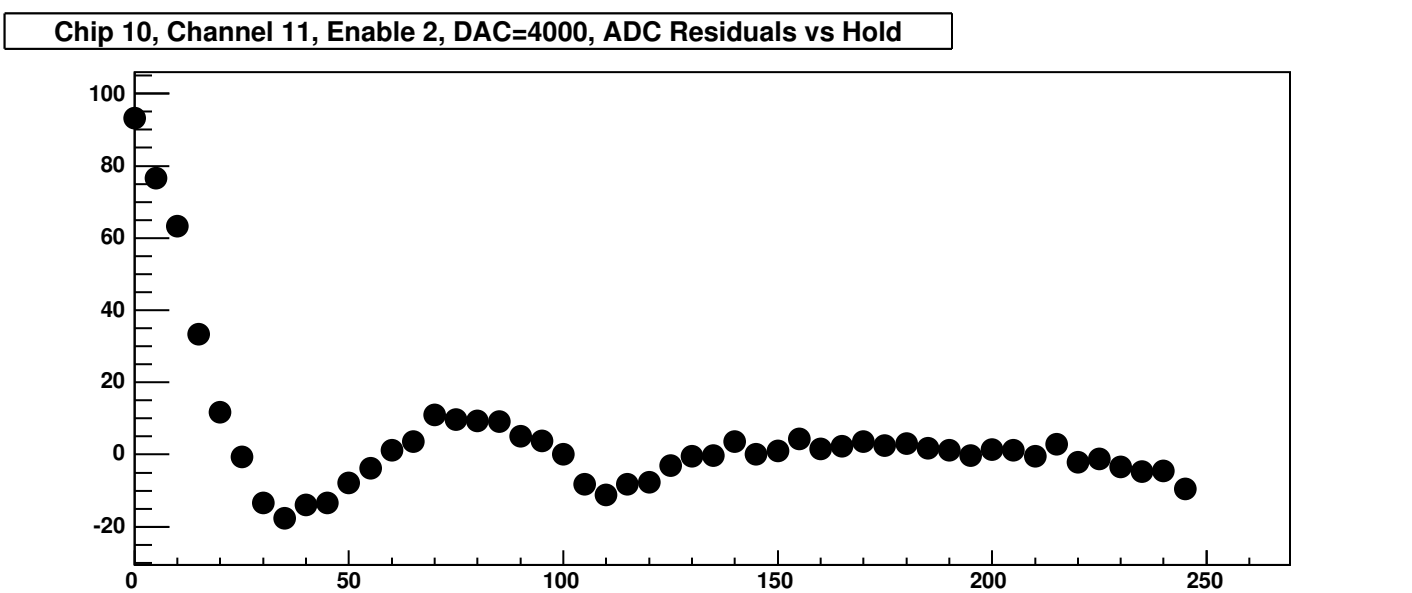

Chip 10, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.096e+04 / 41
p0 235.3 ± 0.4829
p1 239.3 ± 0.3136
p2 -1.668e+04 ± 7582
p3 362.7 ± 134.8
p4 0.2239 ± 0.003702
240
220
200
180
160
140
120
100
80
0
50
100
150
200
250


Chip 10, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

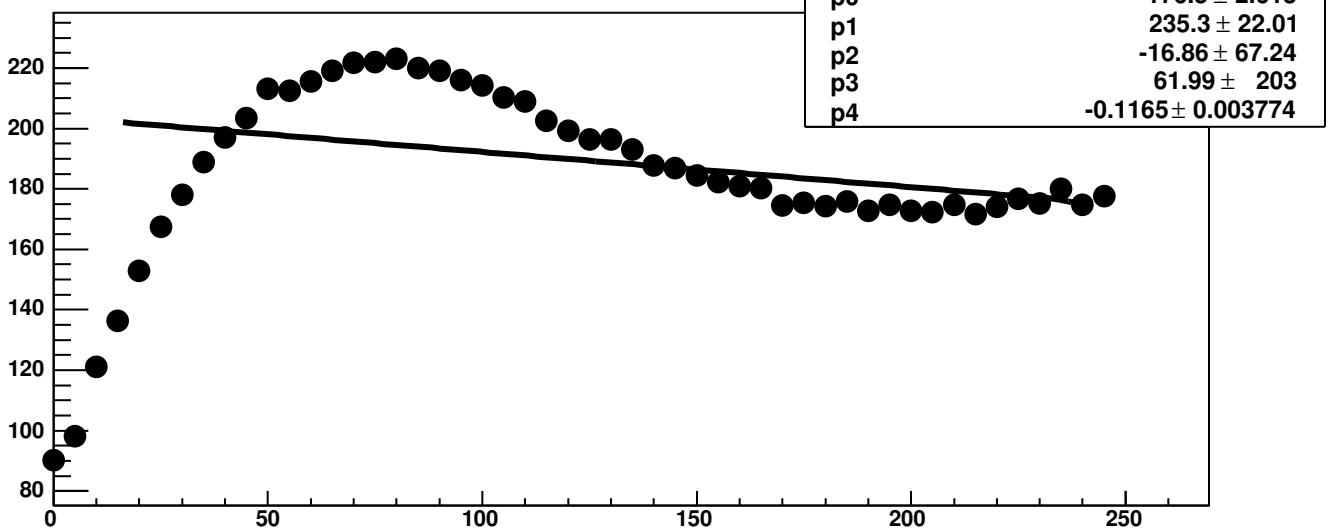

Chip 10, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250


Chip 10, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 1000 / 41
p0 225.8 ± 0.6884
p1 110.5 ± 0.4847
p2 -5.416e+07 ± 1.151e+06
p3 2.59e+07 ± 5.14e+05
p4 2.095 ± 0.01326
250
200
150
100
50
0
50
100
150
200
250

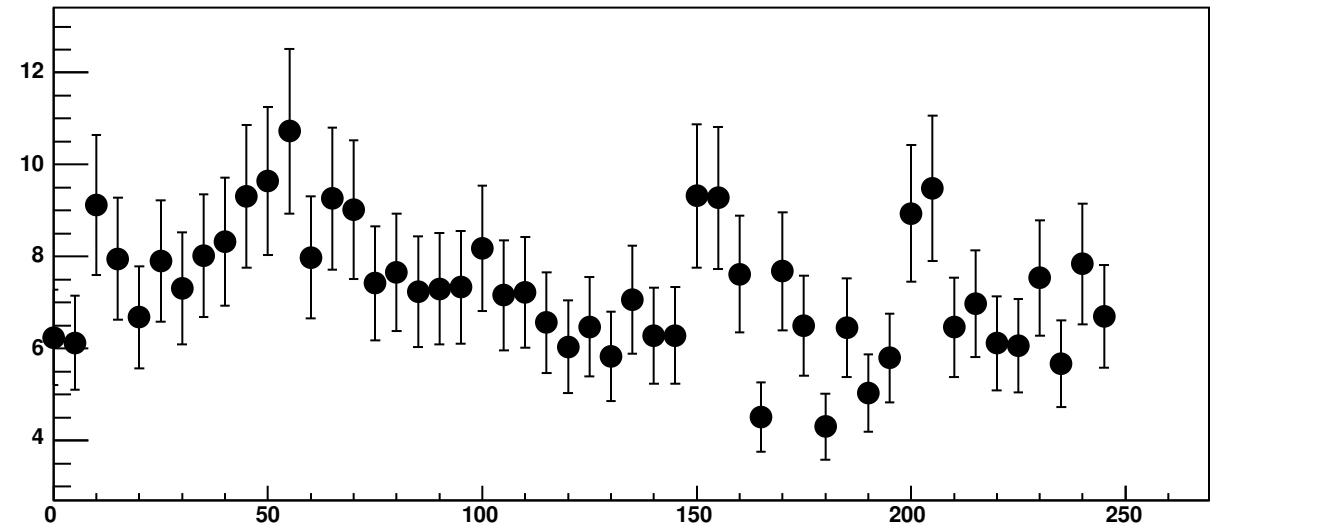

Chip 10, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 10, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold
100
80
60
40
20
0
-20
0
50
100
150
200
250

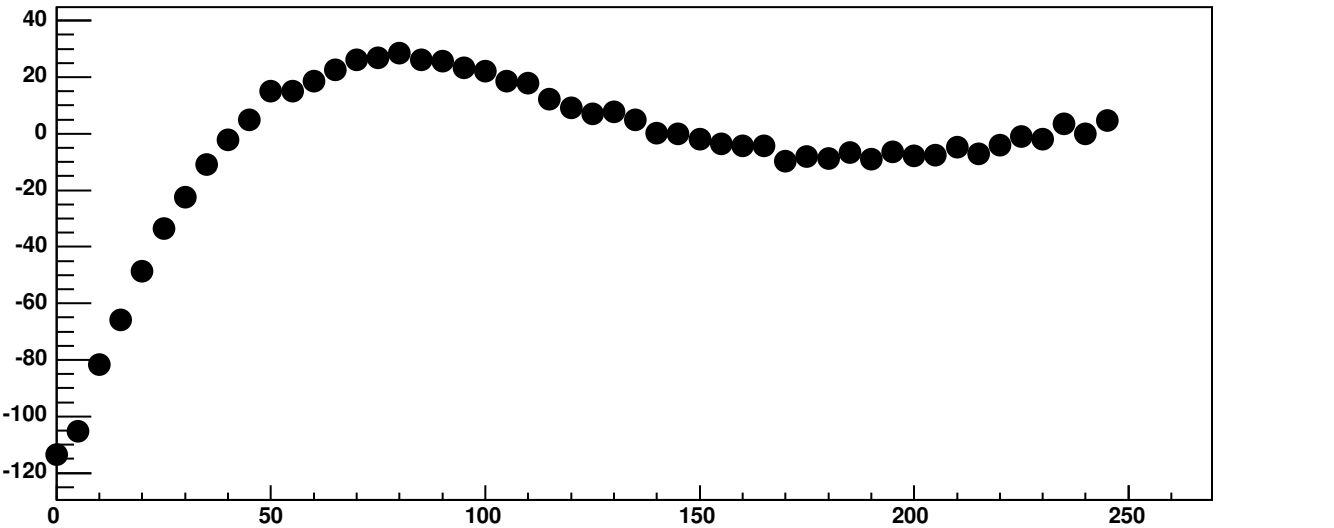
Chip 10, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold

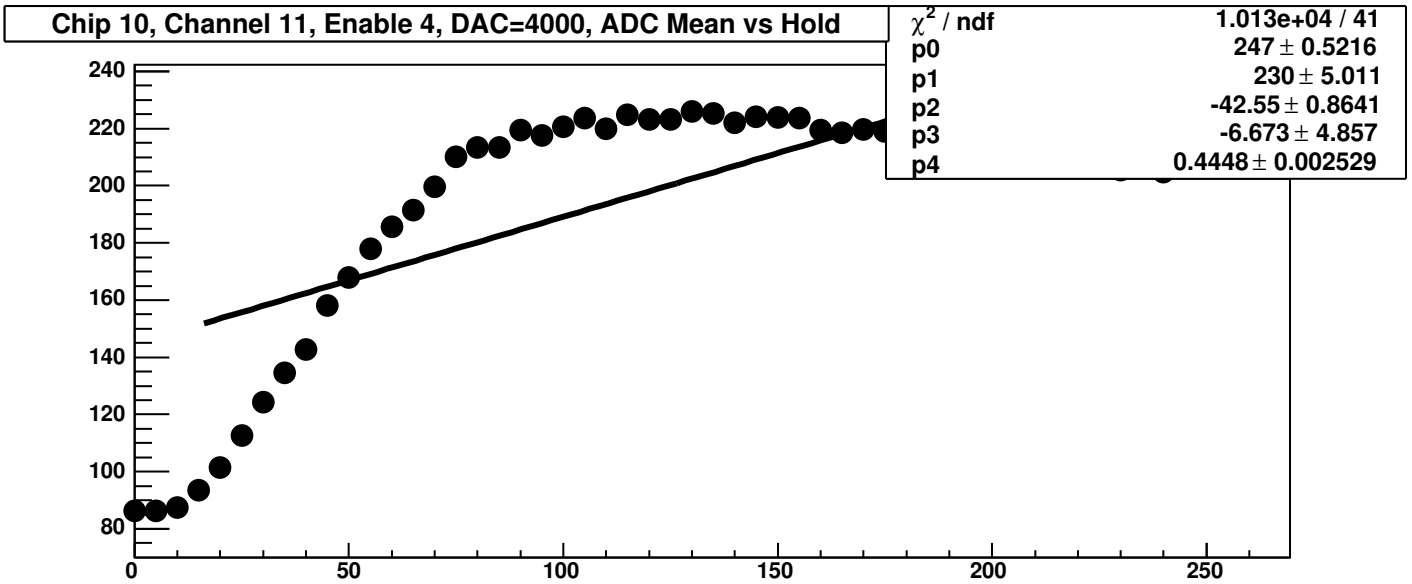


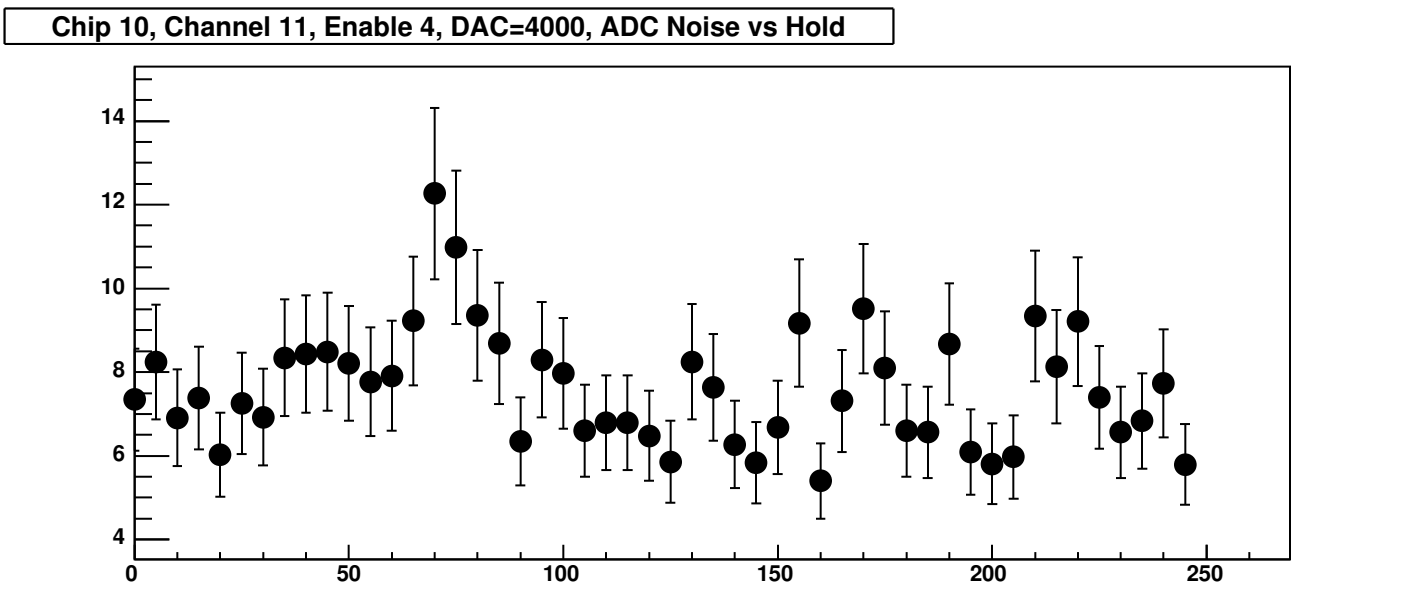
Chip 10, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold

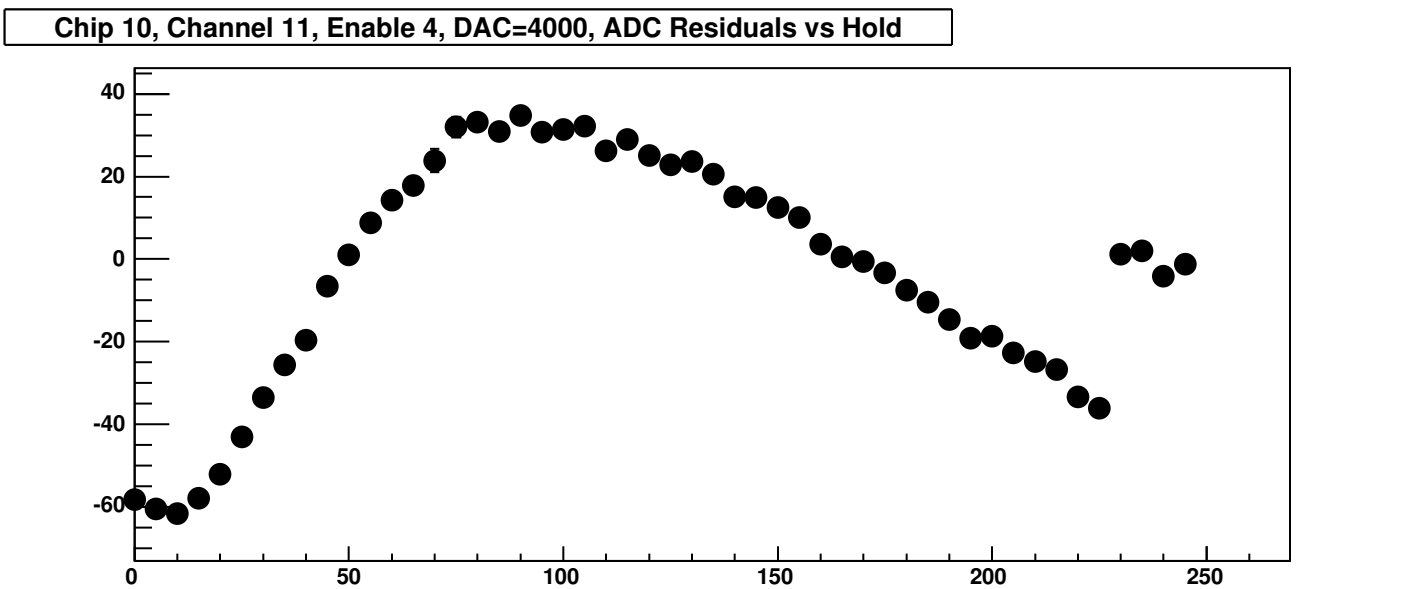


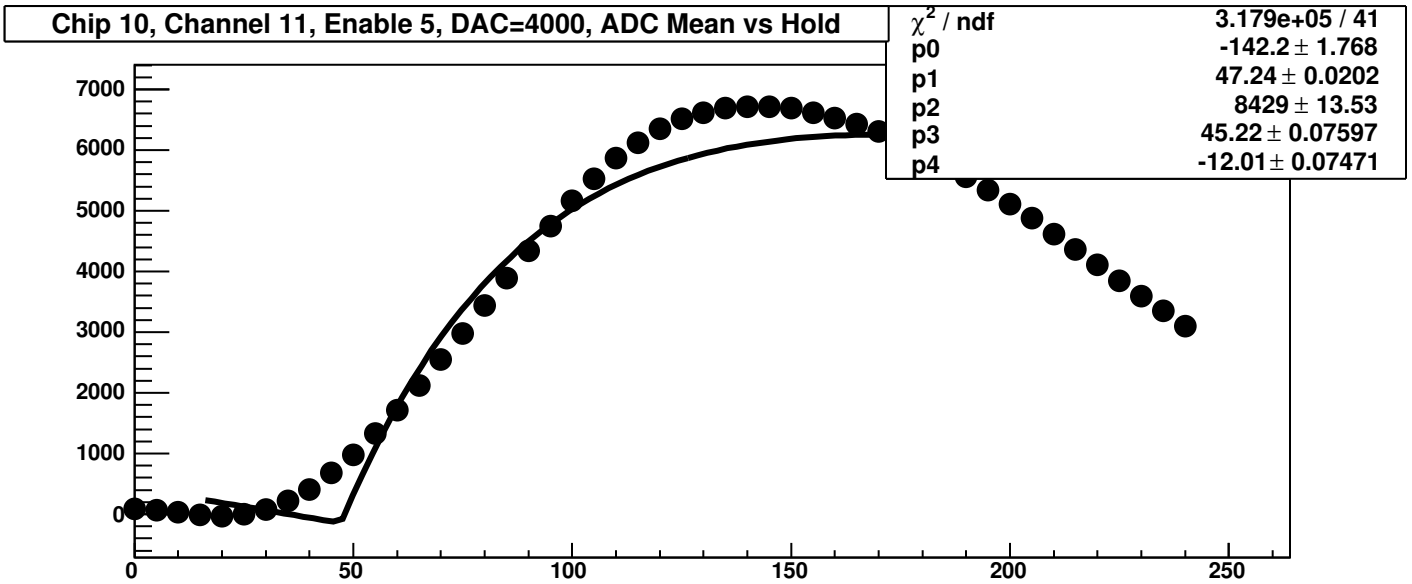
Chip 10, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold

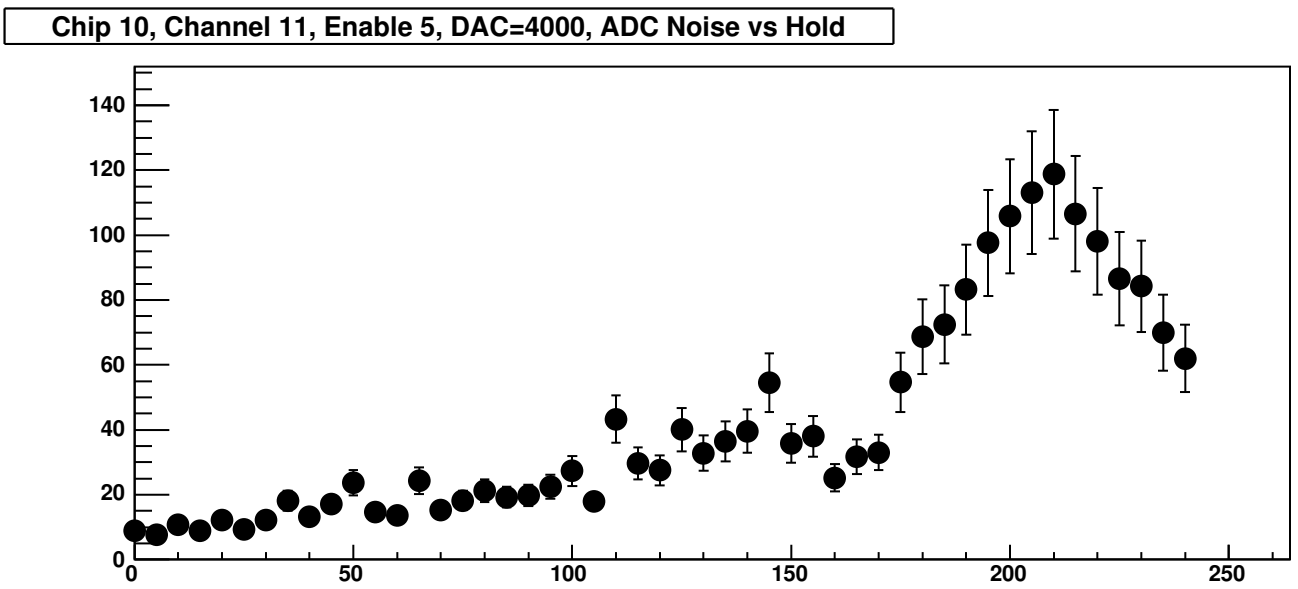


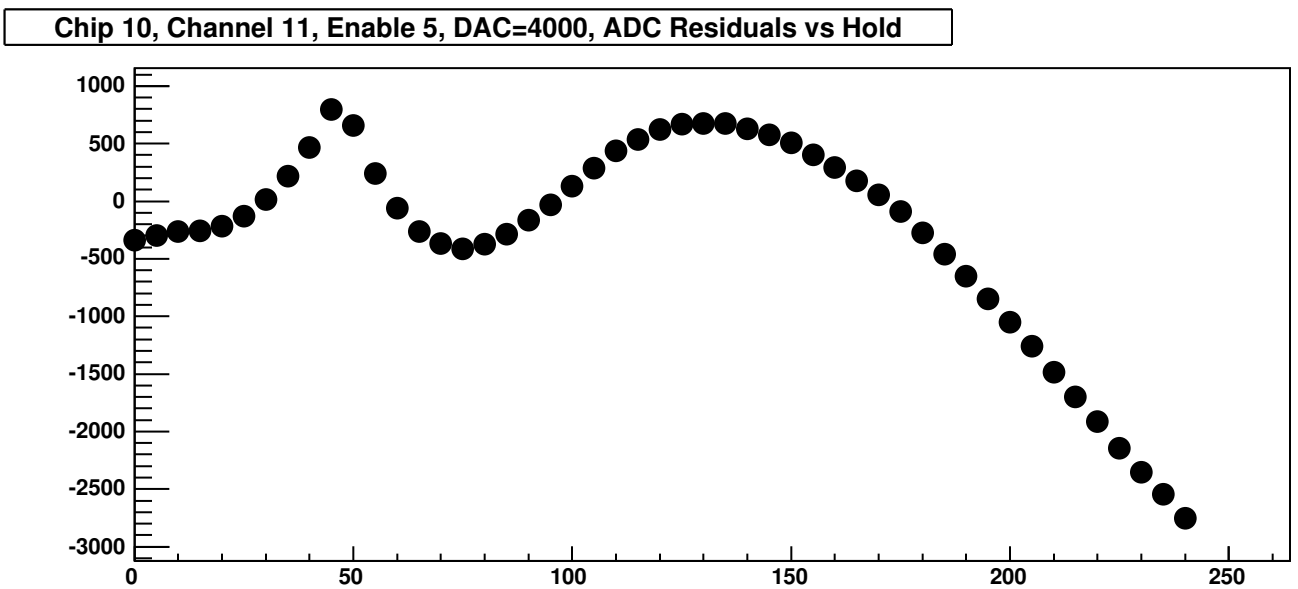

Chip 10, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.013e+04 / 41
p0 247 ± 0.5216
p1 230 ± 5.011
p2 -42.55 ± 0.8641
p3 -6.673 ± 4.857
p4 0.4448 ± 0.002529
240
220
200
180
160
140
120
100
80
0
50
100
150
200
250

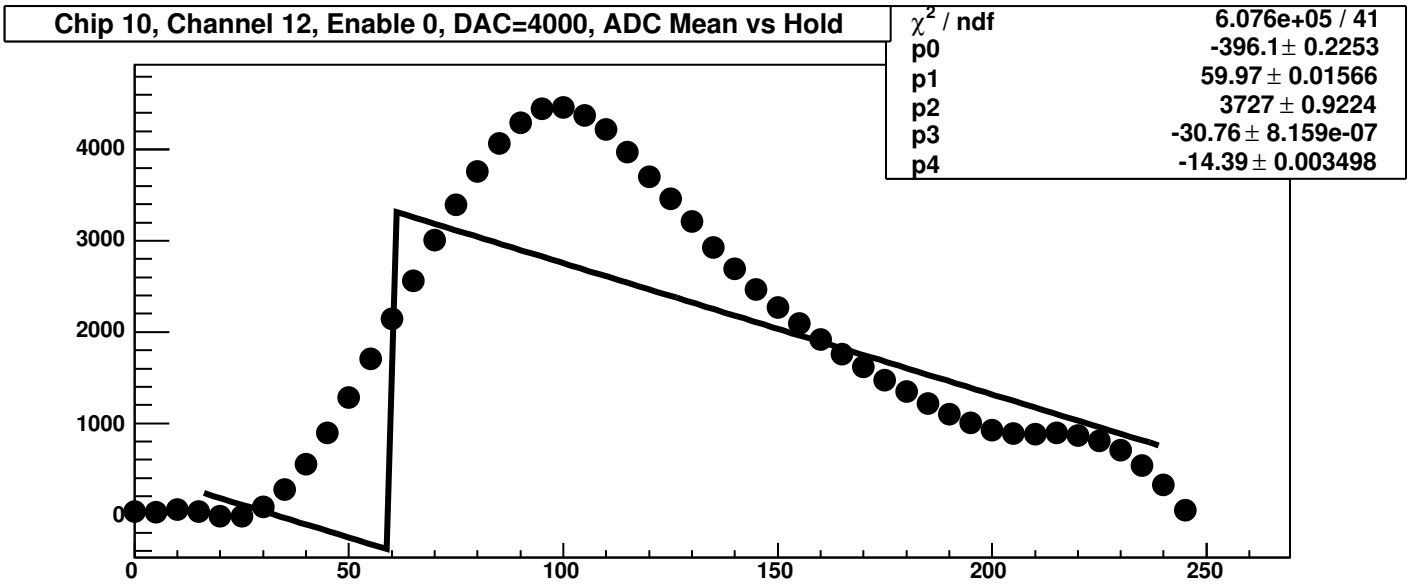

Chip 10, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

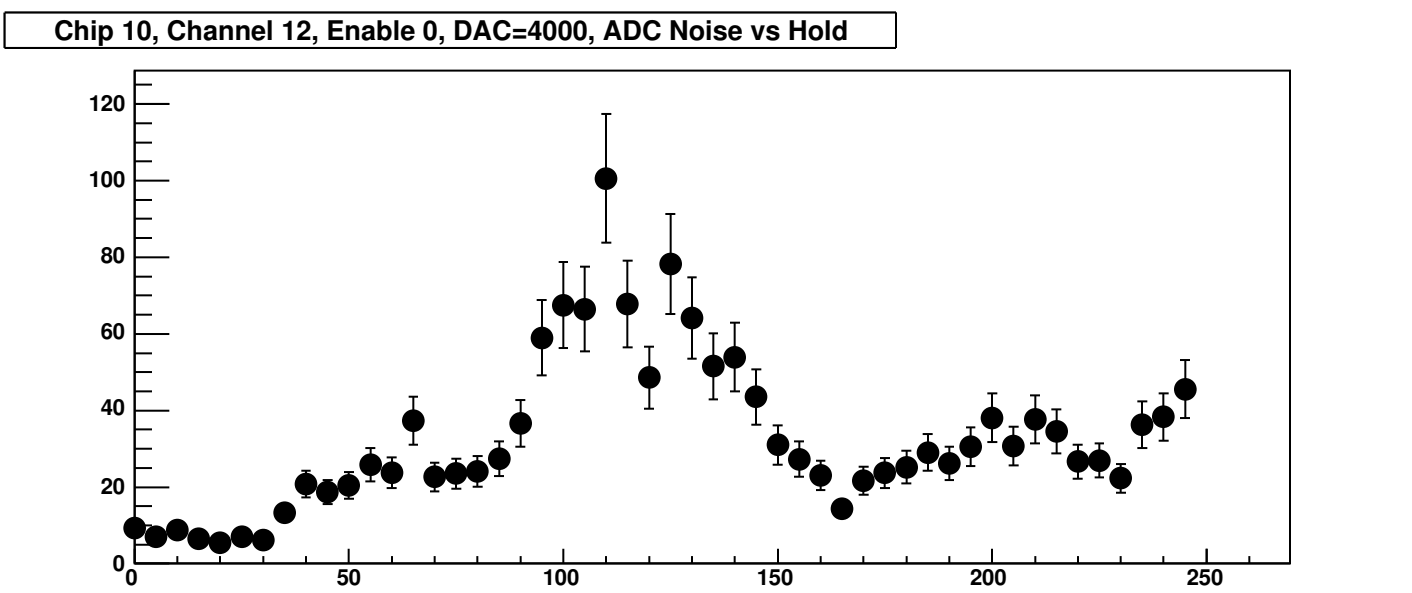

Chip 10, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
0
50
100
150
200
250

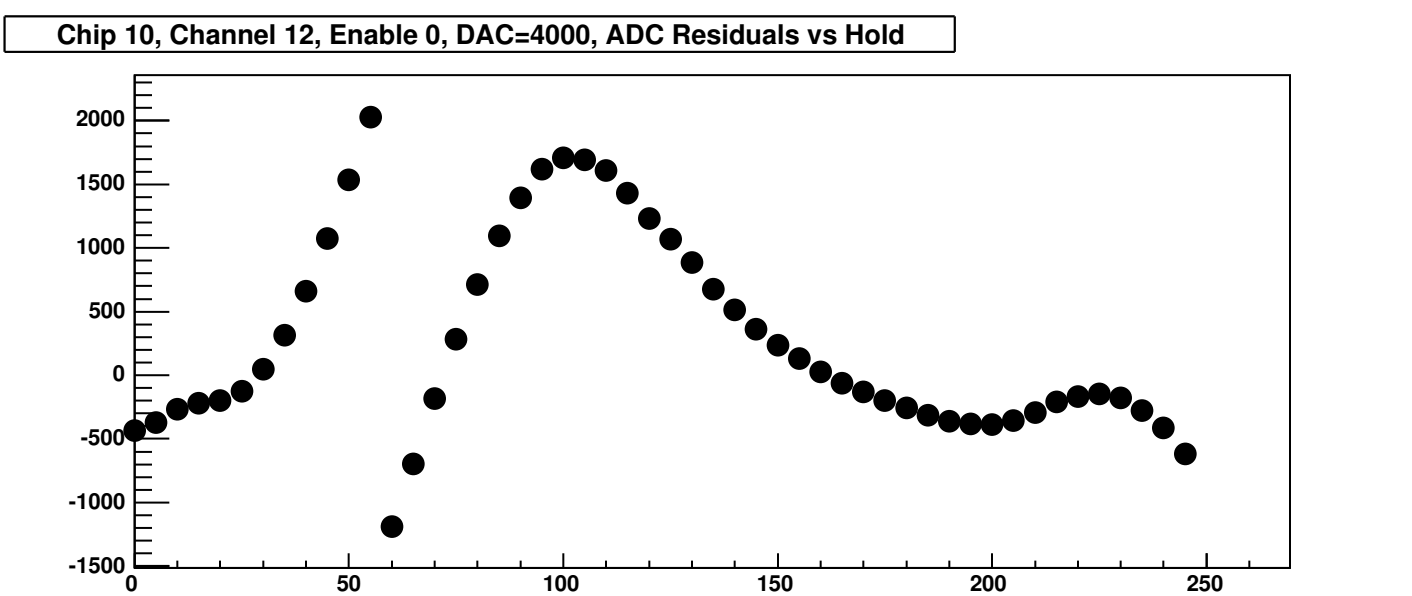

Chip 10, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.179e+05 / 41
p0 -142.2 ± 1.768
p1 47.24 ± 0.0202
p2 8429 ± 13.53
p3 45.22 ± 0.07597
p4 -12.01 ± 0.07471
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

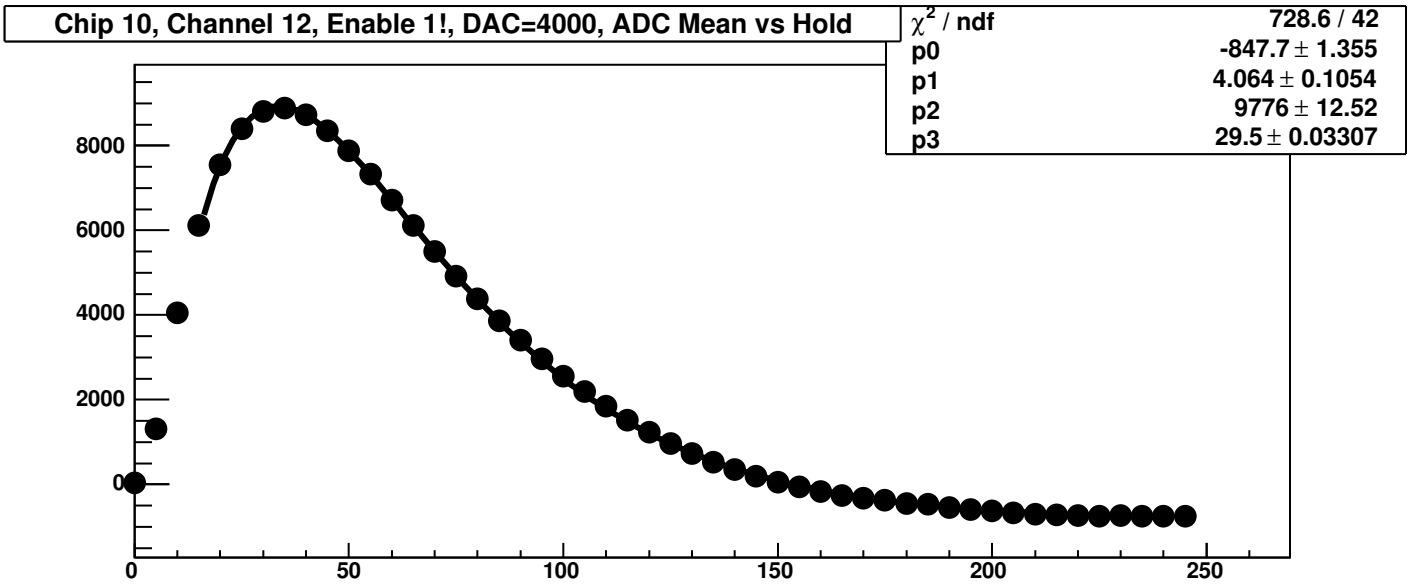

Chip 10, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250

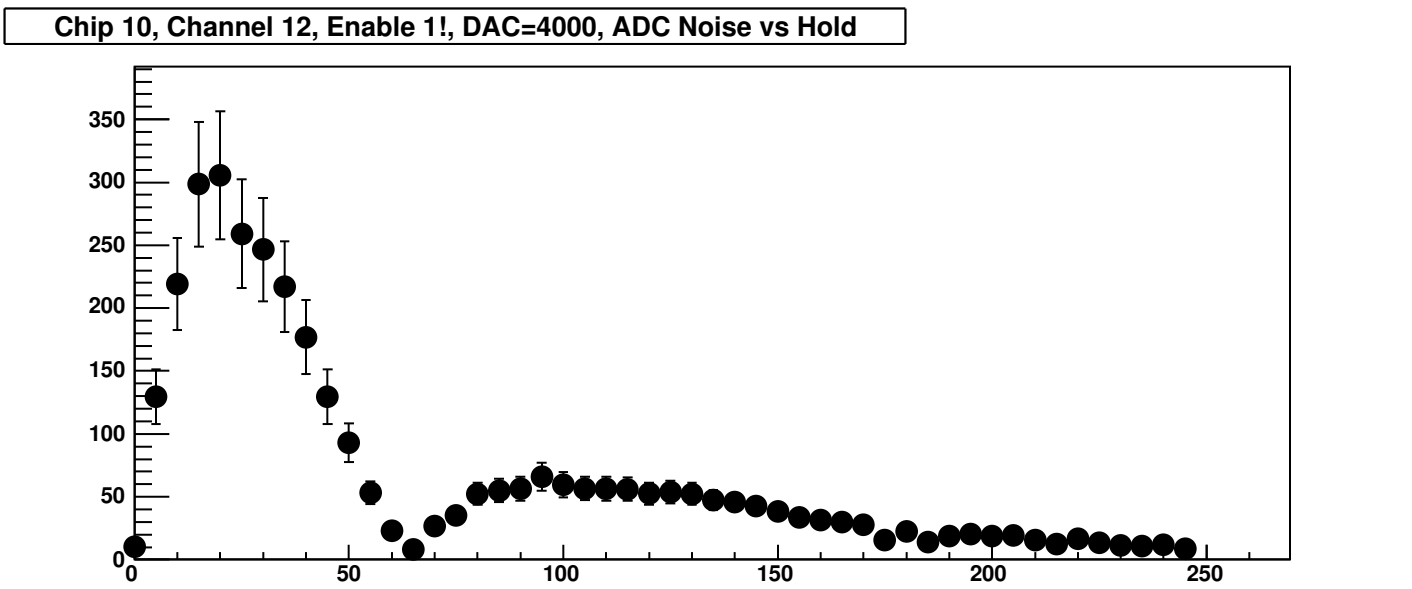

Chip 10, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250

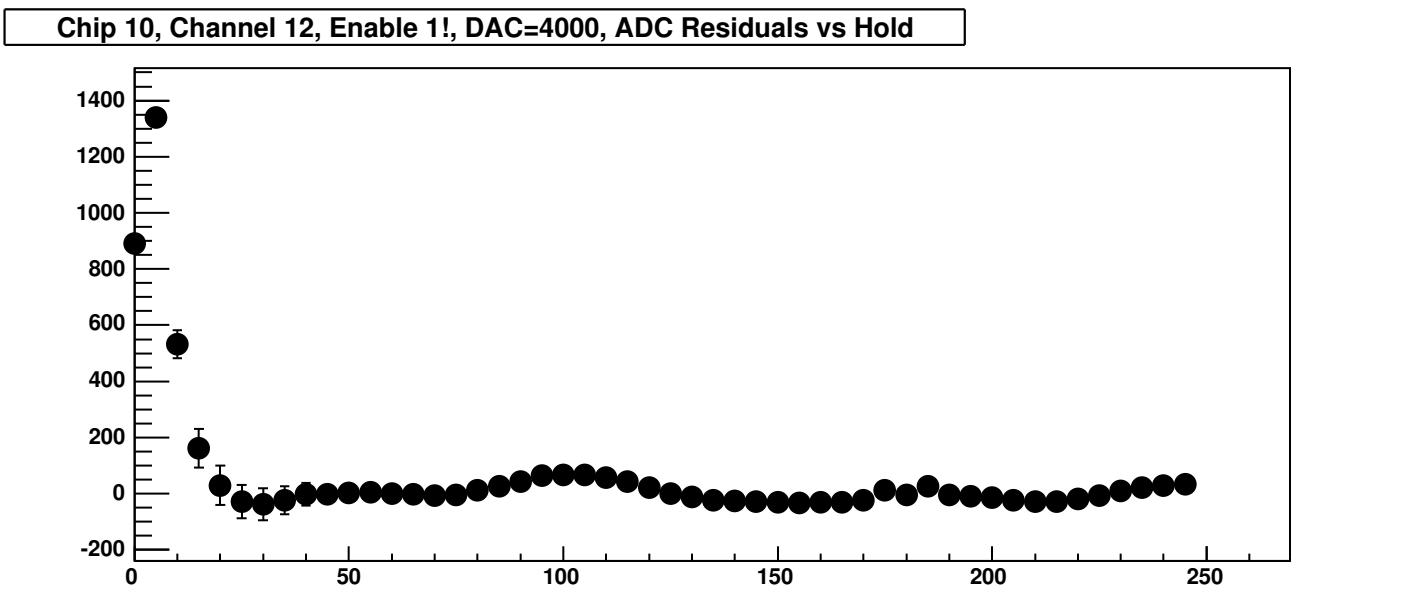

Chip 10, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 6.076e+05 / 41
p0 -396.1 ± 0.2253
p1 59.97 ± 0.01566
p2 3727 ± 0.9224
p3 -30.76 ± 8.159e-07
p4 -14.39 ± 0.003498
4000
3000
2000
1000
0
0
50
100
150
200
250

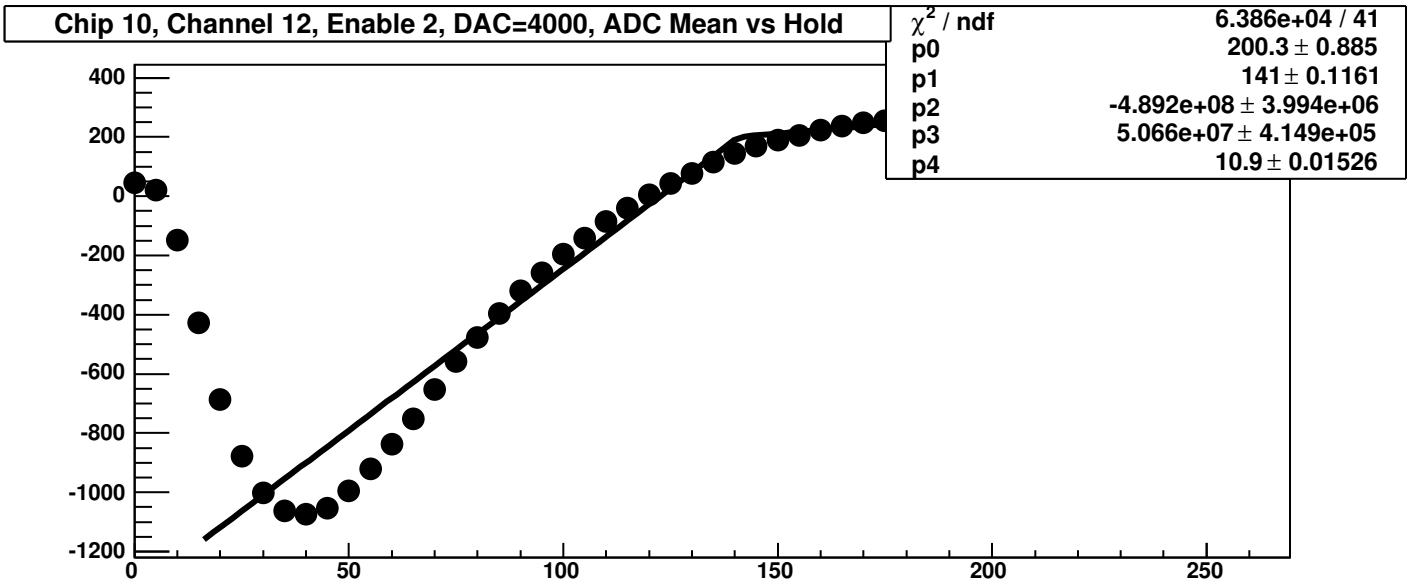

Chip 10, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250

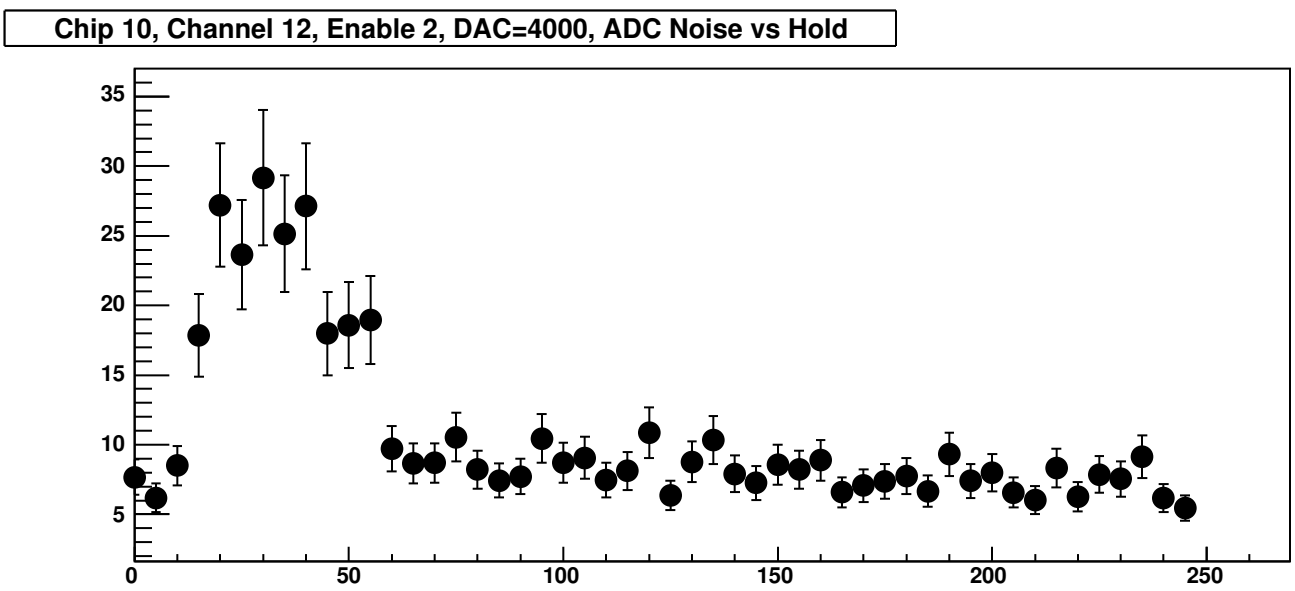

Chip 10, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

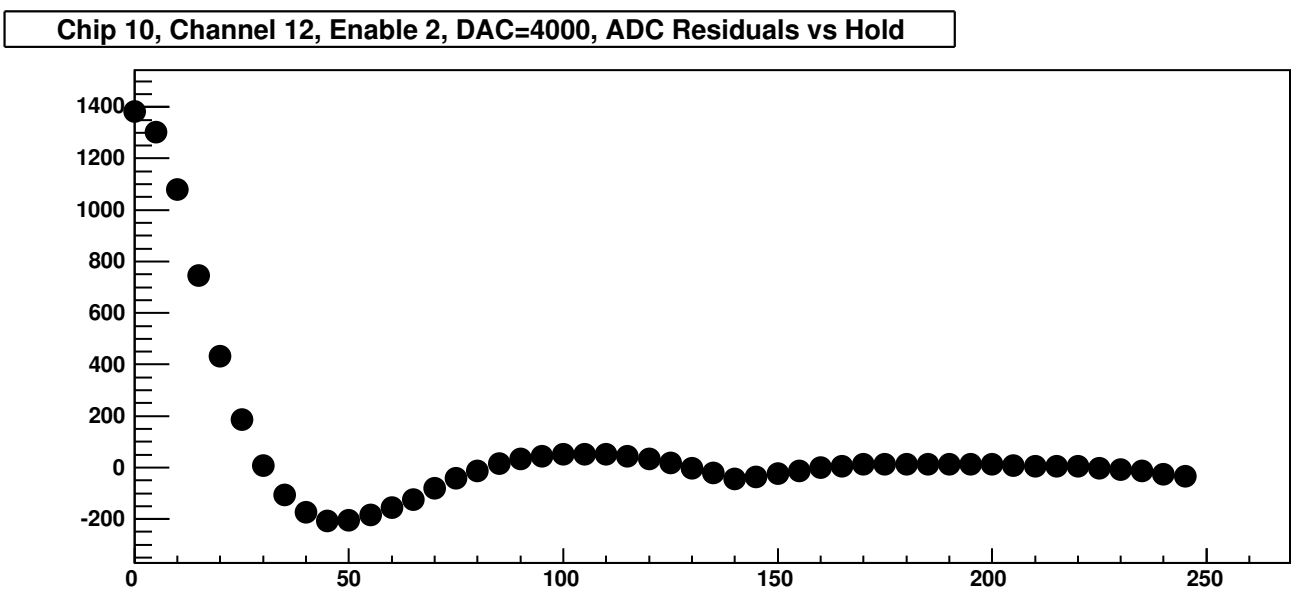

Chip 10, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 728.6 / 42
p0 -847.7 ± 1.355
p1 4.064 ± 0.1054
p2 9776 ± 12.52
p3 29.5 ± 0.03307
8000
6000
4000
2000
0
0
50
100
150
200
250

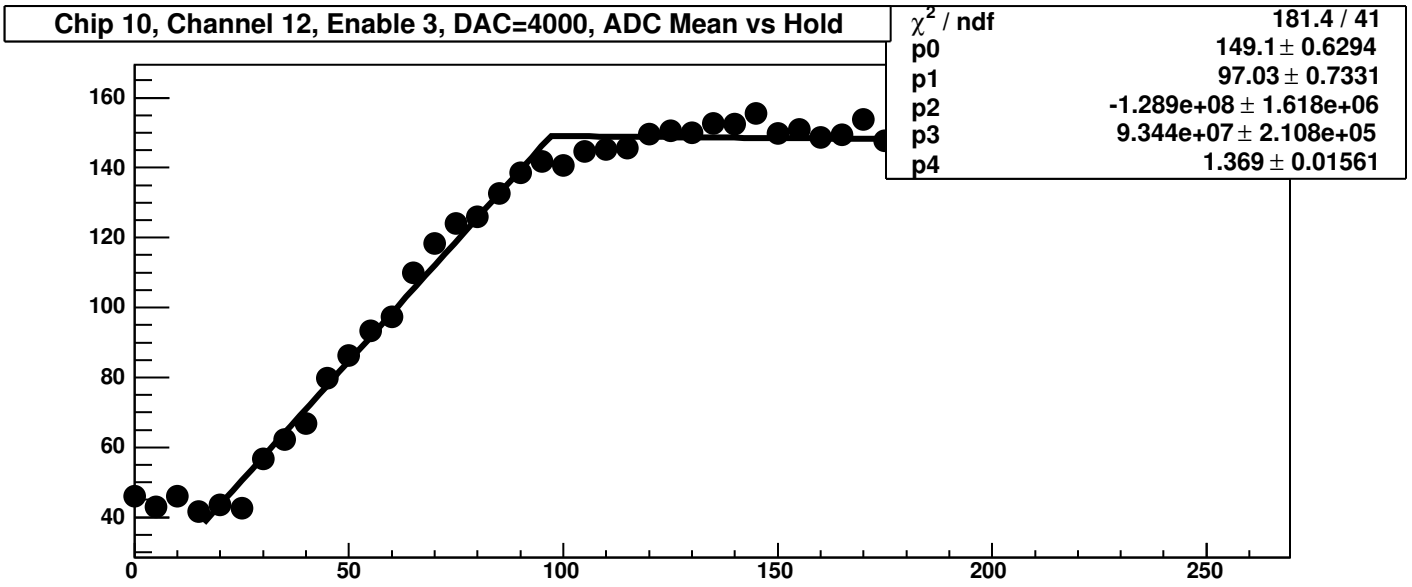

Chip 10, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold
350
300
250
200
150
100
50
0
0
50
100
150
200
250

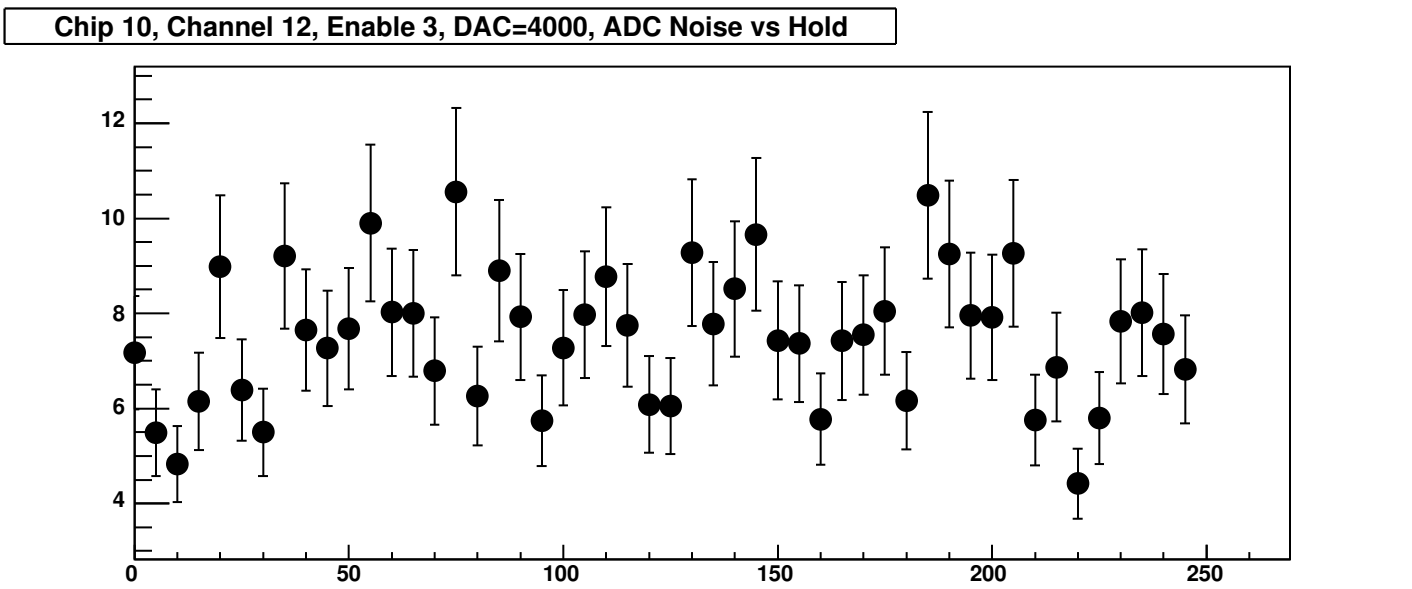

Chip 10, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

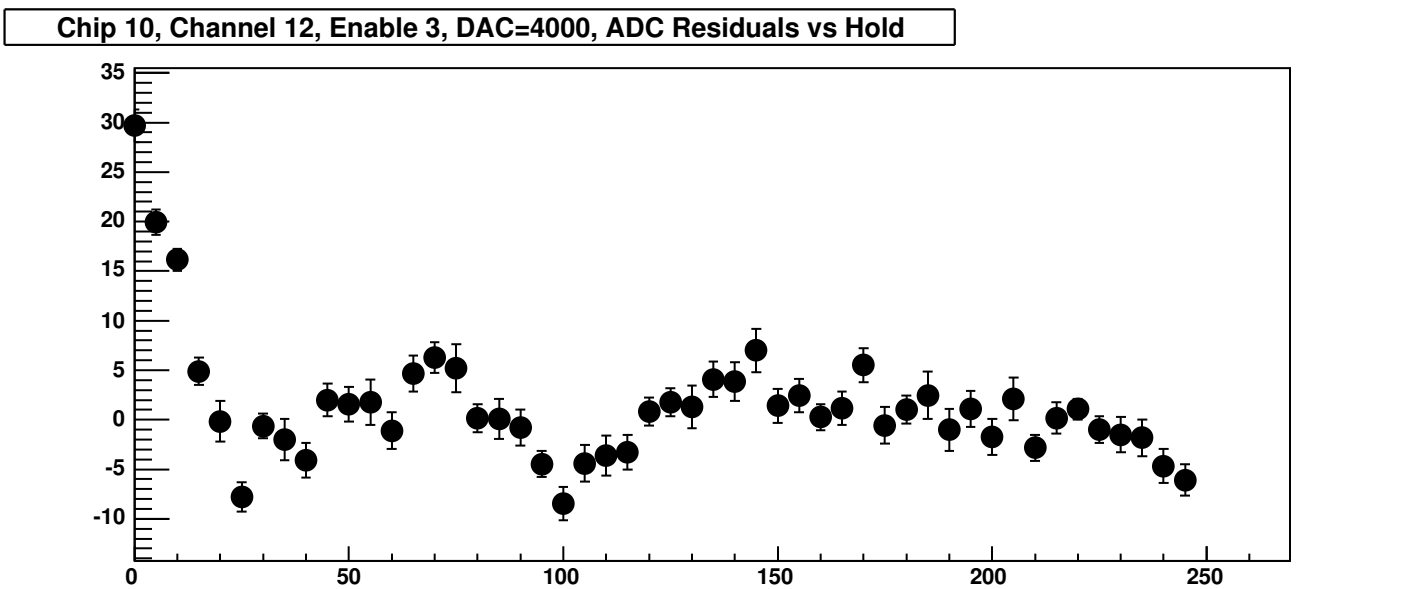

Chip 10, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 6.386e+04 / 41
p0 200.3 ± 0.885
p1 141 ± 0.1161
p2 -4.892e+08 ± 3.994e+06
p3 5.066e+07 ± 4.149e+05
p4 10.9 ± 0.01526
400
200
0
-200
-400
-600
-800
-1000
-1200
0
50
100
150
200
250

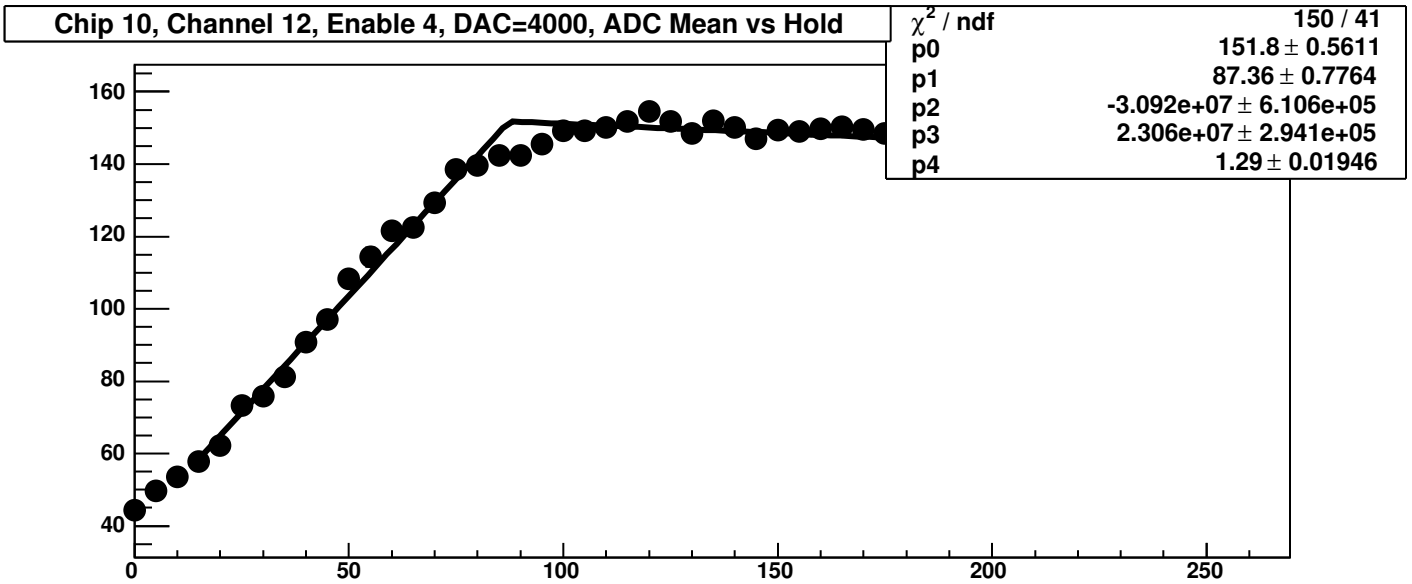

Chip 10, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold
35
30
25
20
15
10
5
0
50
100
150
200
250

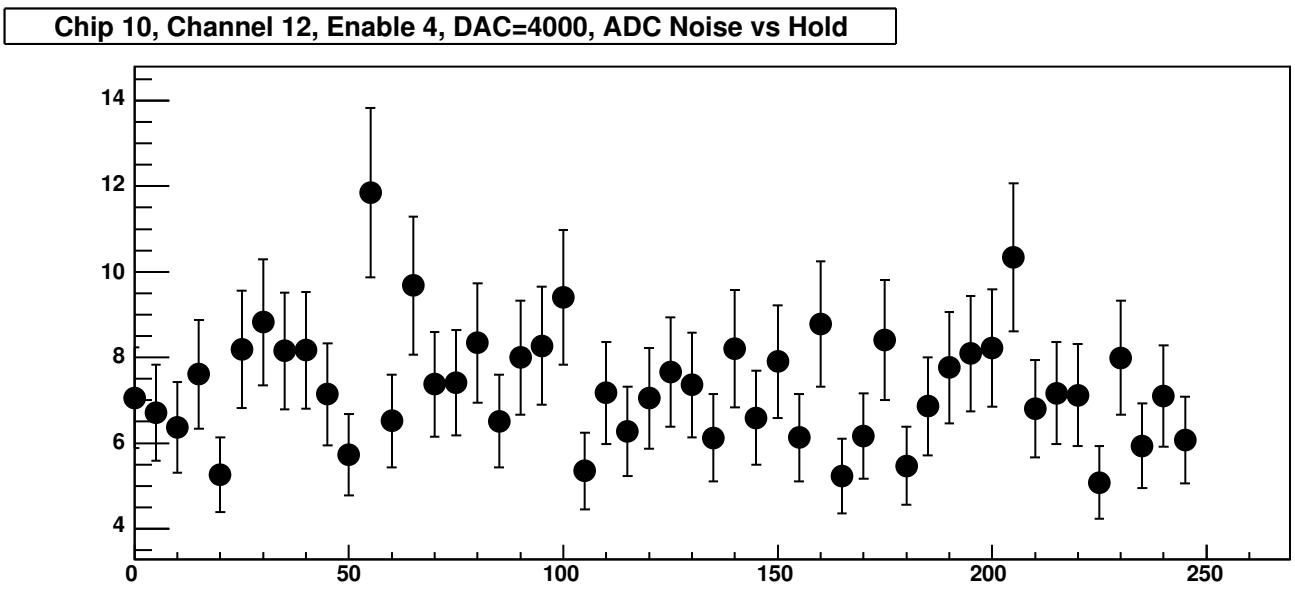

Chip 10, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

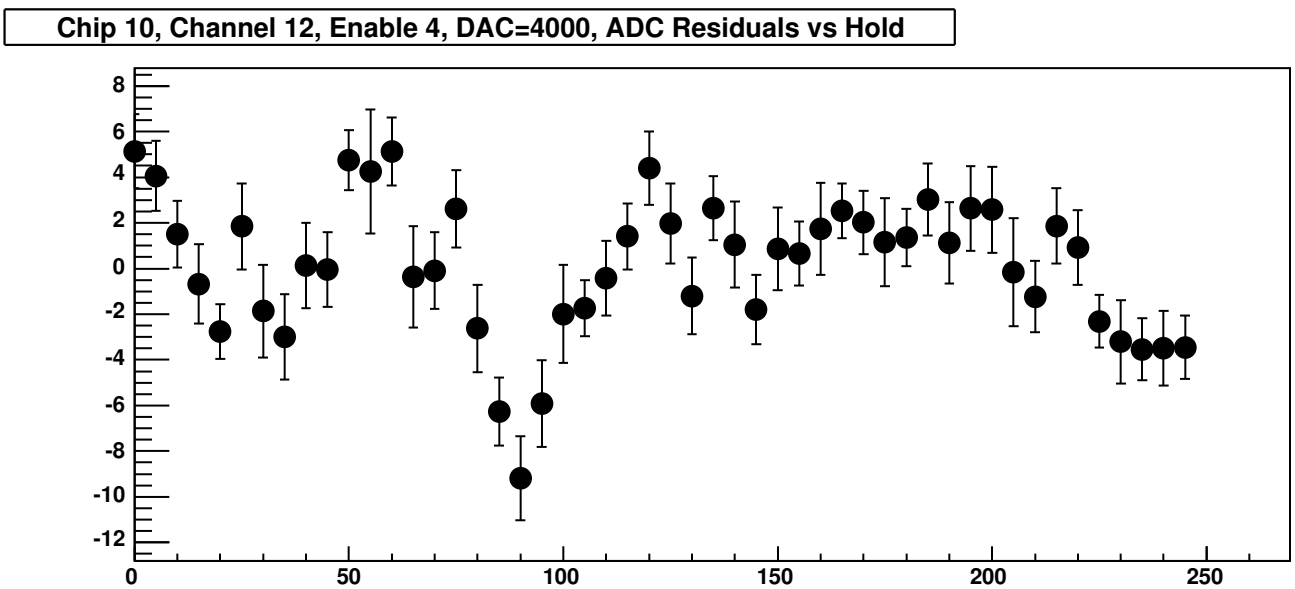

Chip 10, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 181.4 / 41
p0 149.1 ± 0.6294
p1 97.03 ± 0.7331
p2 -1.289e+08 ± 1.618e+06
p3 9.344e+07 ± 2.108e+05
p4 1.369 ± 0.01561
160
140
120
100
80
60
40
0
50
100
150
200
250

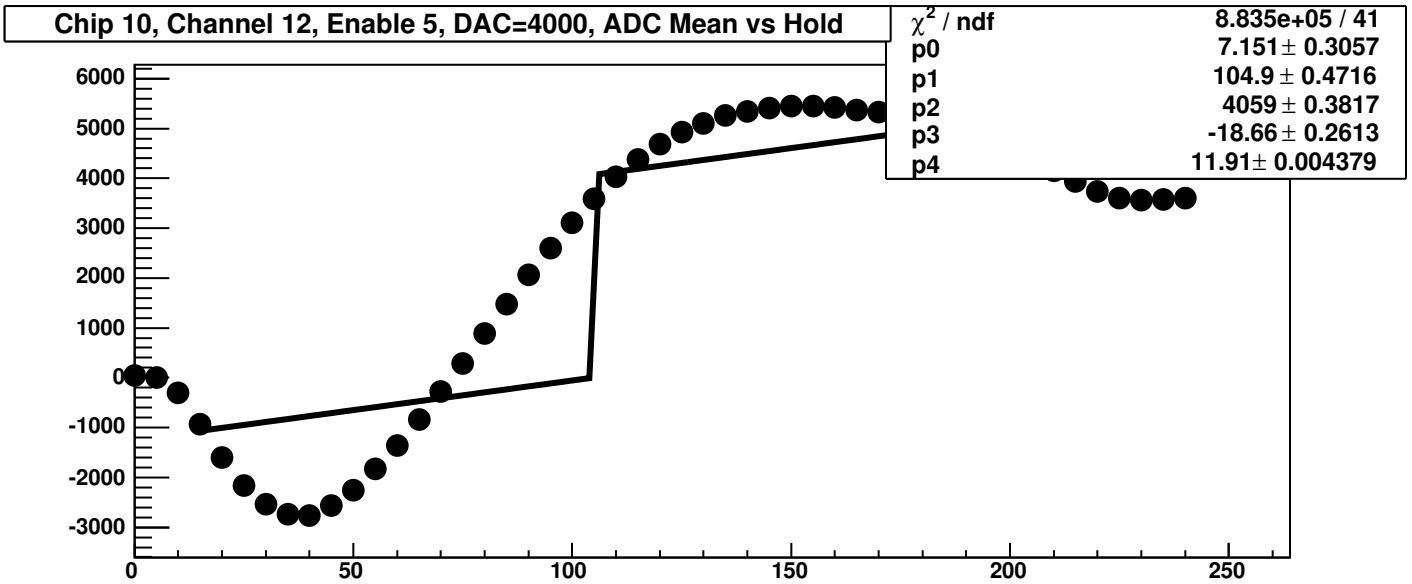

Chip 10, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

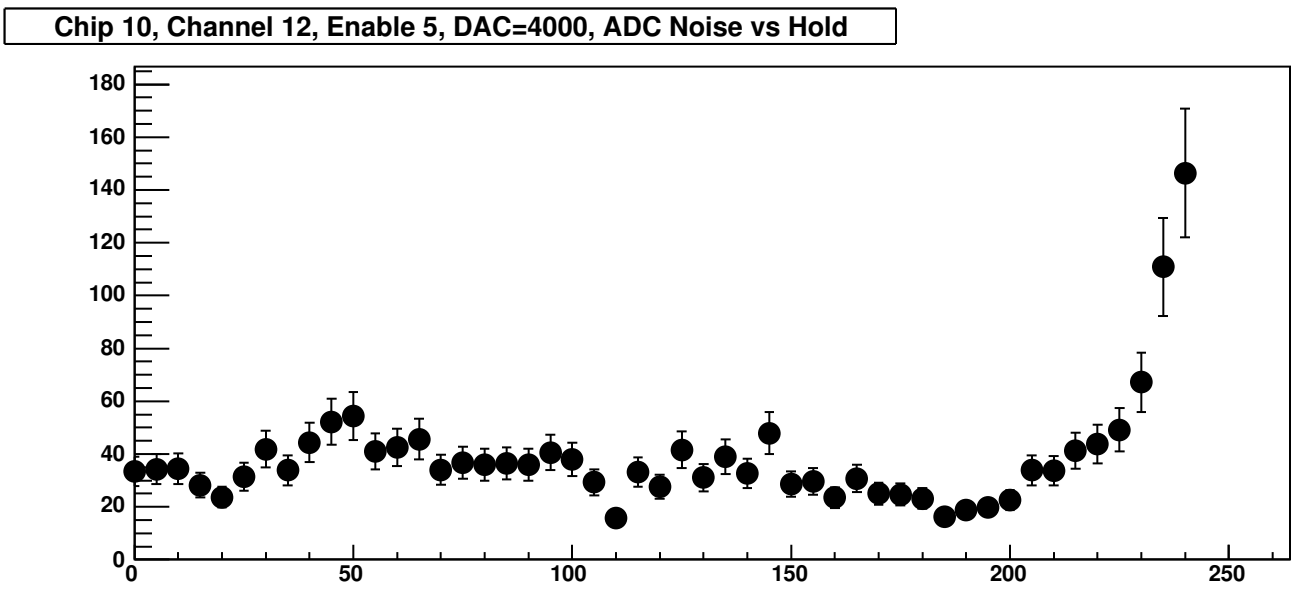

Chip 10, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold
35
30
25
20
15
10
5
0
-5
-10
0
50
100
150
200
250

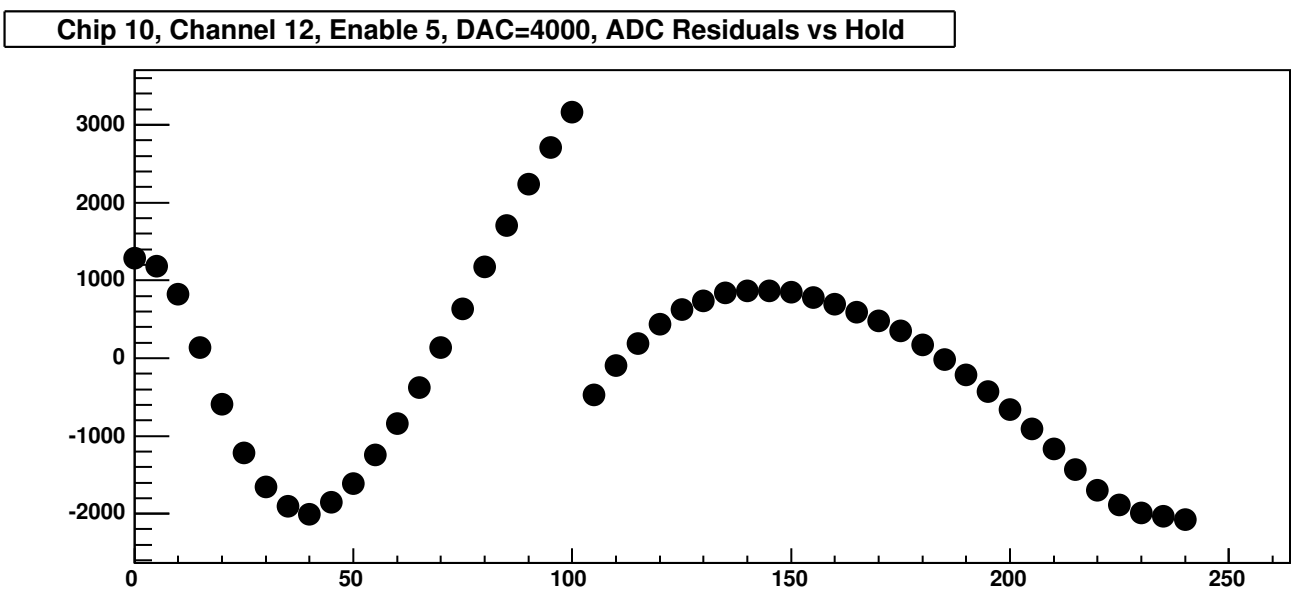

Chip 10, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 150 / 41
p0 151.8 ± 0.5611
p1 87.36 ± 0.7764
p2 -3.092e+07 ± 6.106e+05
p3 2.306e+07 ± 2.941e+05
p4 1.29 ± 0.01946

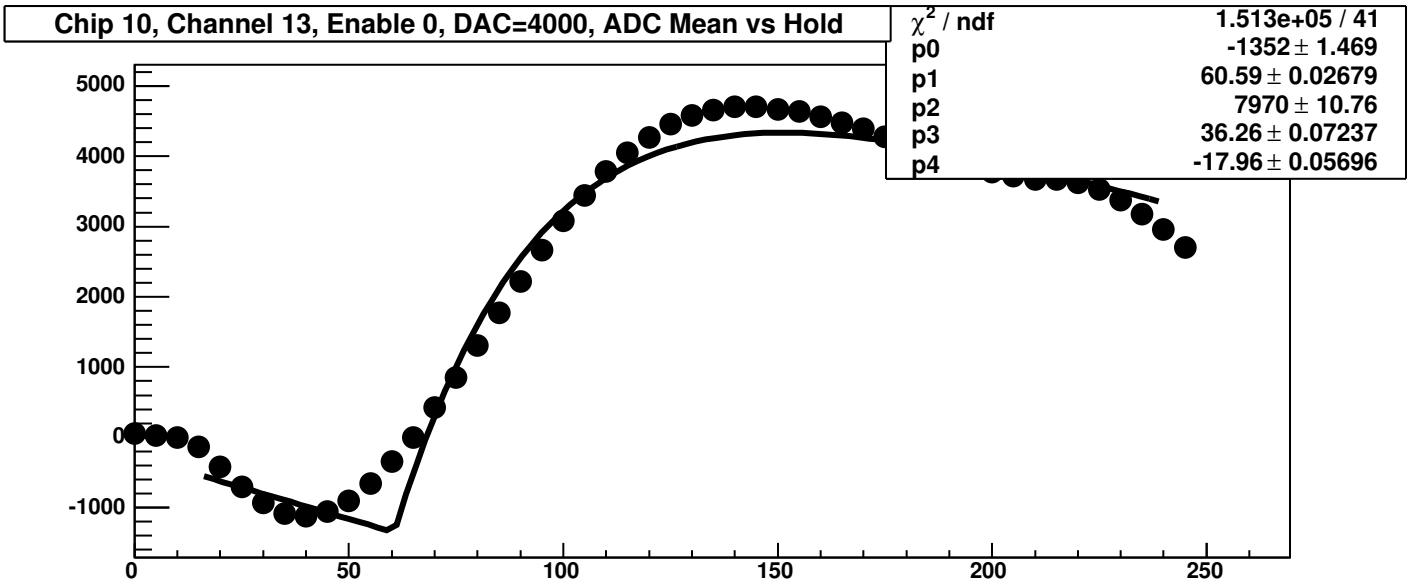

Chip 10, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold

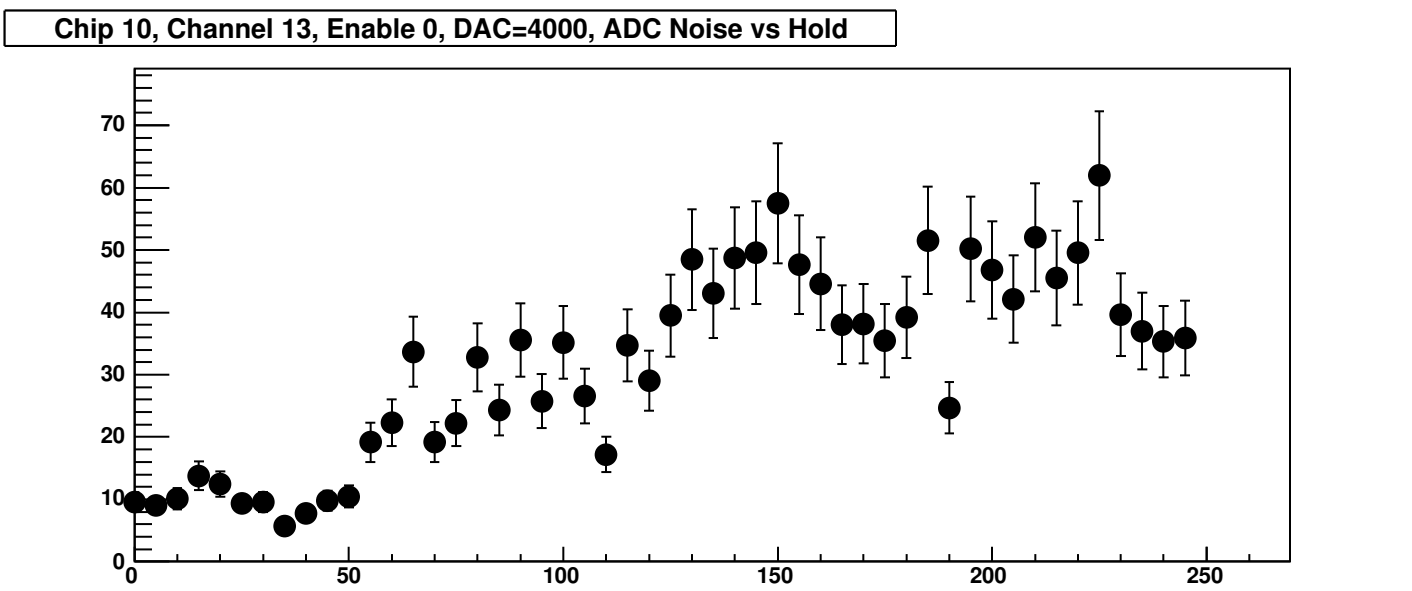

Chip 10, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold

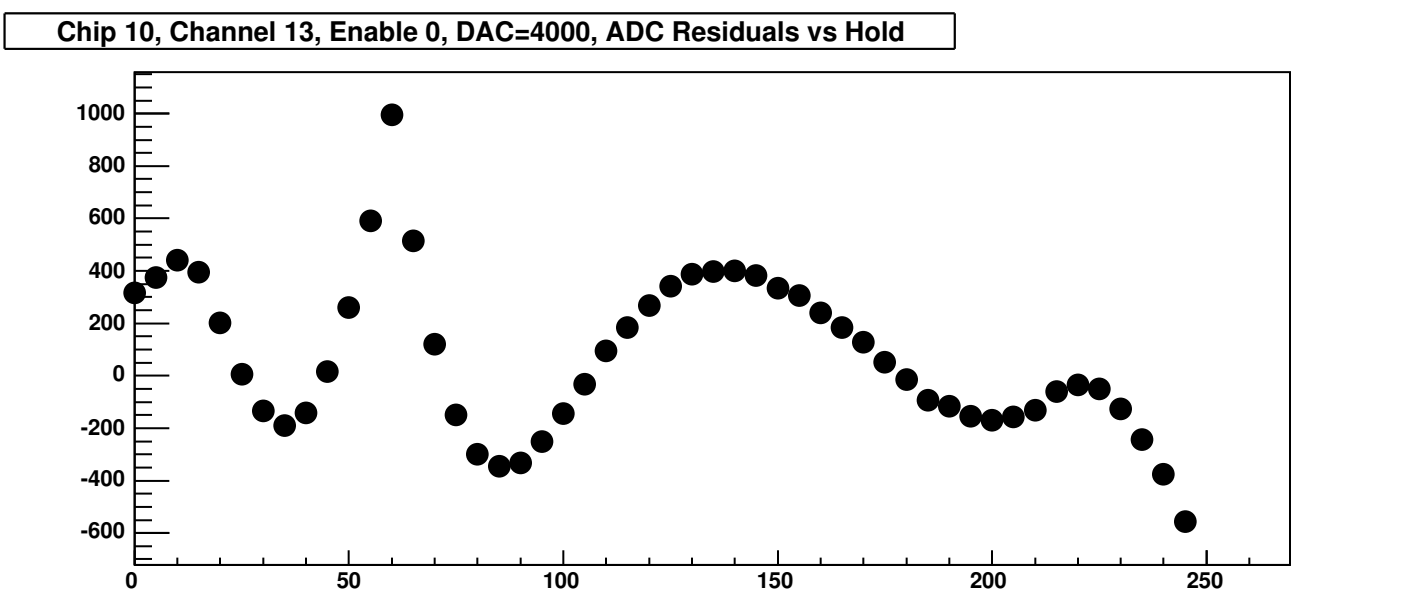

Chip 10, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 8.835e+05 / 41
p0 7.151 ± 0.3057
p1 104.9 ± 0.4716
p2 4059 ± 0.3817
p3 -18.66 ± 0.2613
p4 11.91 ± 0.004379
6000
5000
4000
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

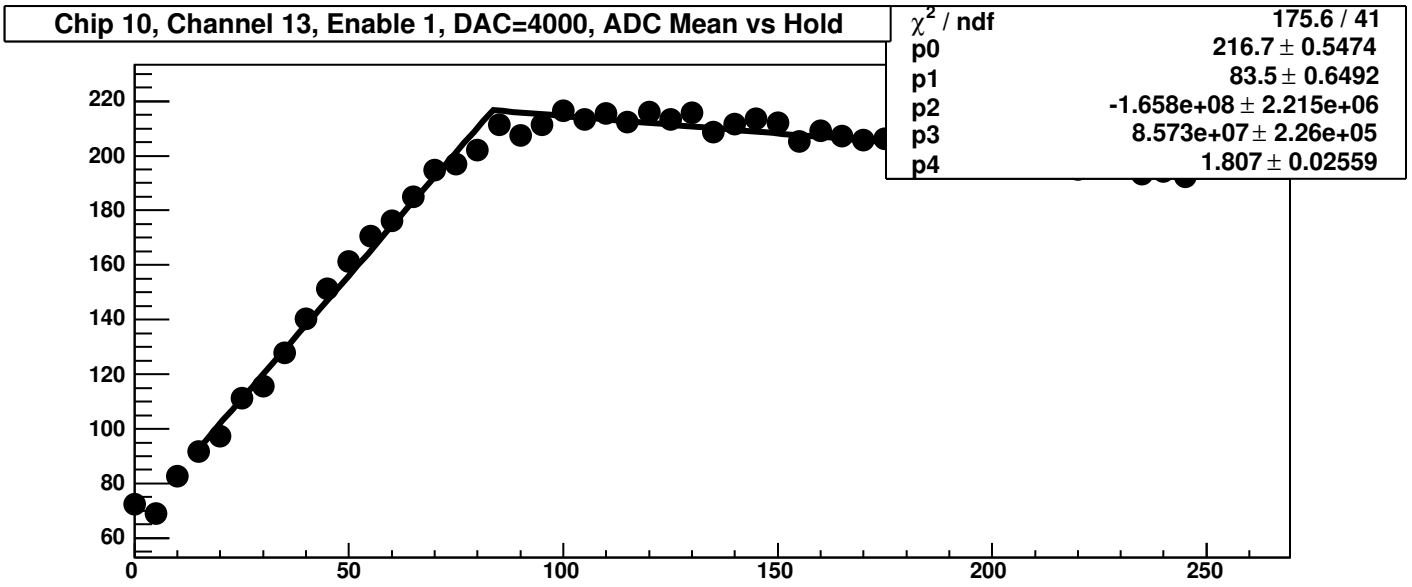

Chip 10, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

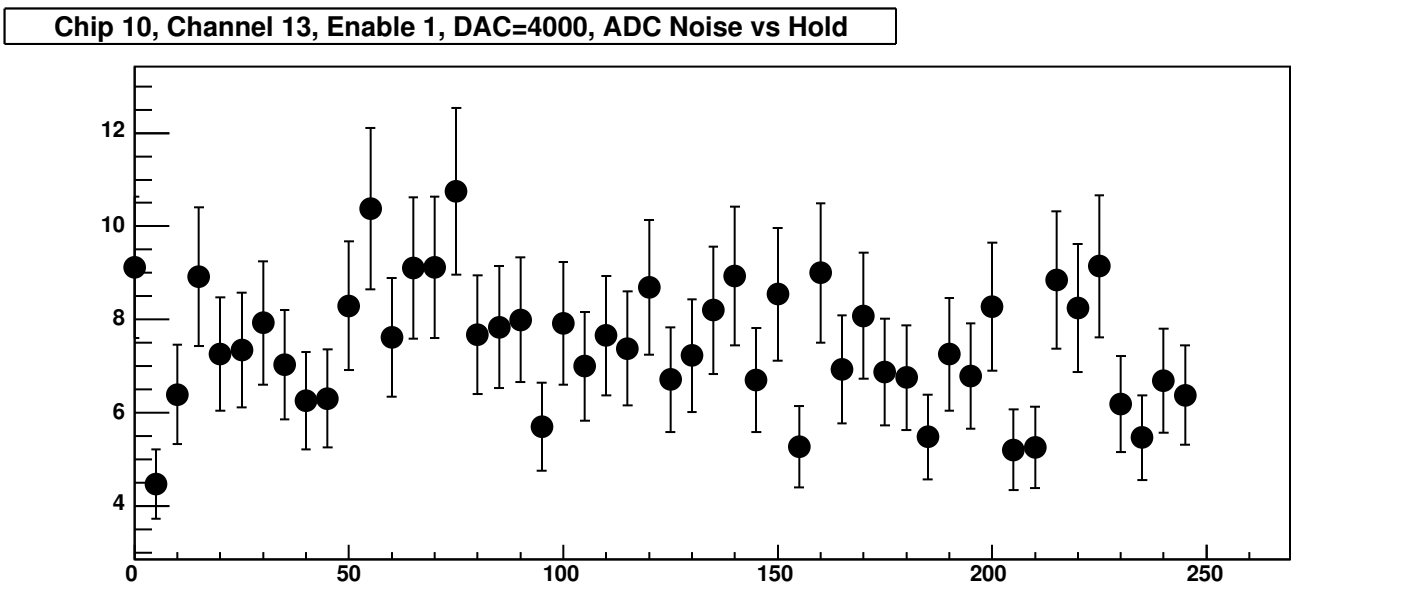

Chip 10, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
0
50
100
150
200
250

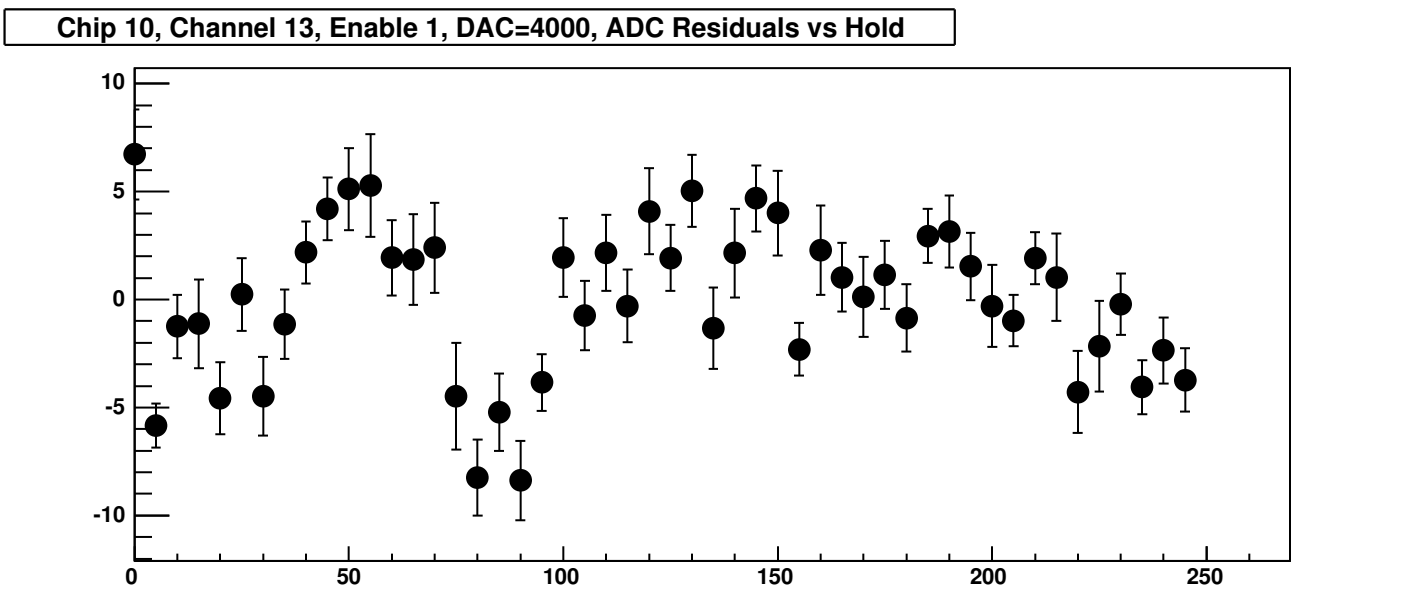

Chip 10, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.513e+05 / 41
p0 -1352 ± 1.469
p1 60.59 ± 0.02679
p2 7970 ± 10.76
p3 36.26 ± 0.07237
p4 -17.96 ± 0.05696
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

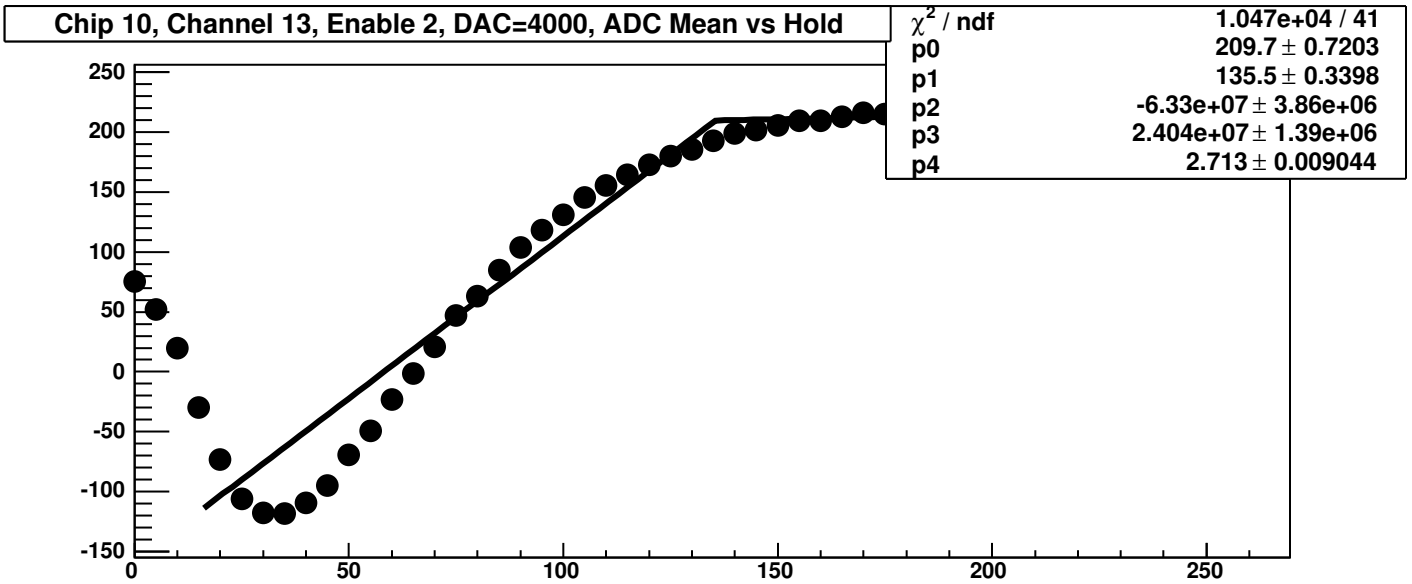

Chip 10, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold
70
60
50
40
30
20
10
0
0
50
100
150
200
250

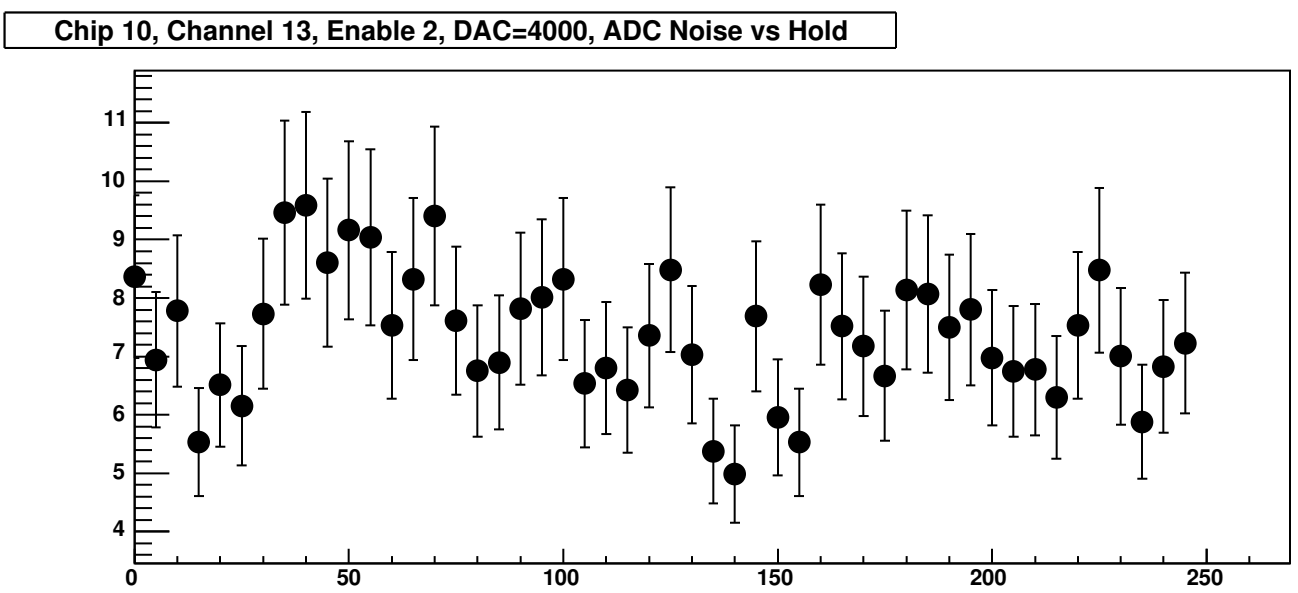

Chip 10, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
800
600
400
200
0
-200
-400
-600
0
50
100
150
200
250

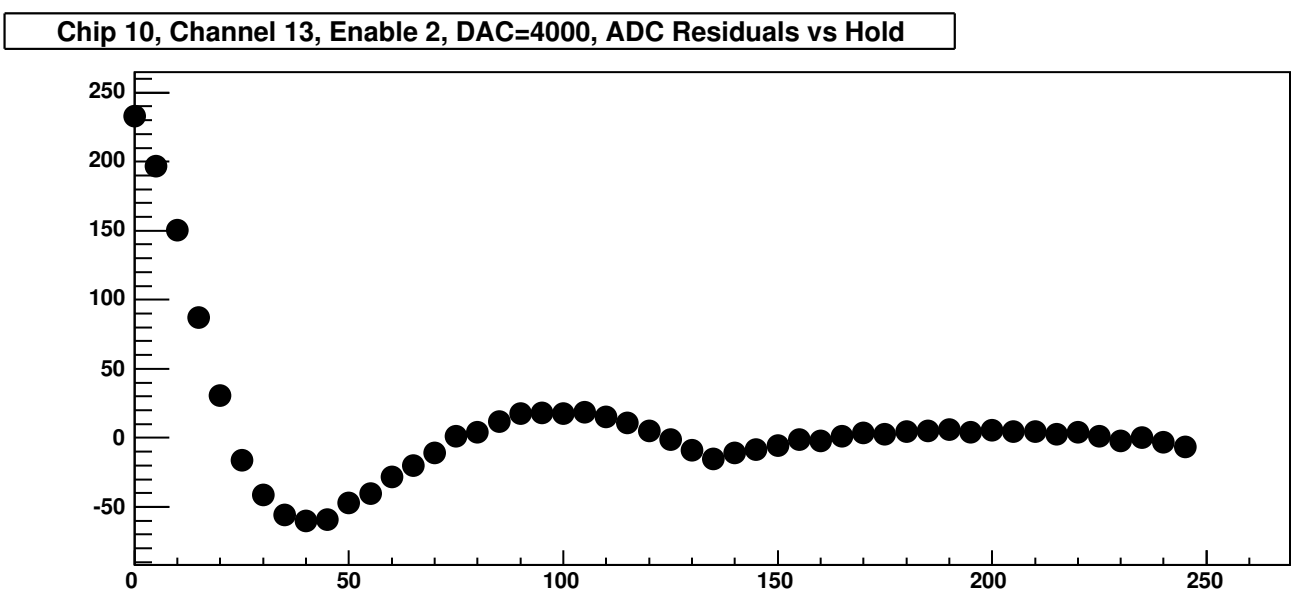

Chip 10, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 175.6 / 41
p0 216.7 ± 0.5474
p1 83.5 ± 0.6492
p2 -1.658e+08 ± 2.215e+06
p3 8.573e+07 ± 2.26e+05
p4 1.807 ± 0.02559
220
200
180
160
140
120
100
80
60
0
50
100
150
200
250

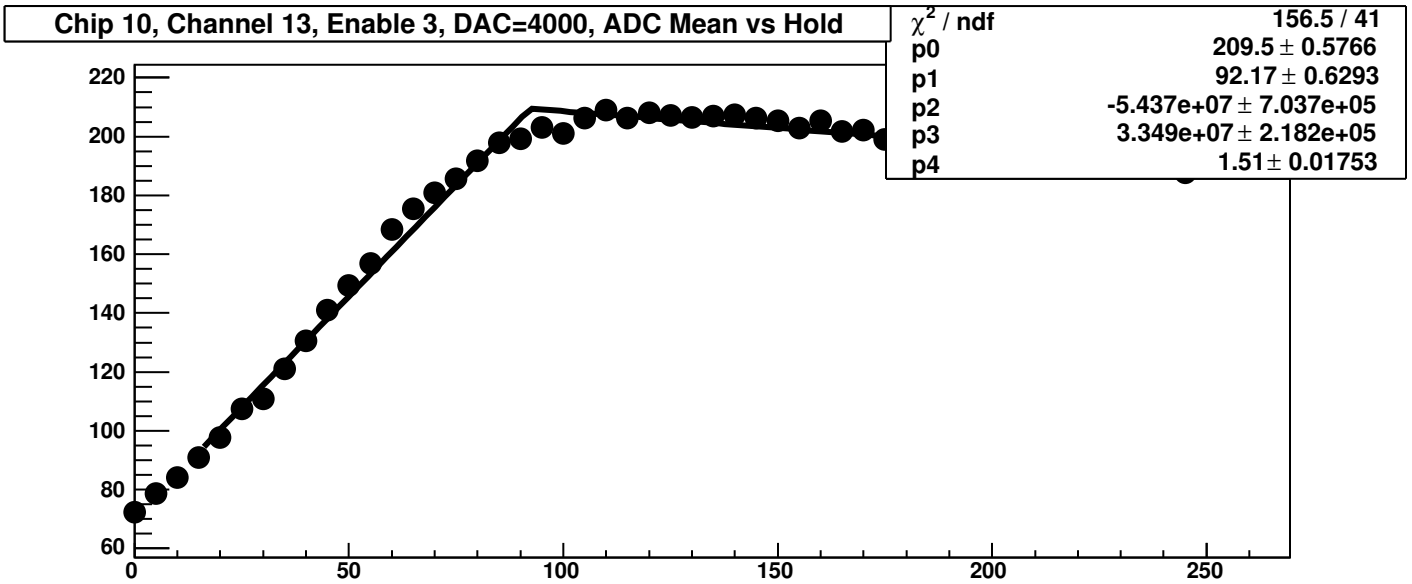

Chip 10, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

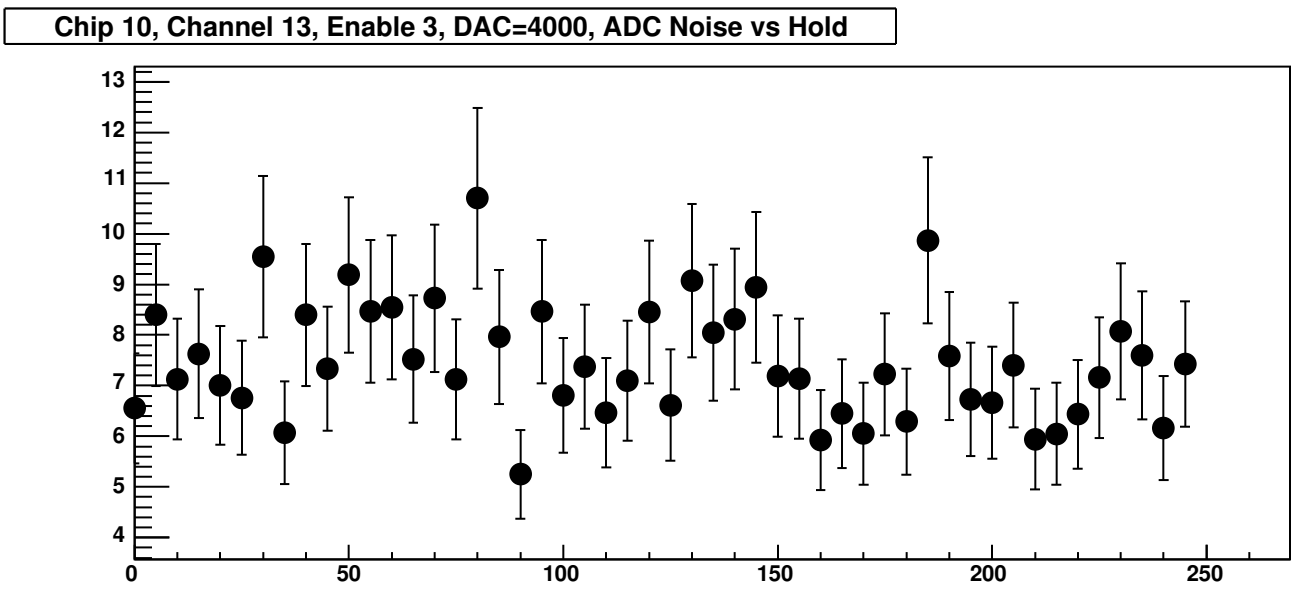

Chip 10, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250

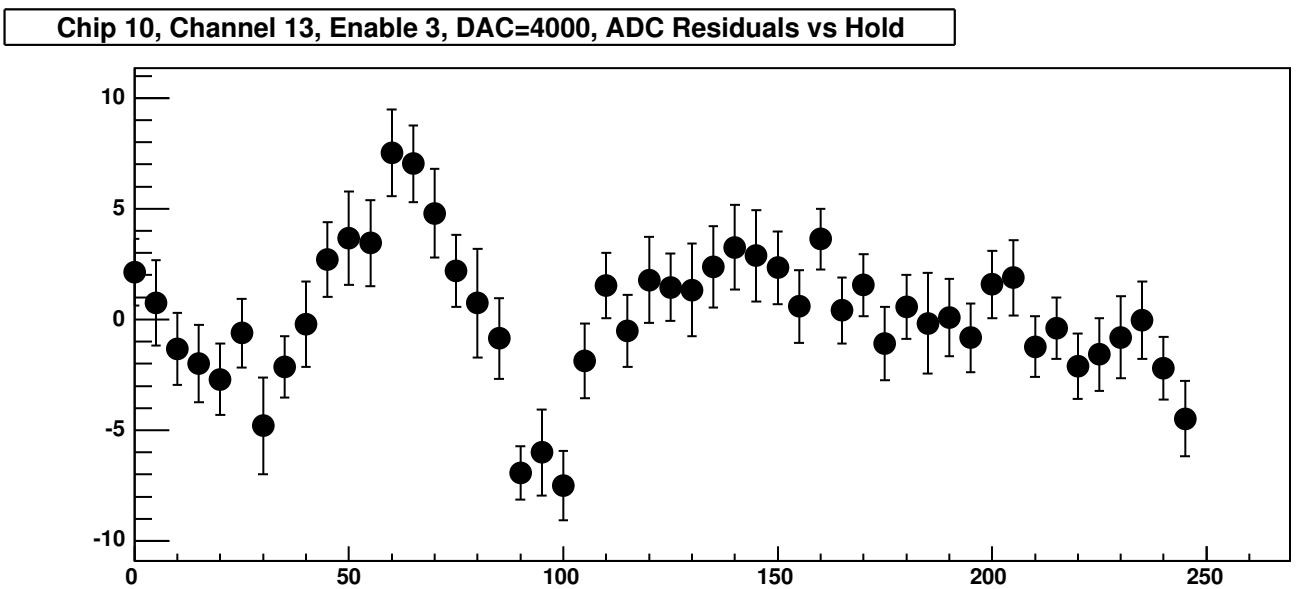

Chip 10, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.047e+04 / 41
p0 209.7 ± 0.7203
p1 135.5 ± 0.3398
p2 -6.33e+07 ± 3.86e+06
p3 2.404e+07 ± 1.39e+06
p4 2.713 ± 0.009044

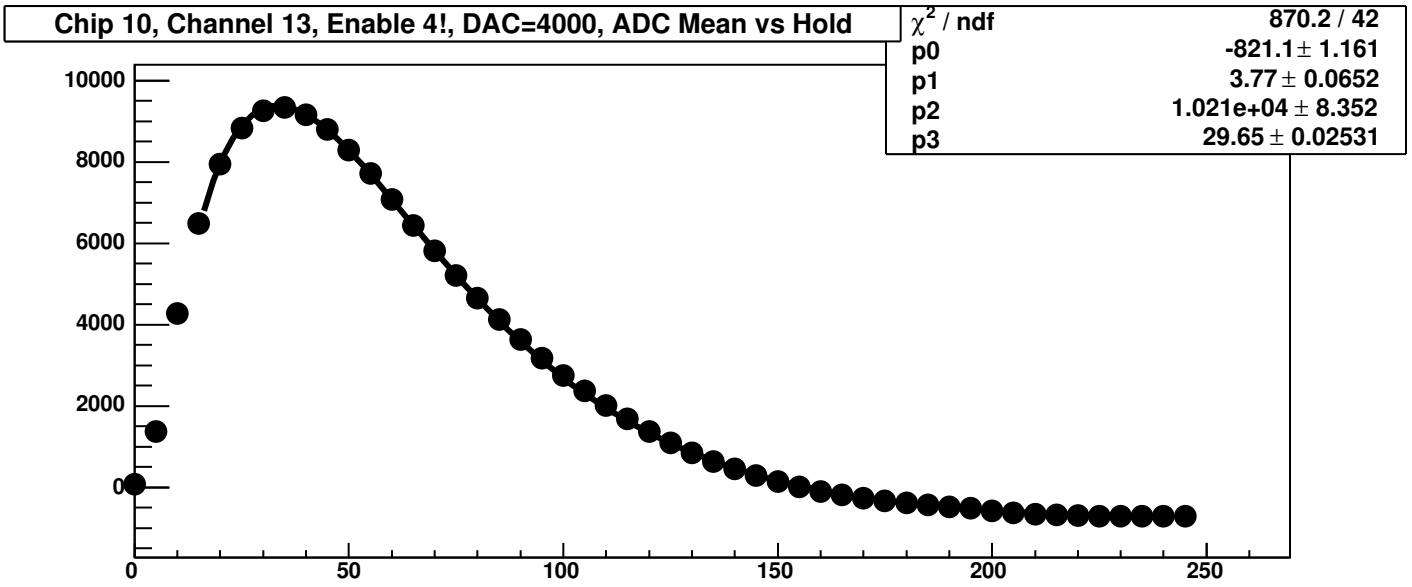

Chip 10, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold

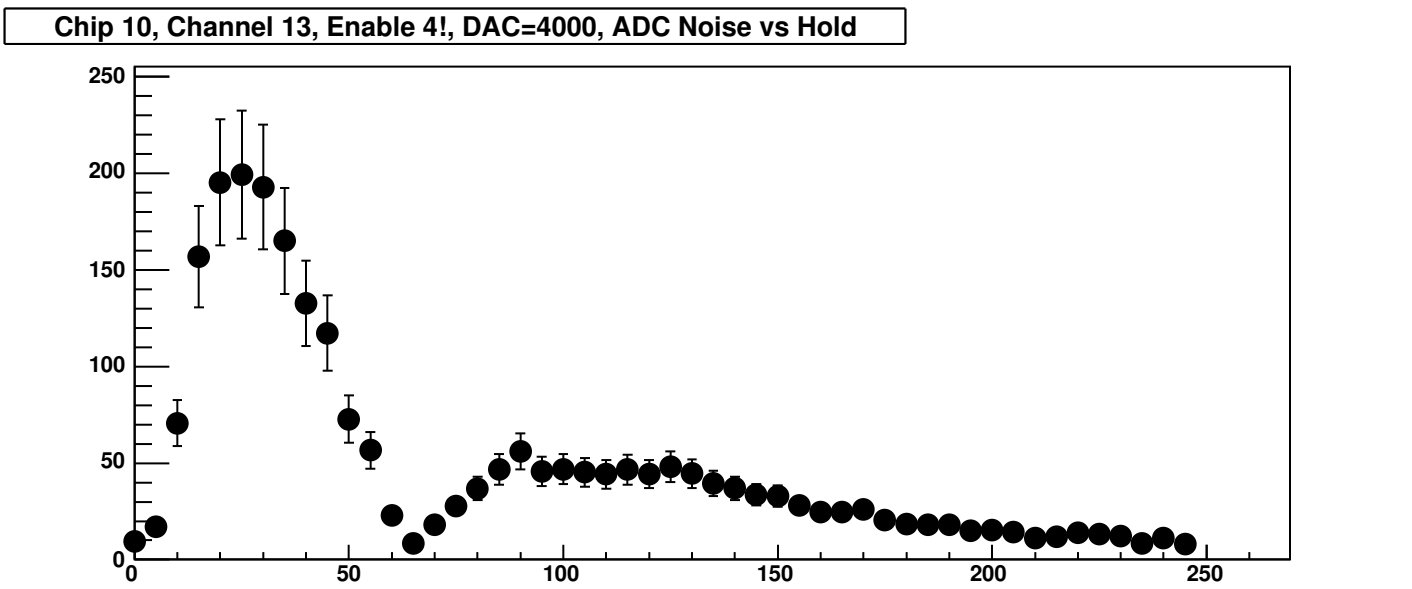

Chip 10, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

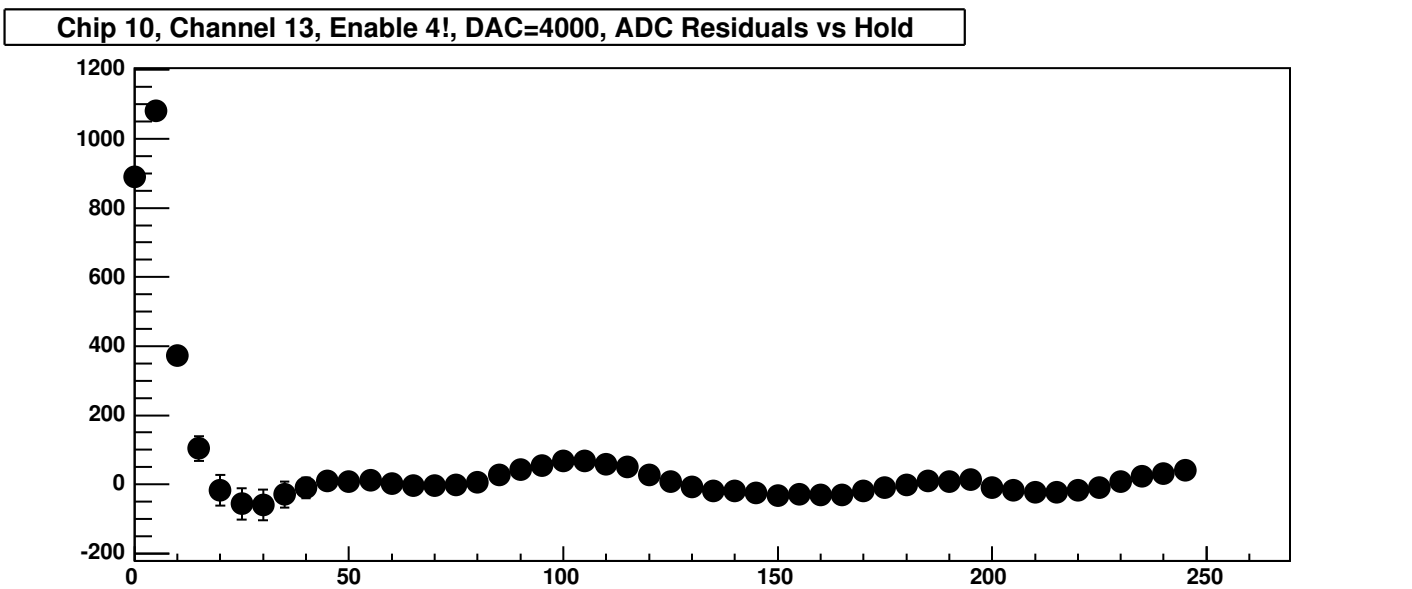

Chip 10, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 156.5 / 41
p0 209.5 ± 0.5766
p1 92.17 ± 0.6293
p2 -5.437e+07 ± 7.037e+05
p3 3.349e+07 ± 2.182e+05
p4 1.51 ± 0.01753

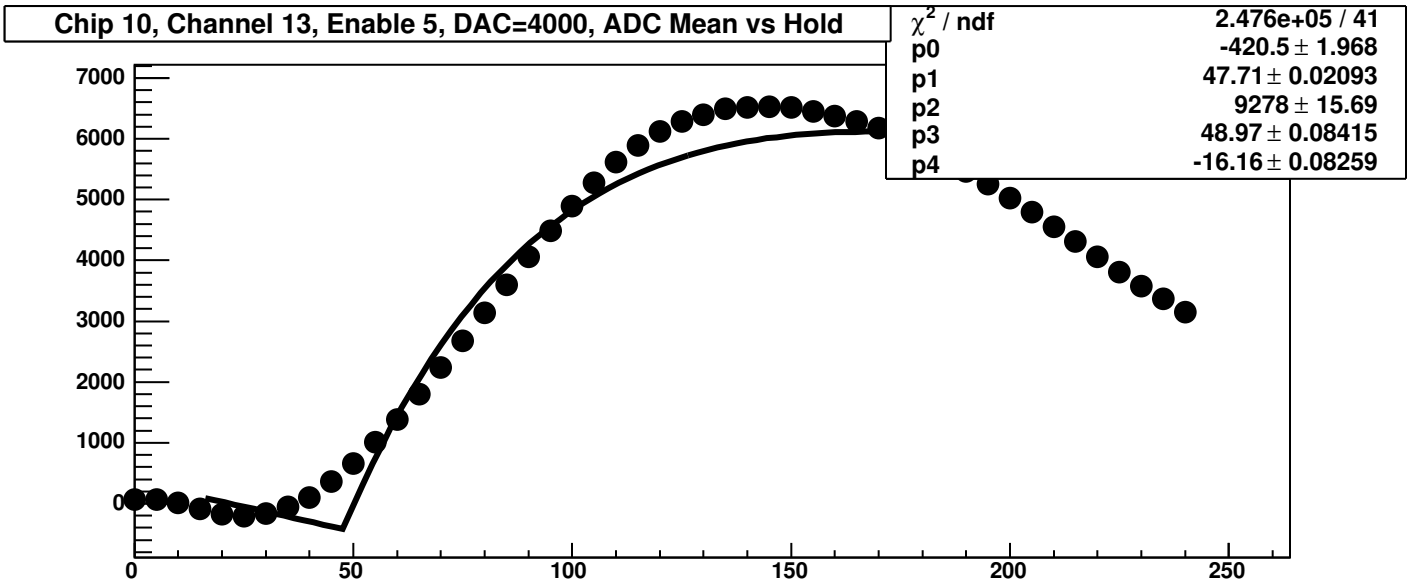

Chip 10, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold

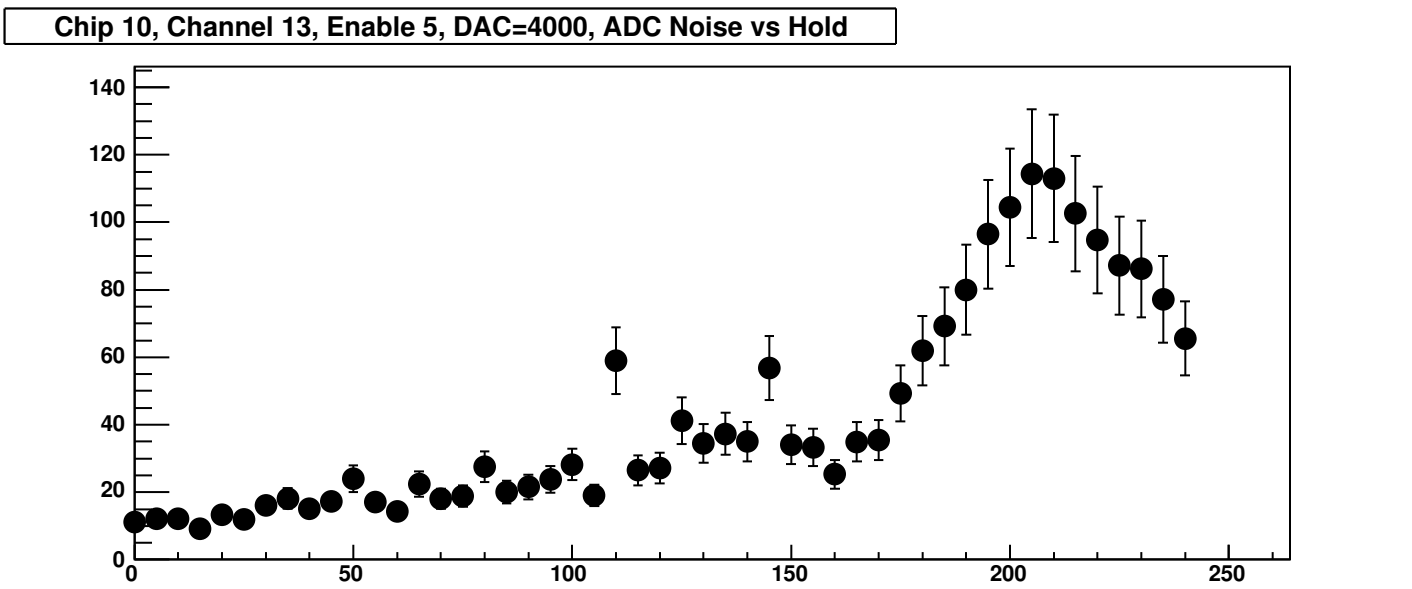

Chip 10, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

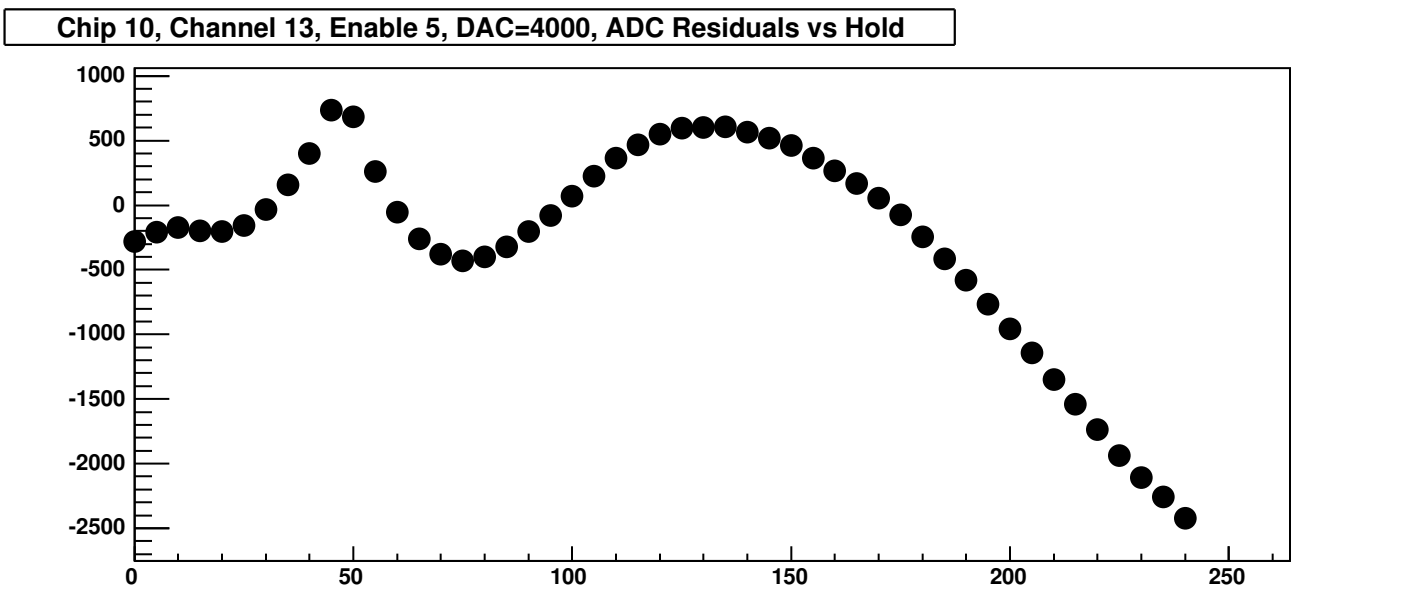

Chip 10, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 870.2 / 42
p0 -821.1 ± 1.161
p1 3.77 ± 0.0652
p2 1.021e+04 ± 8.352
p3 29.65 ± 0.02531
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 10, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250

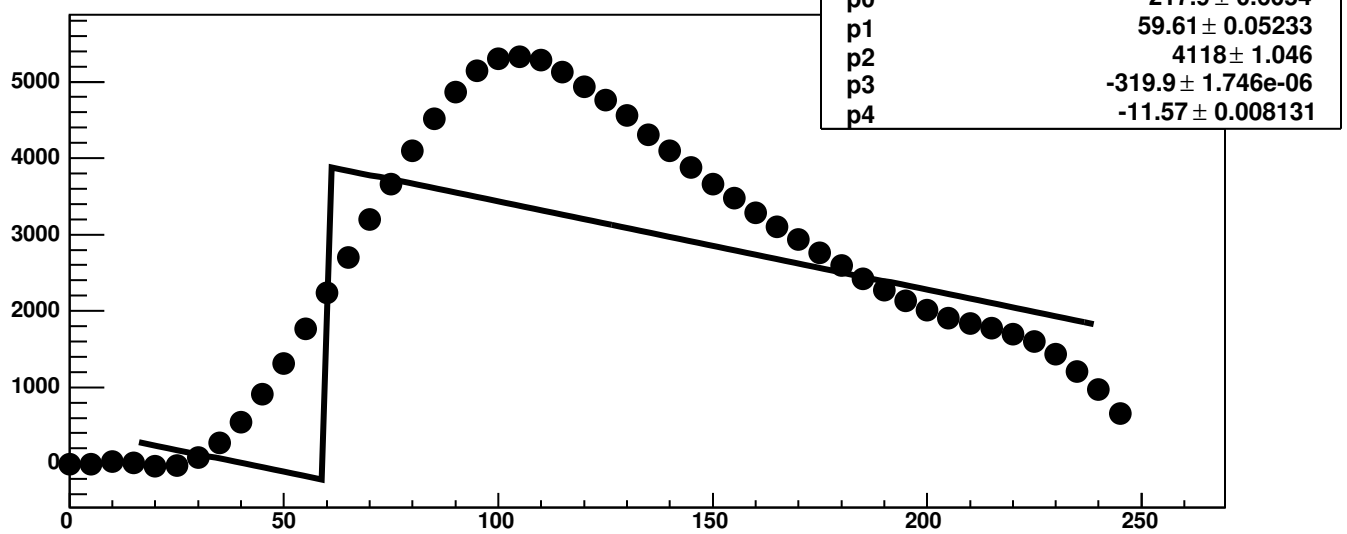

Chip 10, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250


Chip 10, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.476e+05 / 41
p0 -420.5 ± 1.968
p1 47.71 ± 0.02093
p2 9278 ± 15.69
p3 48.97 ± 0.08415
p4 -16.16 ± 0.08259
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

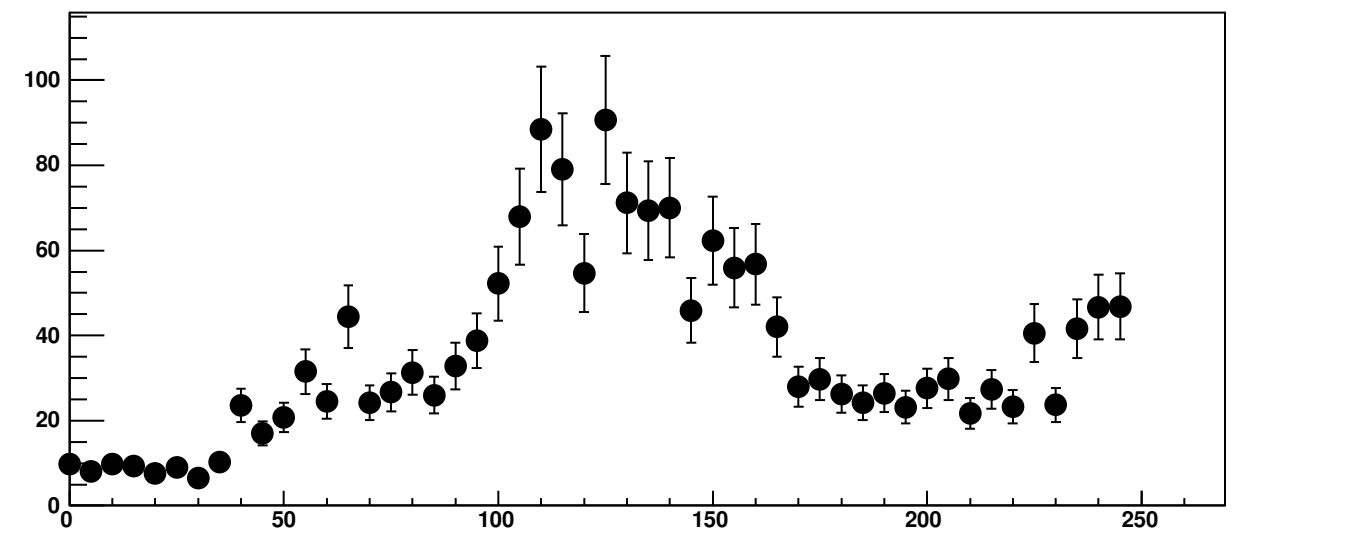

Chip 10, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 10, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
0
50
100
150
200
250

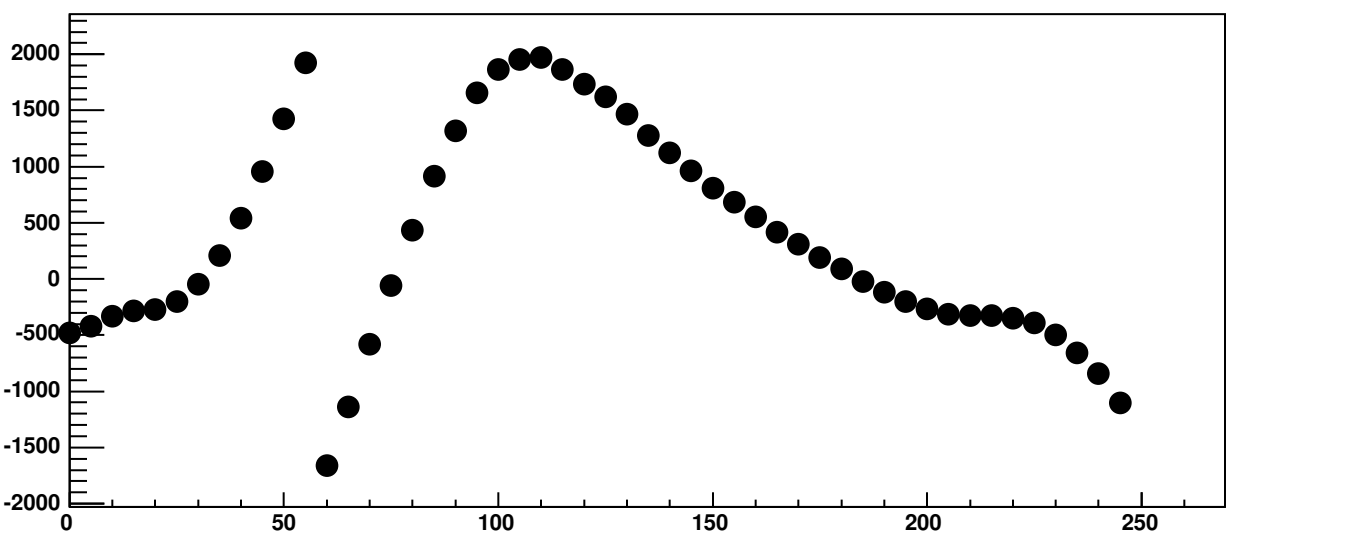
Chip 10, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold

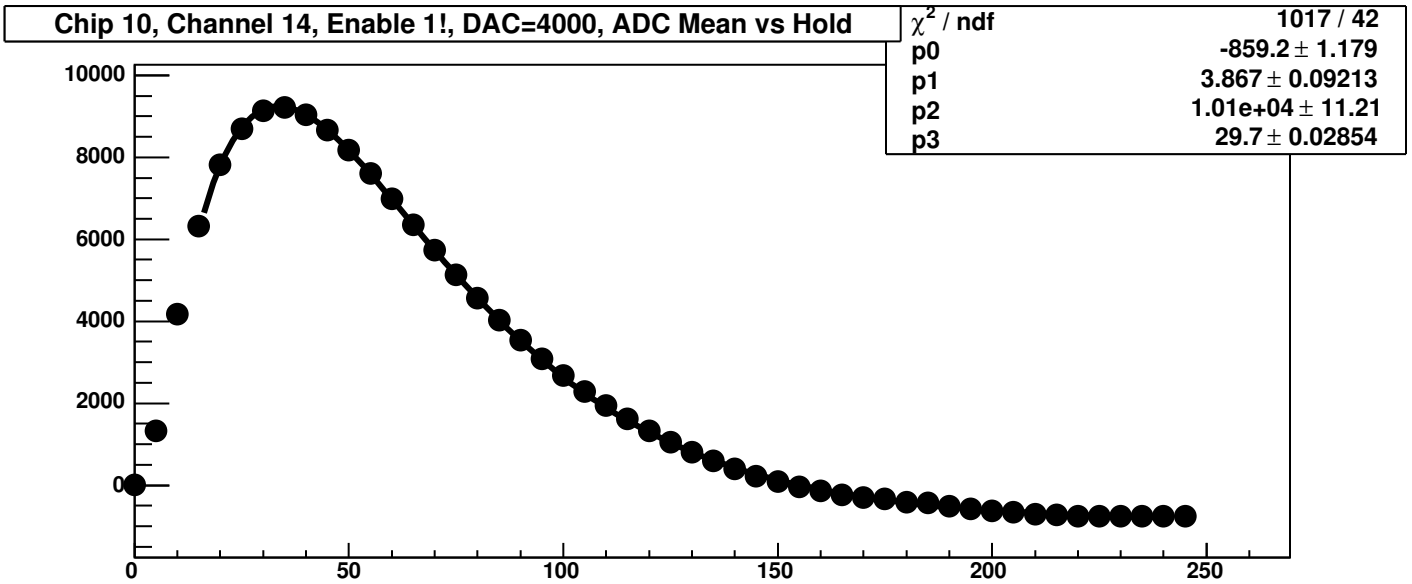


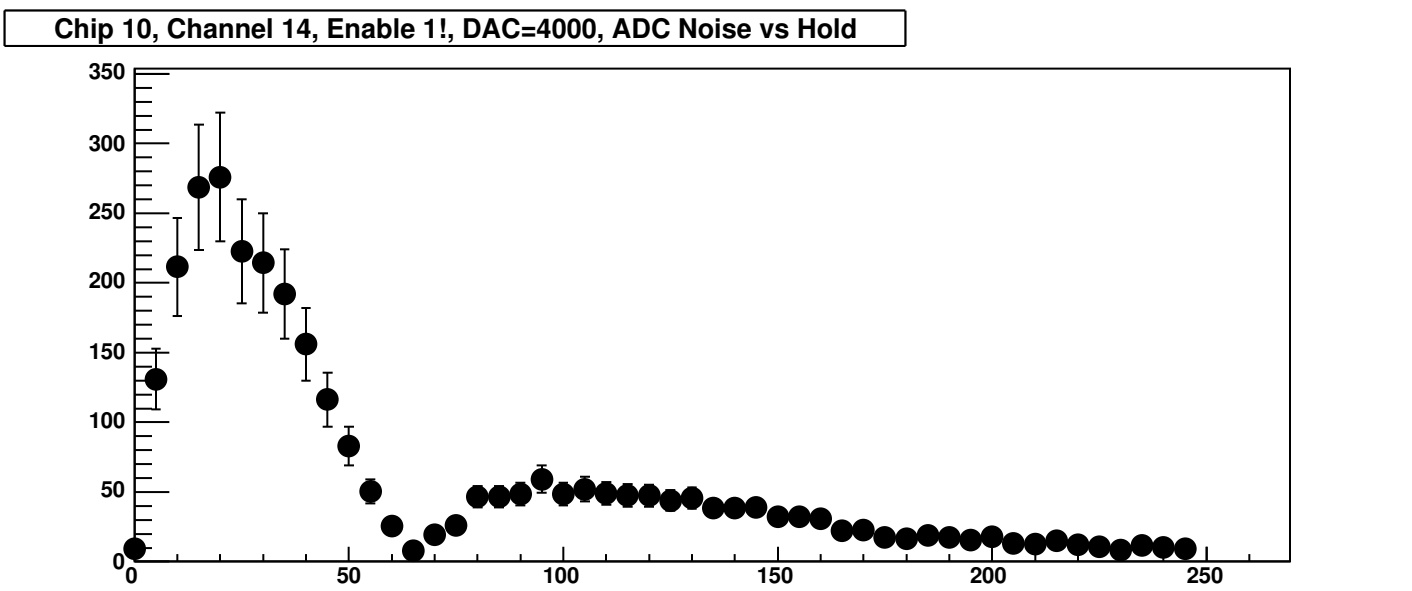
Chip 10, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold

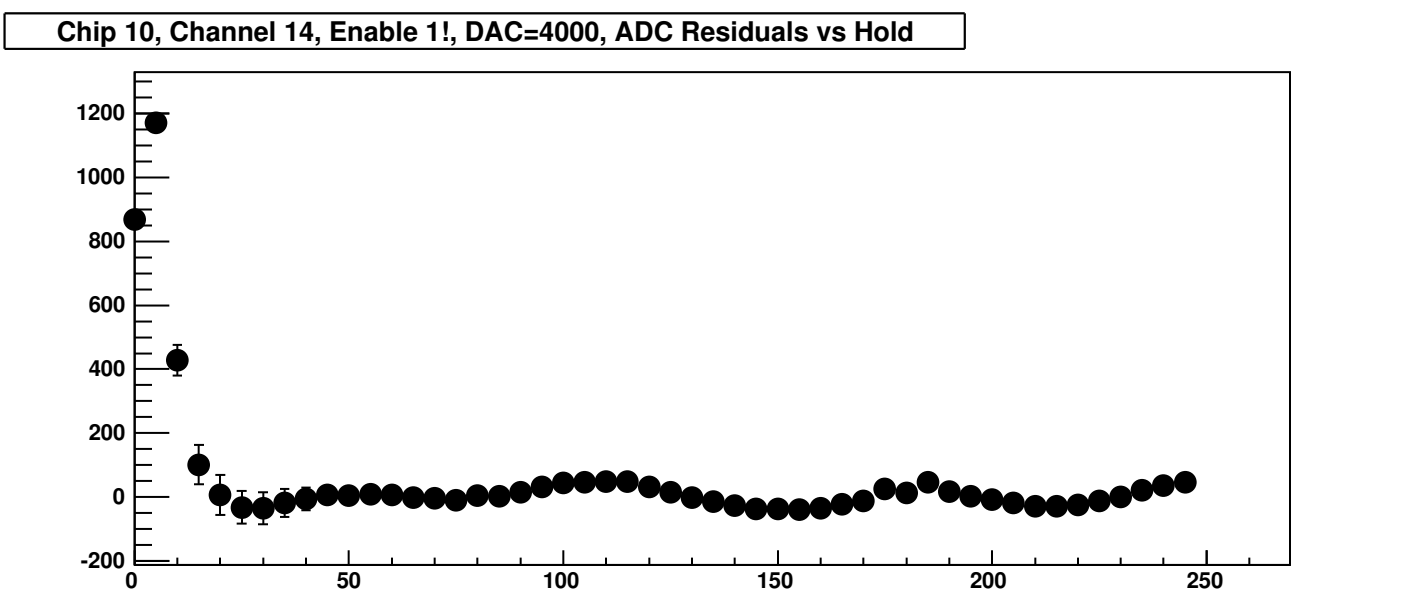


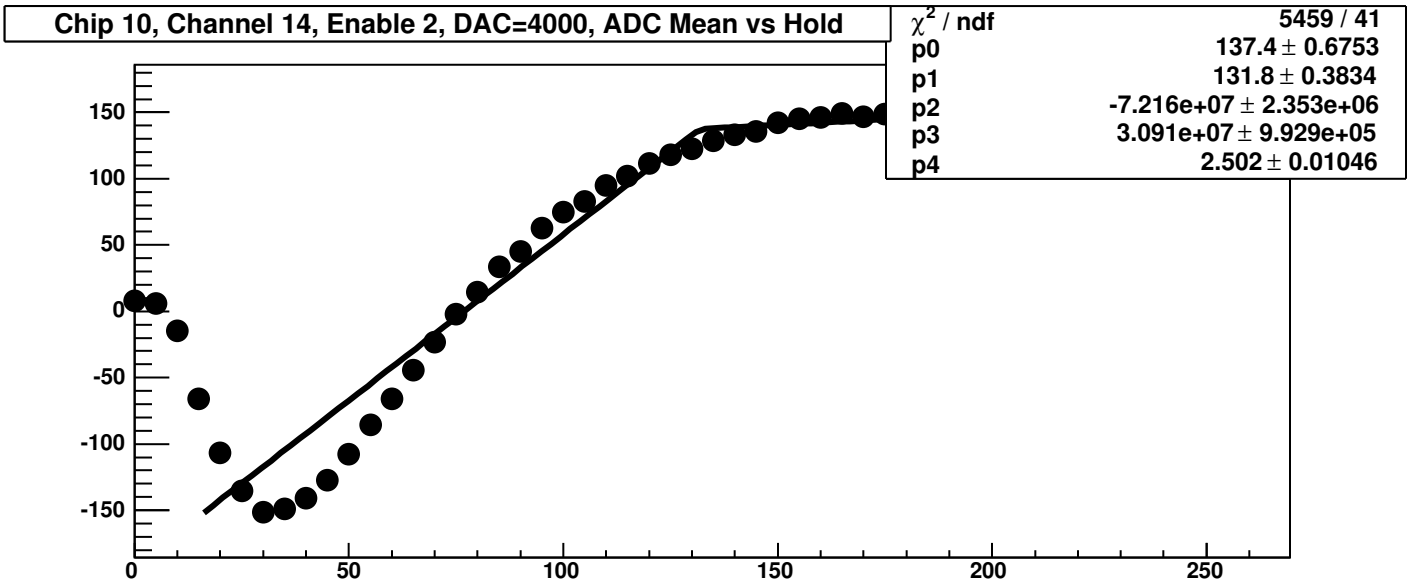
Chip 10, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

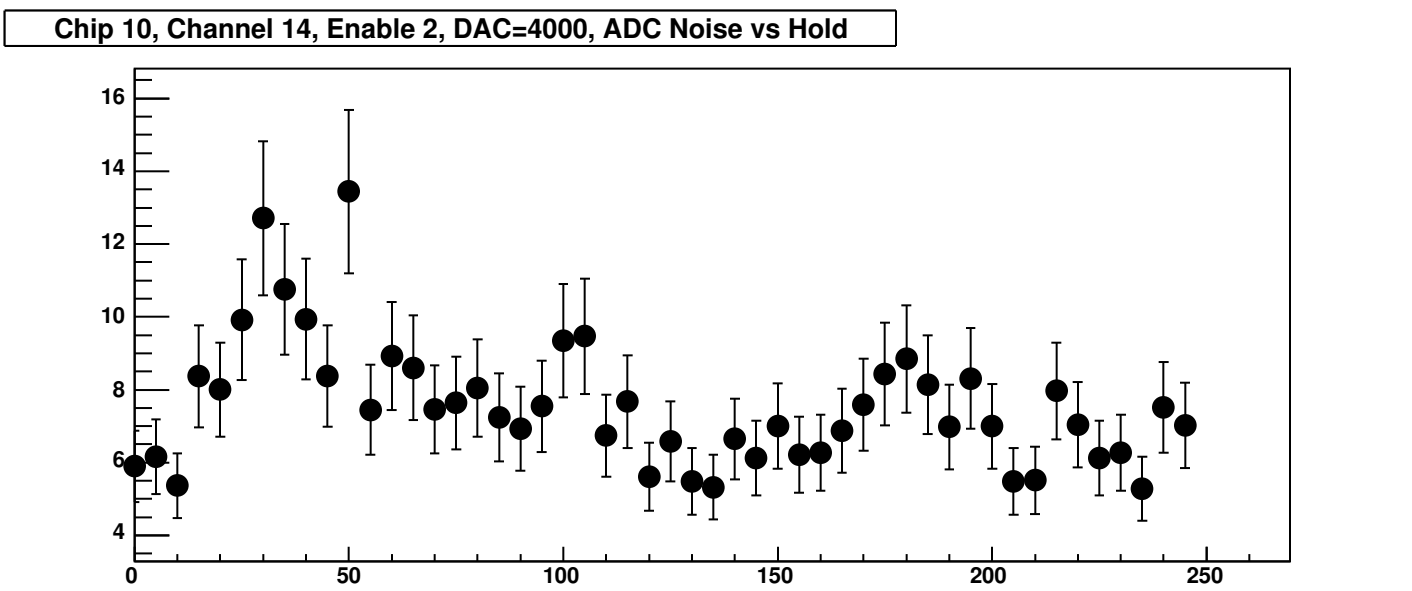


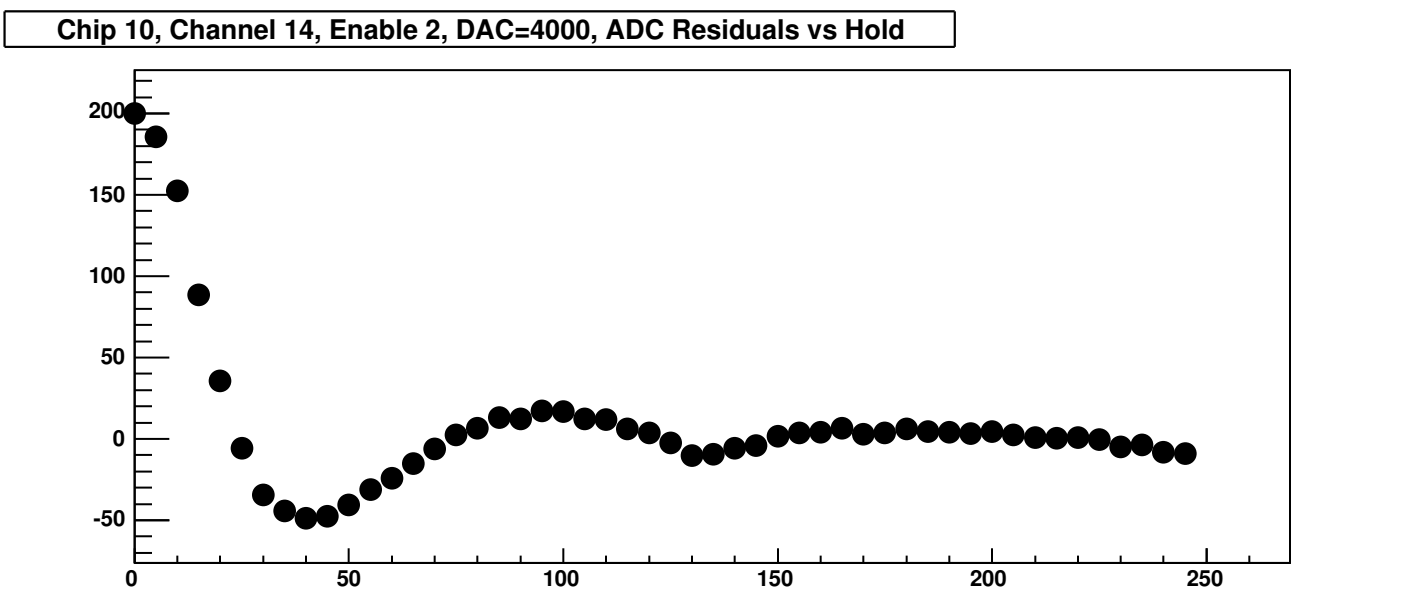

Chip 10, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1017 / 42
p0 -859.2 ± 1.179
p1 3.867 ± 0.09213
p2 1.01e+04 ± 11.21
p3 29.7 ± 0.02854
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

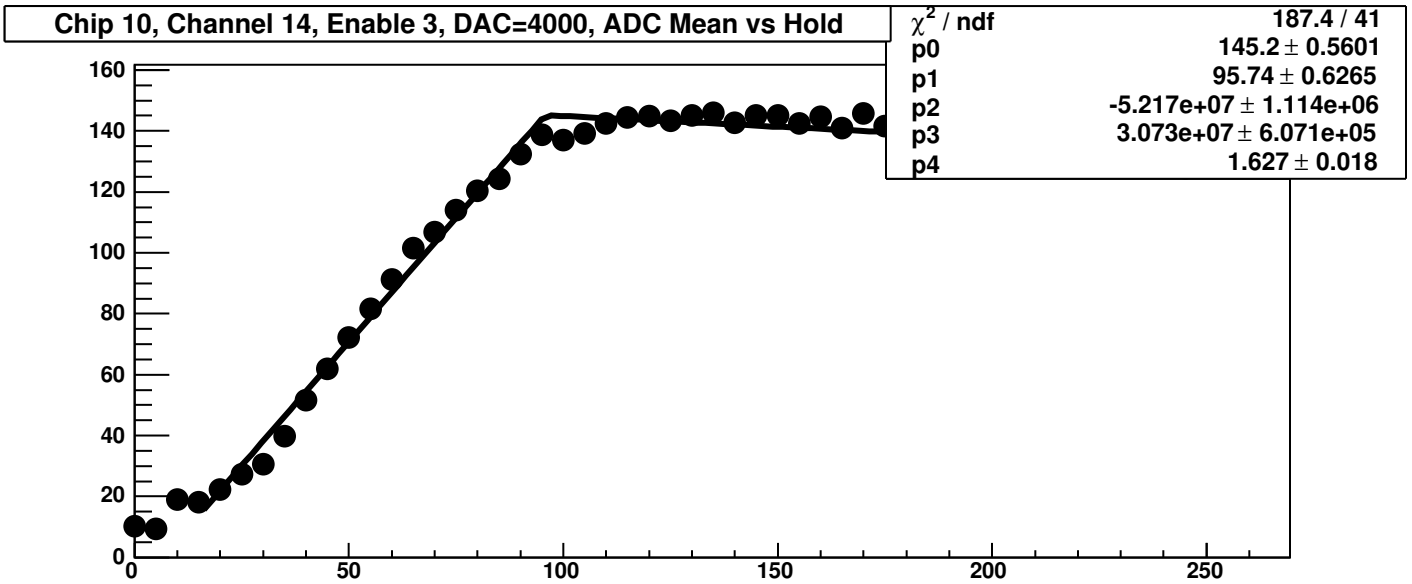

Chip 10, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold
350
300
250
200
150
100
50
0
0
50
100
150
200
250

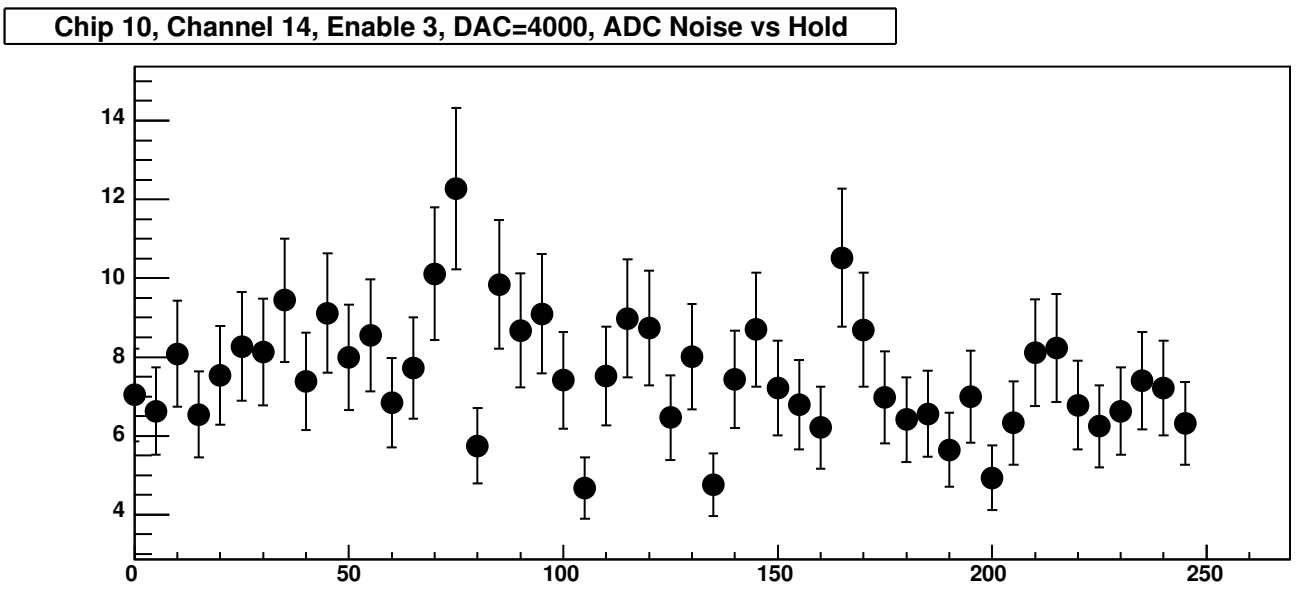

Chip 10, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

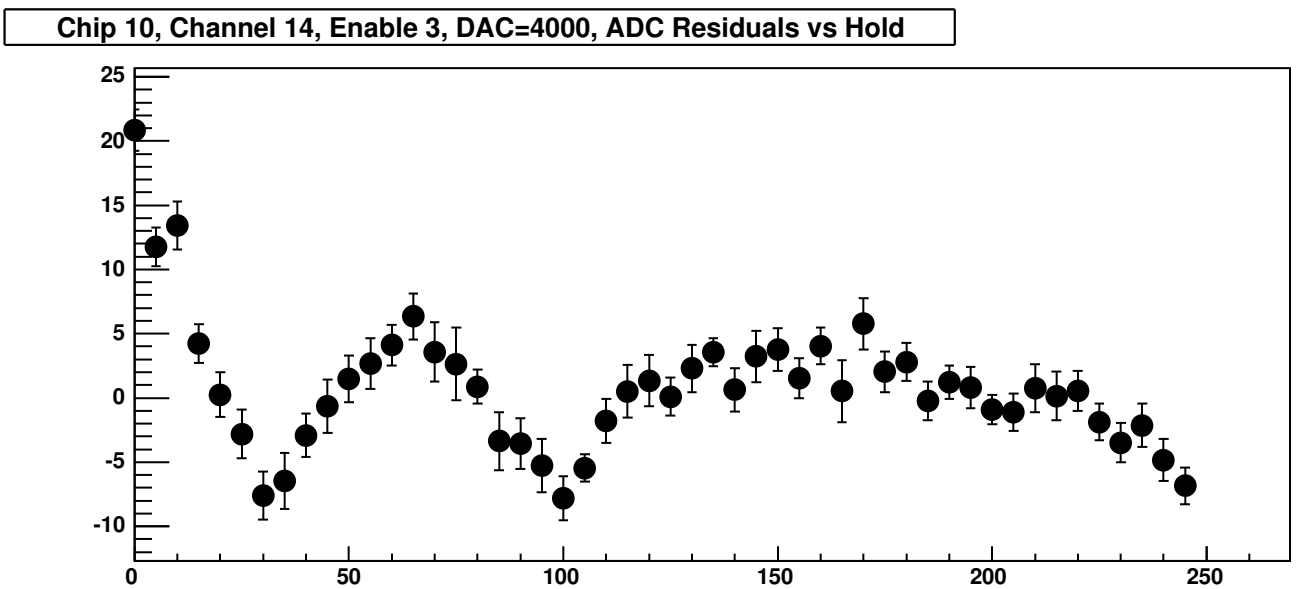

Chip 10, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 5459 / 41
p0 137.4 ± 0.6753
p1 131.8 ± 0.3834
p2 -7.216e+07 ± 2.353e+06
p3 3.091e+07 ± 9.929e+05
p4 2.502 ± 0.01046
150
100
50
0
-50
-100
-150
0
50
100
150
200
250

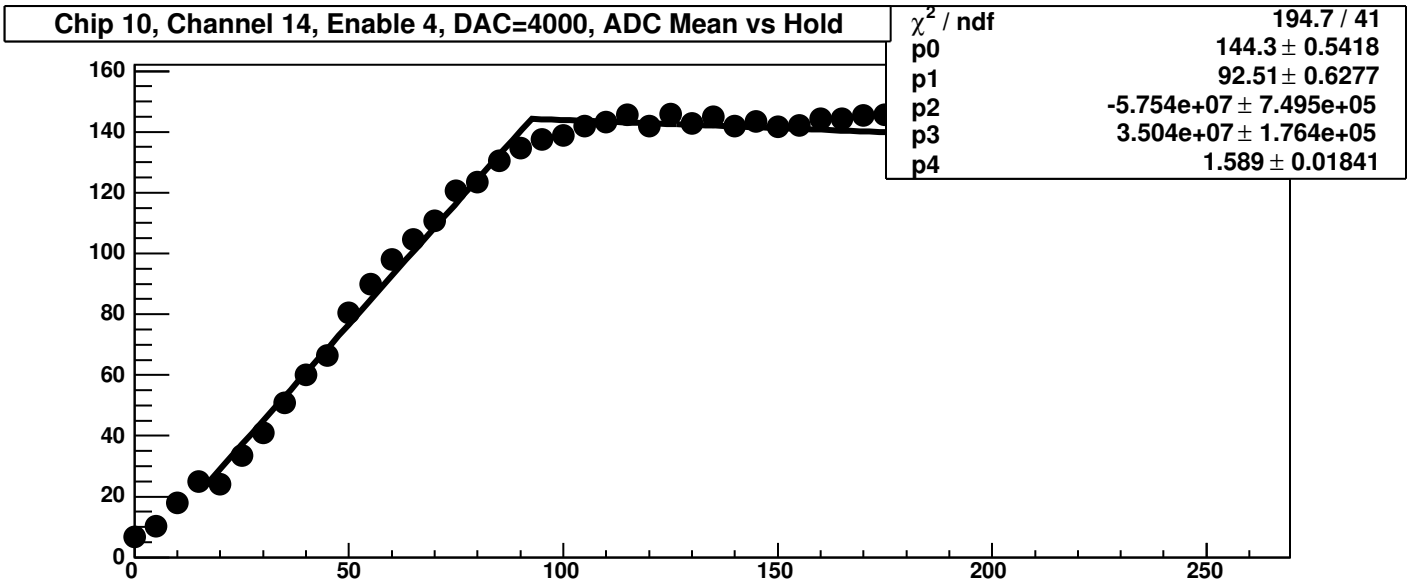

Chip 10, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold
16
14
12
10
8
6
4
0
50
100
150
200
250

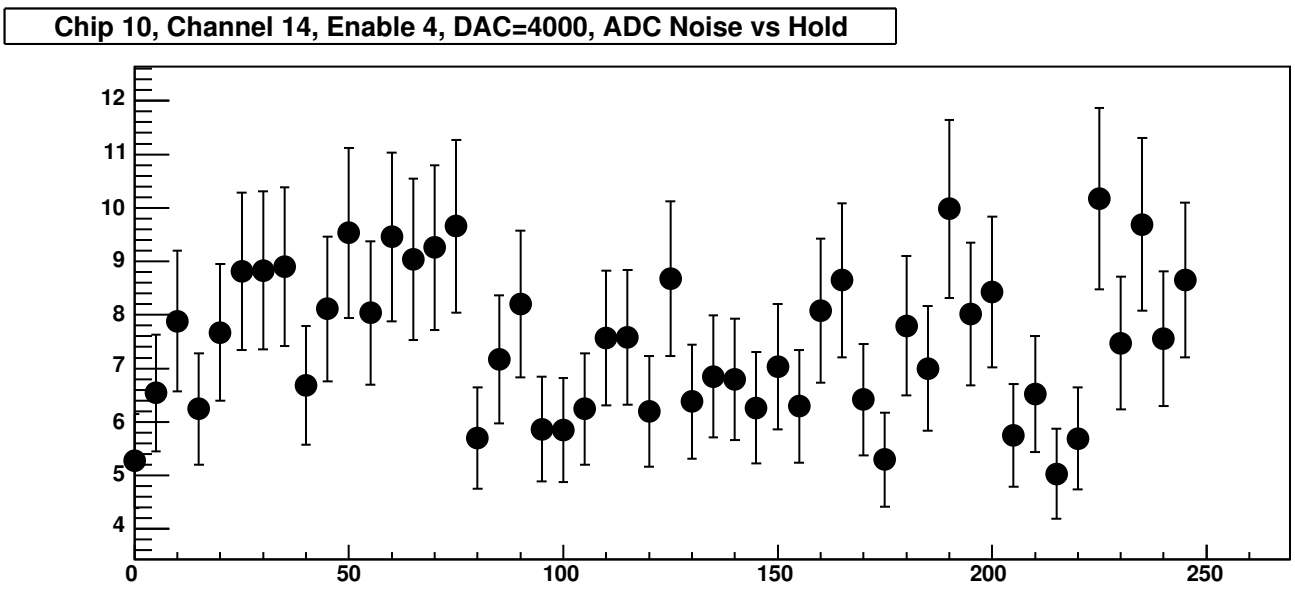

Chip 10, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold
200
150
100
50
0
-50
0
50
100
150
200
250

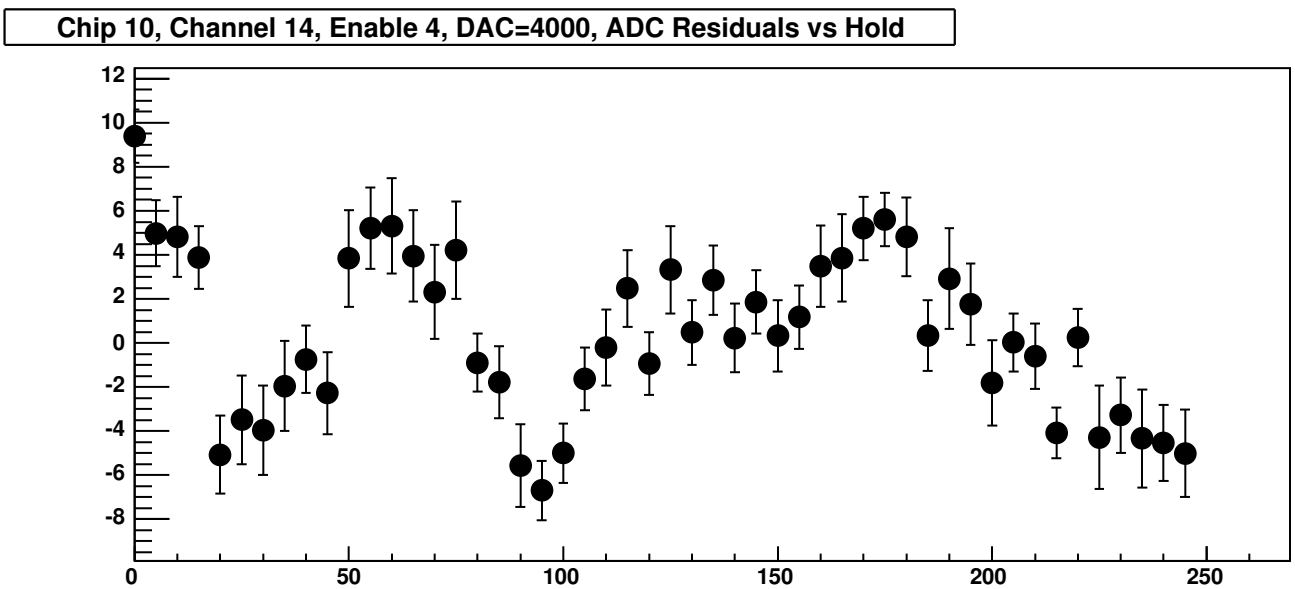

Chip 10, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 187.4 / 41
p0 145.2 ± 0.5601
p1 95.74 ± 0.6265
p2 -5.217e+07 ± 1.114e+06
p3 3.073e+07 ± 6.071e+05
p4 1.627 ± 0.018
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

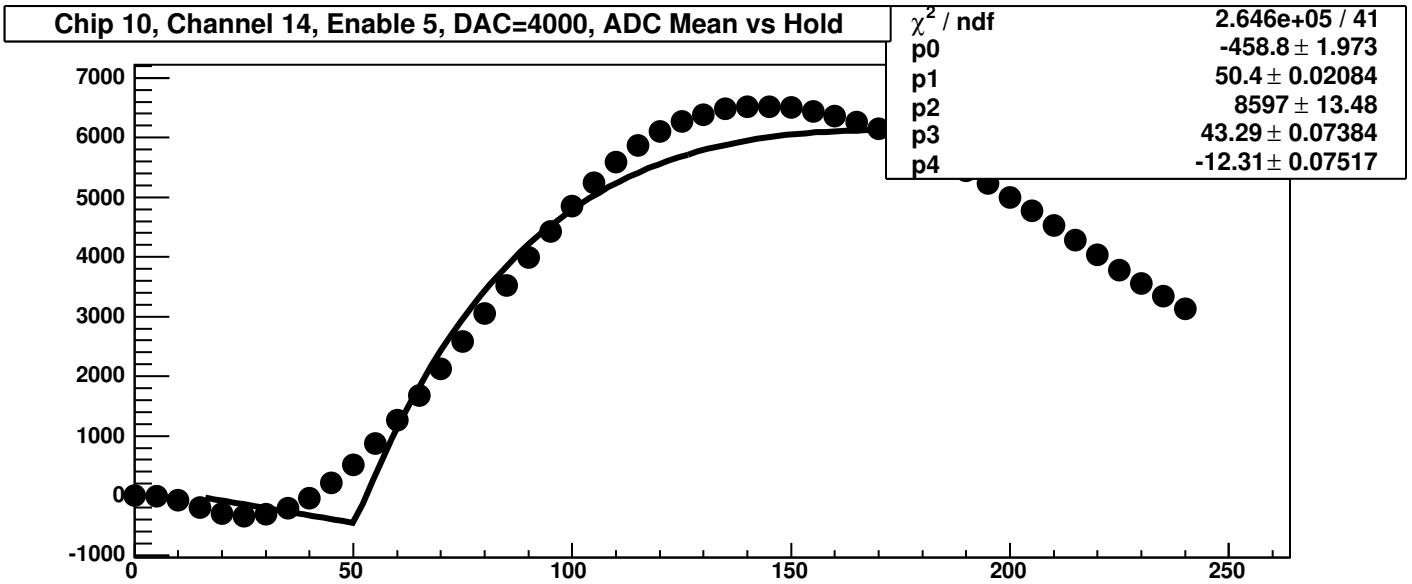

Chip 10, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

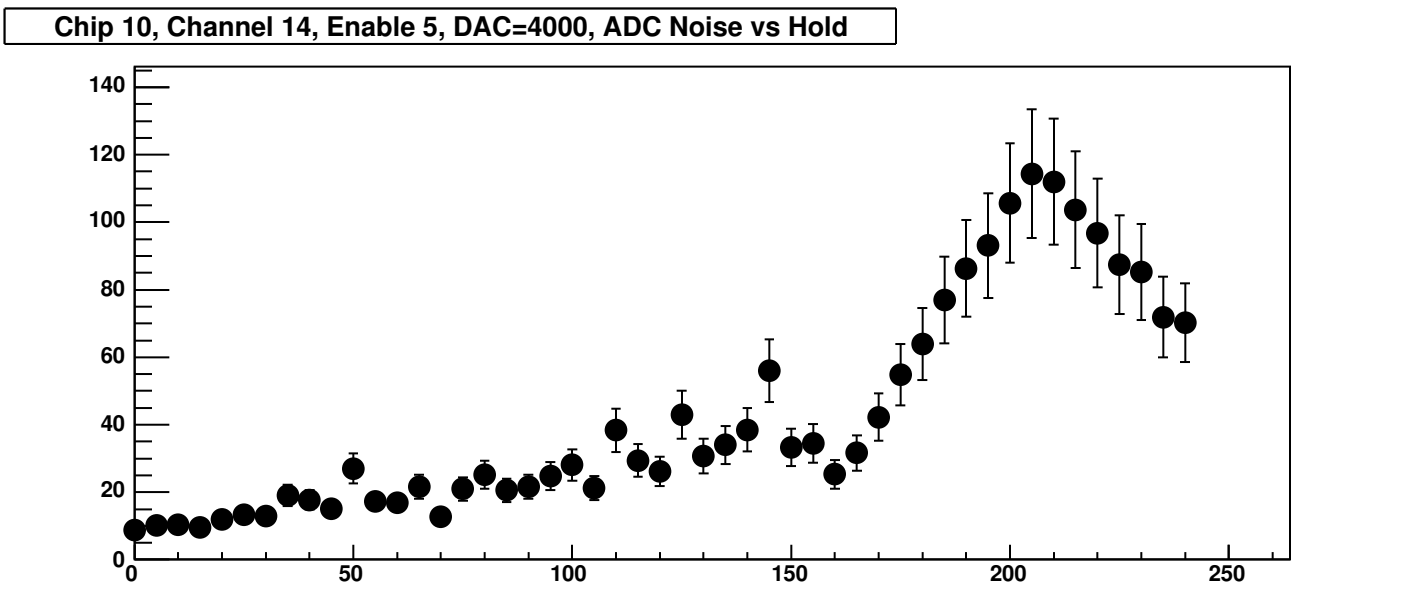

Chip 10, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
0
50
100
150
200
250

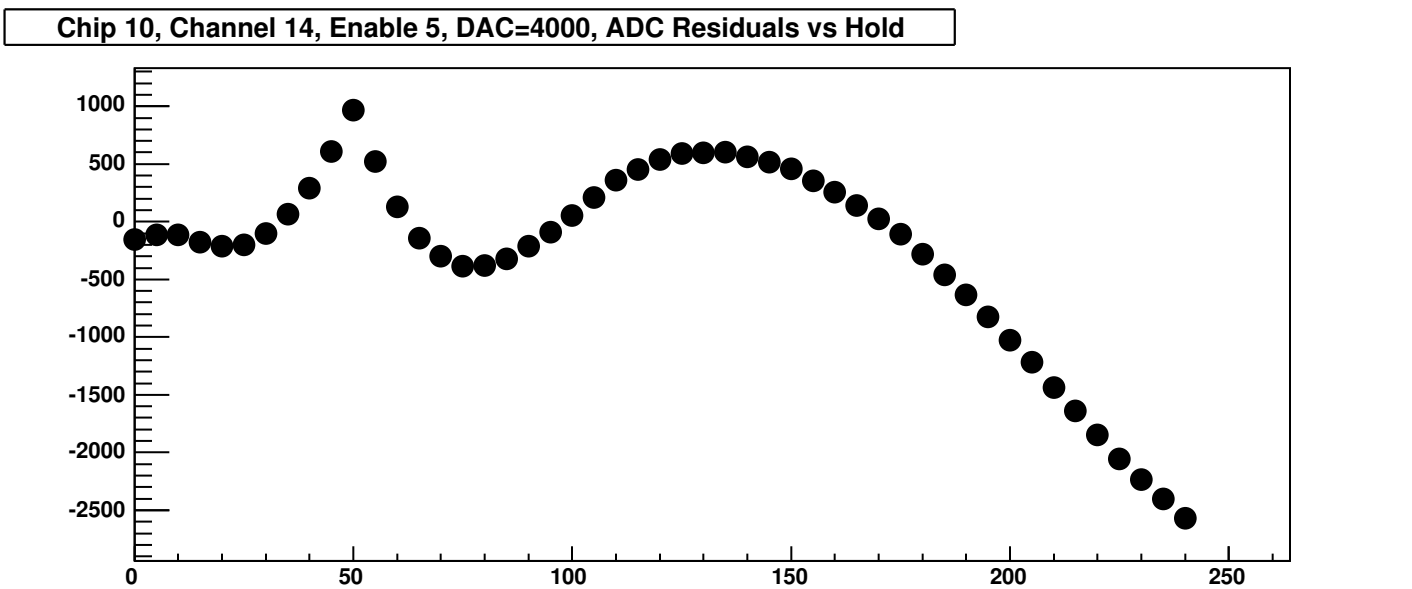

Chip 10, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 194.7 / 41
p0 144.3 ± 0.5418
p1 92.51 ± 0.6277
p2 -5.754e+07 ± 7.495e+05
p3 3.504e+07 ± 1.764e+05
p4 1.589 ± 0.01841

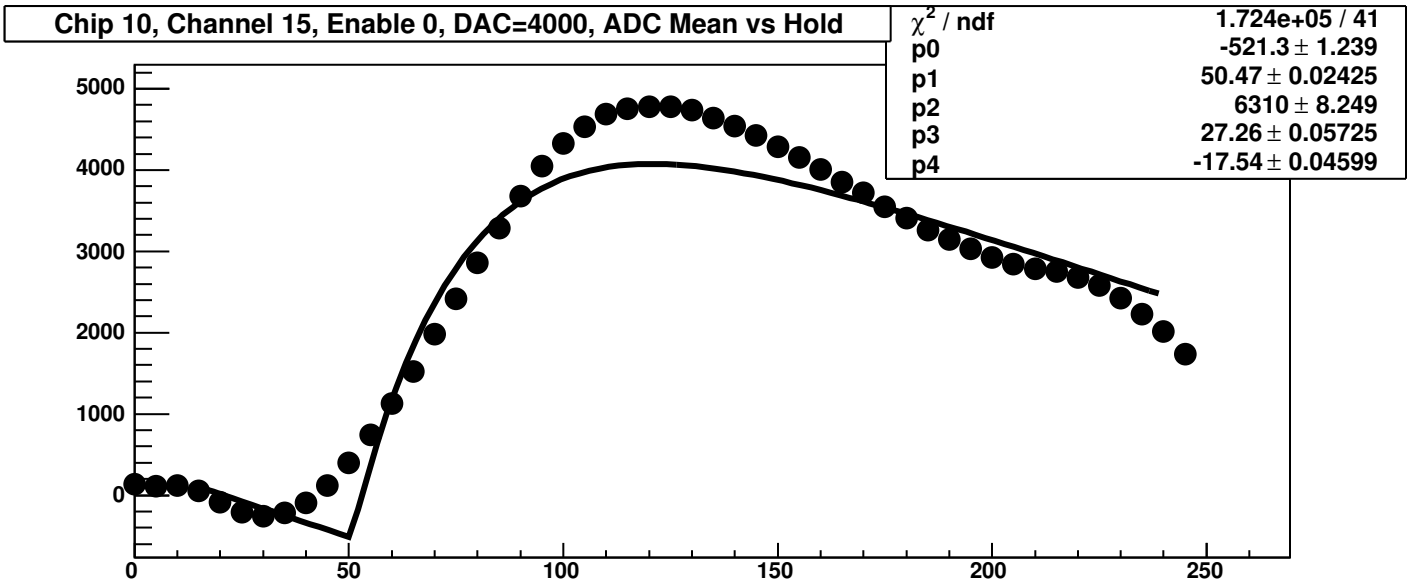

Chip 10, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold

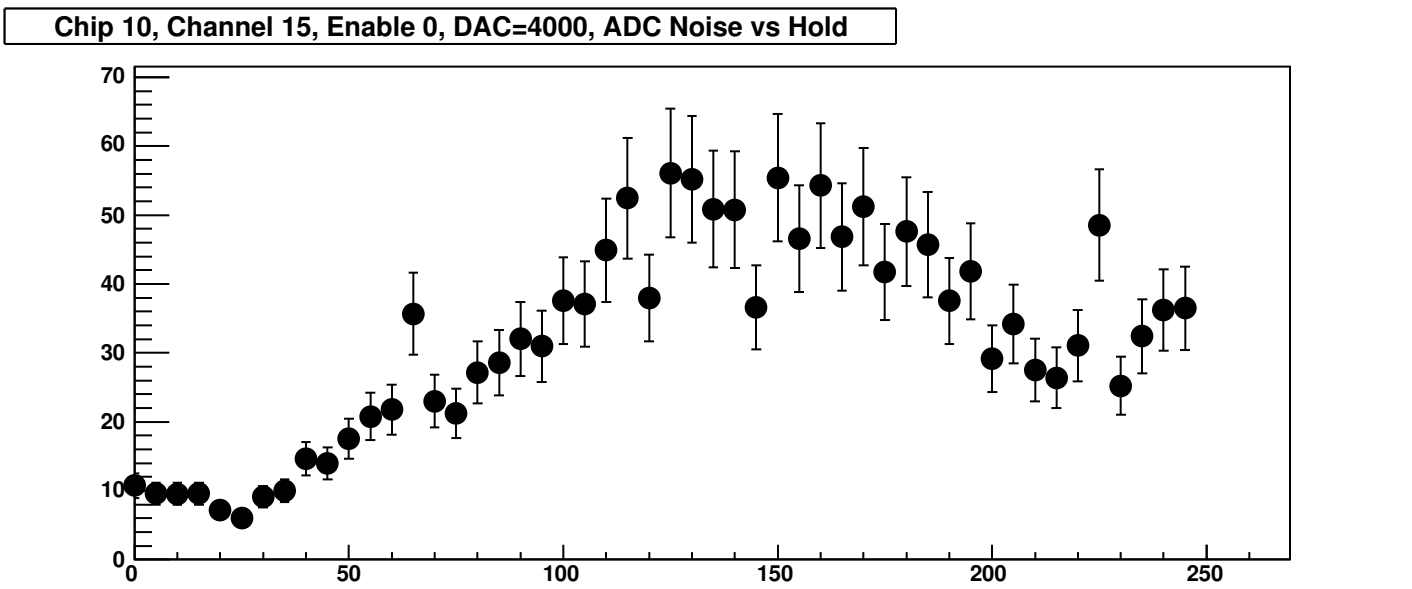

Chip 10, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold

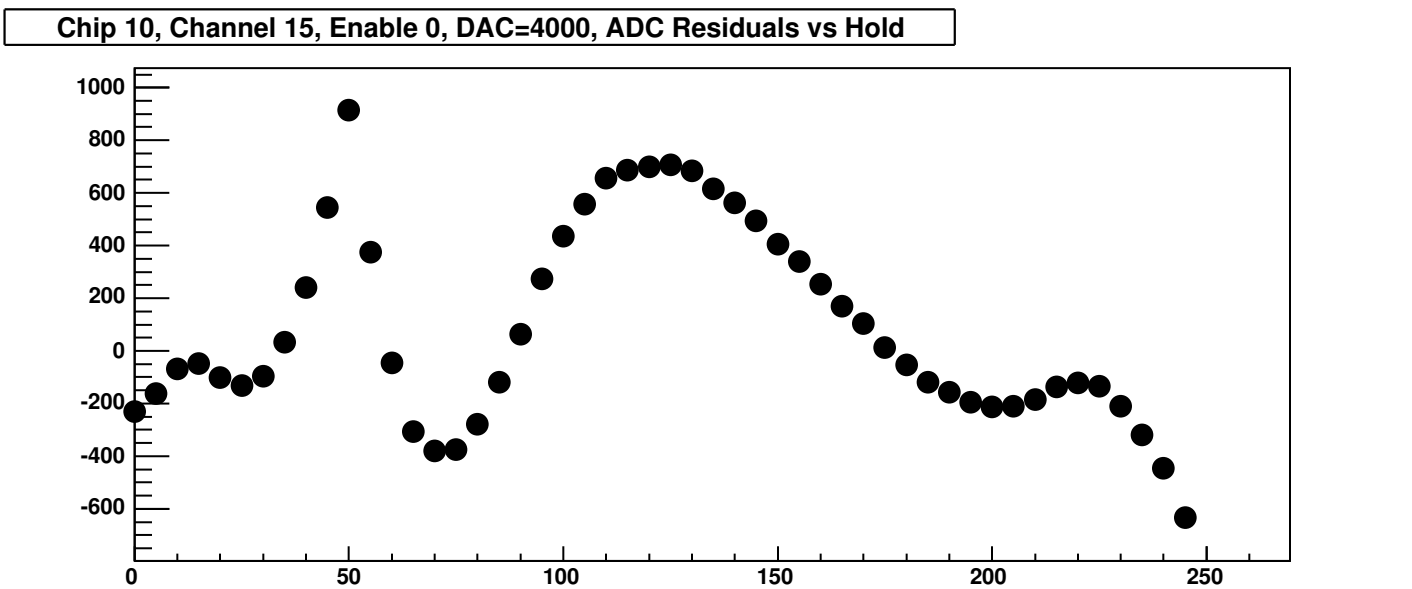

Chip 10, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.646e+05 / 41
p0 -458.8 ± 1.973
p1 50.4 ± 0.02084
p2 8597 ± 13.48
p3 43.29 ± 0.07384
p4 -12.31 ± 0.07517
7000
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

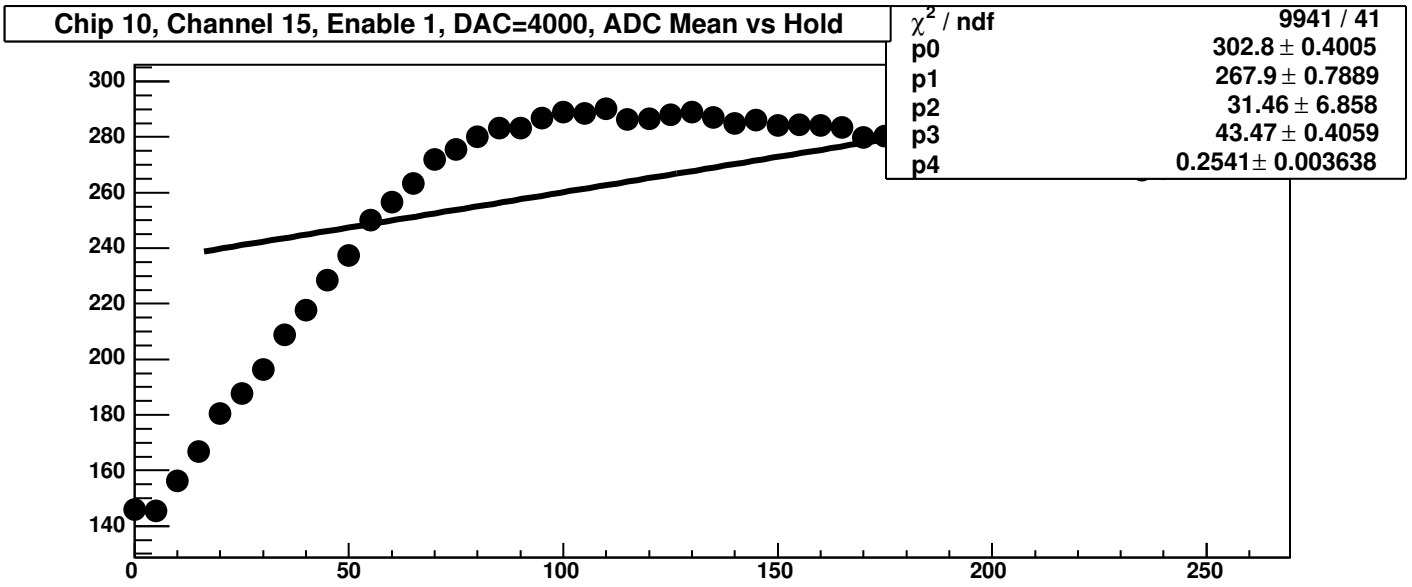

Chip 10, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250

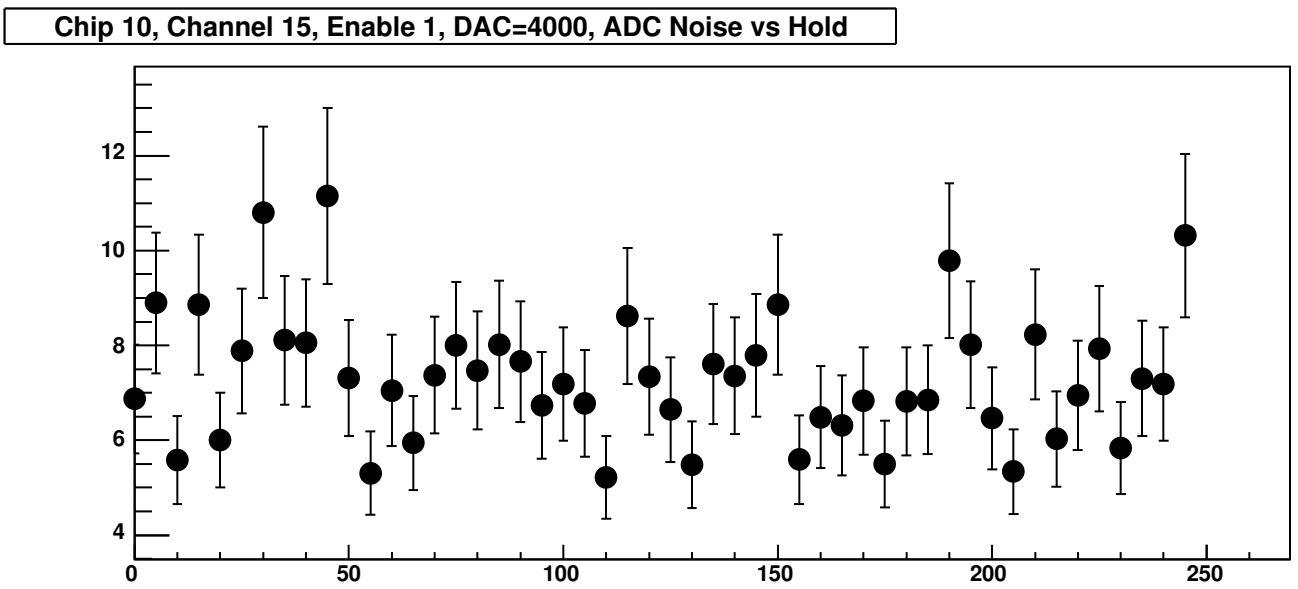

Chip 10, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
0
50
100
150
200
250

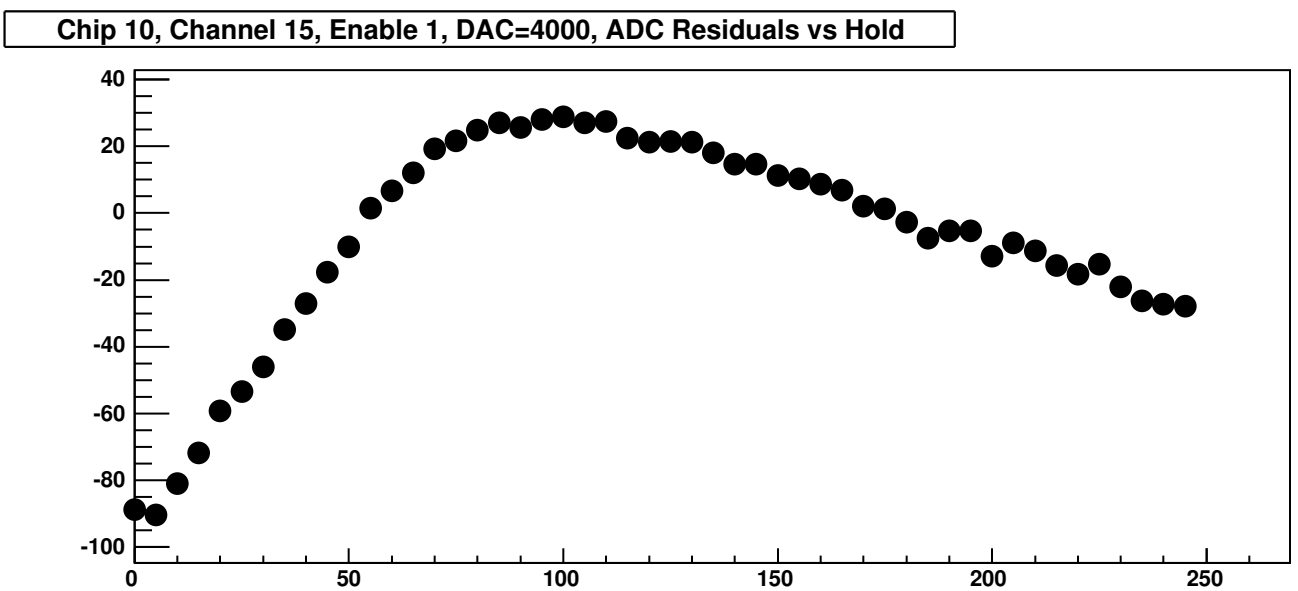

Chip 10, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.724e+05 / 41
p0 -521.3 ± 1.239
p1 50.47 ± 0.02425
p2 6310 ± 8.249
p3 27.26 ± 0.05725
p4 -17.54 ± 0.04599
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

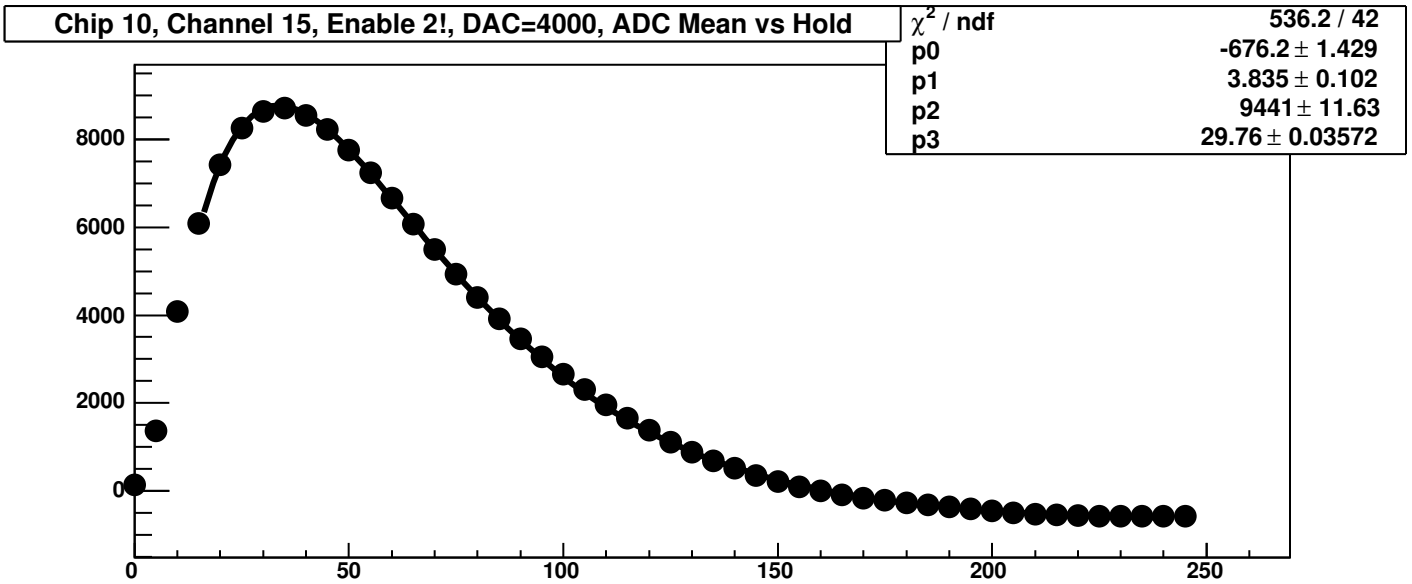

Chip 10, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold
70
60
50
40
30
20
10
0
0
50
100
150
200
250

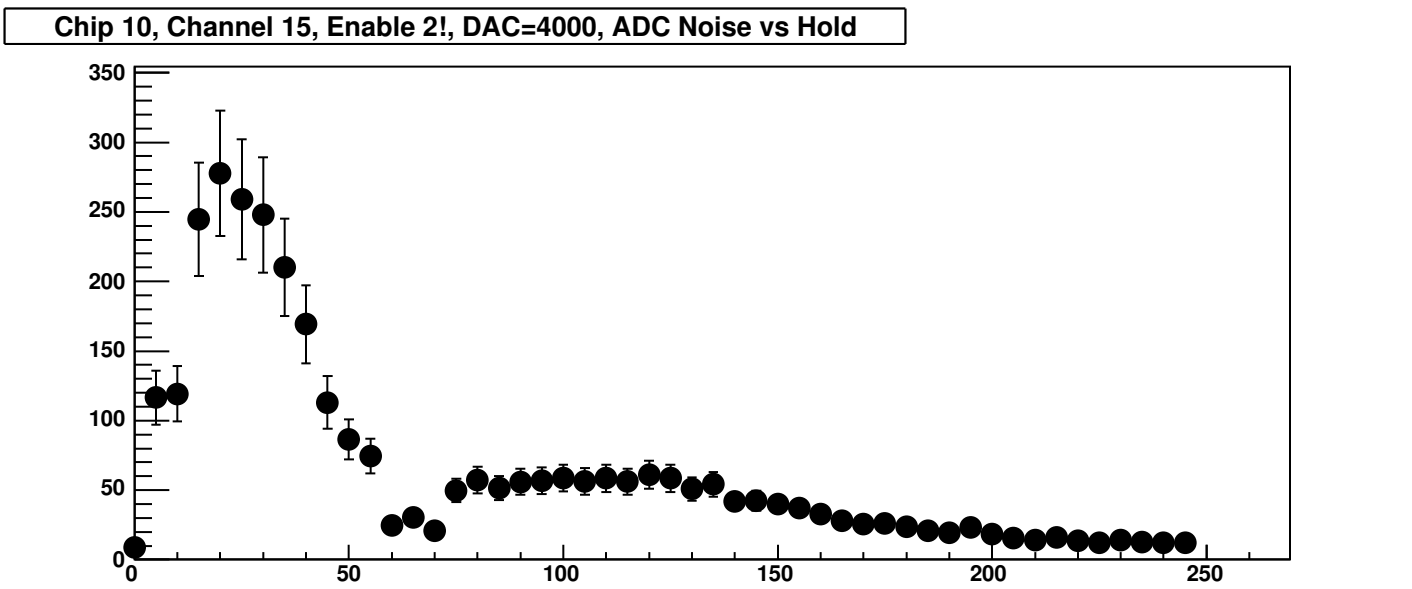

Chip 10, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold
1000
800
600
400
200
0
-200
-400
-600
0
50
100
150
200
250

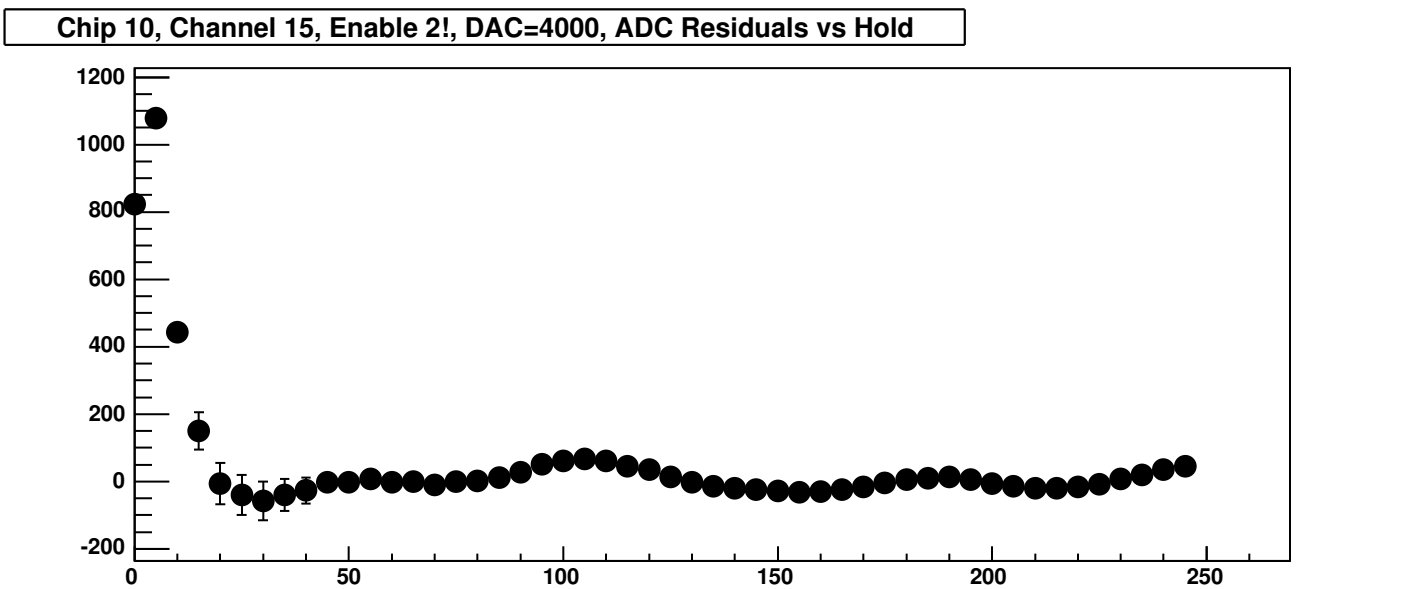

Chip 10, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 9941 / 41
p0 302.8 ± 0.4005
p1 267.9 ± 0.7889
p2 31.46 ± 6.858
p3 43.47 ± 0.4059
p4 0.2541 ± 0.003638
300
280
260
240
220
200
180
160
140
0
50
100
150
200
250

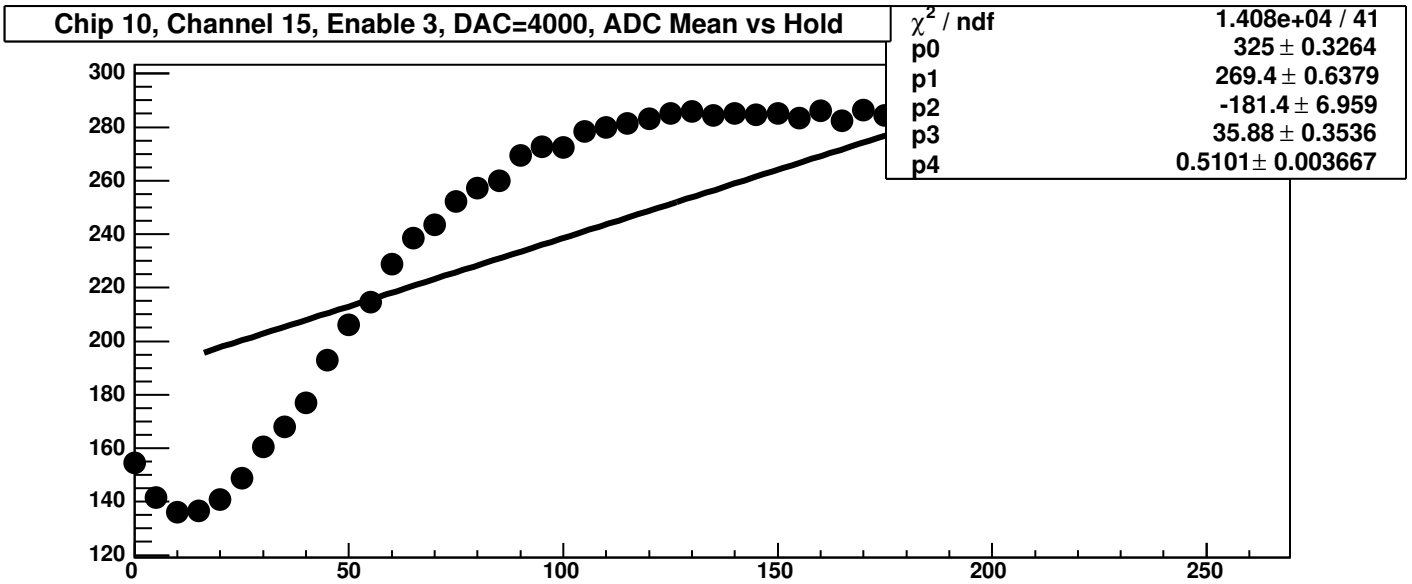

Chip 10, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

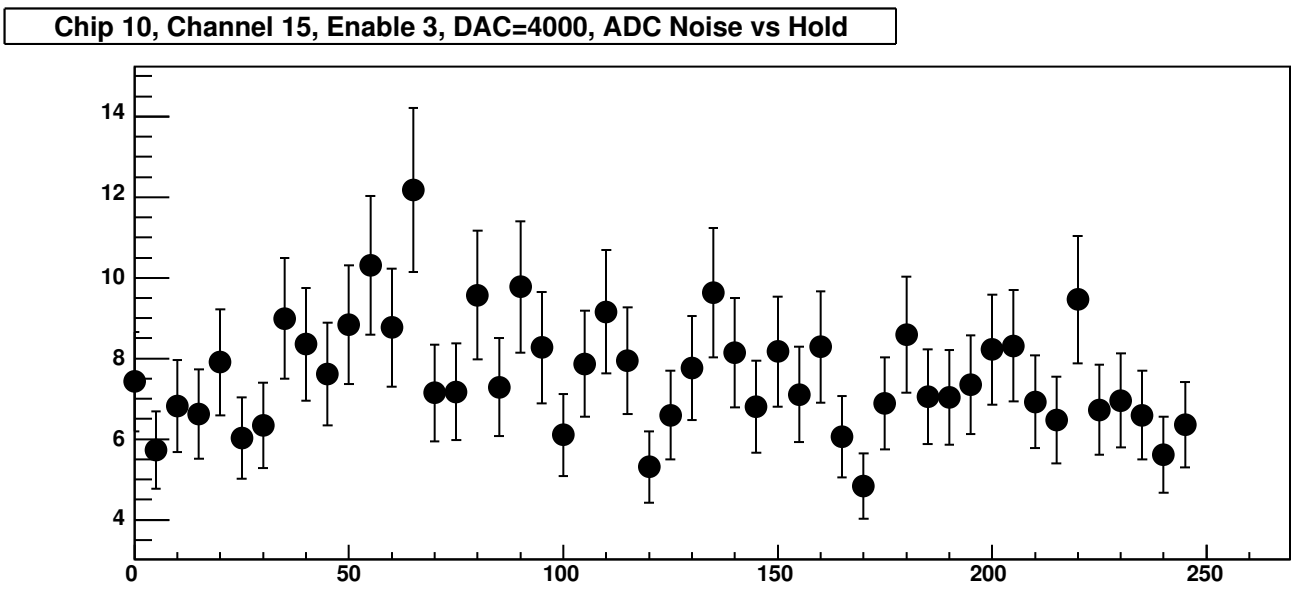

Chip 10, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
-100
0
50
100
150
200
250

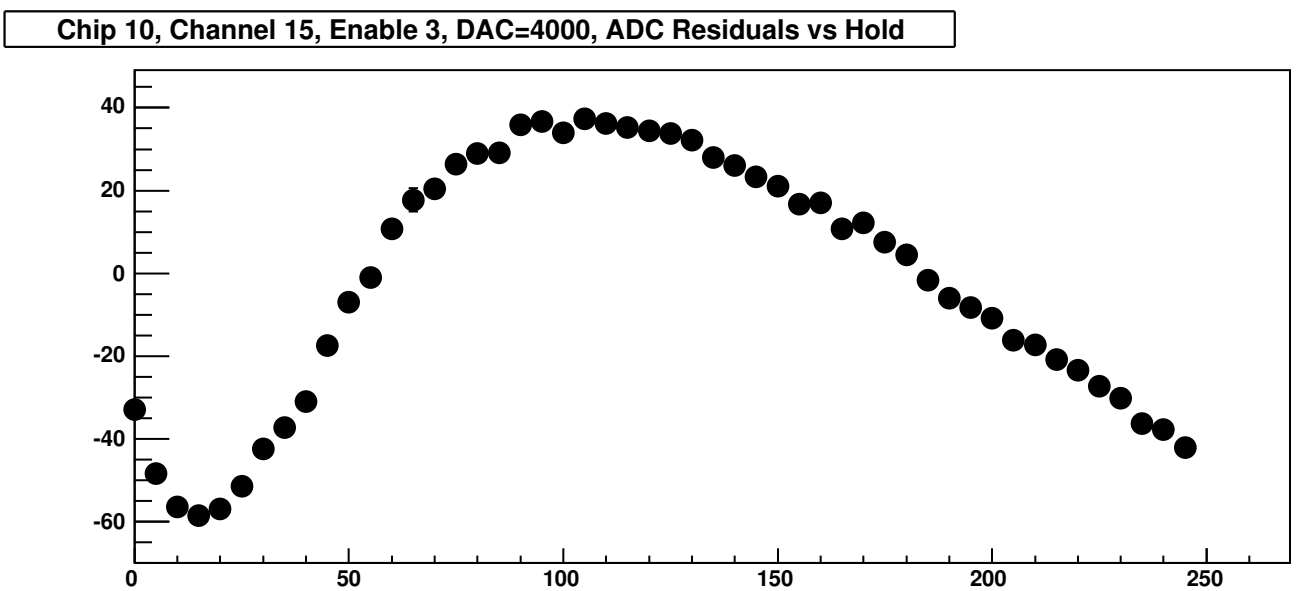

Chip 10, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 536.2 / 42
p0 -676.2 ± 1.429
p1 3.835 ± 0.102
p2 9441 ± 11.63
p3 29.76 ± 0.03572
8000
6000
4000
2000
0
0
50
100
150
200
250

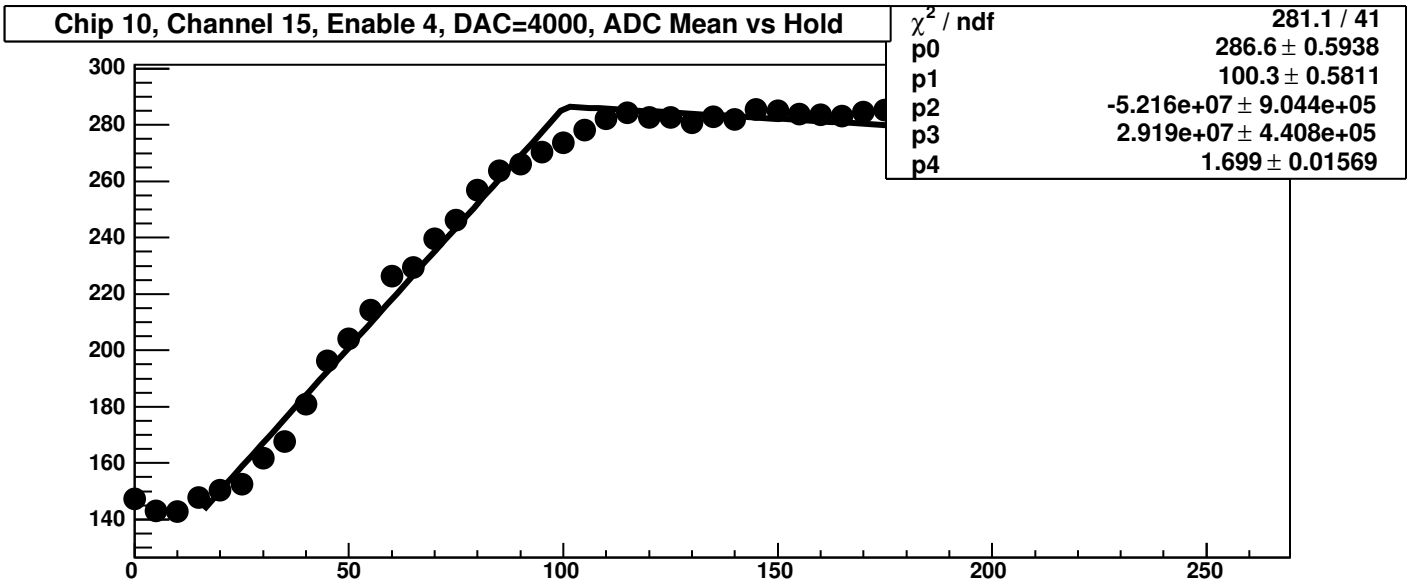

Chip 10, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold
350
300
250
200
150
100
50
0
0
50
100
150
200
250

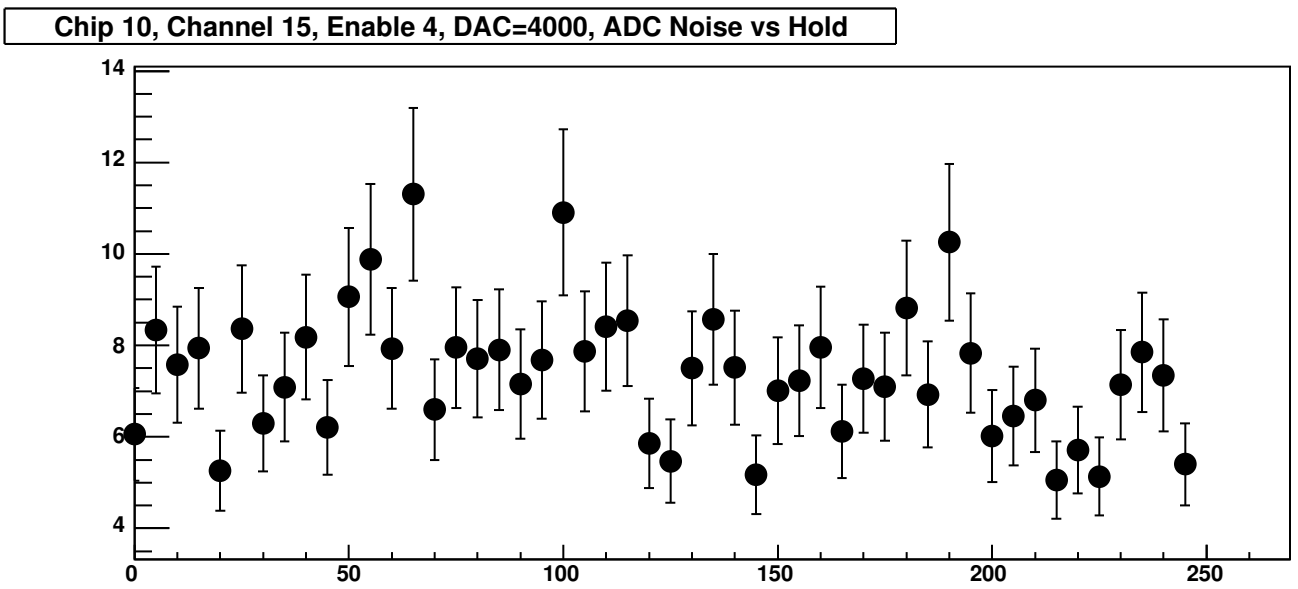

Chip 10, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

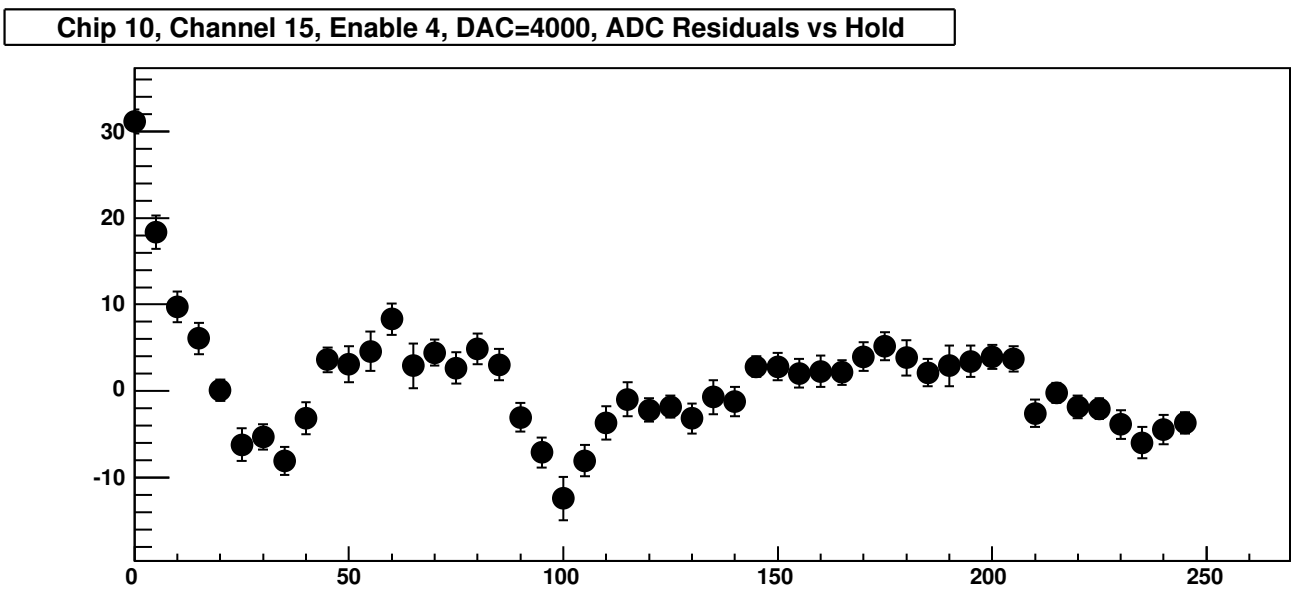

Chip 10, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.408e+04 / 41
p0 325 ± 0.3264
p1 269.4 ± 0.6379
p2 -181.4 ± 6.959
p3 35.88 ± 0.3536
p4 0.5101 ± 0.003667
300
280
260
240
220
200
180
160
140
120
0
50
100
150
200
250

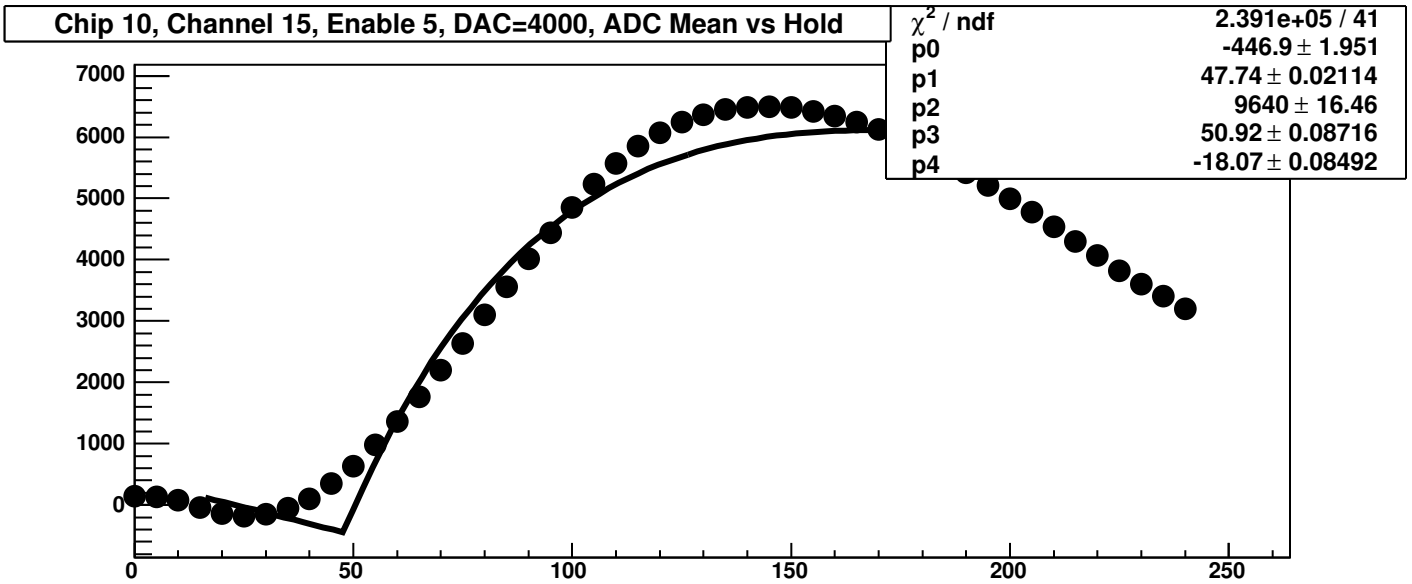

Chip 10, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

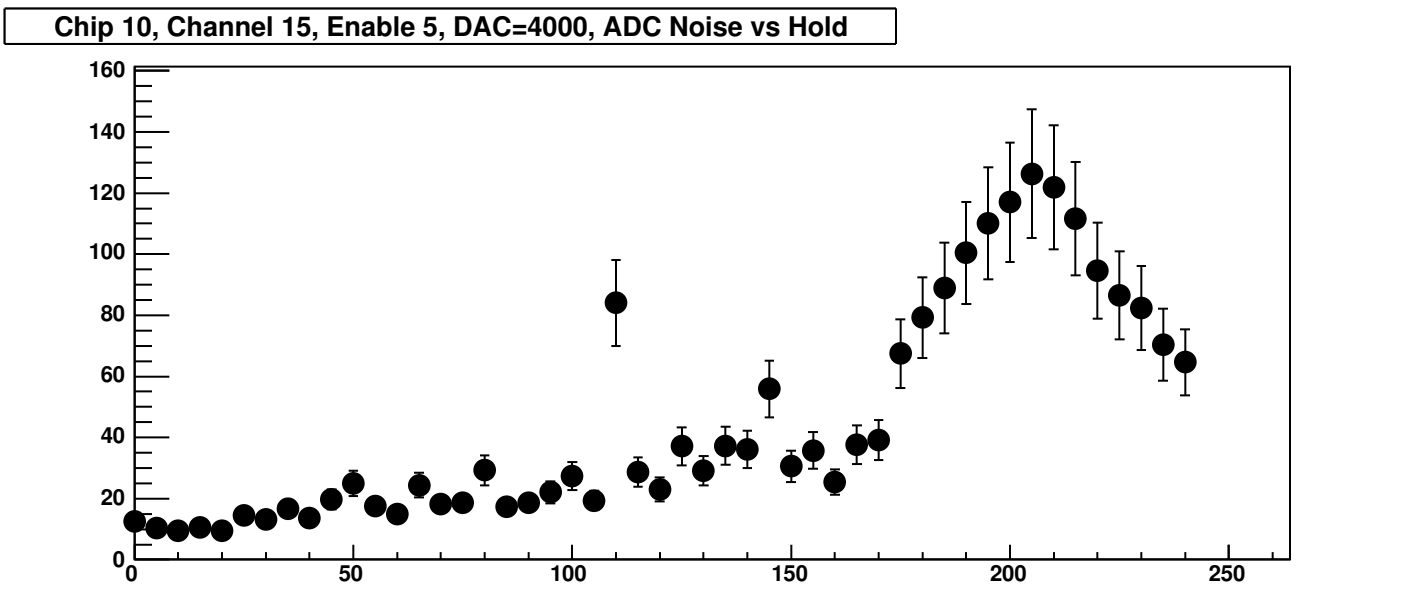

Chip 10, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
0
50
100
150
200
250

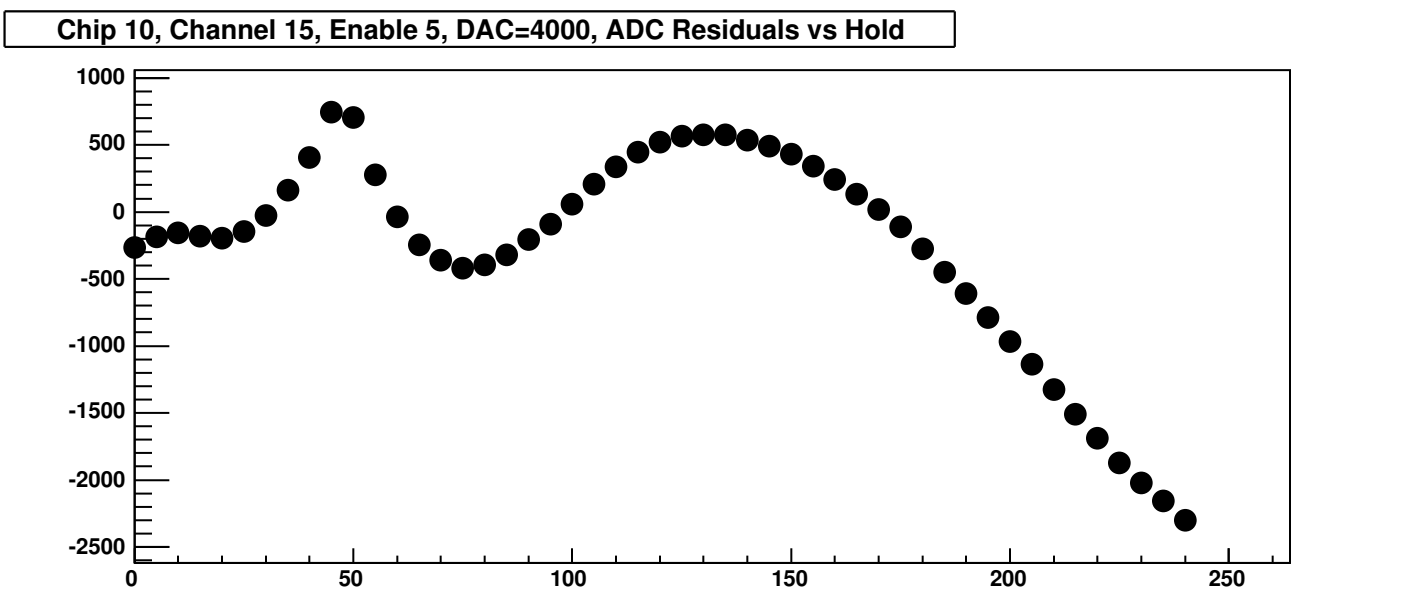

Chip 10, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 281.1 / 41
p0 286.6 ± 0.5938
p1 100.3 ± 0.5811
p2 -5.216e+07 ± 9.044e+05
p3 2.919e+07 ± 4.408e+05
p4 1.699 ± 0.01569
300
280
260
240
220
200
180
160
140
0
50
100
150
200
250


Chip 10, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

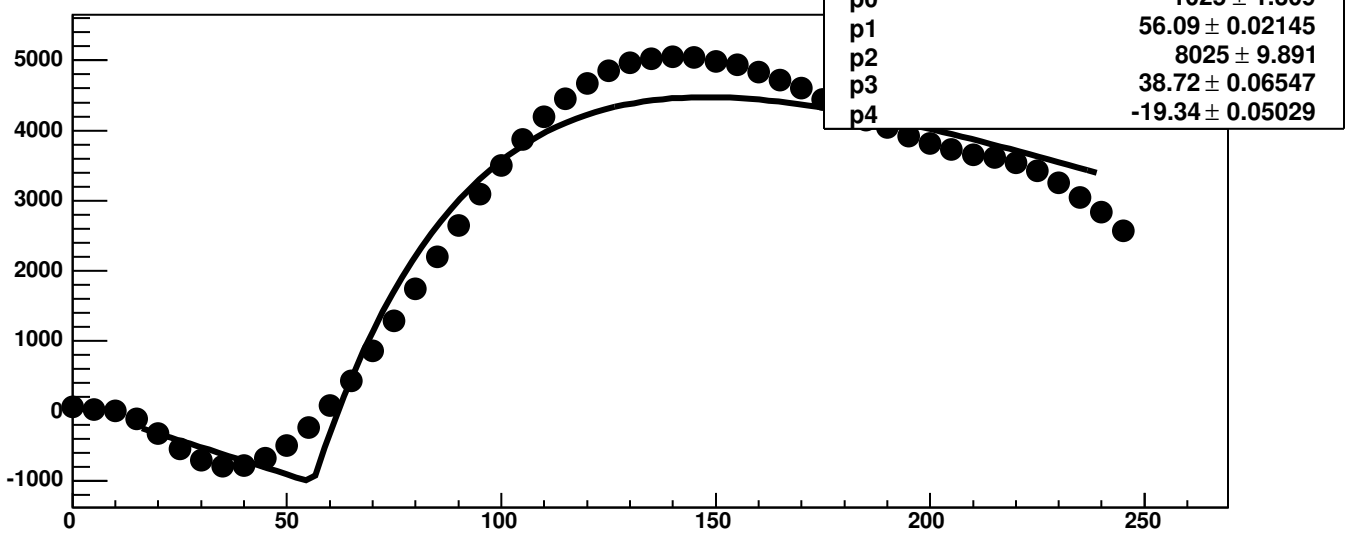

Chip 10, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
0
50
100
150
200
250


Chip 10, Channel 15, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.391e+05 / 41
p0 -446.9 ± 1.951
p1 47.74 ± 0.02114
p2 9640 ± 16.46
p3 50.92 ± 0.08716
p4 -18.07 ± 0.08492
7000
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

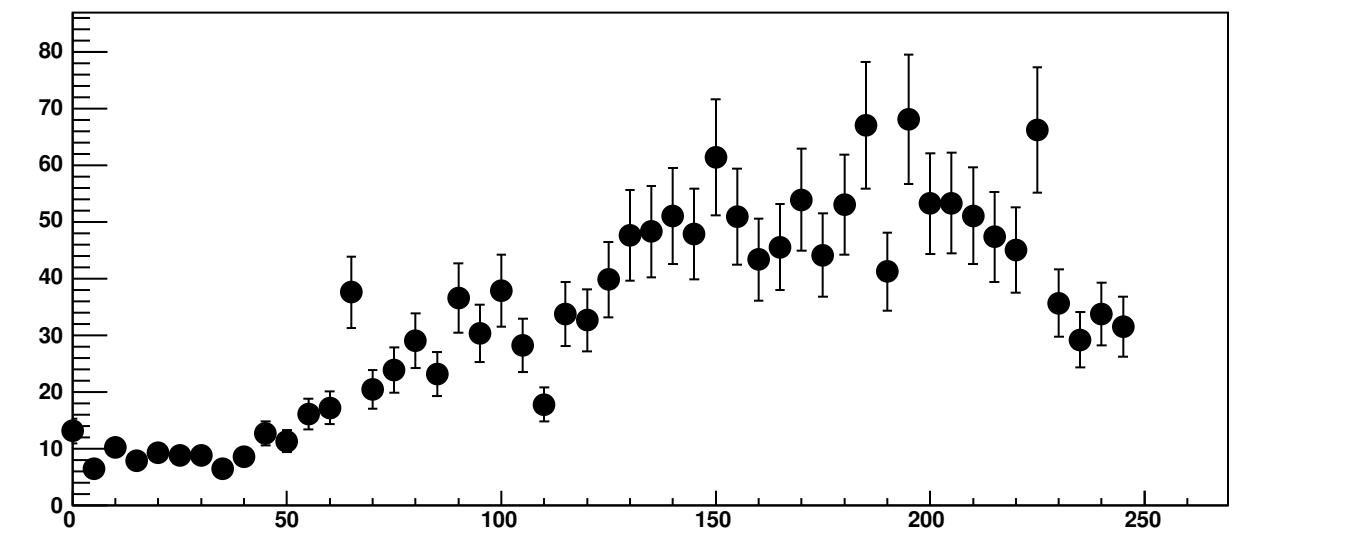

Chip 10, Channel 15, Enable 5, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 10, Channel 15, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
0
50
100
150
200
250

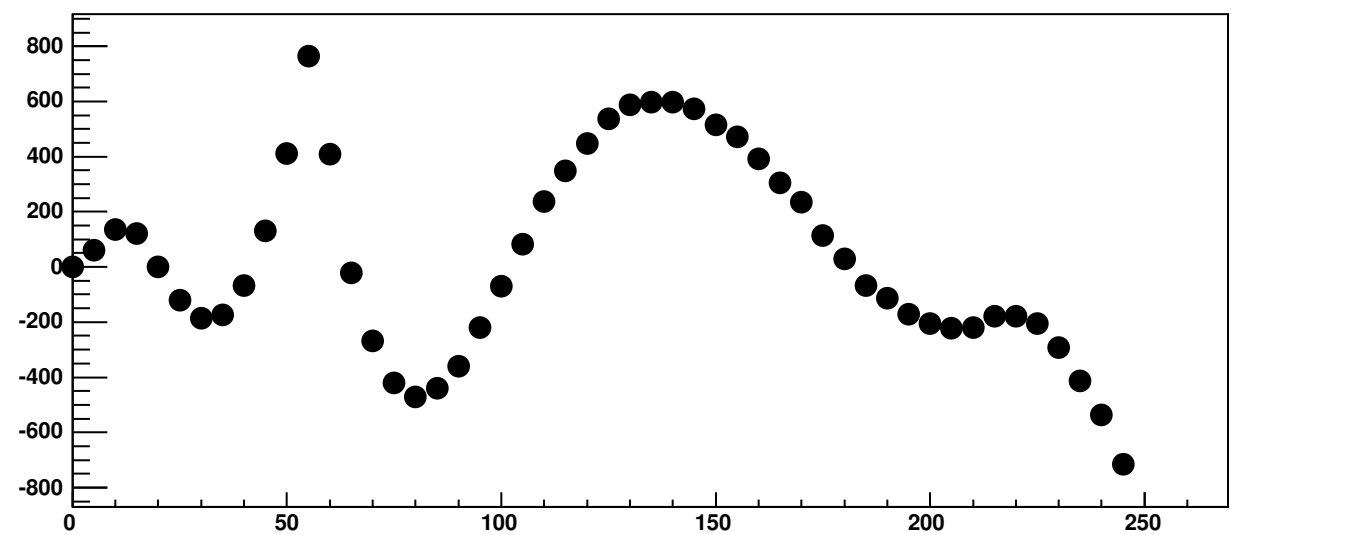
Chip 10, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

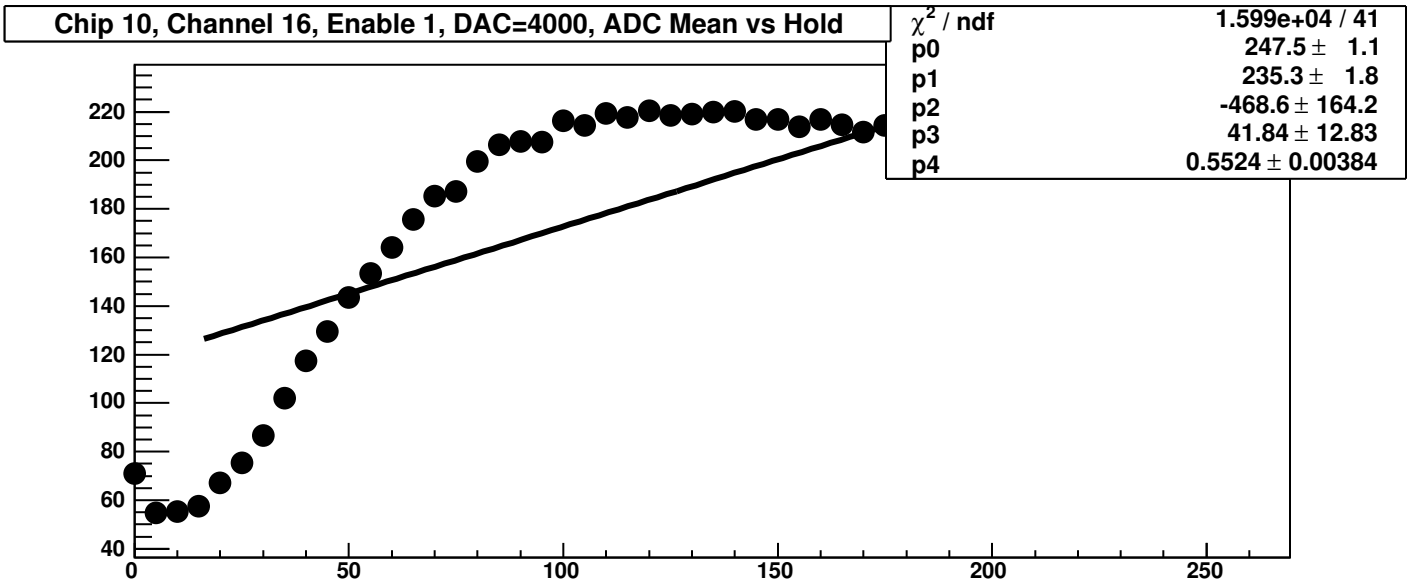


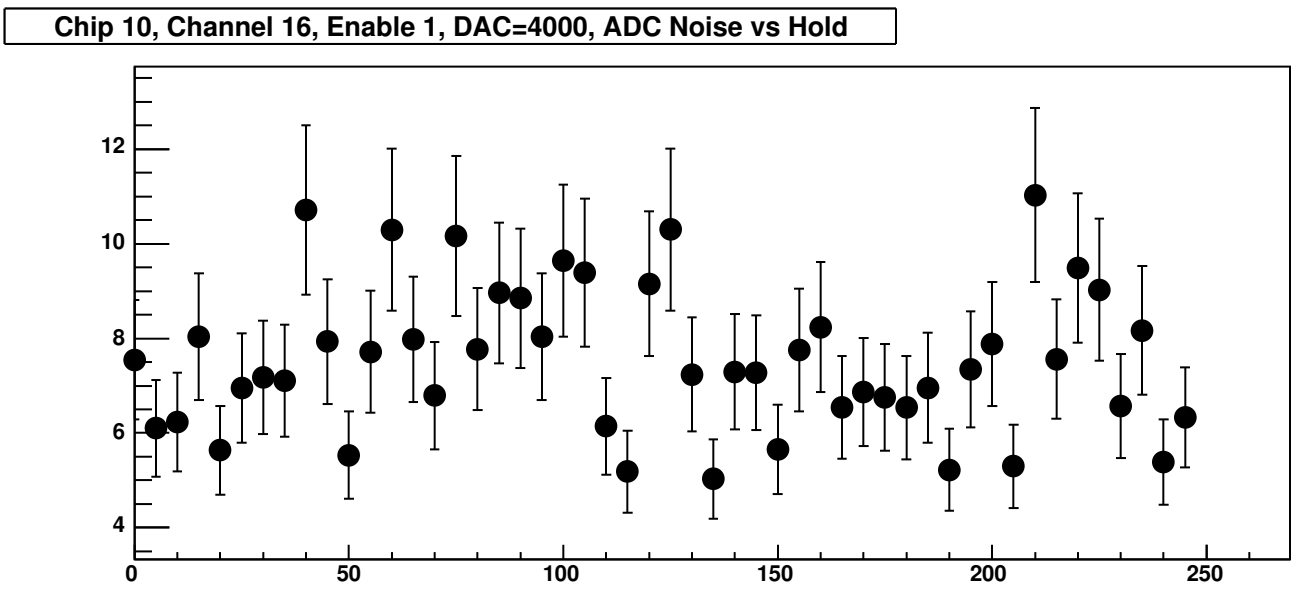
Chip 10, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold

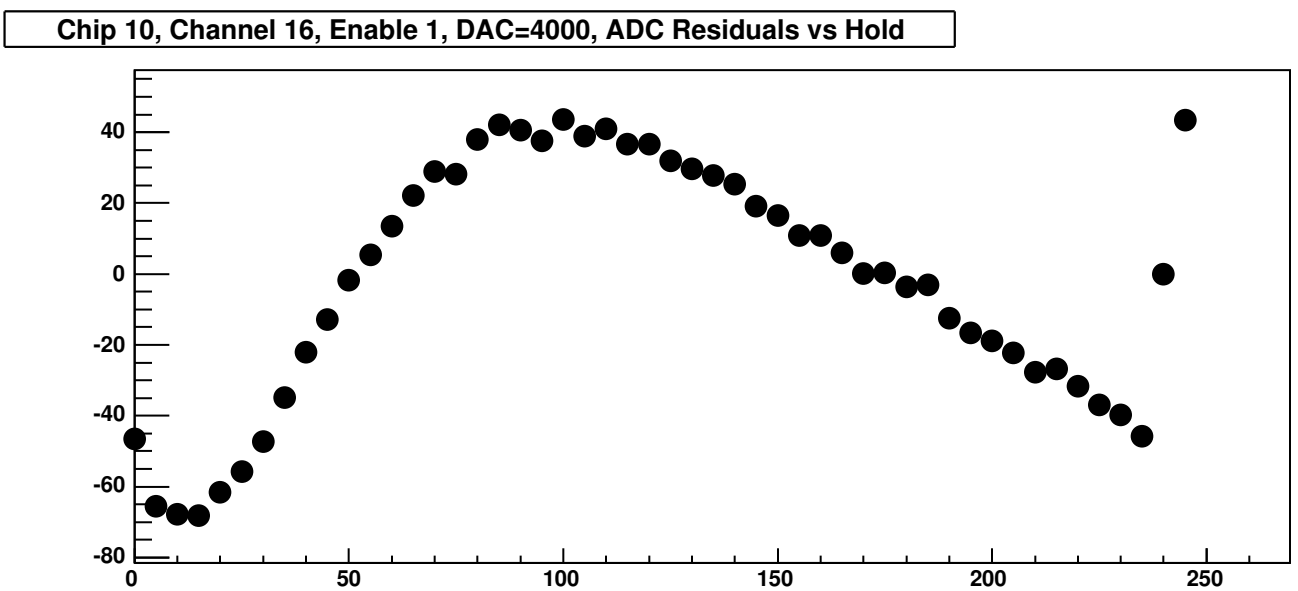


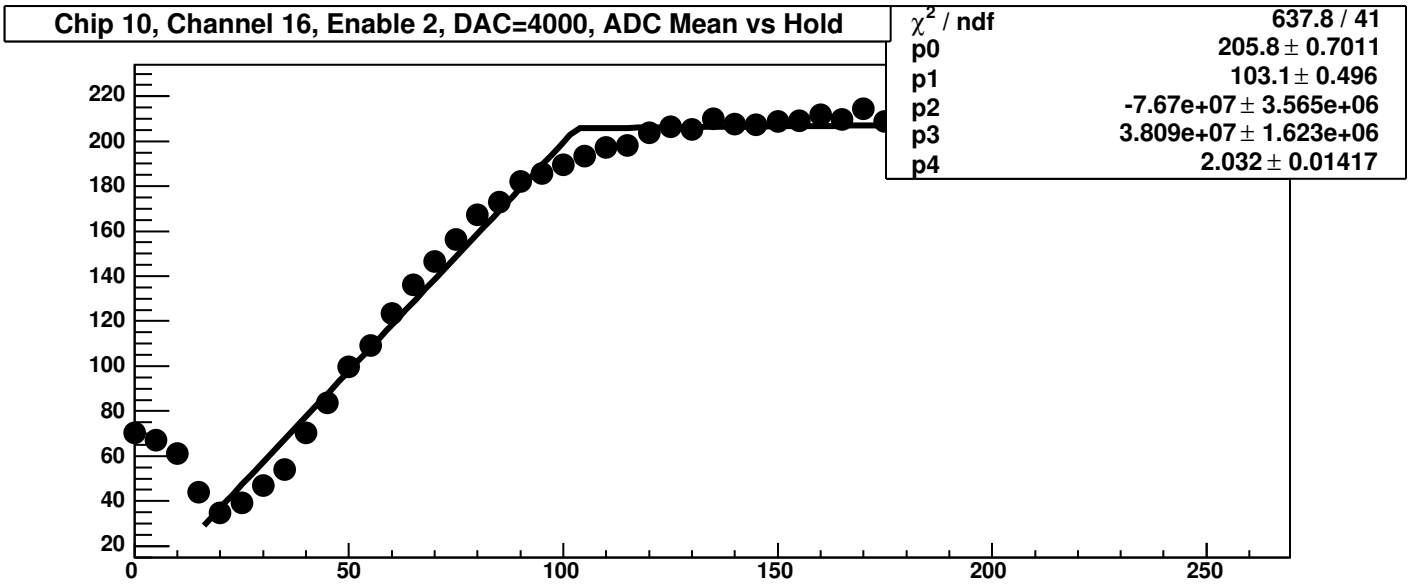
Chip 10, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

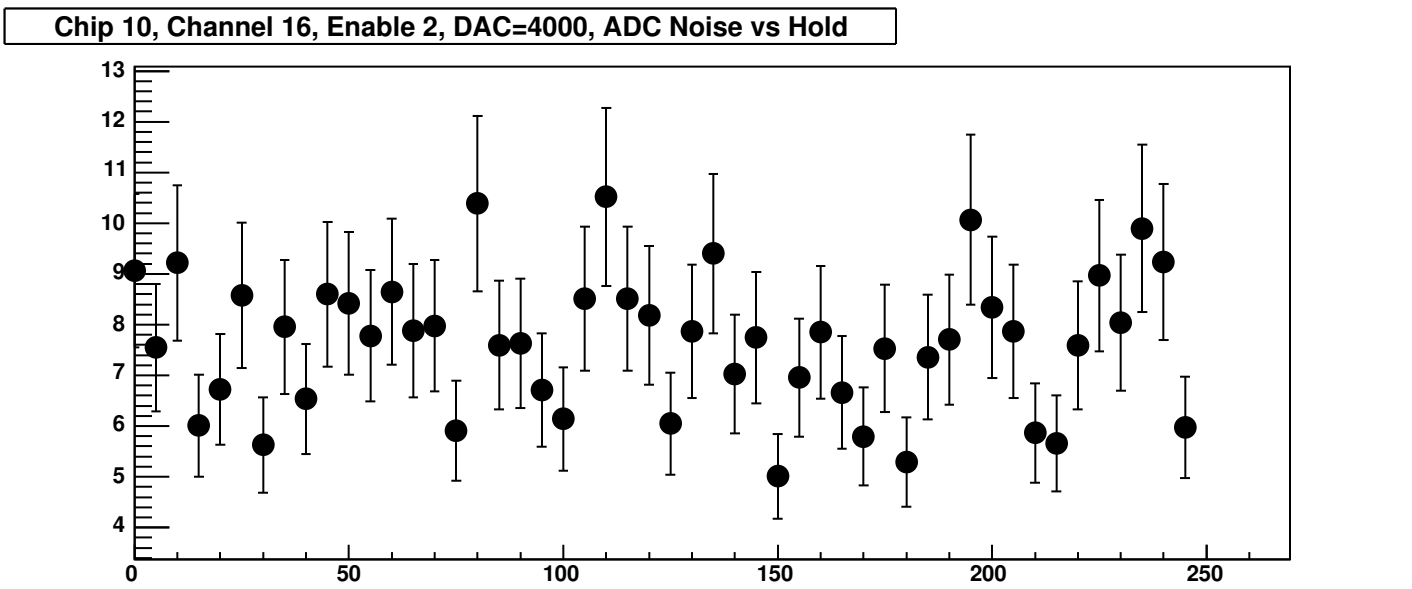


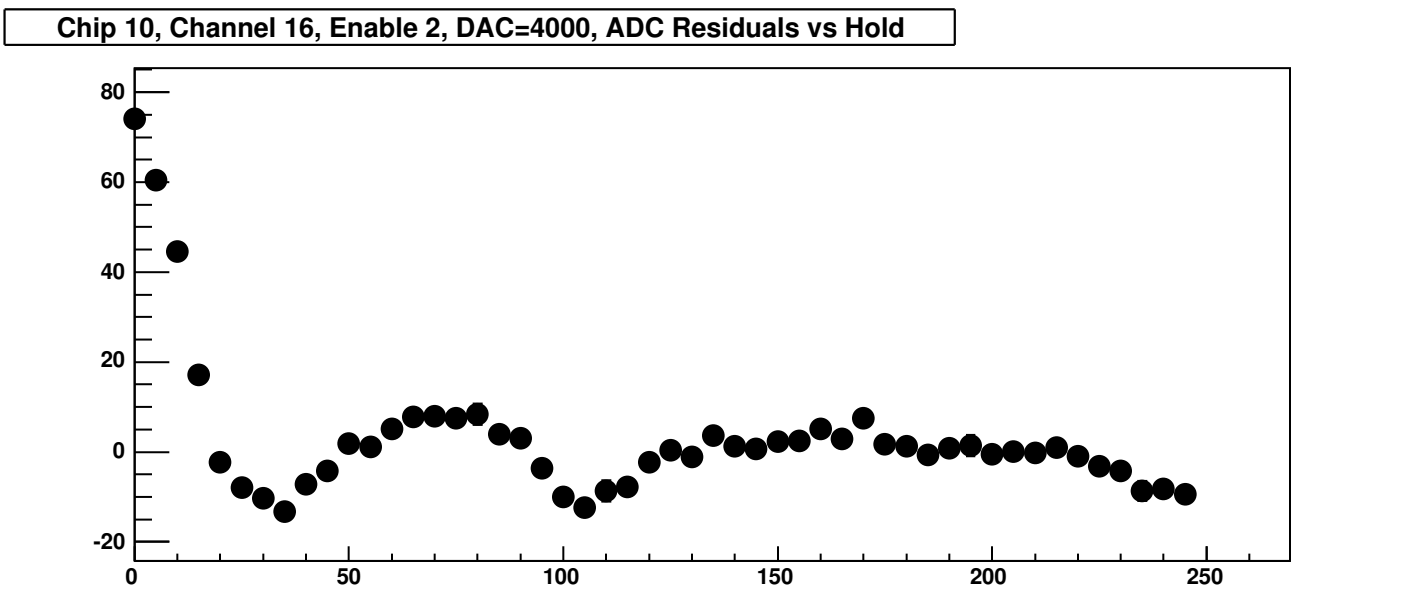

Chip 10, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.599e+04 / 41
p0 247.5 ± 1.1
p1 235.3 ± 1.8
p2 -468.6 ± 164.2
p3 41.84 ± 12.83
p4 0.5524 ± 0.00384
220
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250

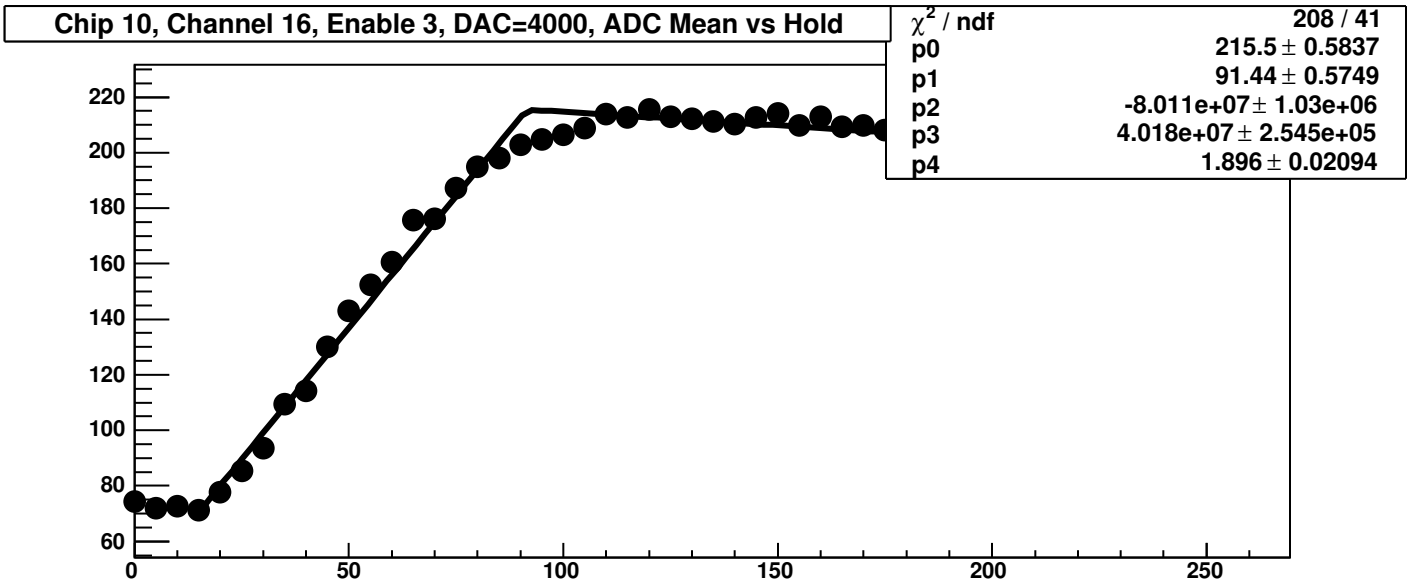

Chip 10, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

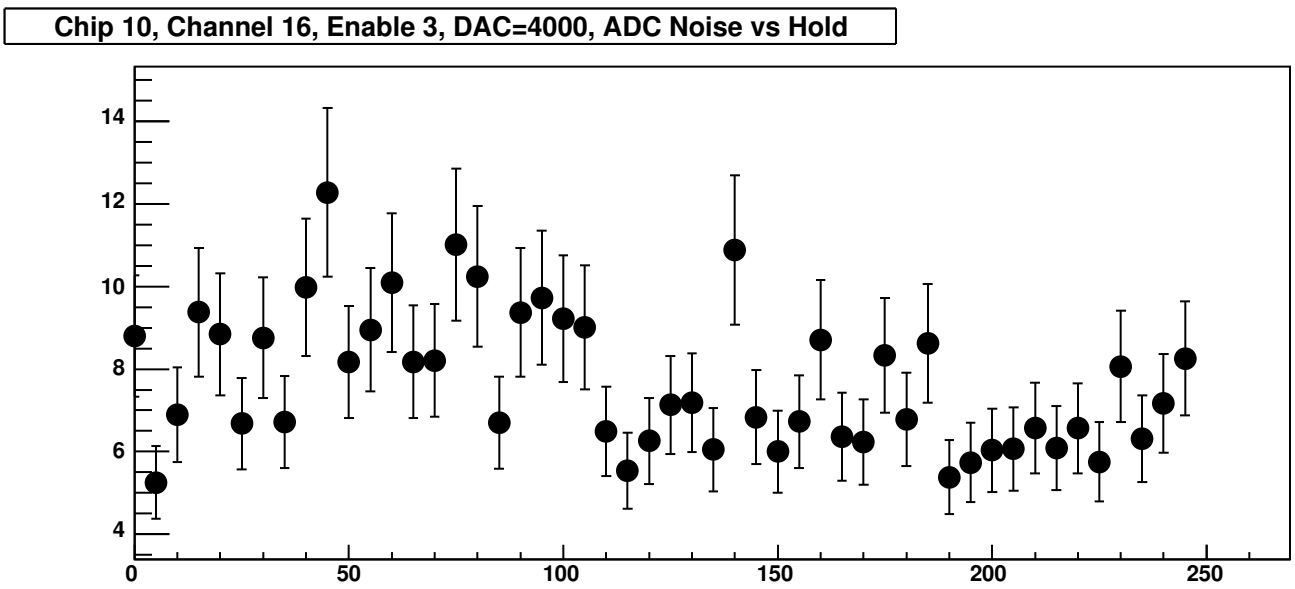

Chip 10, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
0
50
100
150
200
250

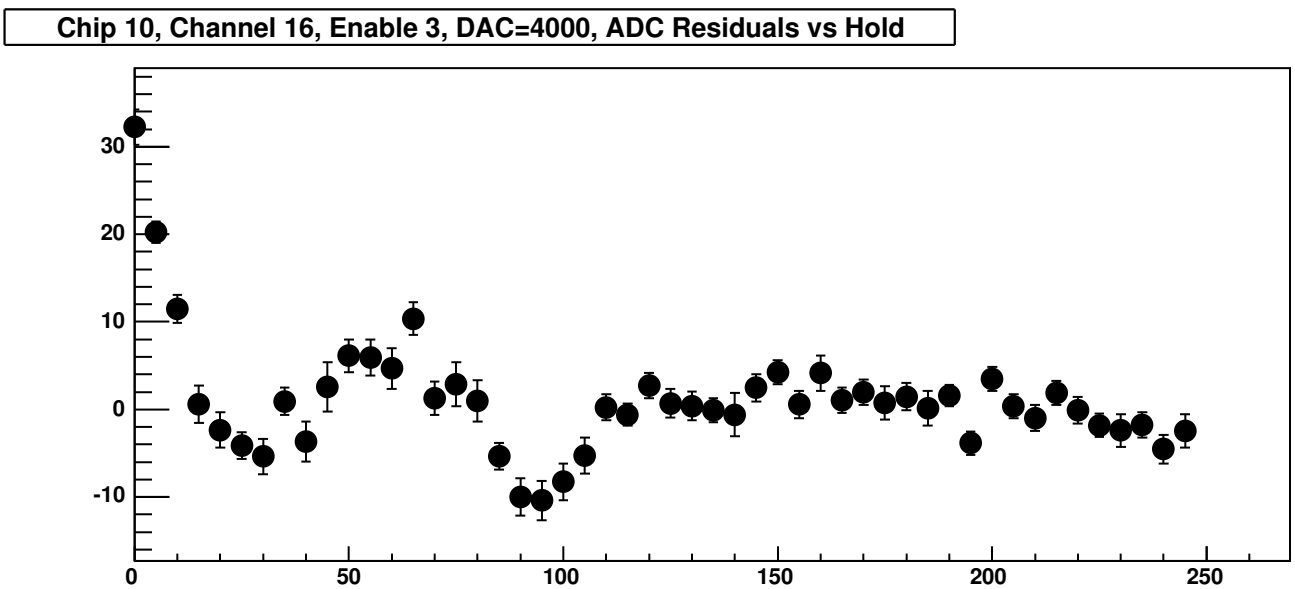

Chip 10, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 637.8 / 41
p0 205.8 ± 0.7011
p1 103.1 ± 0.496
p2 -7.67e+07 ± 3.565e+06
p3 3.809e+07 ± 1.623e+06
p4 2.032 ± 0.01417

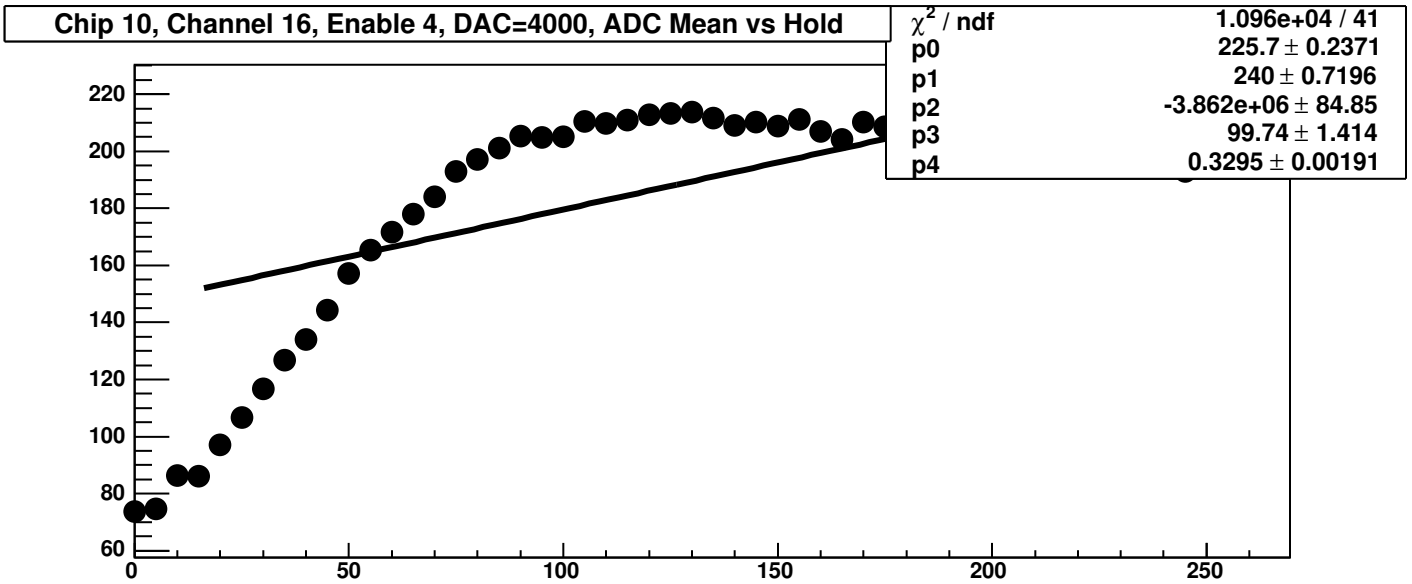

Chip 10, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold

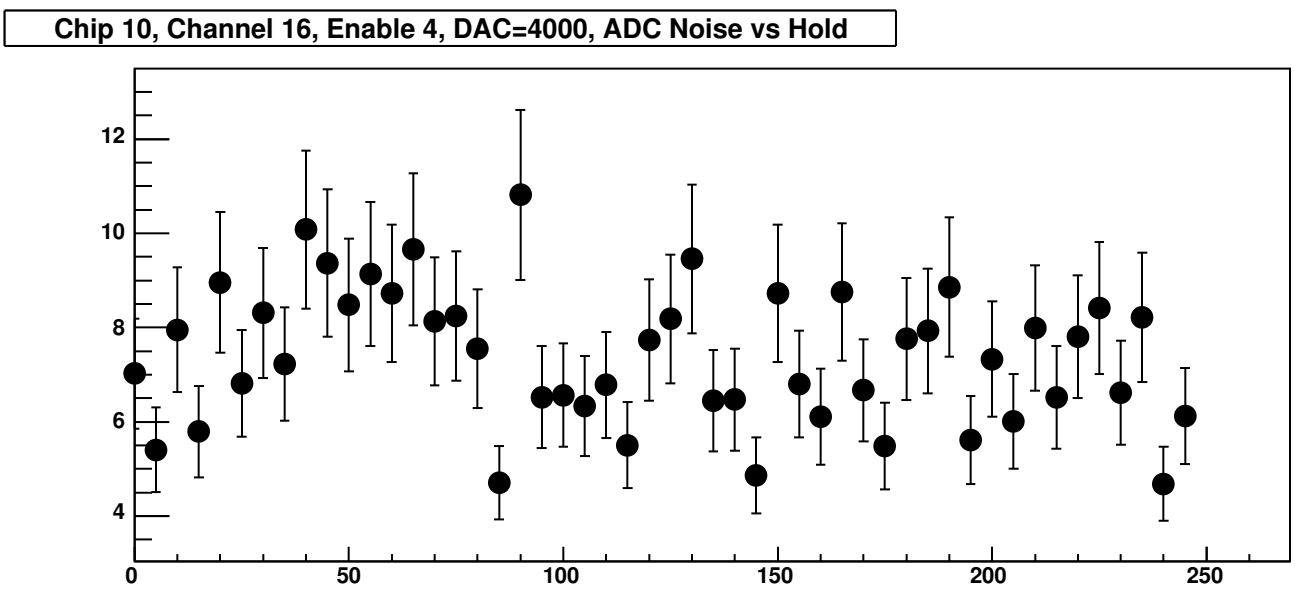

Chip 10, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold

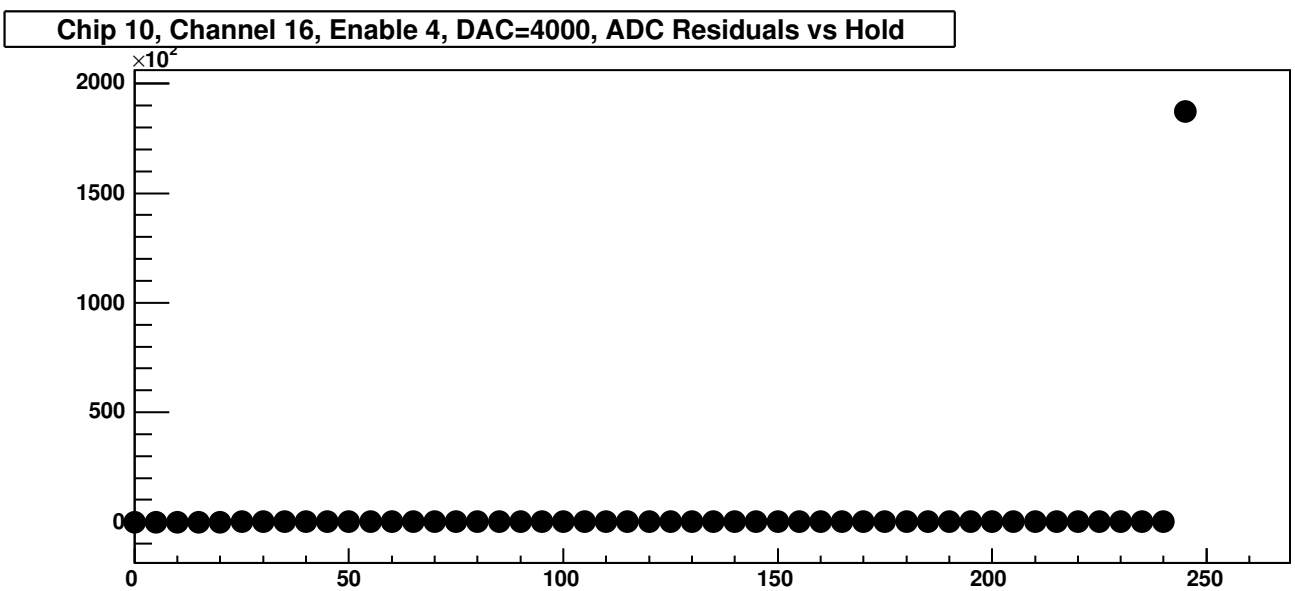

Chip 10, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 208 / 41
p0 215.5 ± 0.5837
p1 91.44 ± 0.5749
p2 -8.011e+07 ± 1.03e+06
p3 4.018e+07 ± 2.545e+05
p4 1.896 ± 0.02094
220
200
180
160
140
120
100
80
60
0
50
100
150
200
250

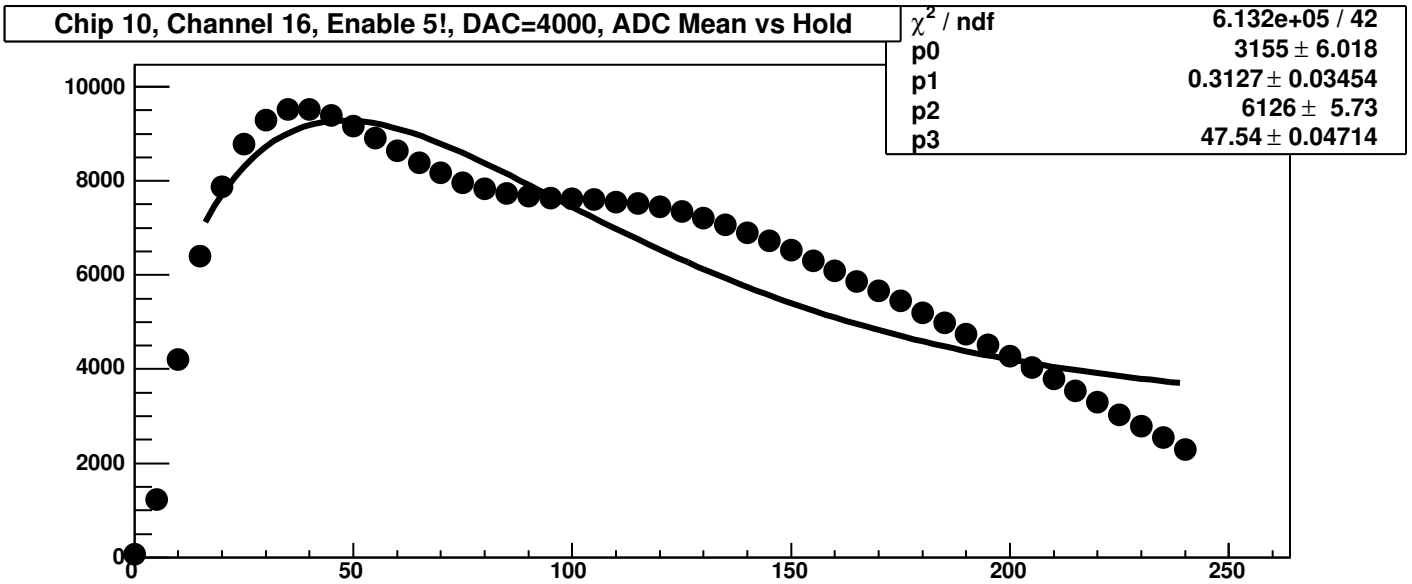

Chip 10, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

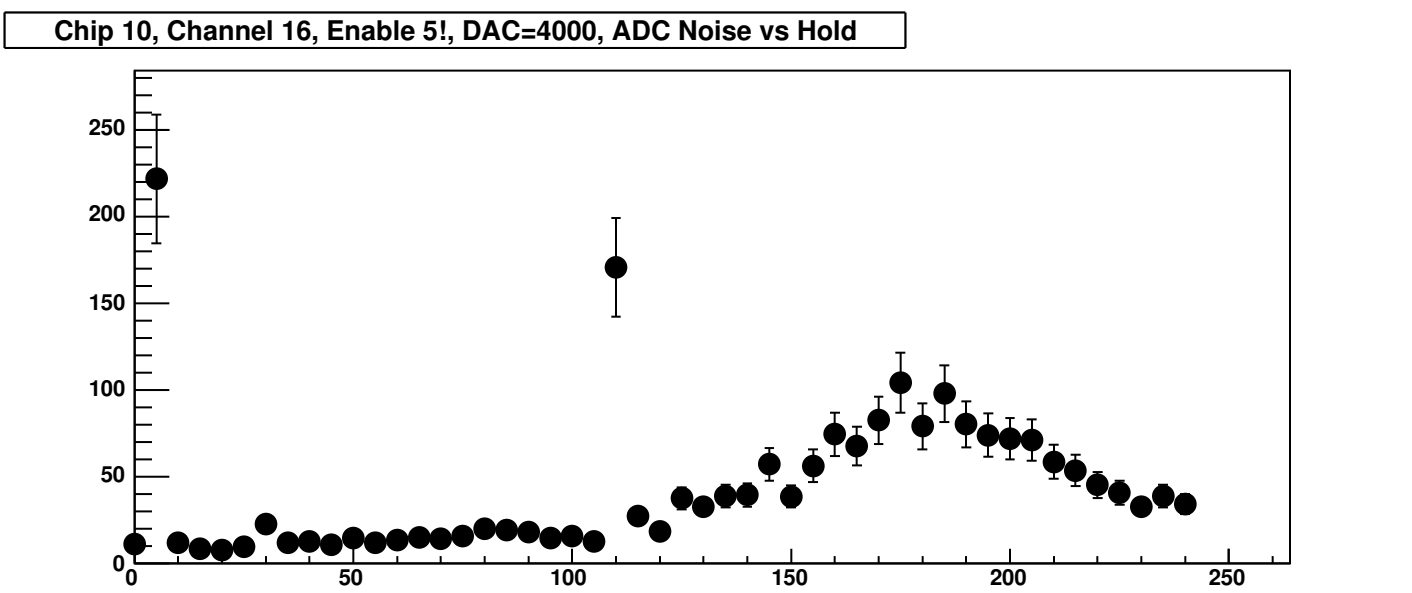

Chip 10, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
0
50
100
150
200
250

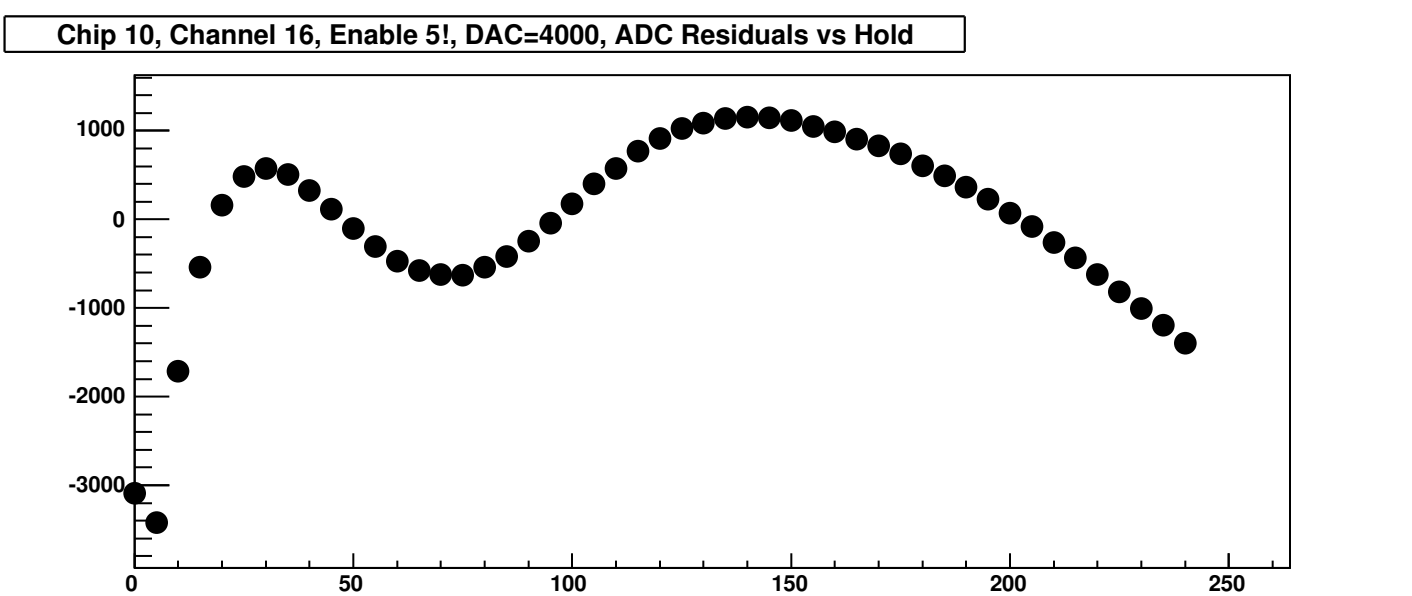

Chip 10, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.096e+04 / 41
p0 225.7 ± 0.2371
p1 240 ± 0.7196
p2 -3.862e+06 ± 84.85
p3 99.74 ± 1.414
p4 0.3295 ± 0.00191
220
200
180
160
140
120
100
80
60
0
50
100
150
200
250

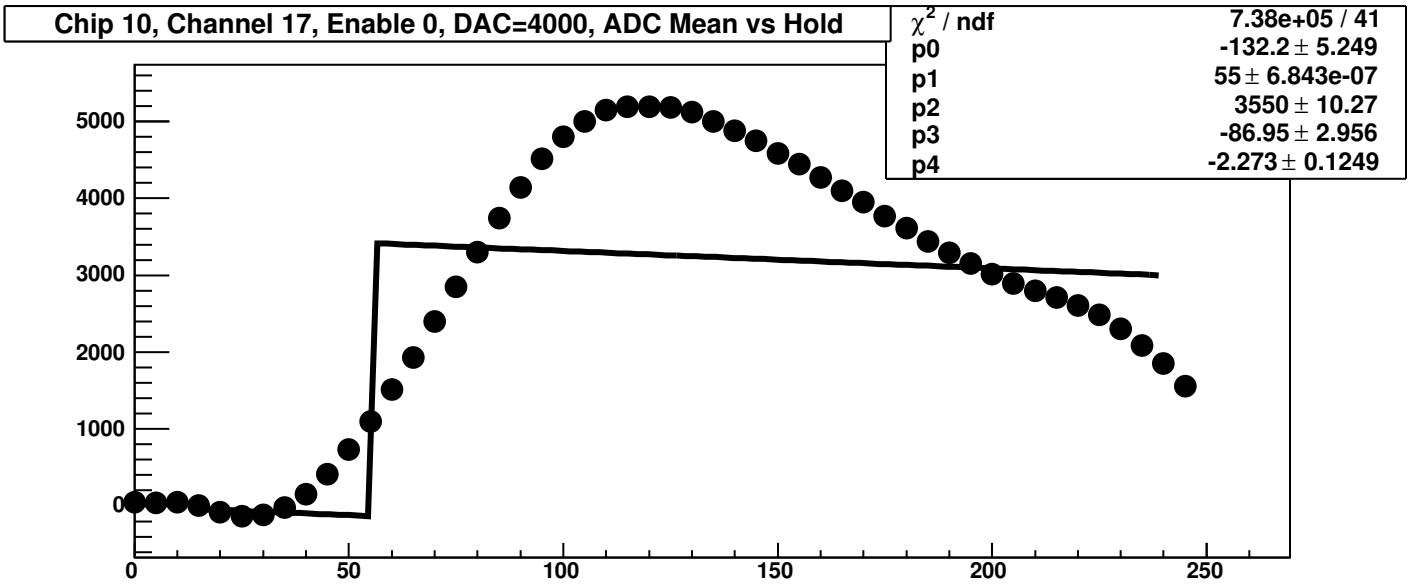

Chip 10, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

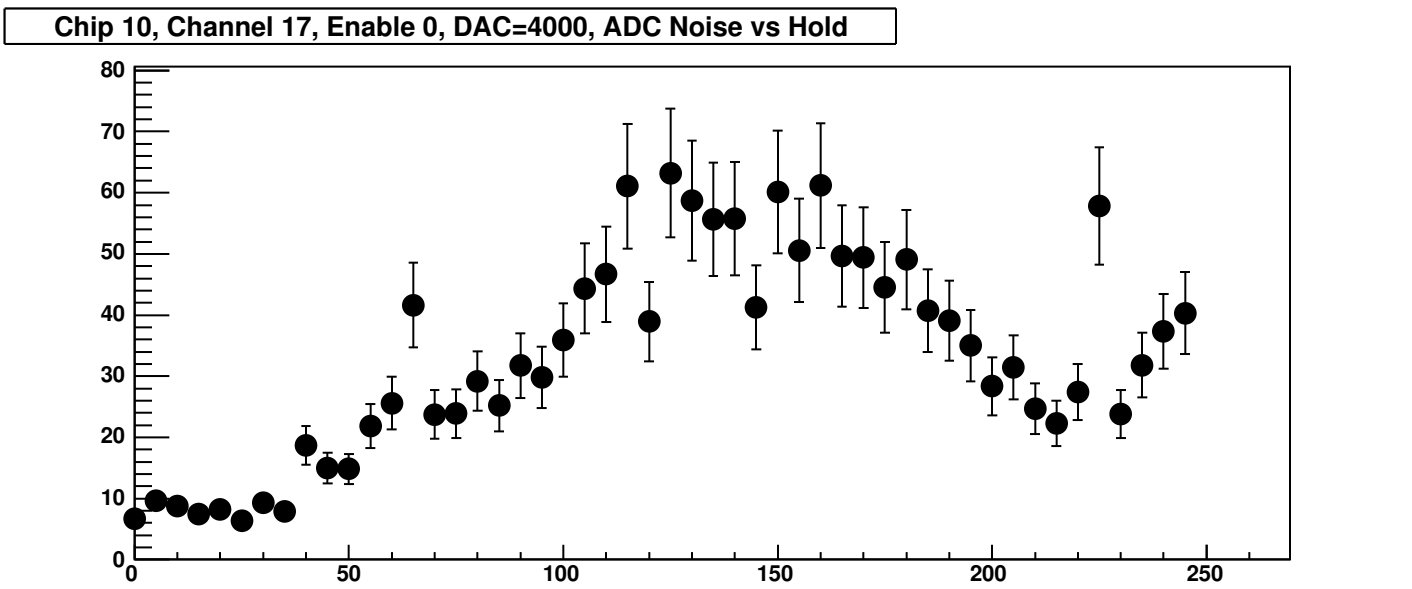

Chip 10, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold
×10²
2000
1500
1000
500
0
0
50
100
150
200
250

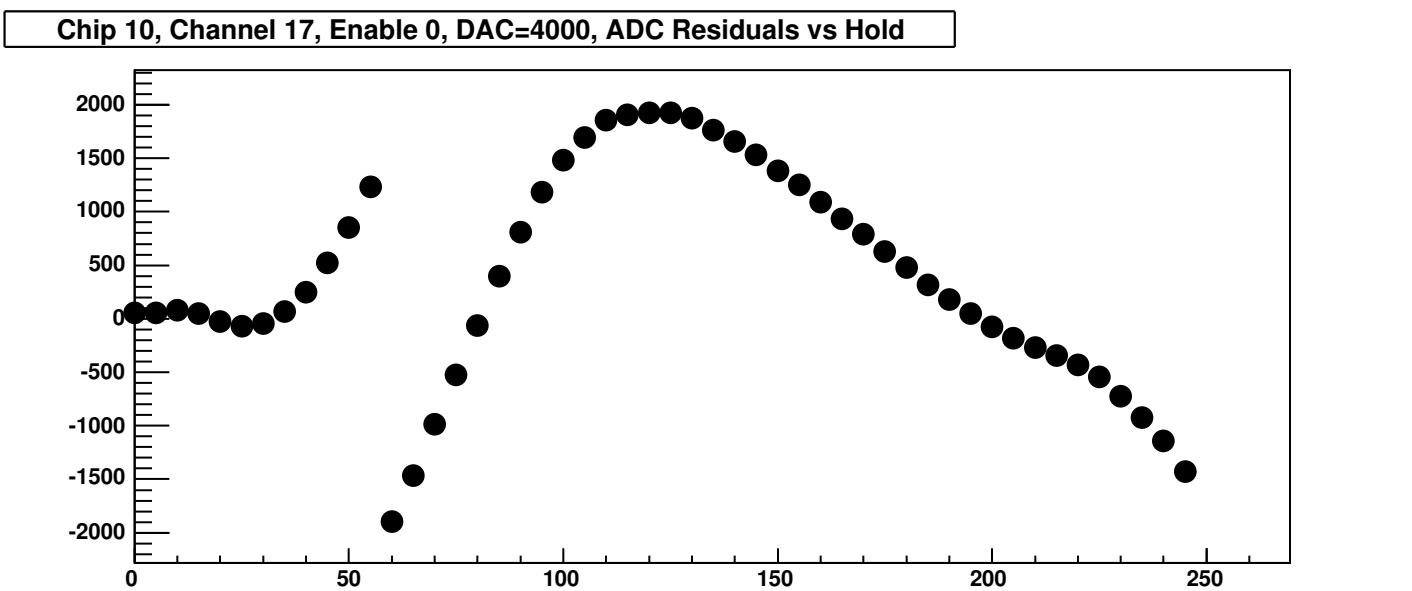

Chip 10, Channel 16, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 6.132e+05 / 42
p0 3155 ± 6.018
p1 0.3127 ± 0.03454
p2 6126 ± 5.73
p3 47.54 ± 0.04714
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

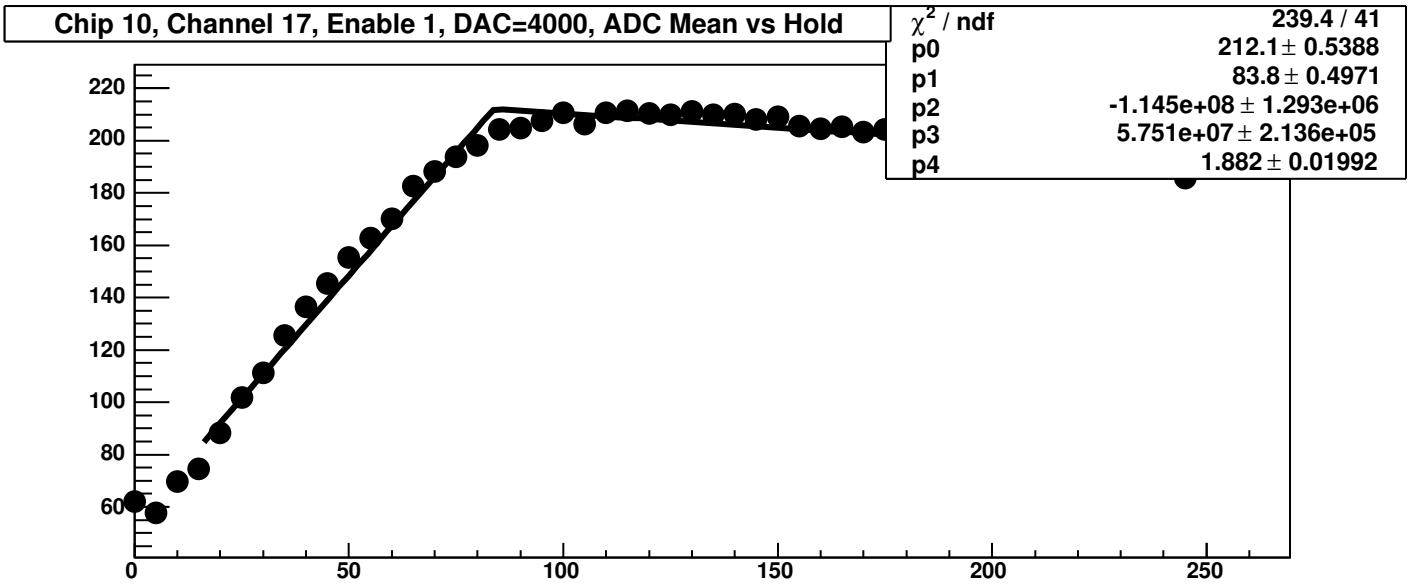

Chip 10, Channel 16, Enable 5!, DAC=4000, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250

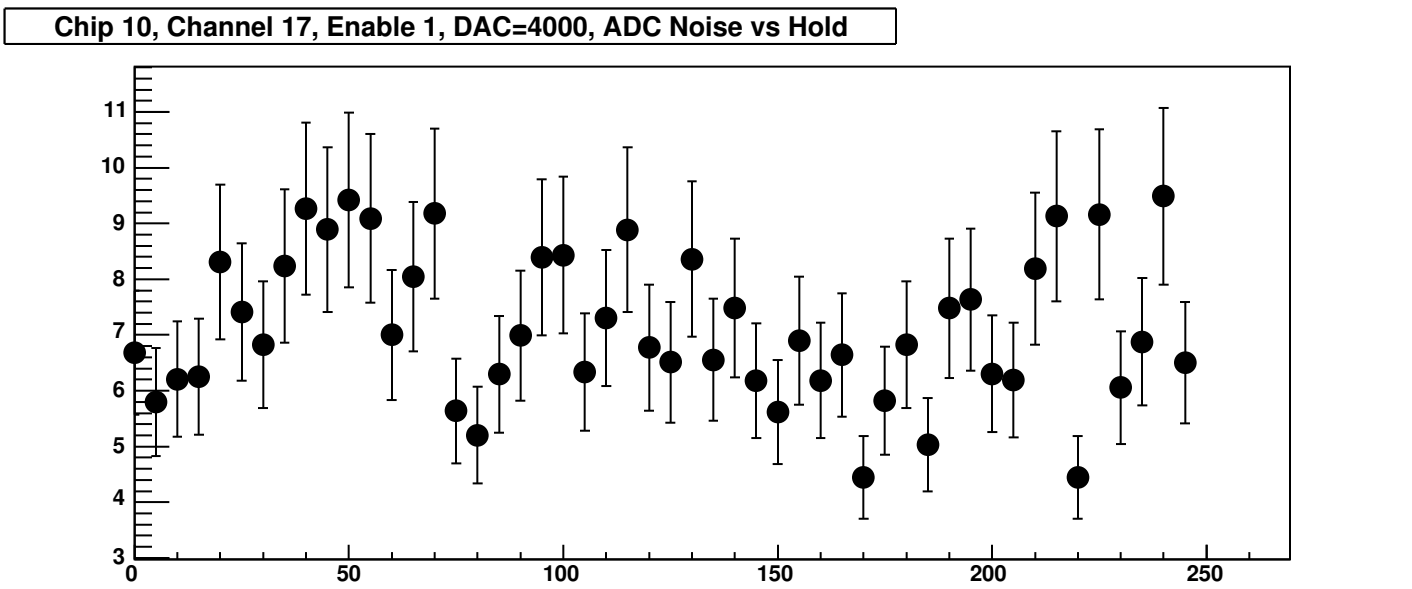

Chip 10, Channel 16, Enable 5!, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

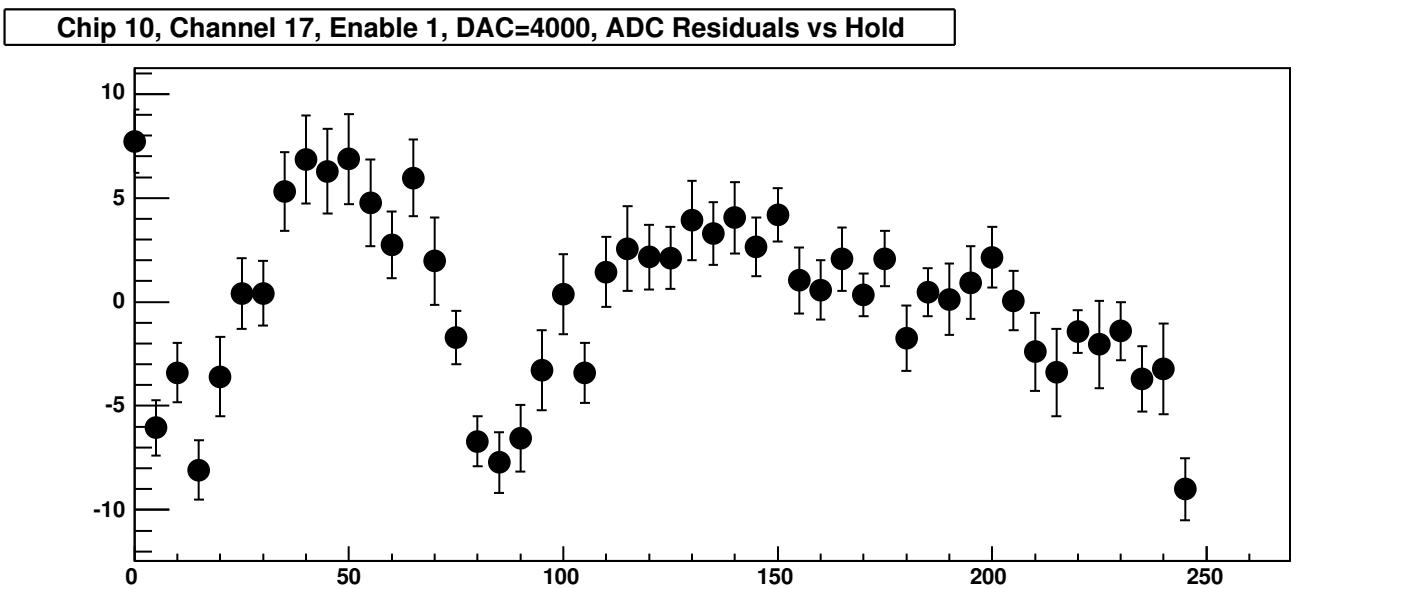

Chip 10, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 7.38e+05 / 41
p0 -132.2 ± 5.249
p1 55 ± 6.843e-07
p2 3550 ± 10.27
p3 -86.95 ± 2.956
p4 -2.273 ± 0.1249
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

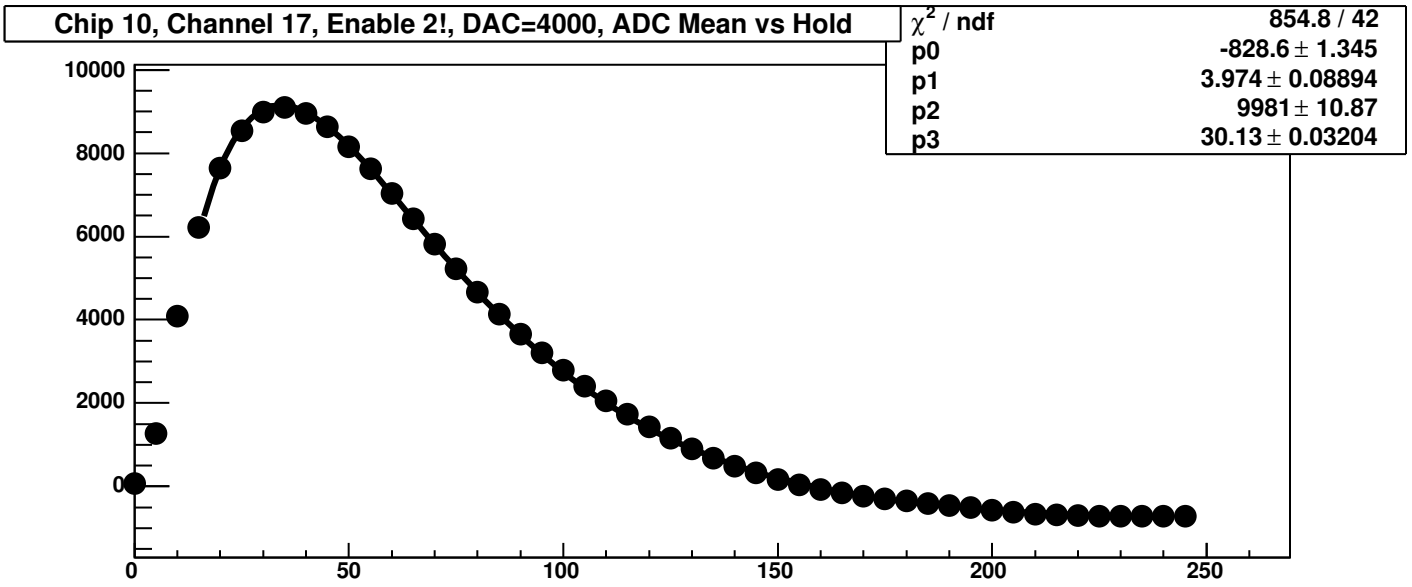

Chip 10, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250

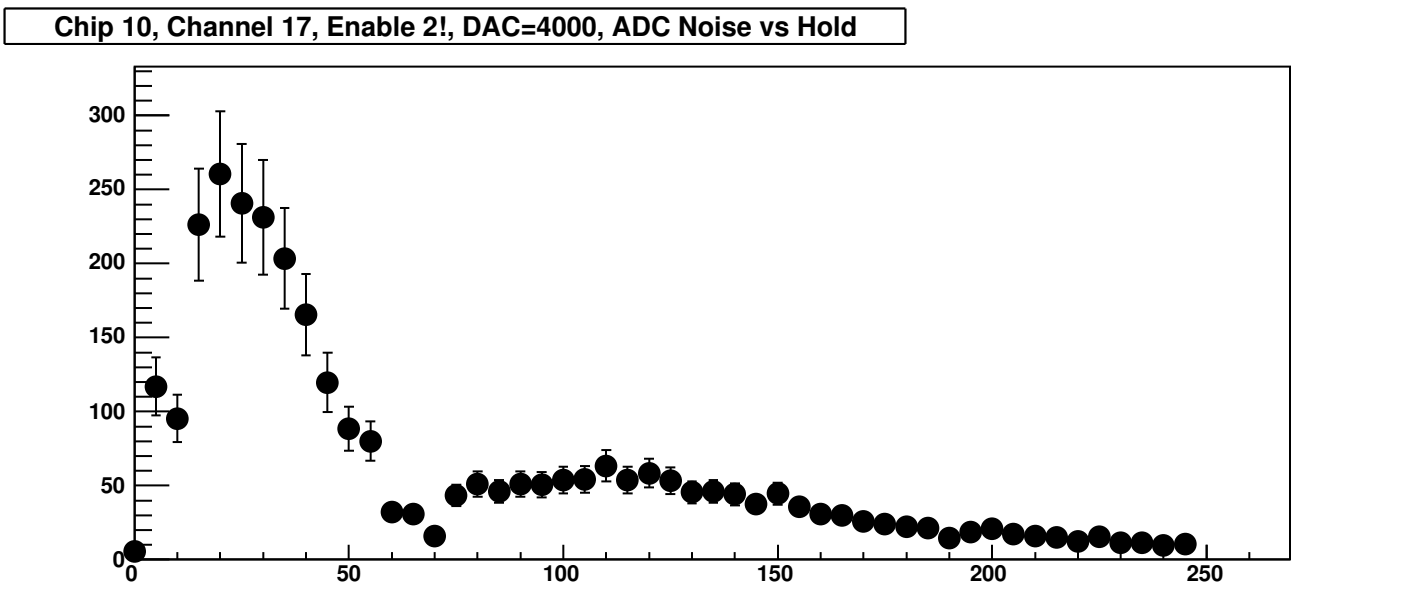

Chip 10, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

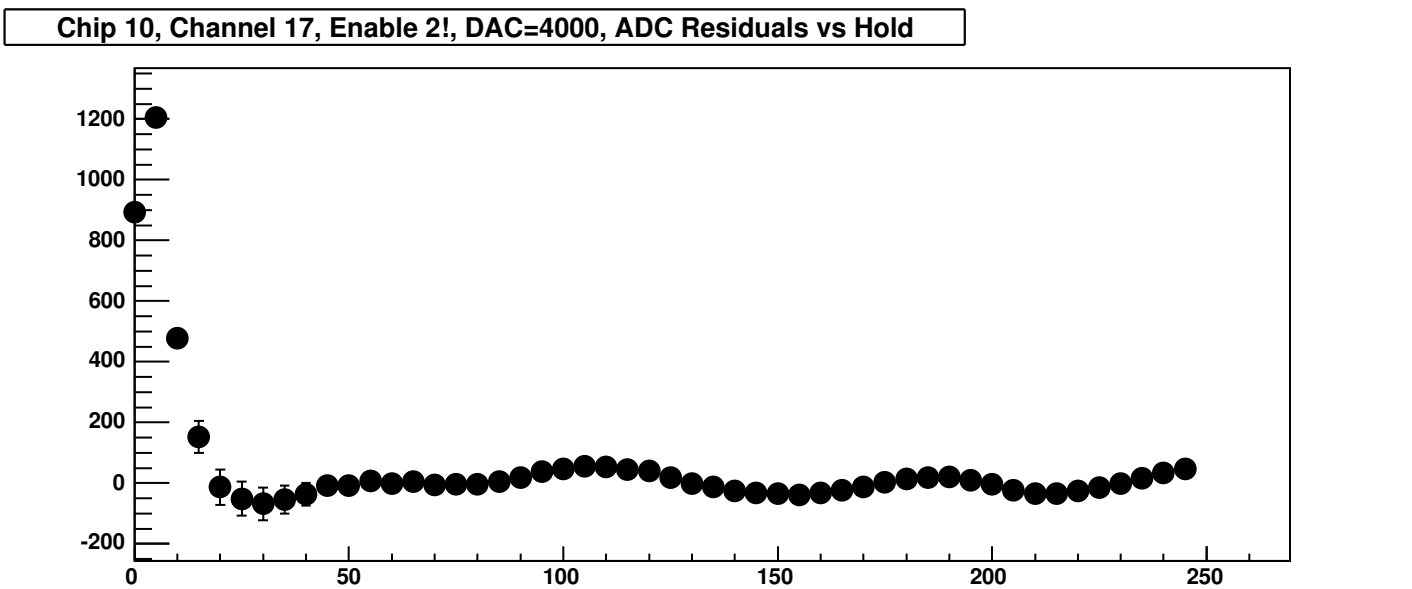

Chip 10, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 239.4 / 41
p0 212.1 ± 0.5388
p1 83.8 ± 0.4971
p2 -1.145e+08 ± 1.293e+06
p3 5.751e+07 ± 2.136e+05
p4 1.882 ± 0.01992

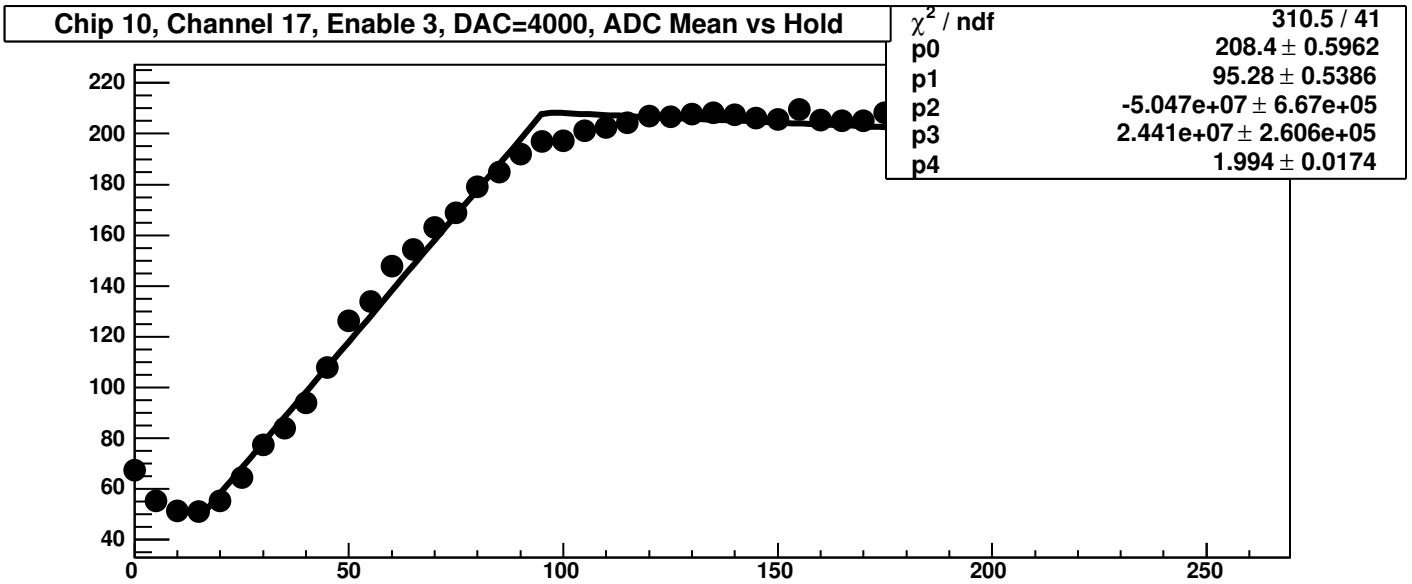

Chip 10, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold

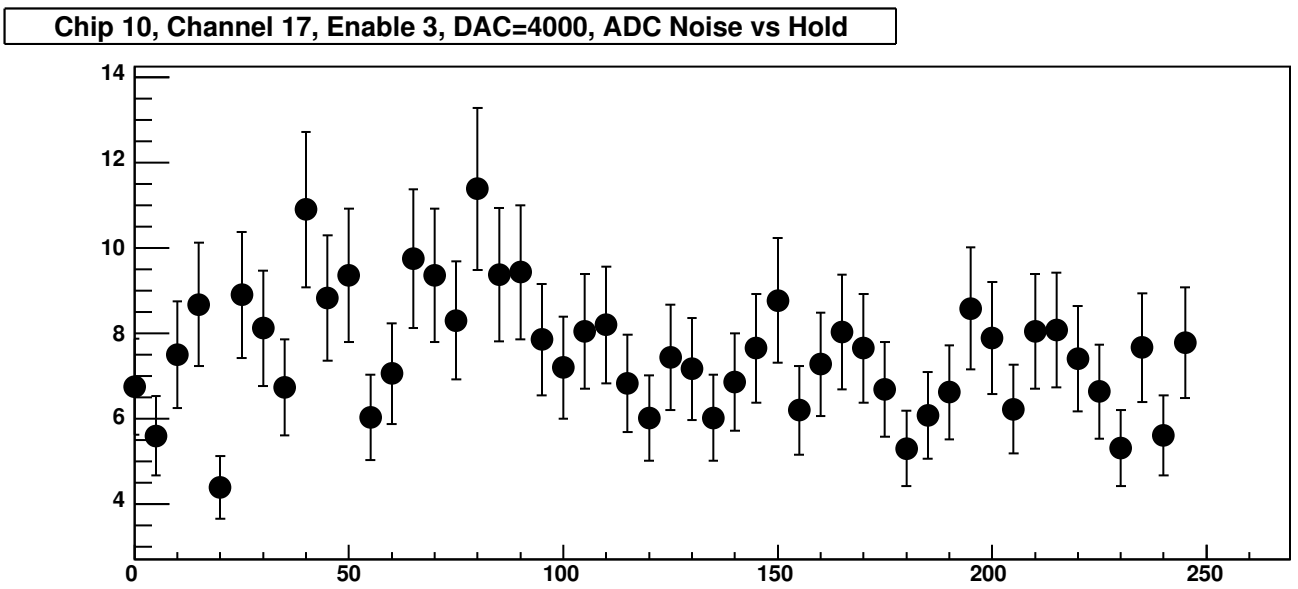

Chip 10, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

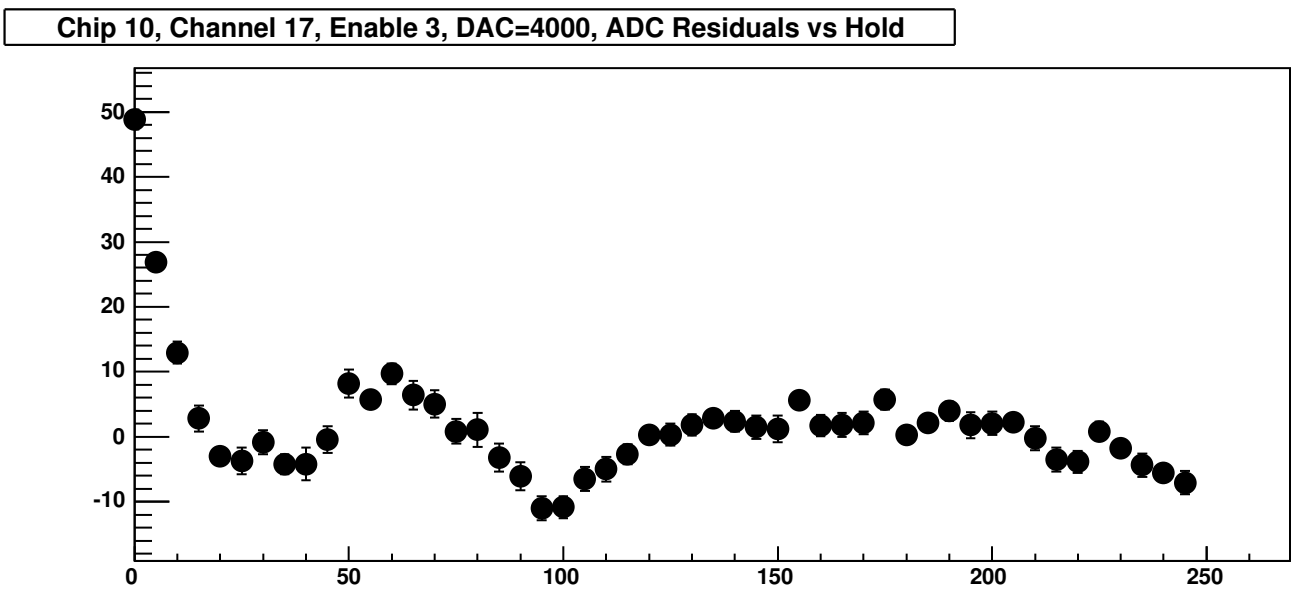

Chip 10, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 854.8 / 42
p0 -828.6 ± 1.345
p1 3.974 ± 0.08894
p2 9981 ± 10.87
p3 30.13 ± 0.03204
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

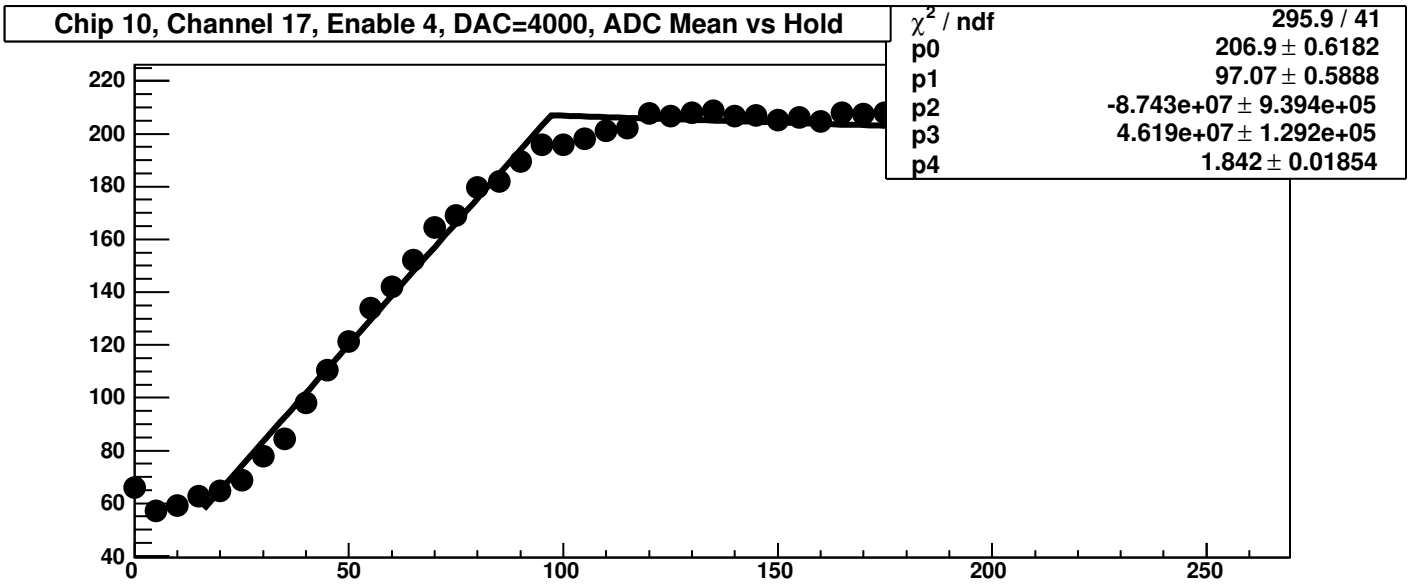

Chip 10, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold
300
250
200
150
100
50
0
0
50
100
150
200
250

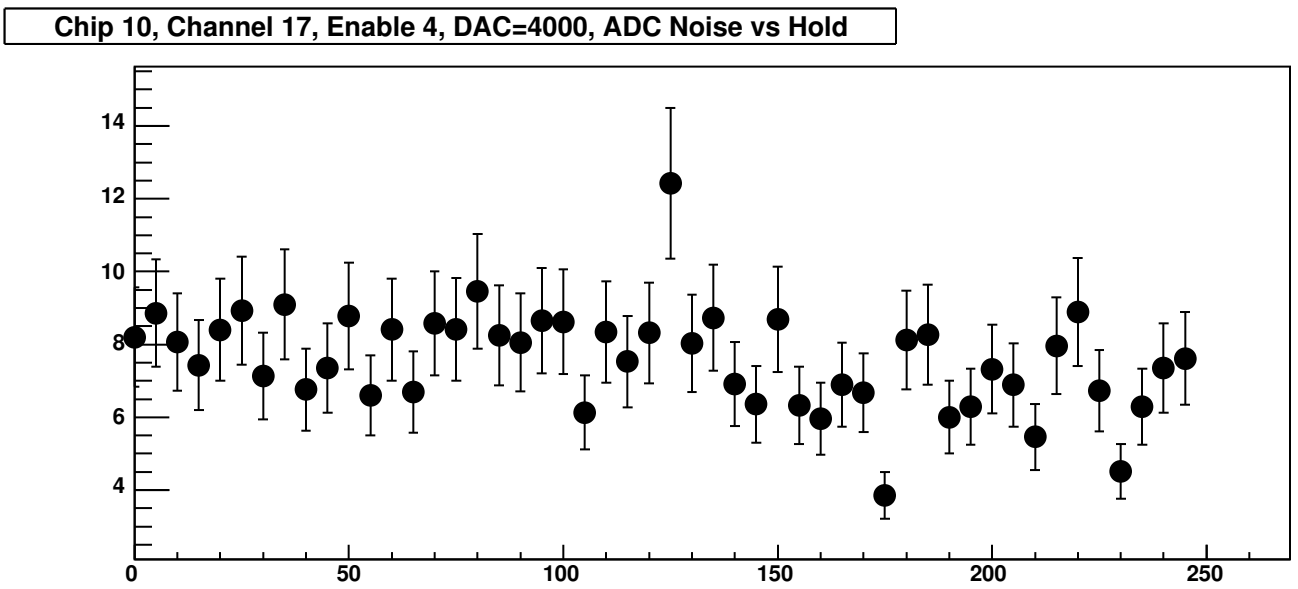

Chip 10, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

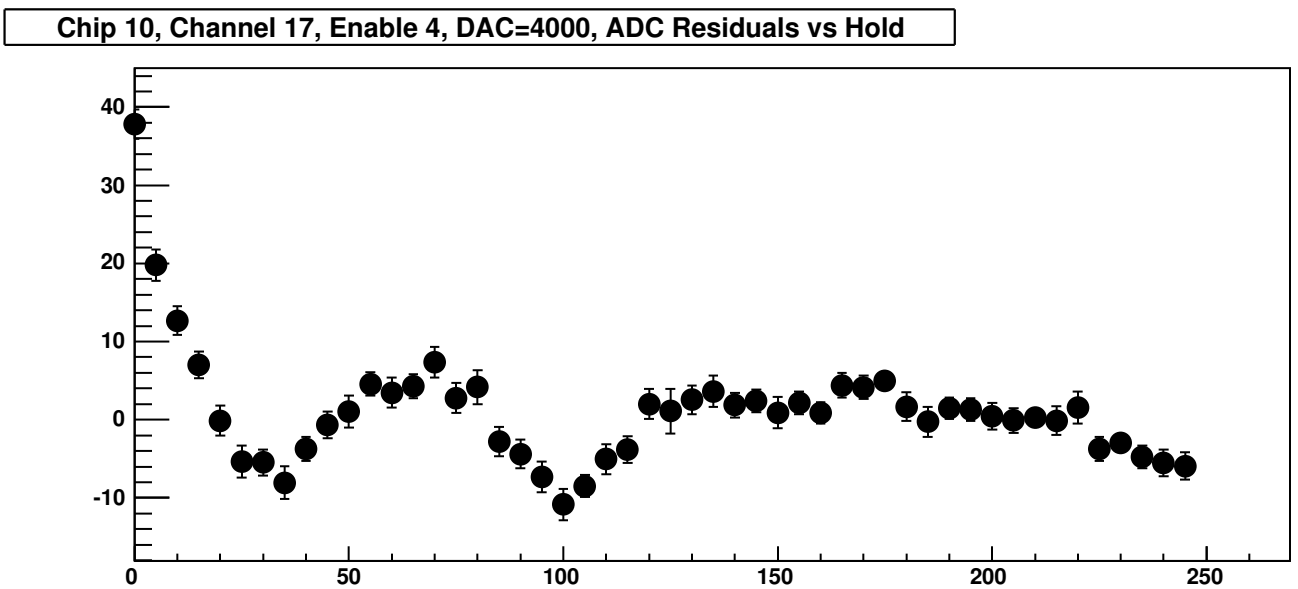

Chip 10, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 310.5 / 41
p0 208.4 ± 0.5962
p1 95.28 ± 0.5386
p2 -5.047e+07 ± 6.67e+05
p3 2.441e+07 ± 2.606e+05
p4 1.994 ± 0.0174
220
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250

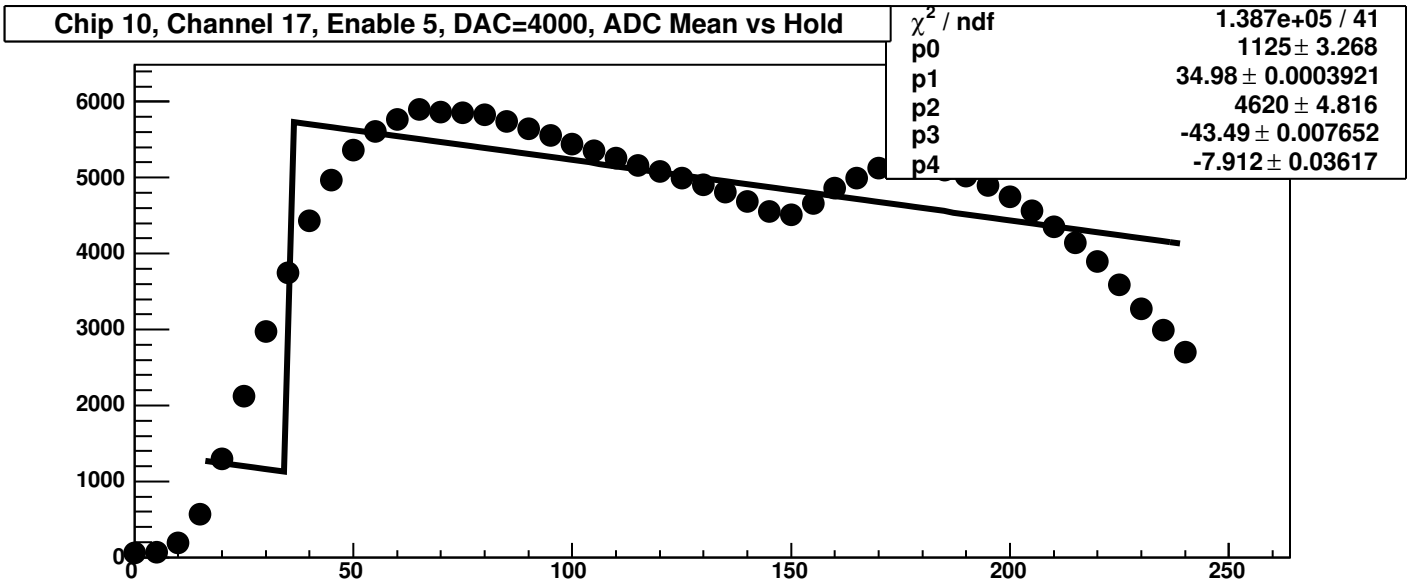

Chip 10, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

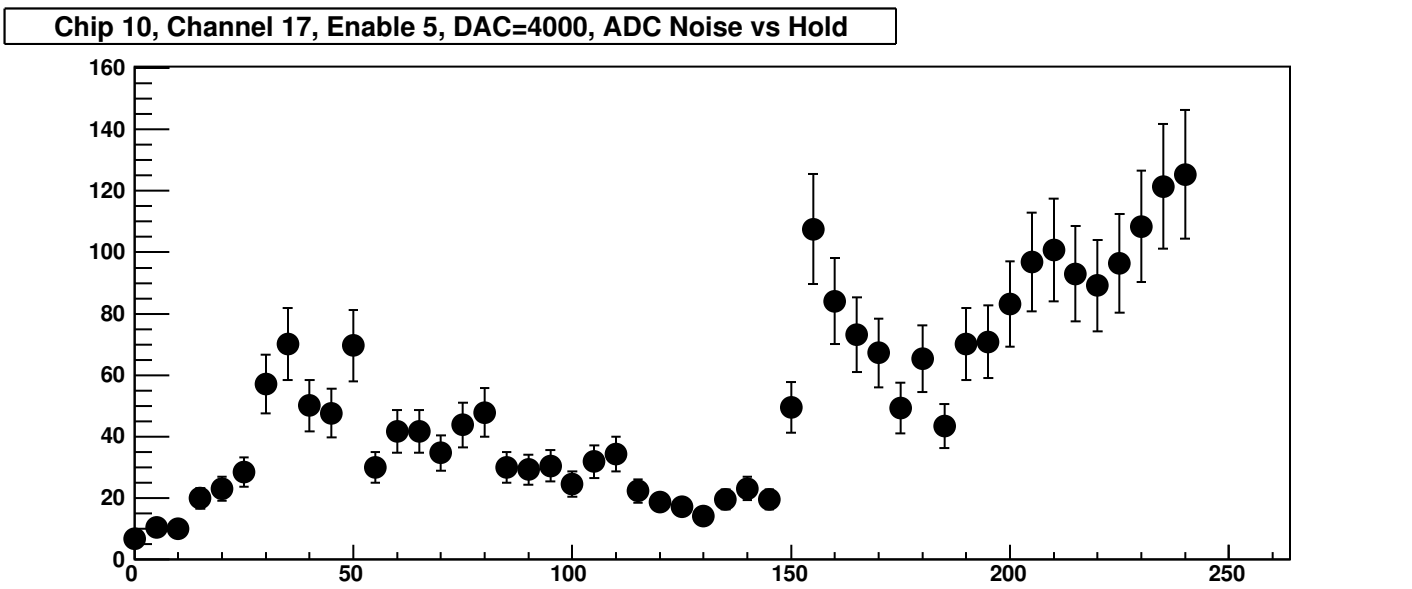

Chip 10, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250

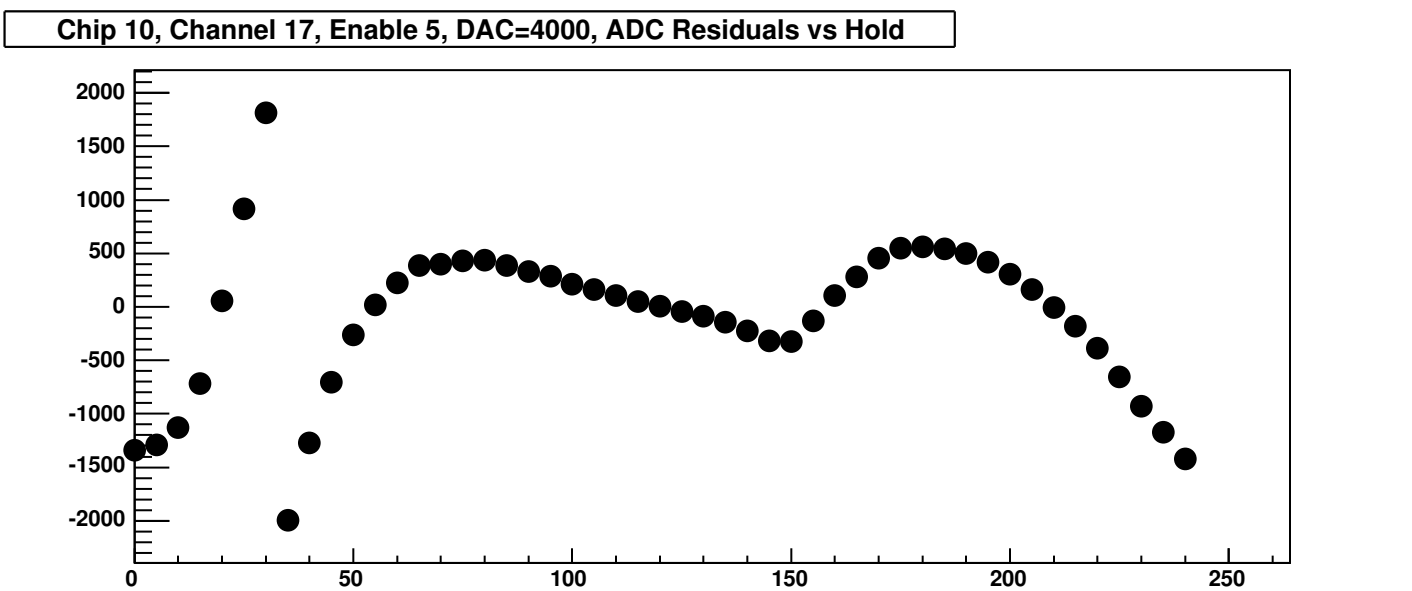

Chip 10, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 295.9 / 41
p0 206.9 ± 0.6182
p1 97.07 ± 0.5888
p2 -8.743e+07 ± 9.394e+05
p3 4.619e+07 ± 1.292e+05
p4 1.842 ± 0.01854
220
200
180
160
140
120
100
80
60
40
0
50
100
150
200
250

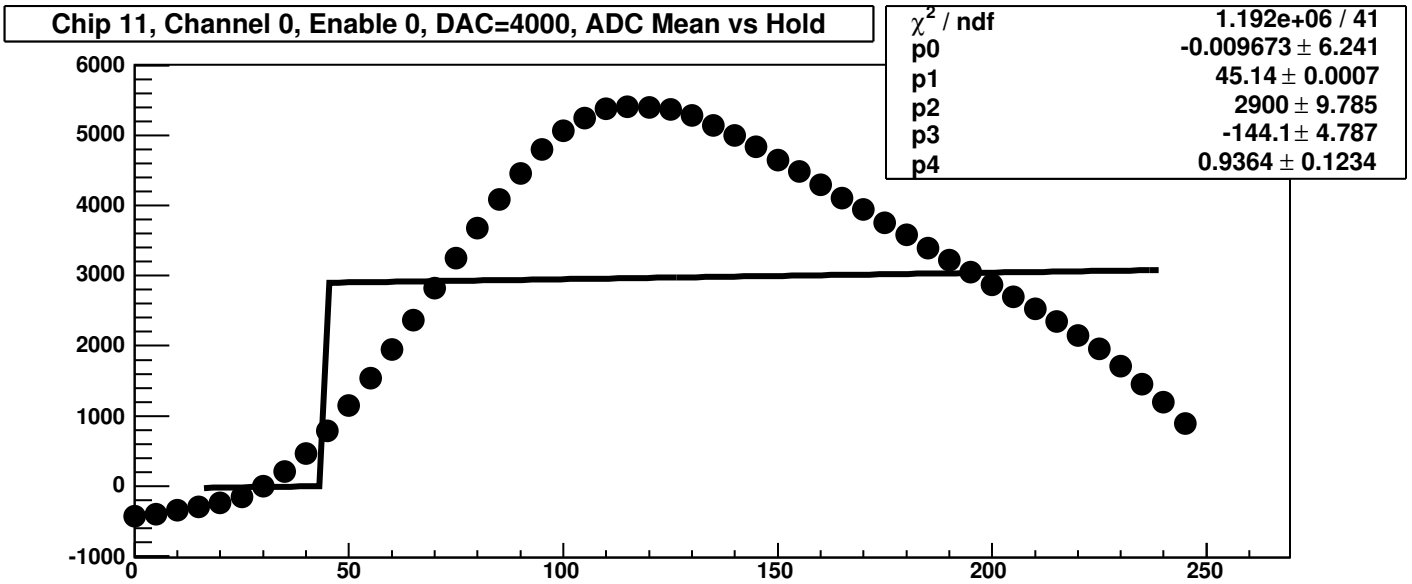

Chip 10, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

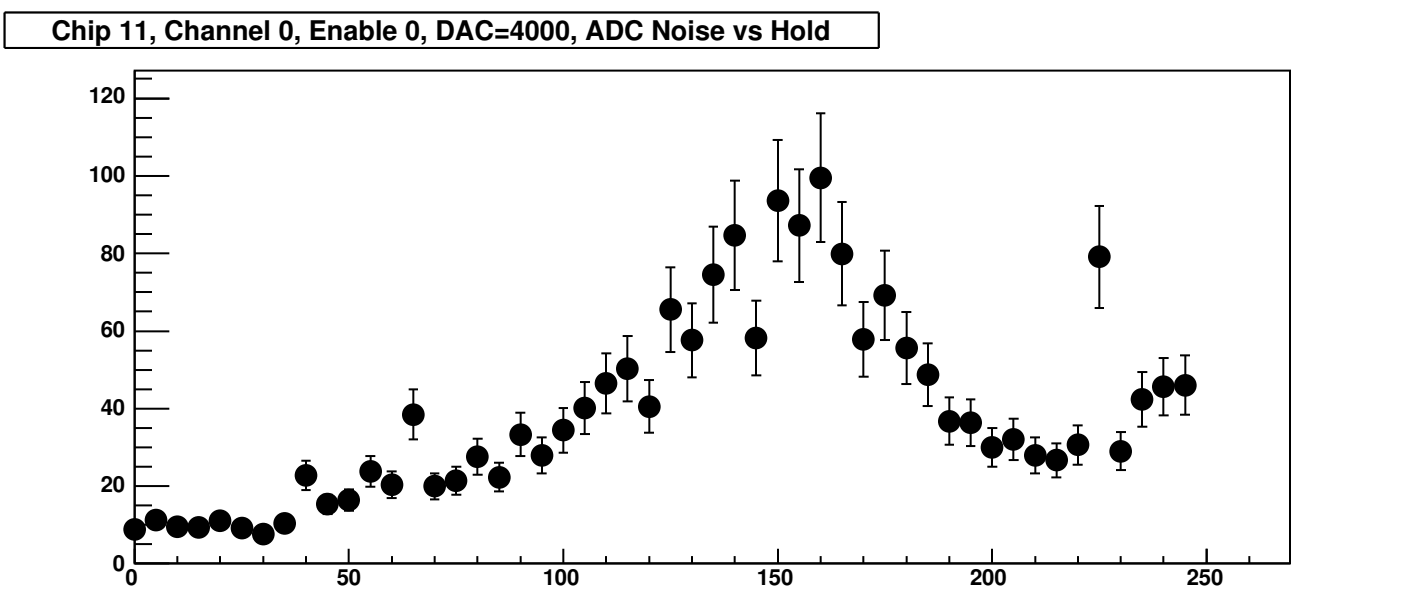

Chip 10, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

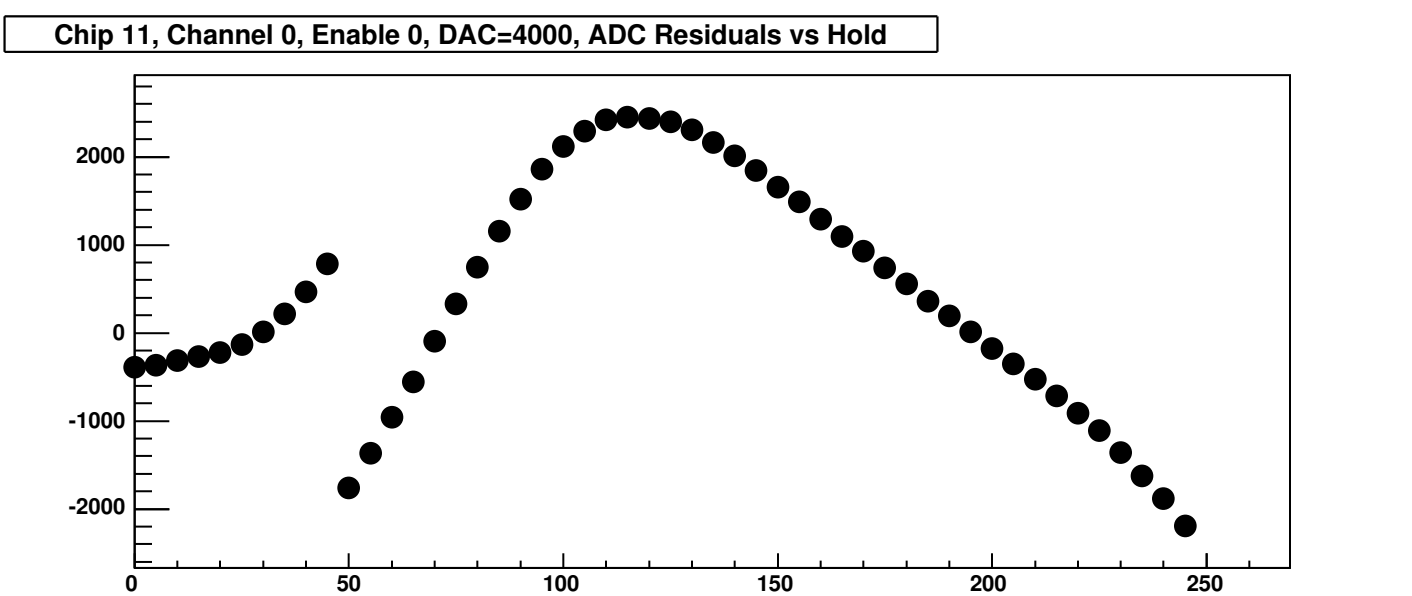

Chip 10, Channel 17, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.387e+05 / 41
p0 1125 ± 3.268
p1 34.98 ± 0.0003921
p2 4620 ± 4.816
p3 -43.49 ± 0.007652
p4 -7.912 ± 0.03617
6000
5000
4000
3000
2000
1000
0
0
50
100
150
200
250


Chip 10, Channel 17, Enable 5, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

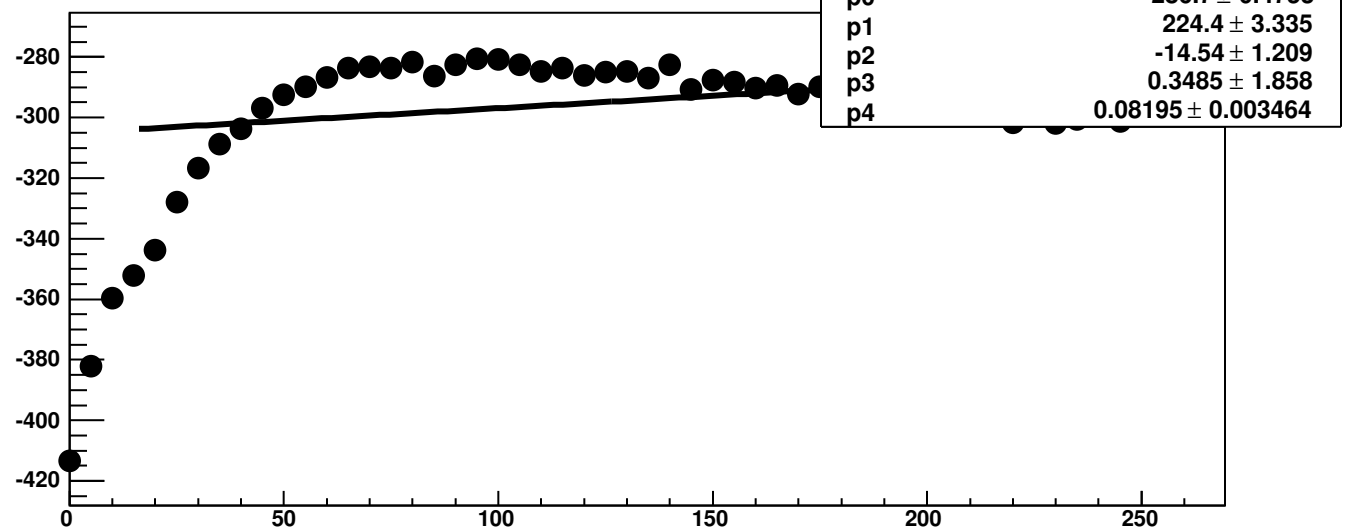

Chip 10, Channel 17, Enable 5, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250


Chip 11, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.192e+06 / 41
p0 -0.009673 ± 6.241
p1 45.14 ± 0.0007
p2 2900 ± 9.785
p3 -144.1 ± 4.787
p4 0.9364 ± 0.1234
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

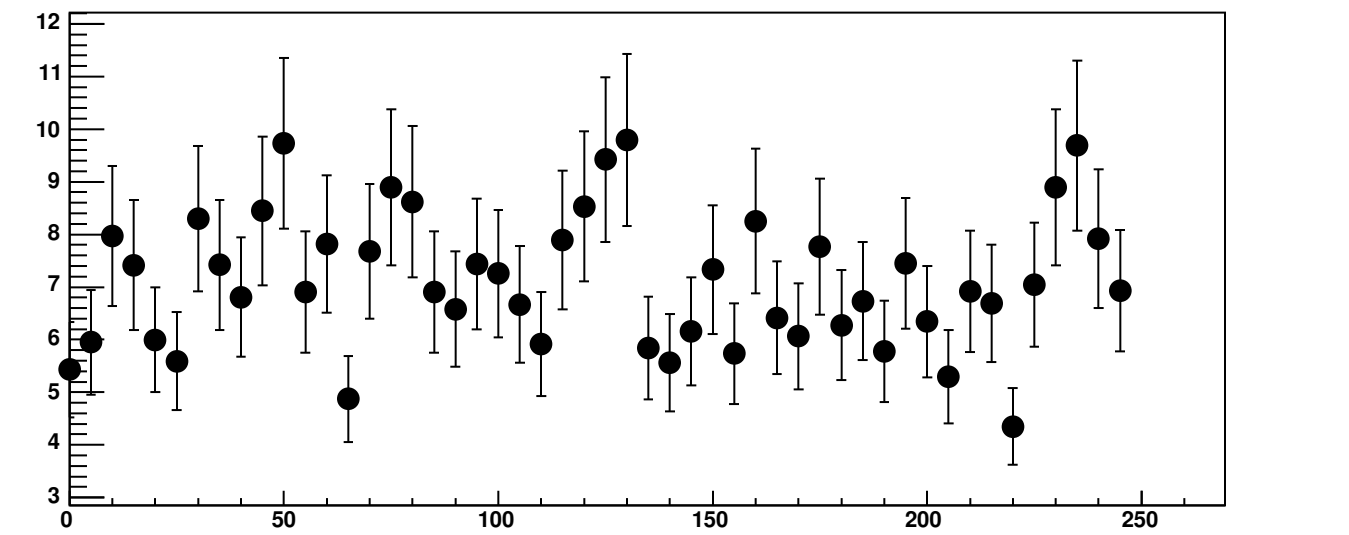

Chip 11, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 11, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
0
50
100
150
200
250

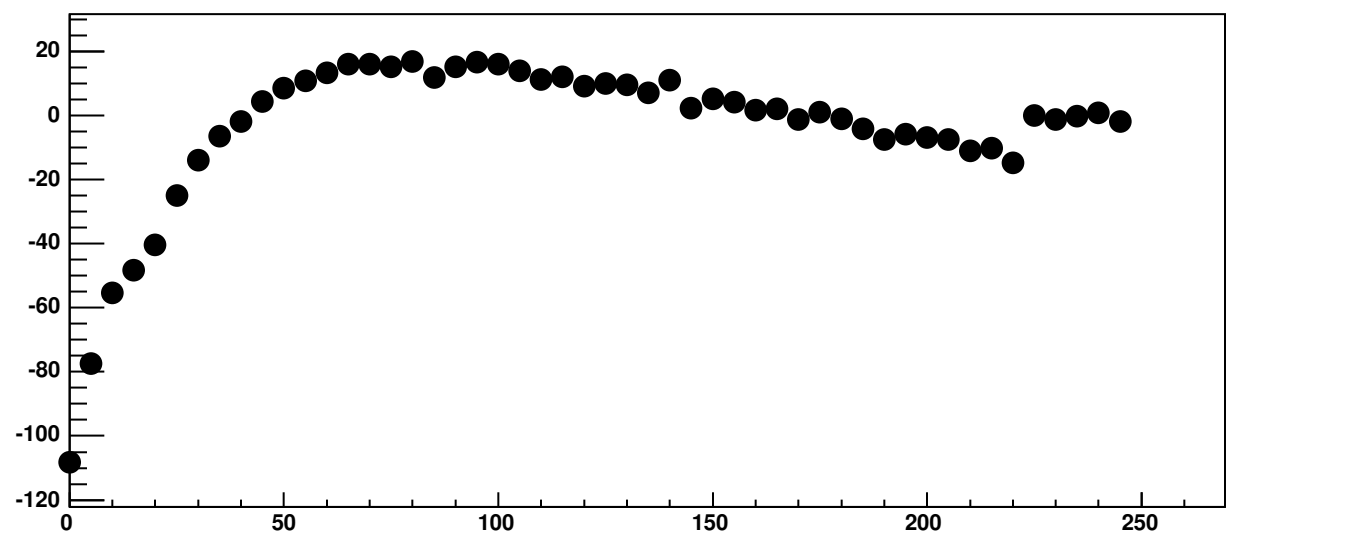
Chip 11, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold

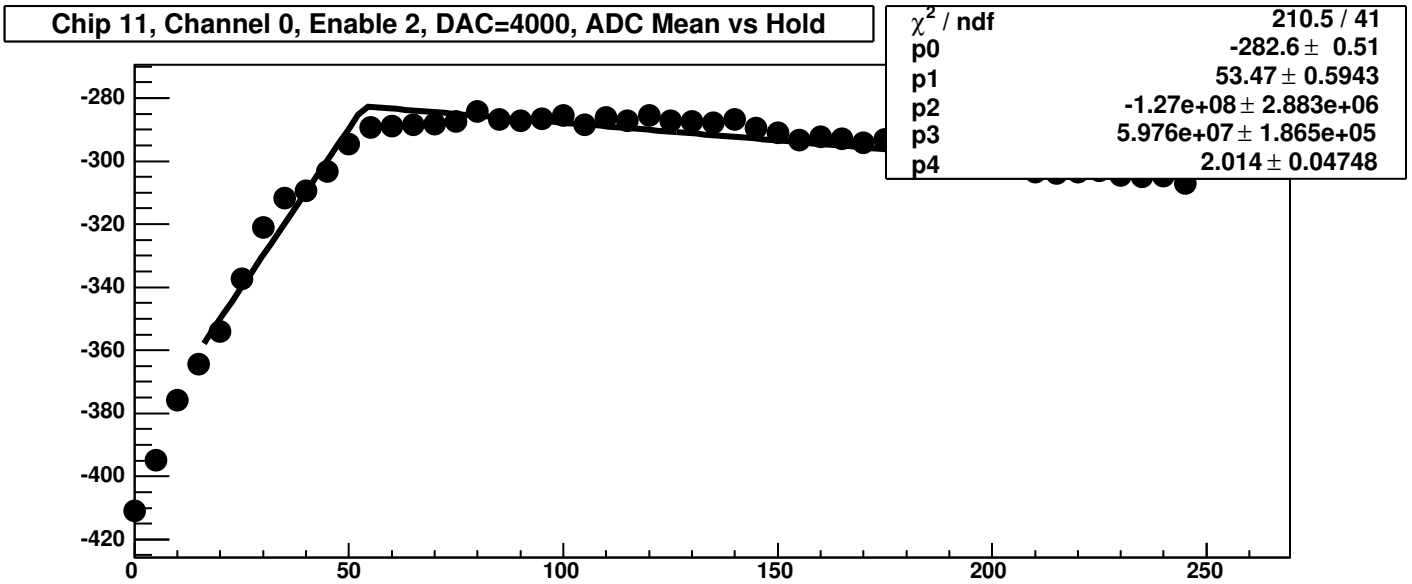


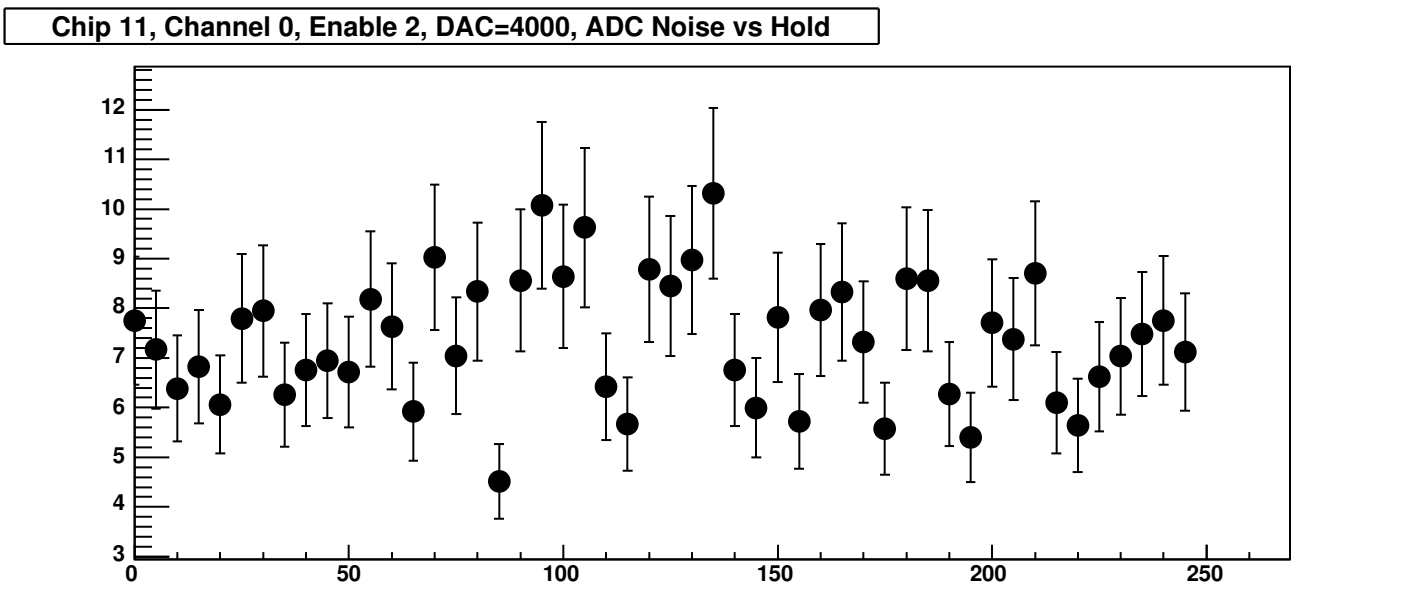
Chip 11, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold

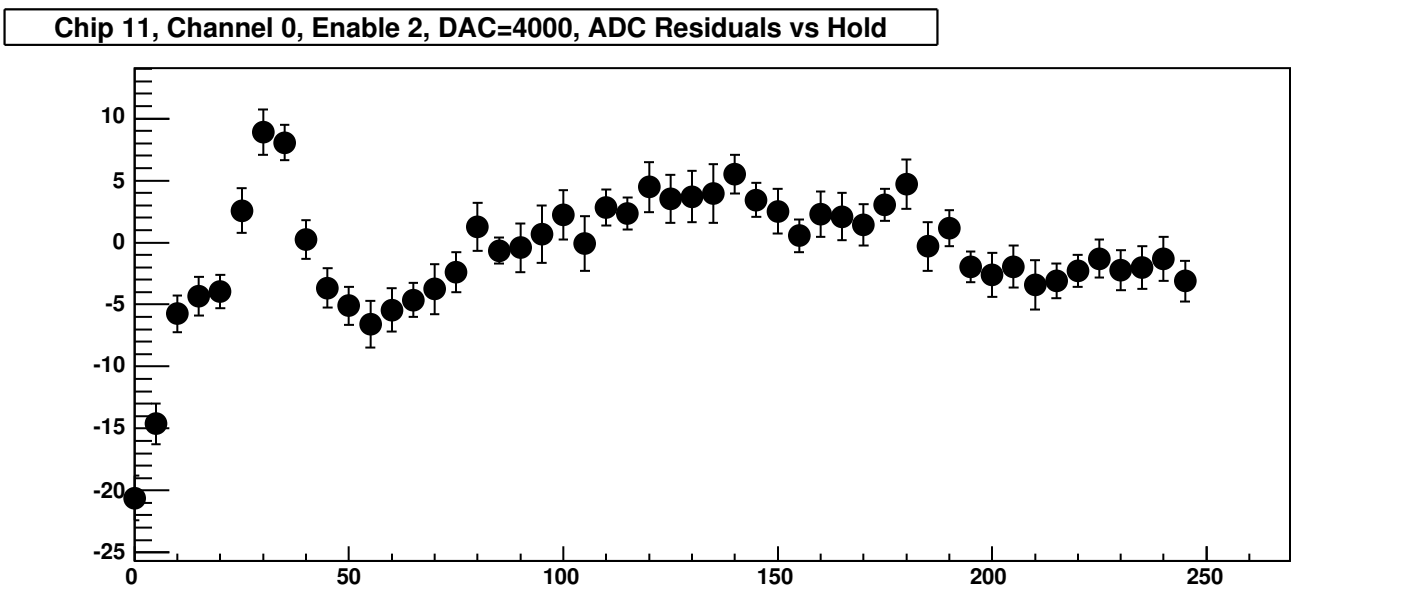


Chip 11, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold

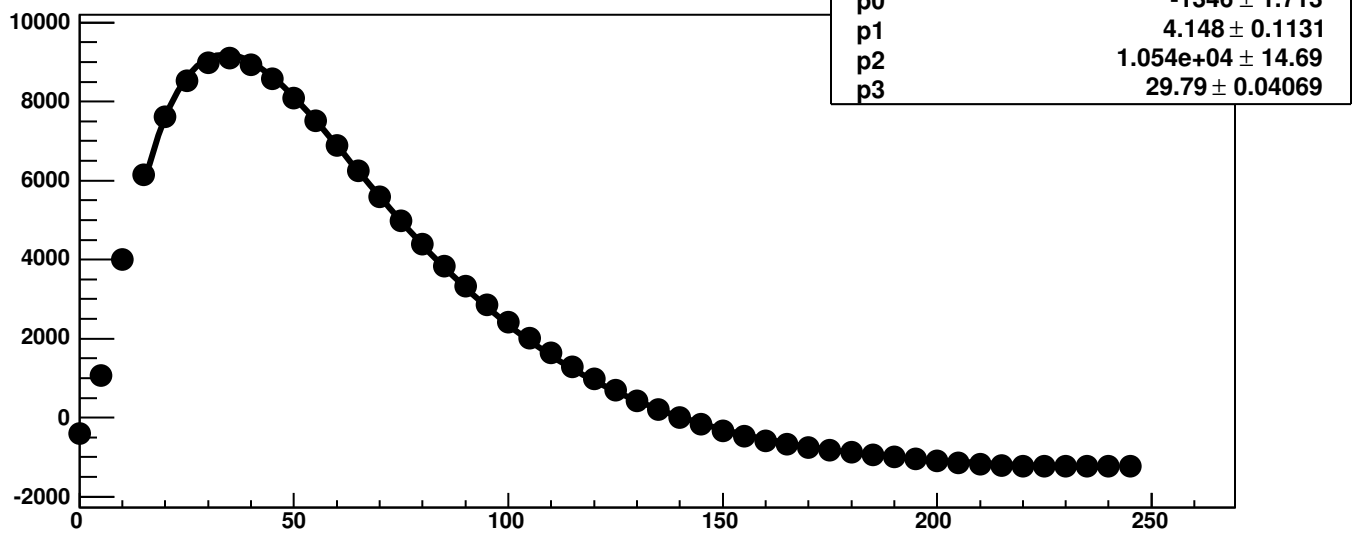



Chip 11, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 210.5 / 41
p0 -282.6 ± 0.51
p1 53.47 ± 0.5943
p2 -1.27e+08 ± 2.883e+06
p3 5.976e+07 ± 1.865e+05
p4 2.014 ± 0.04748
-280
-300
-320
-340
-360
-380
-400
-420
0
50
100
150
200
250

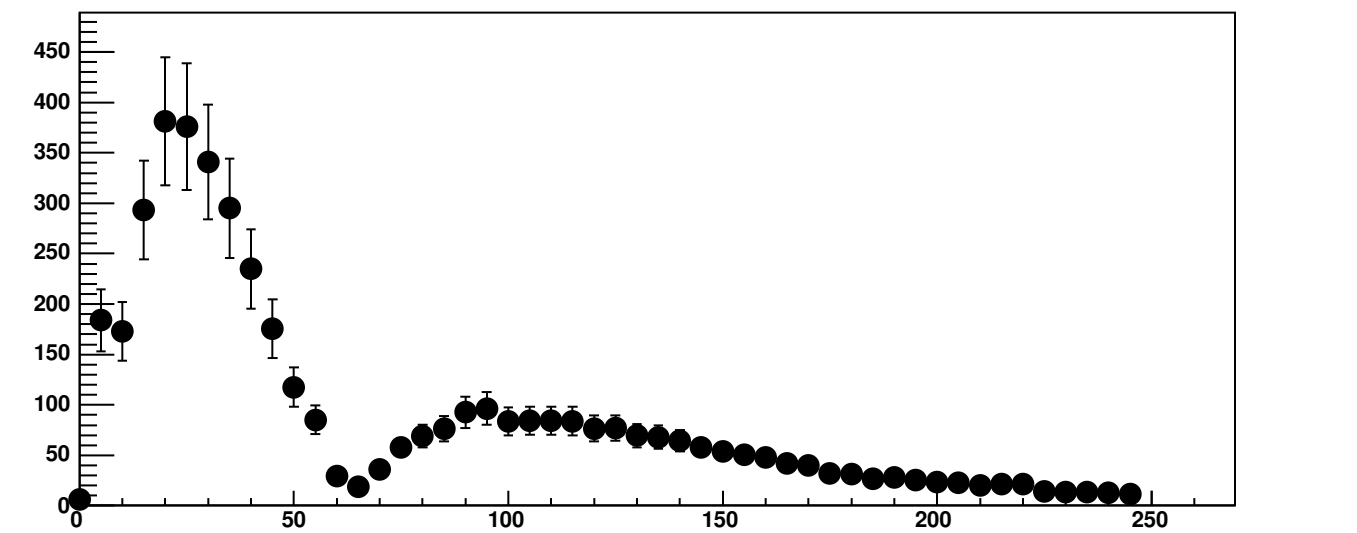

Chip 11, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 11, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
-20
-25
0
50
100
150
200
250

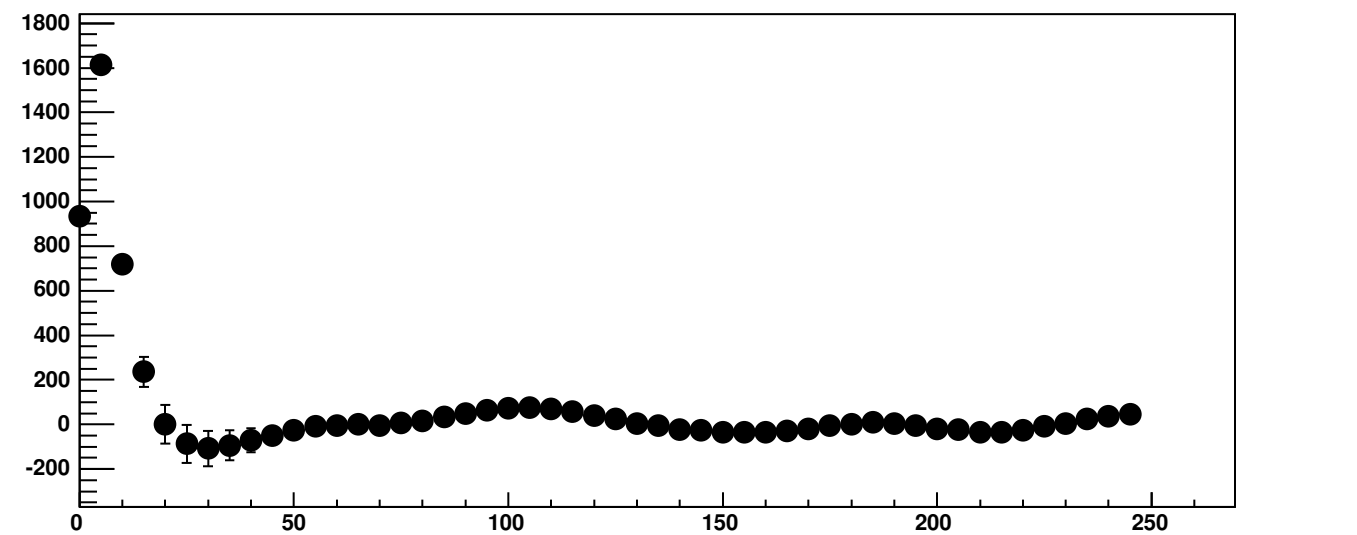
Chip 11, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold



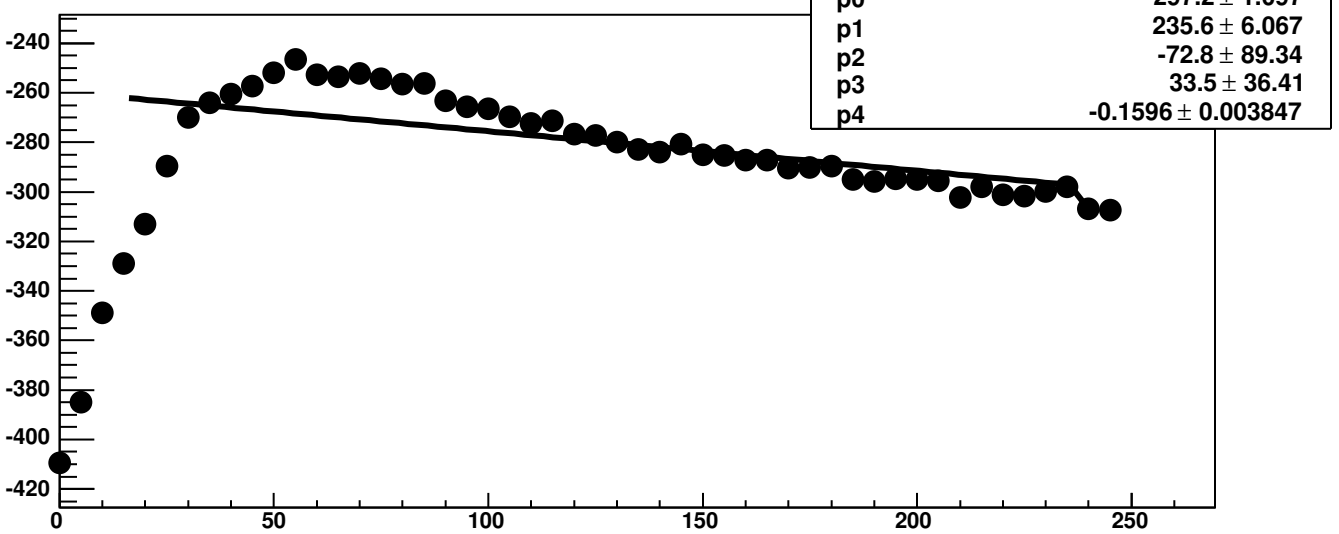
Chip 11, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold



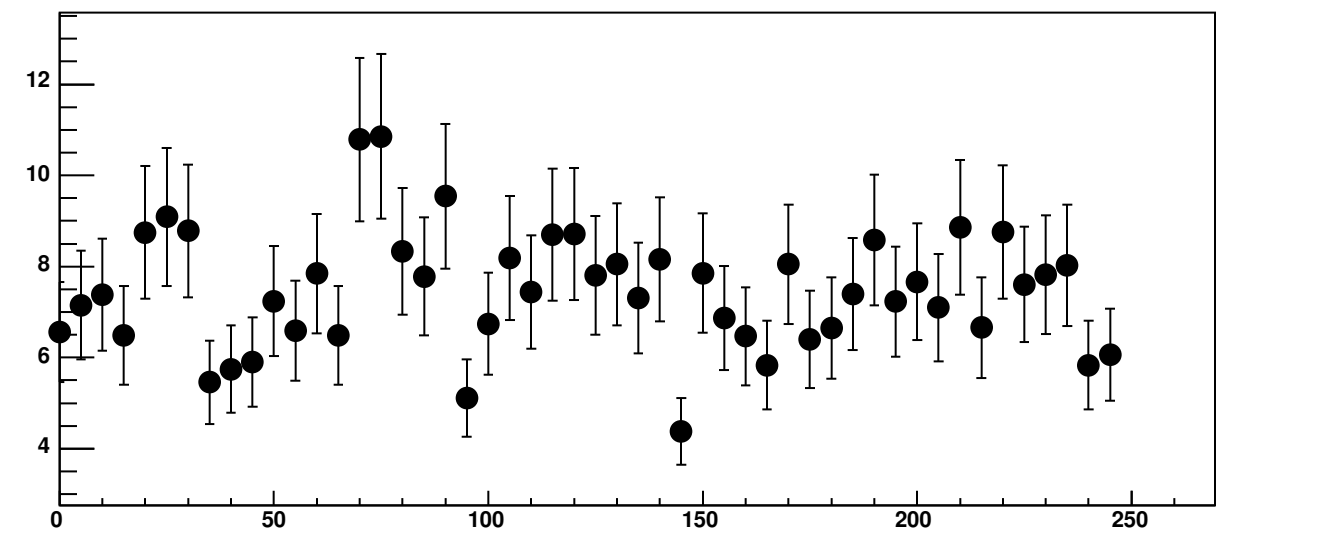
Chip 11, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold



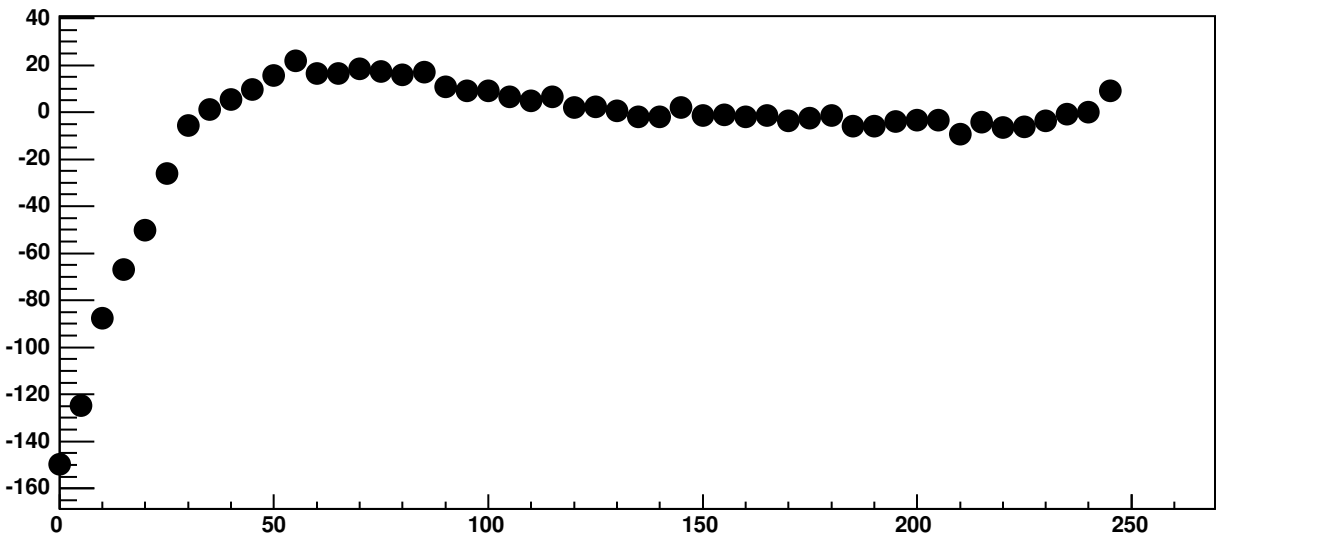
Chip 11, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

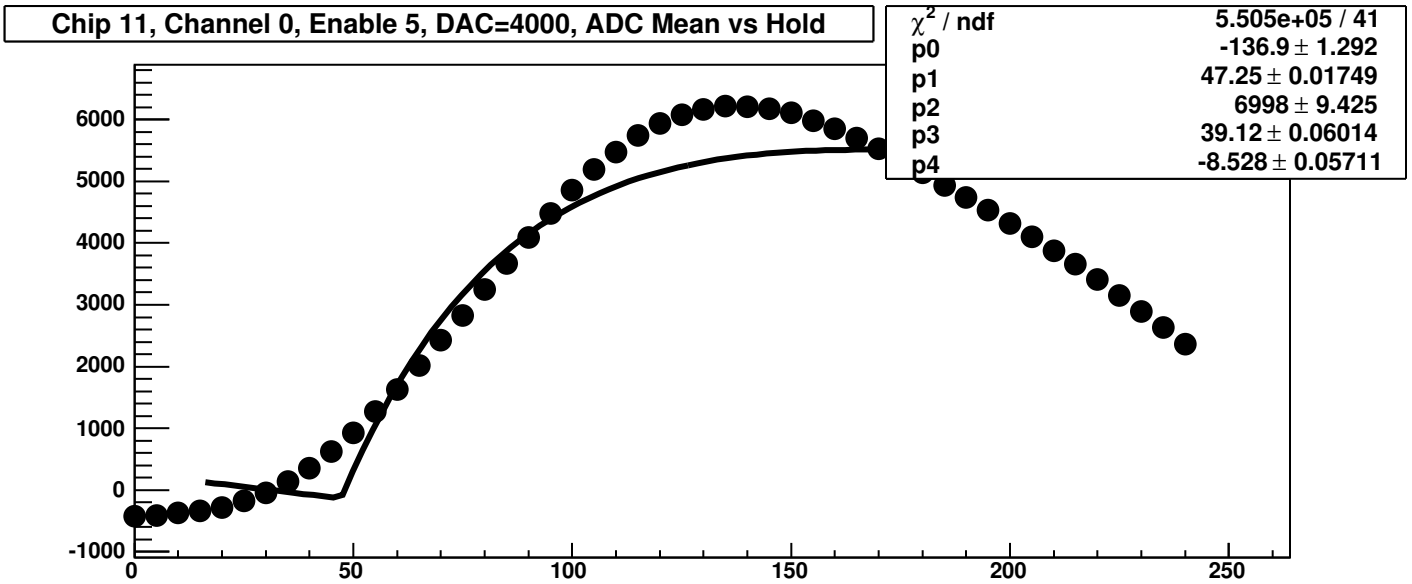


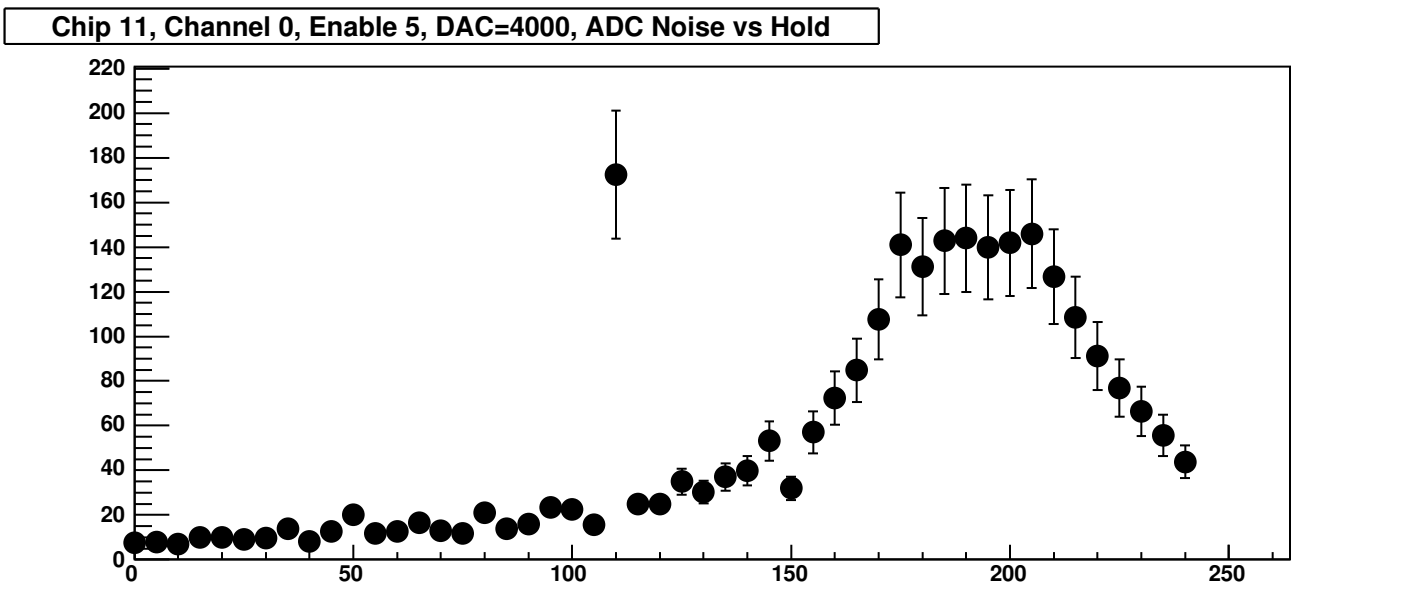
Chip 11, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold

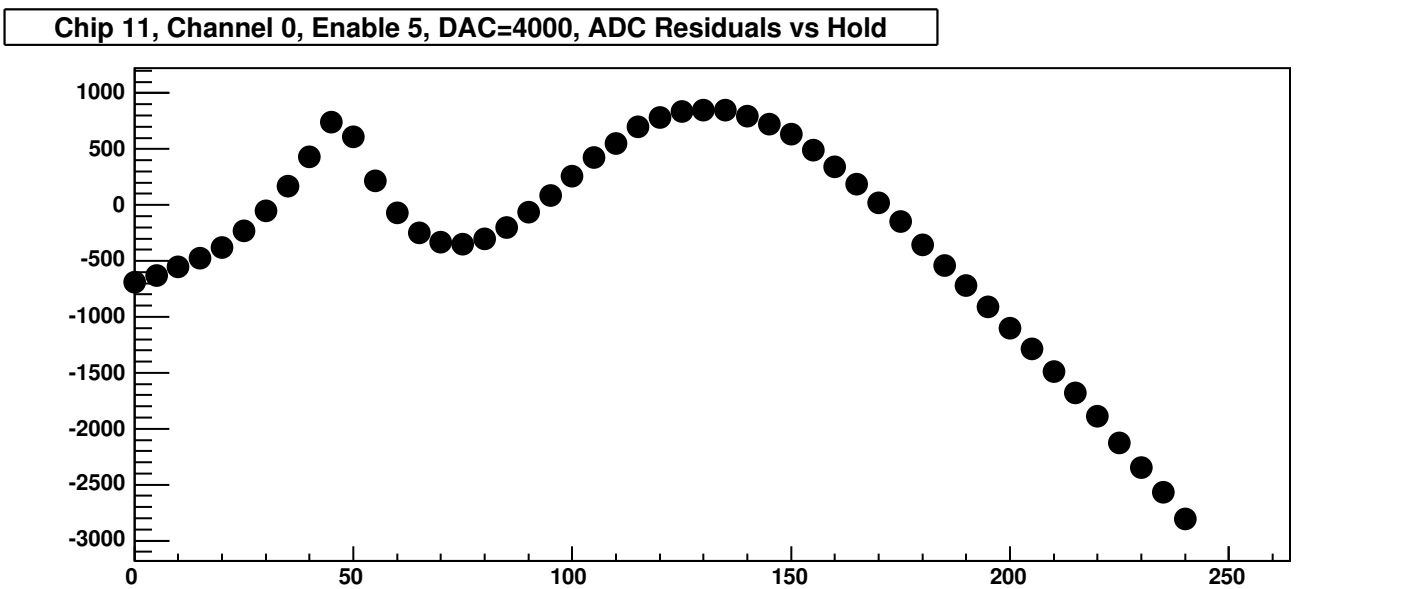


Chip 11, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold



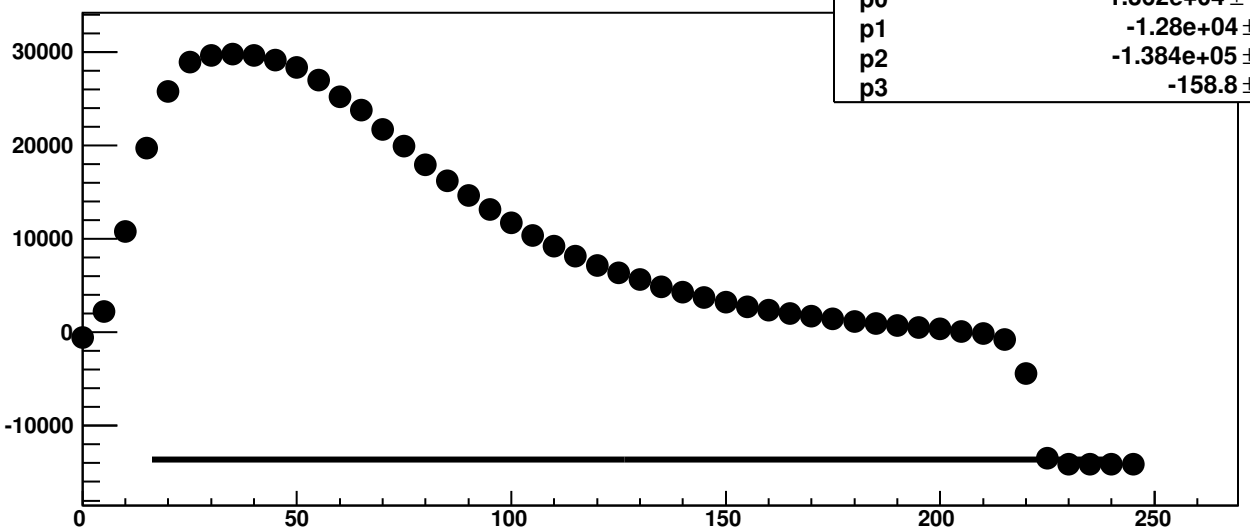

Chip 11, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.505e+05 / 41
p0 -136.9 ± 1.292
p1 47.25 ± 0.01749
p2 6998 ± 9.425
p3 39.12 ± 0.06014
p4 -8.528 ± 0.05711
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 11, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

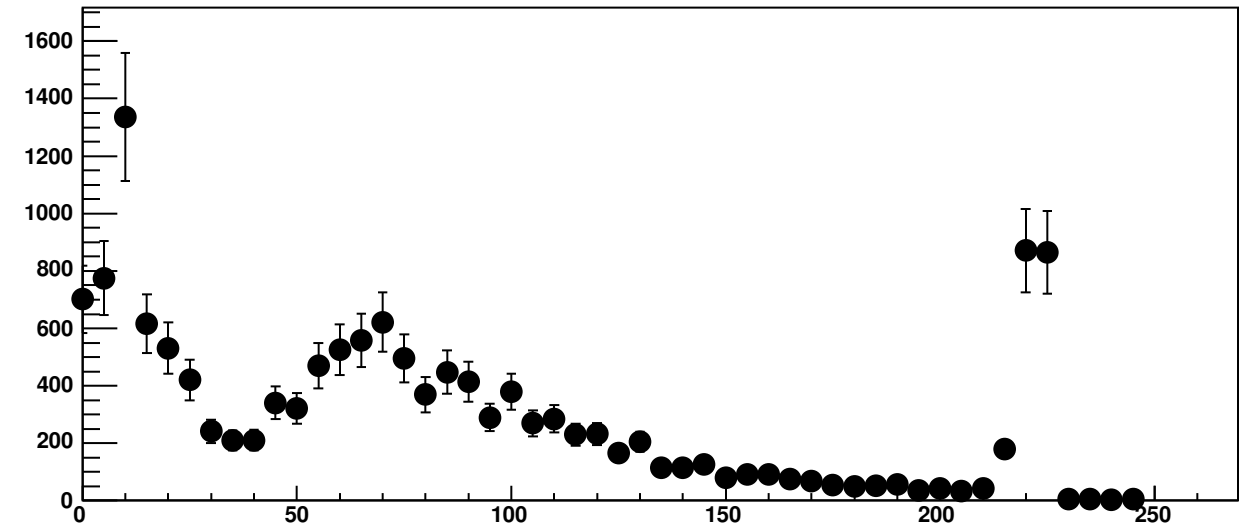

Chip 11, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250

Chip 11, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold

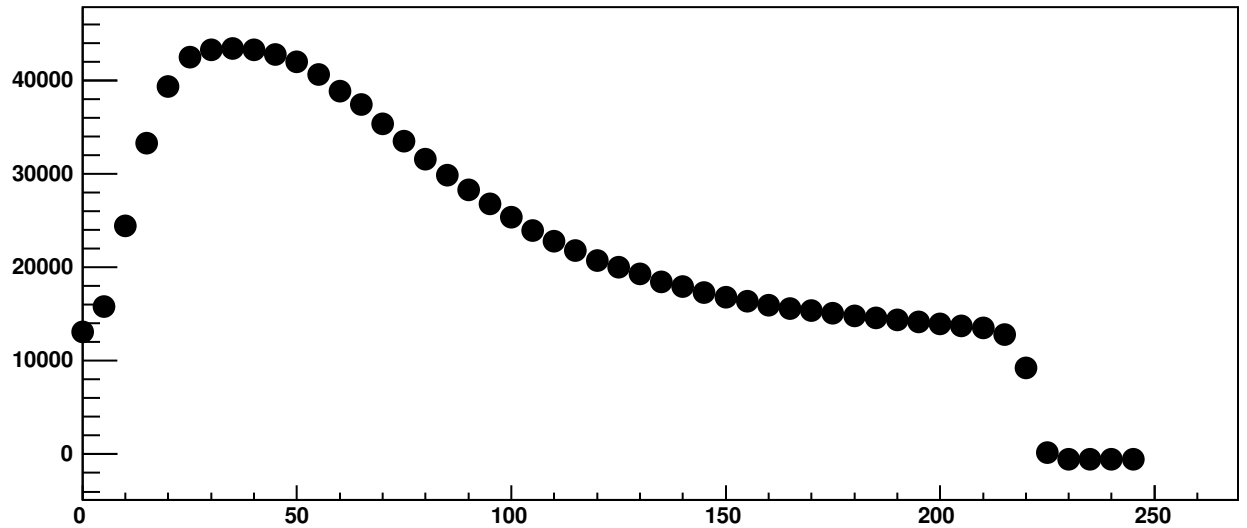
χ^2 / ndf	2.696e+07 / 42
p0	-1.362e+04 ± 0.5559
p1	-1.28e+04 ± 1.909
p2	-1.384e+05 ± 1.414
p3	-158.8 ± 1.414



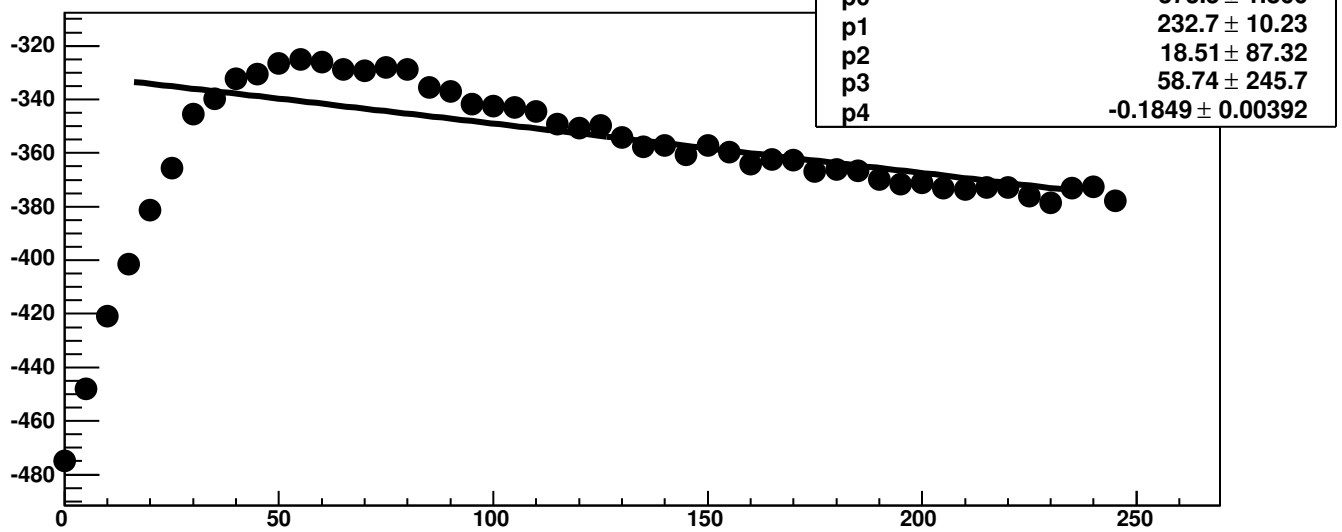
Chip 11, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold



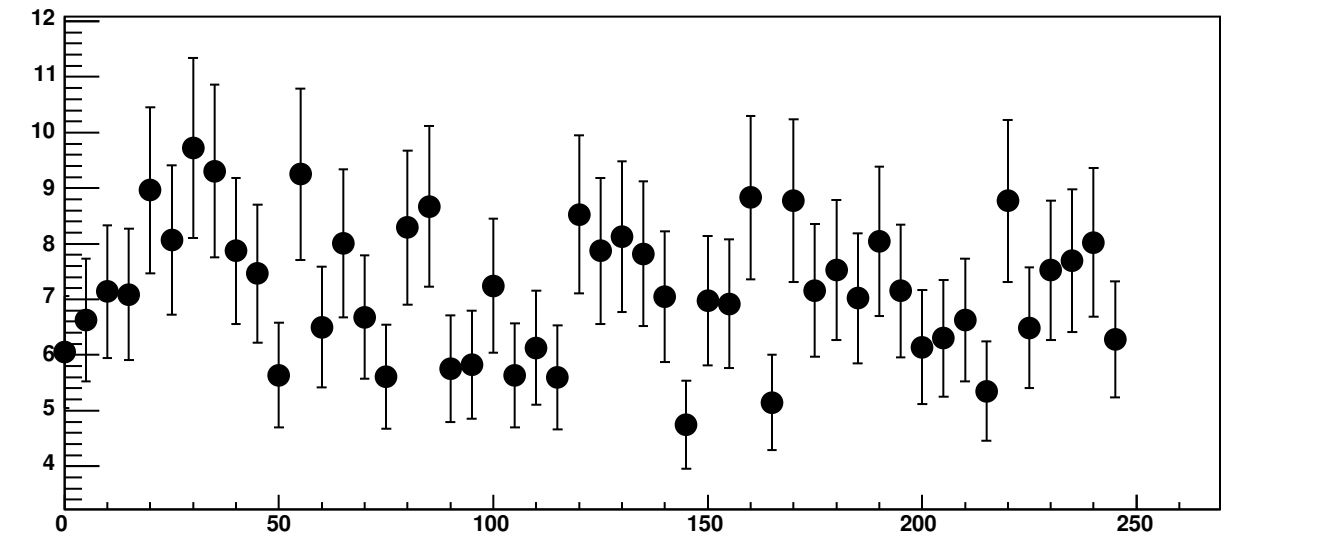
Chip 11, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold



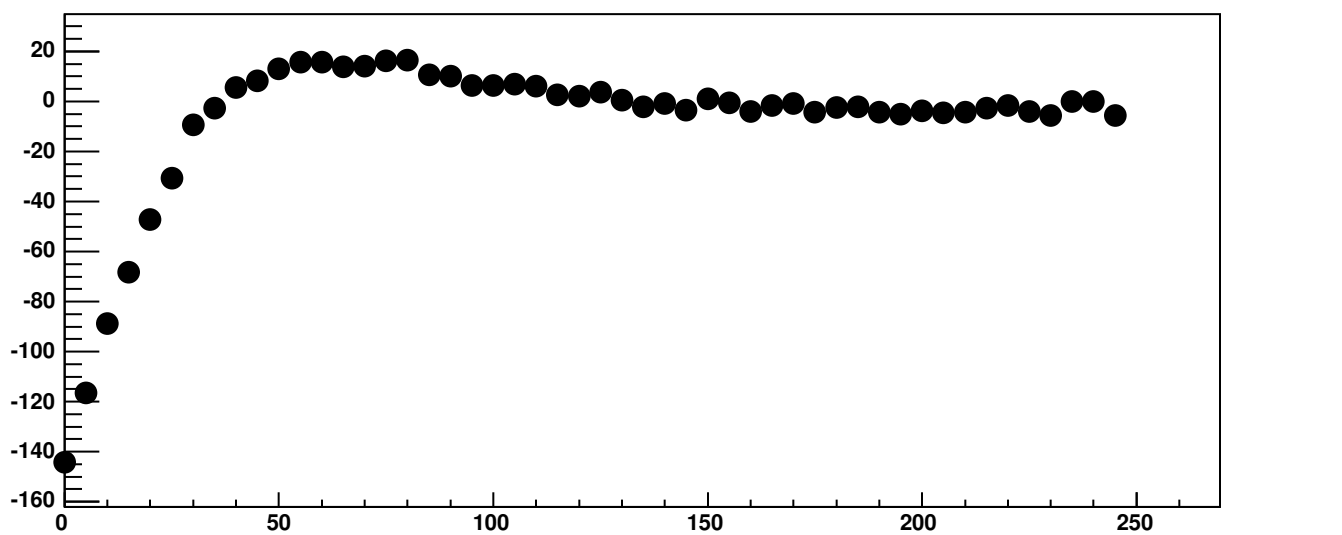
Chip 11, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold



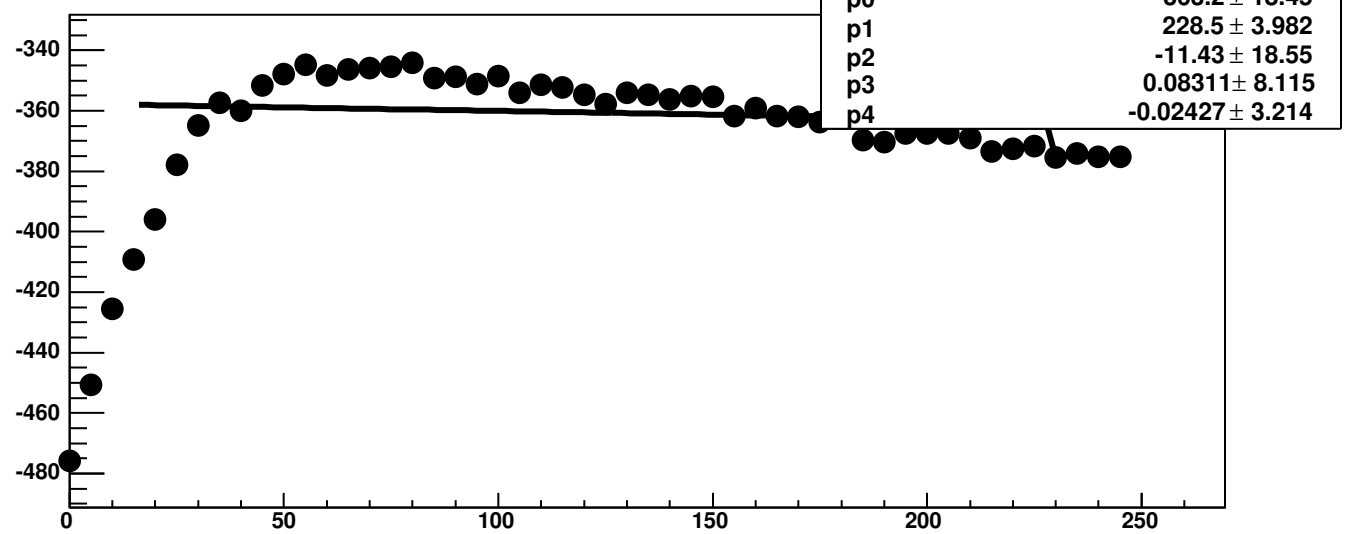
Chip 11, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



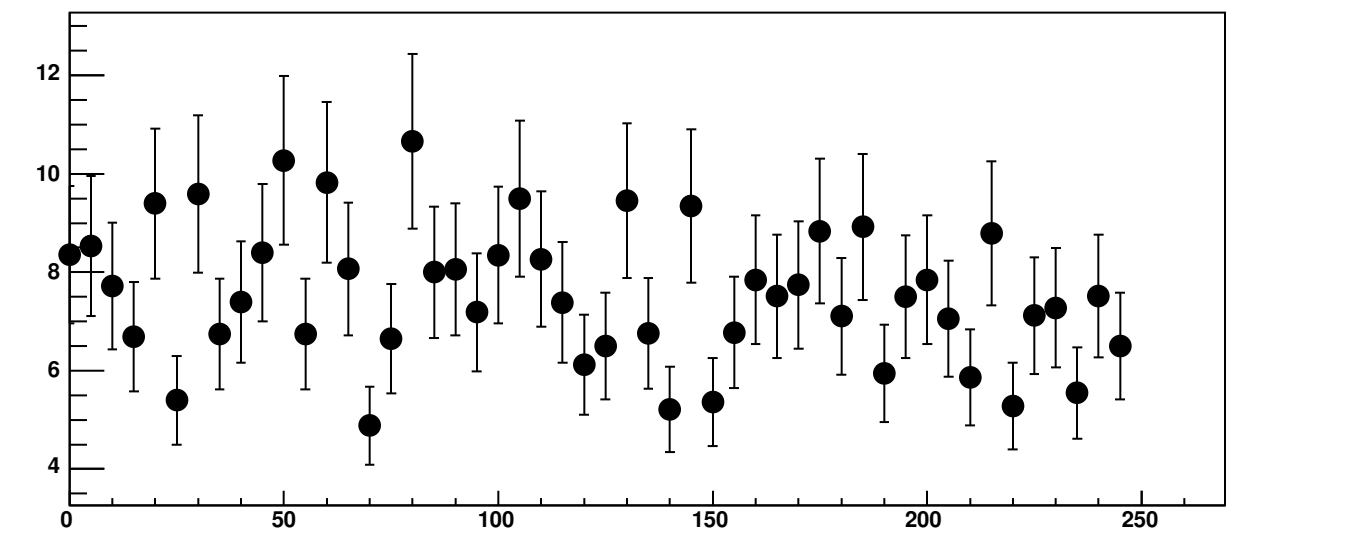
Chip 11, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold



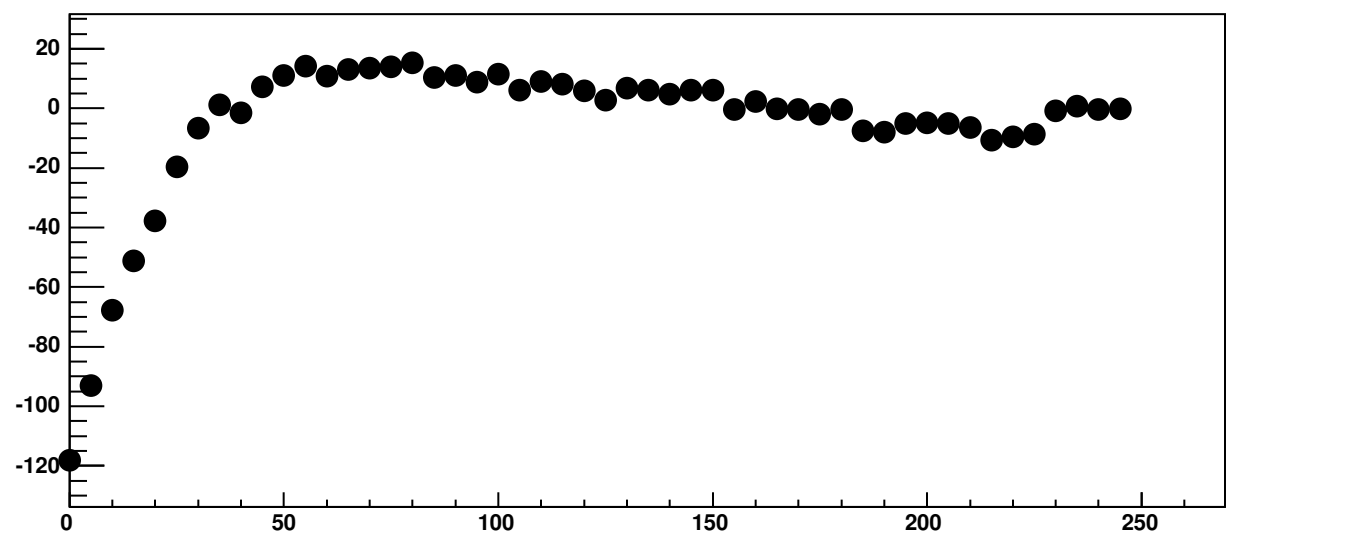
Chip 11, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

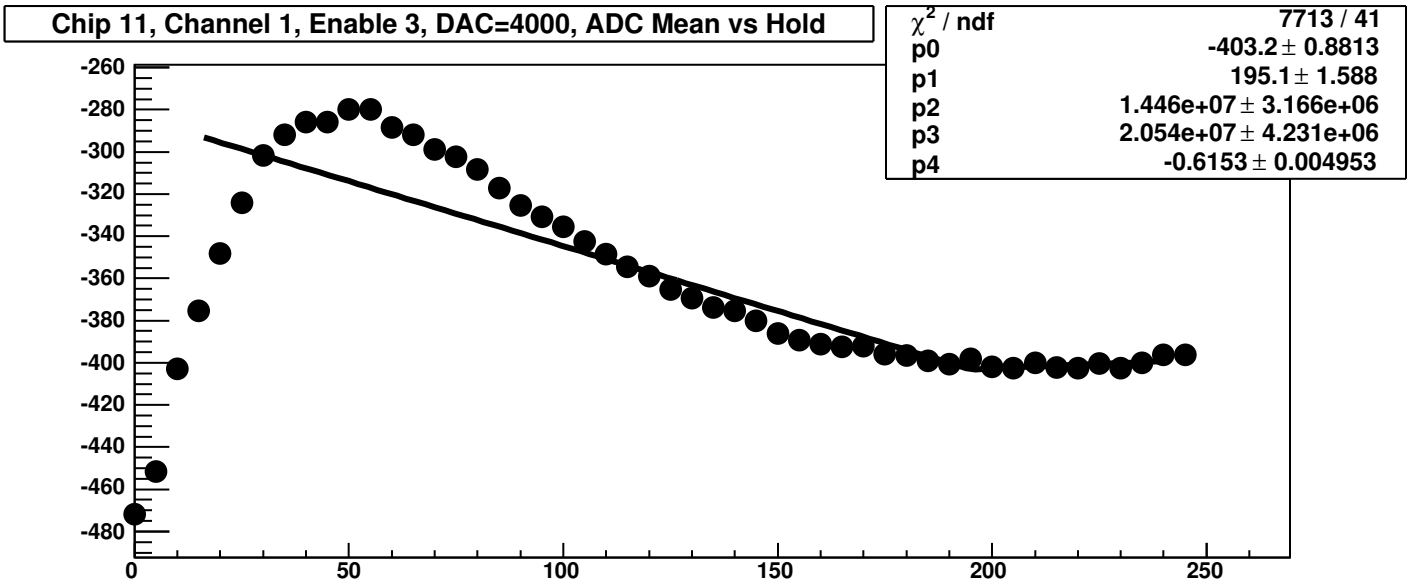


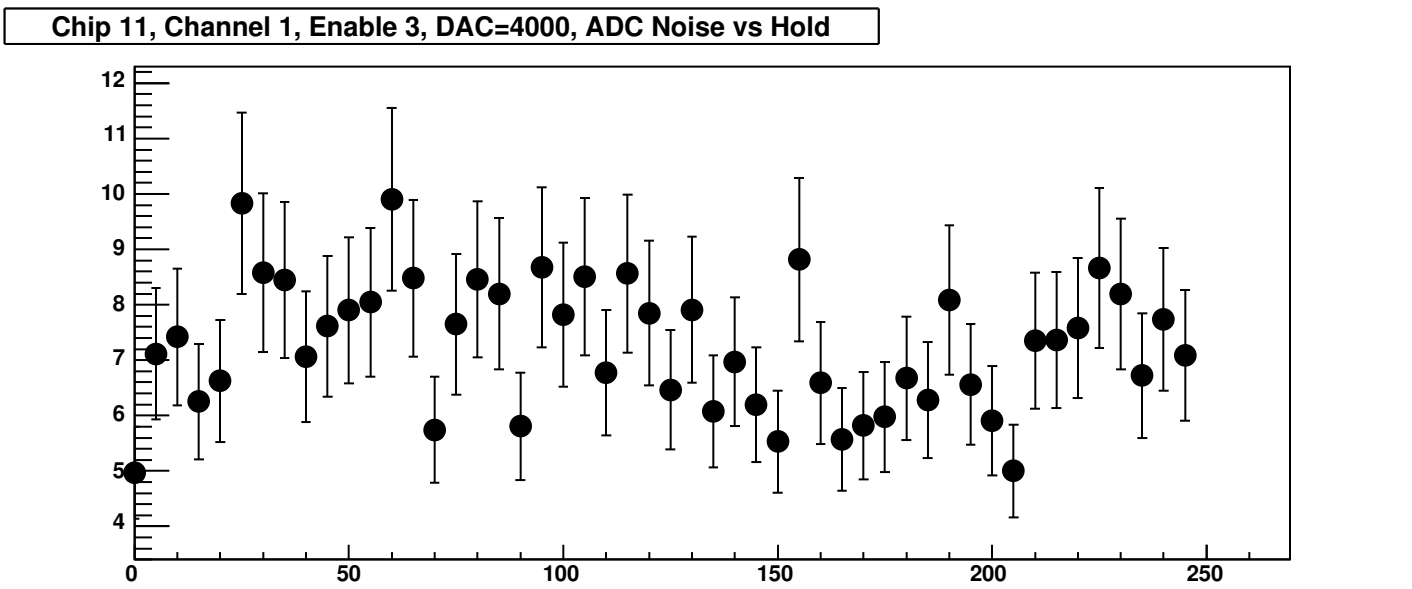
Chip 11, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold

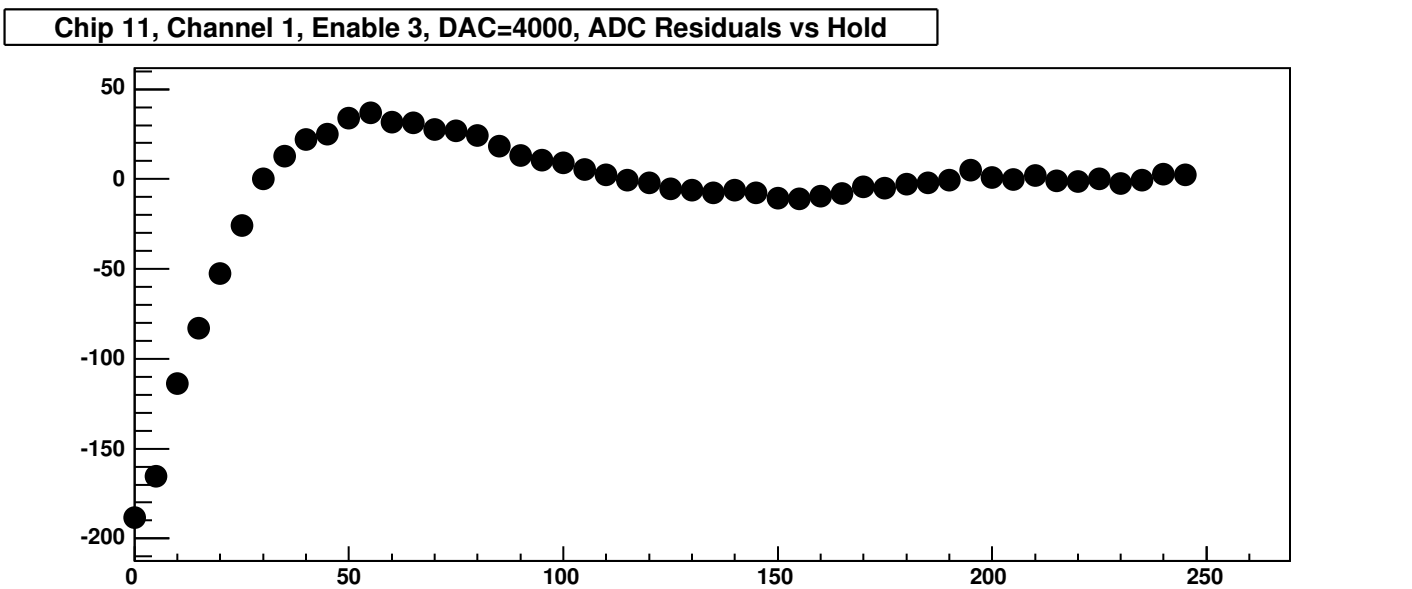


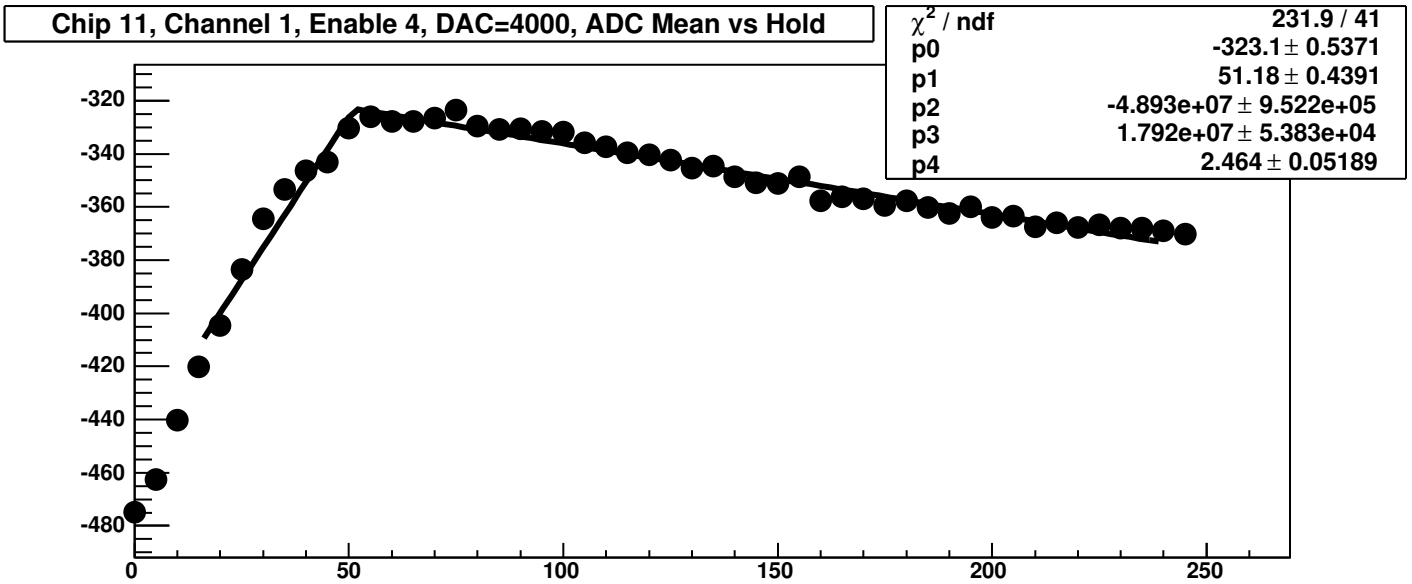
Chip 11, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

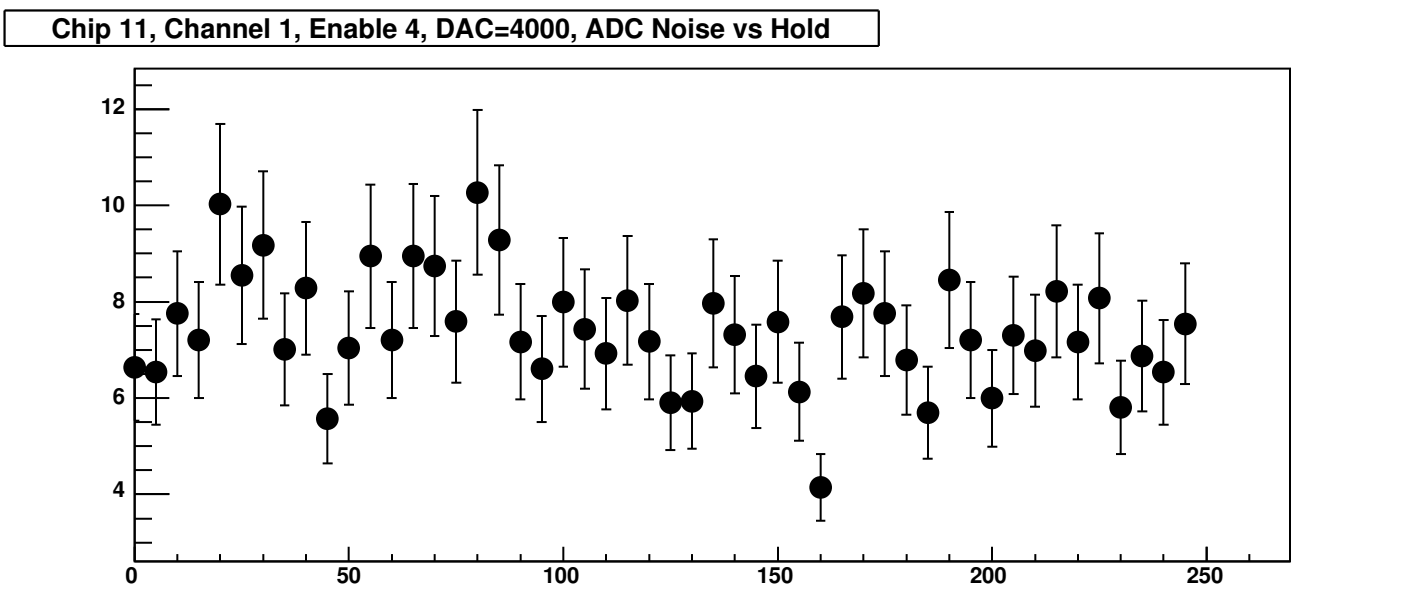


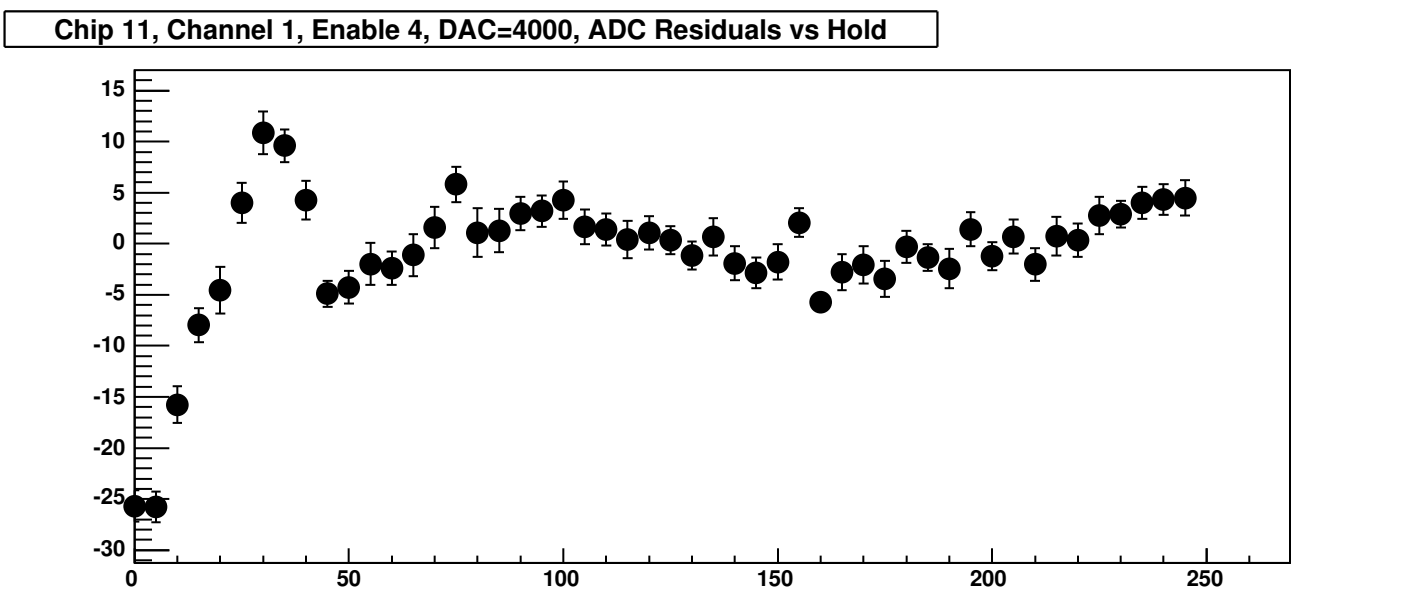

Chip 11, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 7713 / 41
p0 -403.2 ± 0.8813
p1 195.1 ± 1.588
p2 1.446e+07 ± 3.166e+06
p3 2.054e+07 ± 4.231e+06
p4 -0.6153 ± 0.004953
-260
-280
-300
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
50
100
150
200
250


Chip 11, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

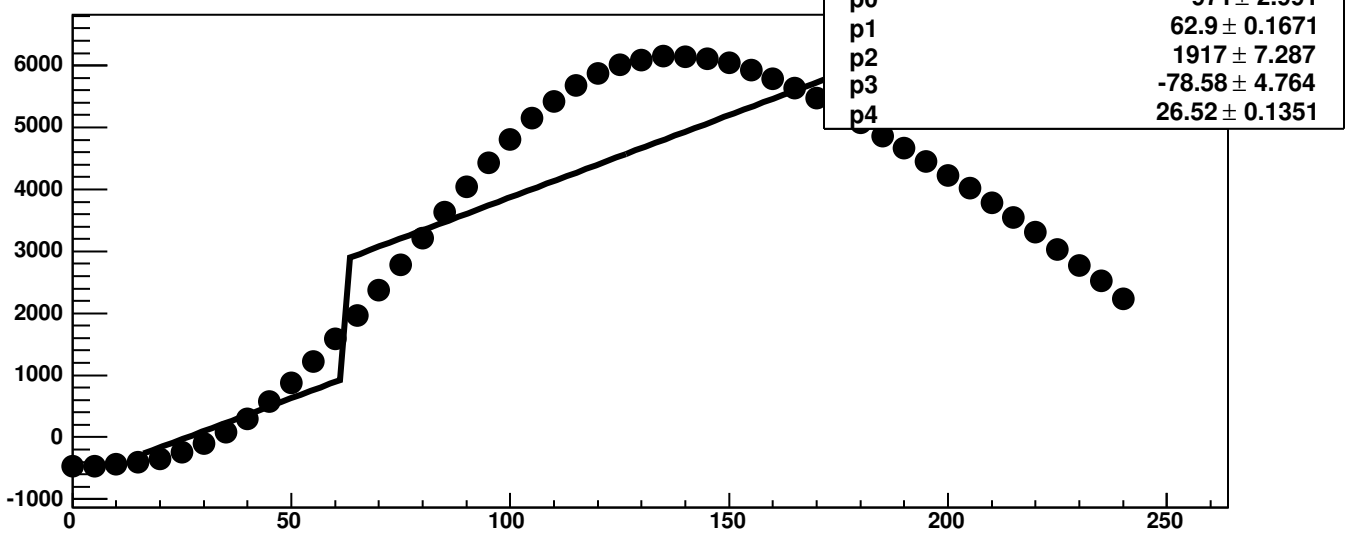

Chip 11, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold
50
0
-50
-100
-150
-200
0
50
100
150
200
250


Chip 11, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 231.9 / 41
p0 -323.1 ± 0.5371
p1 51.18 ± 0.4391
p2 -4.893e+07 ± 9.522e+05
p3 1.792e+07 ± 5.383e+04
p4 2.464 ± 0.05189
-320
-340
-360
-380
-400
-420
-440
-460
-480
0
50
100
150
200
250

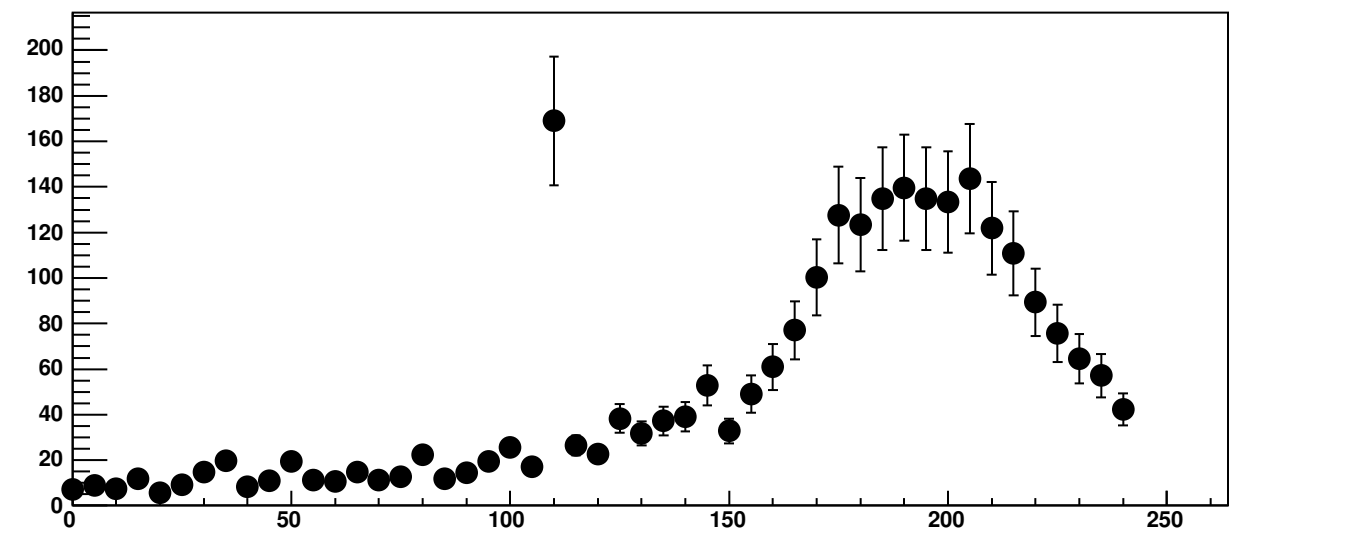

Chip 11, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 11, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
-15
-20
-25
-30
0
50
100
150
200
250

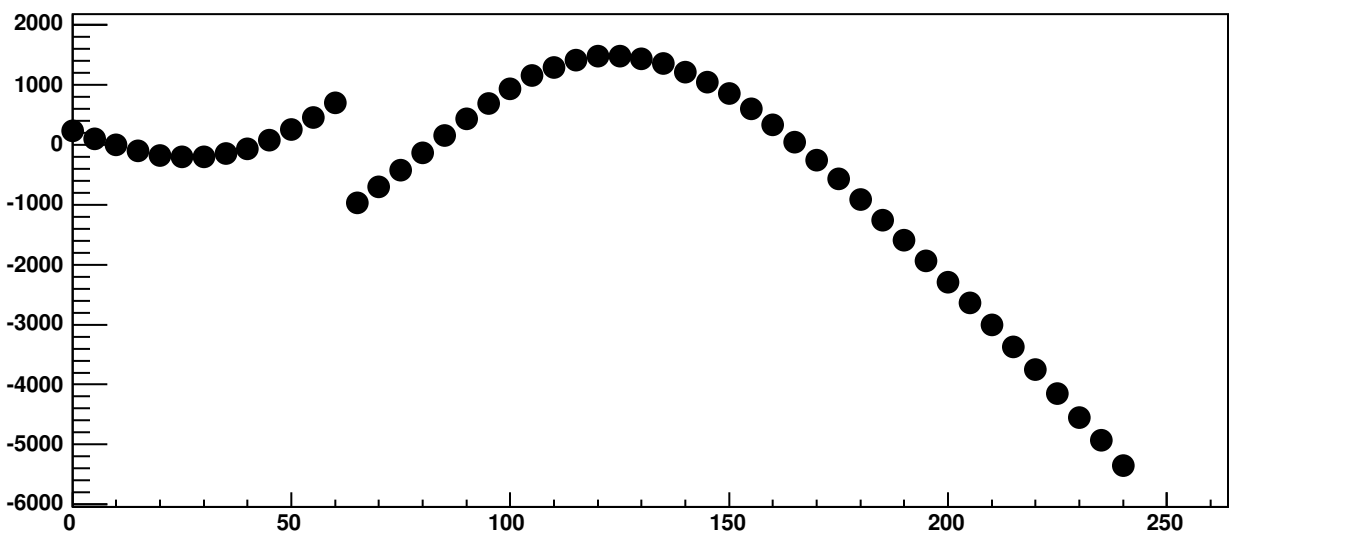
Chip 11, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

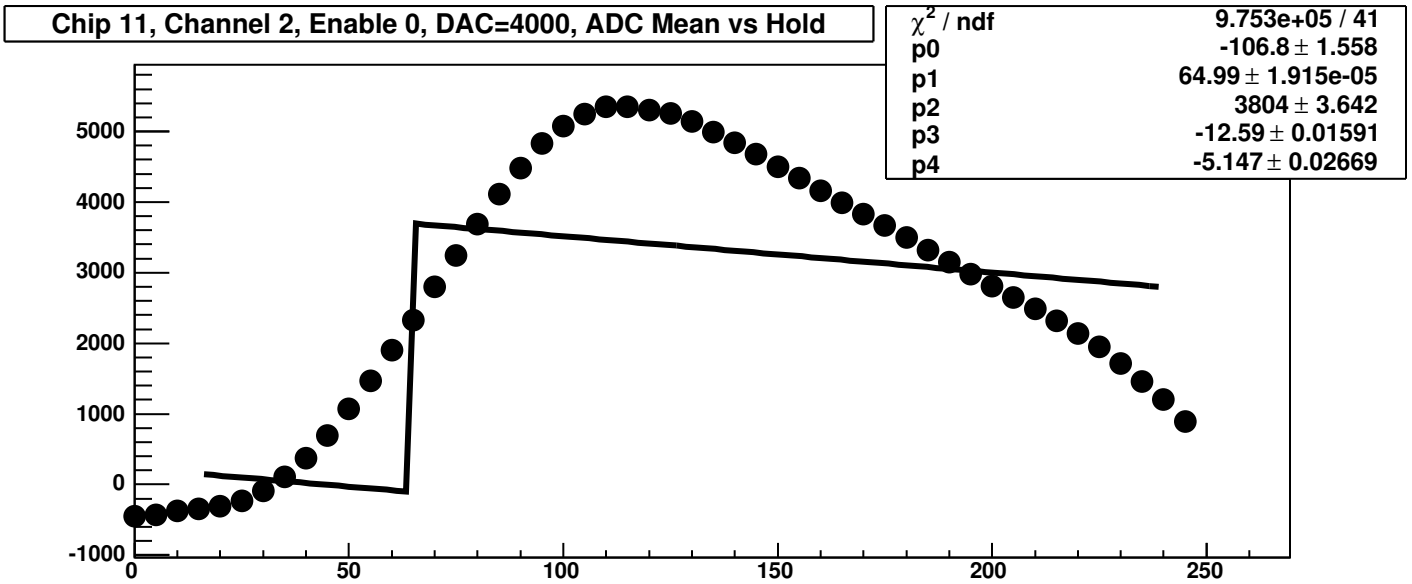


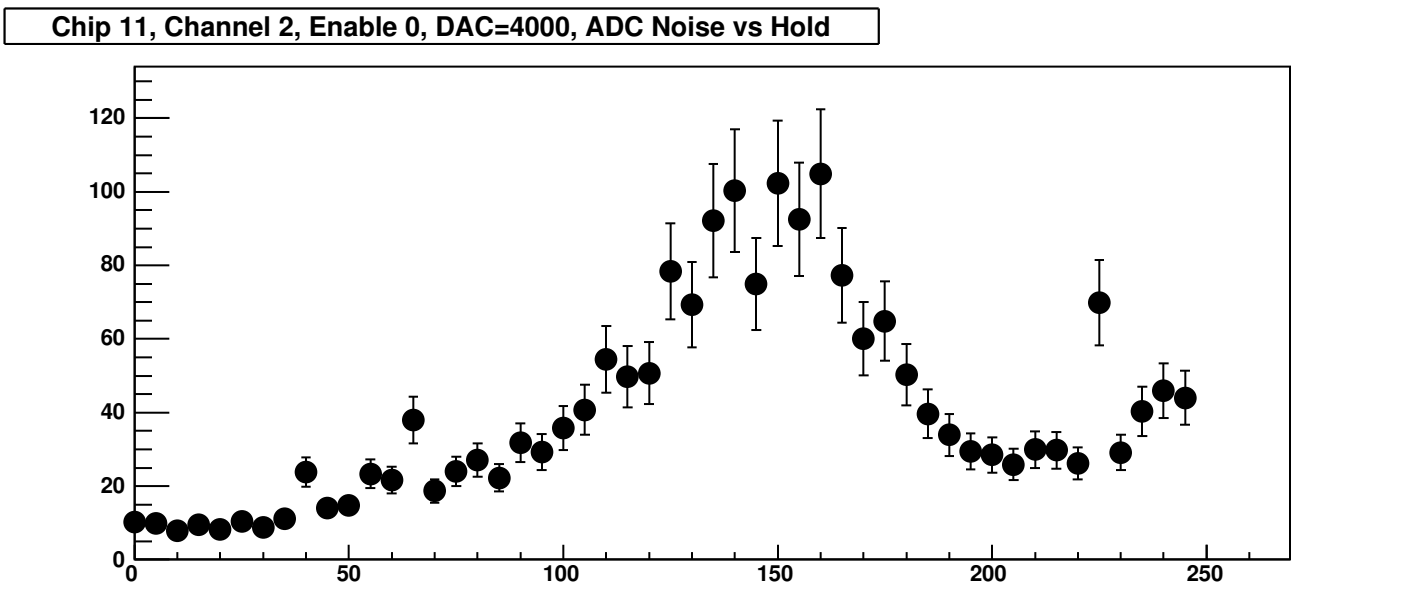
Chip 11, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold

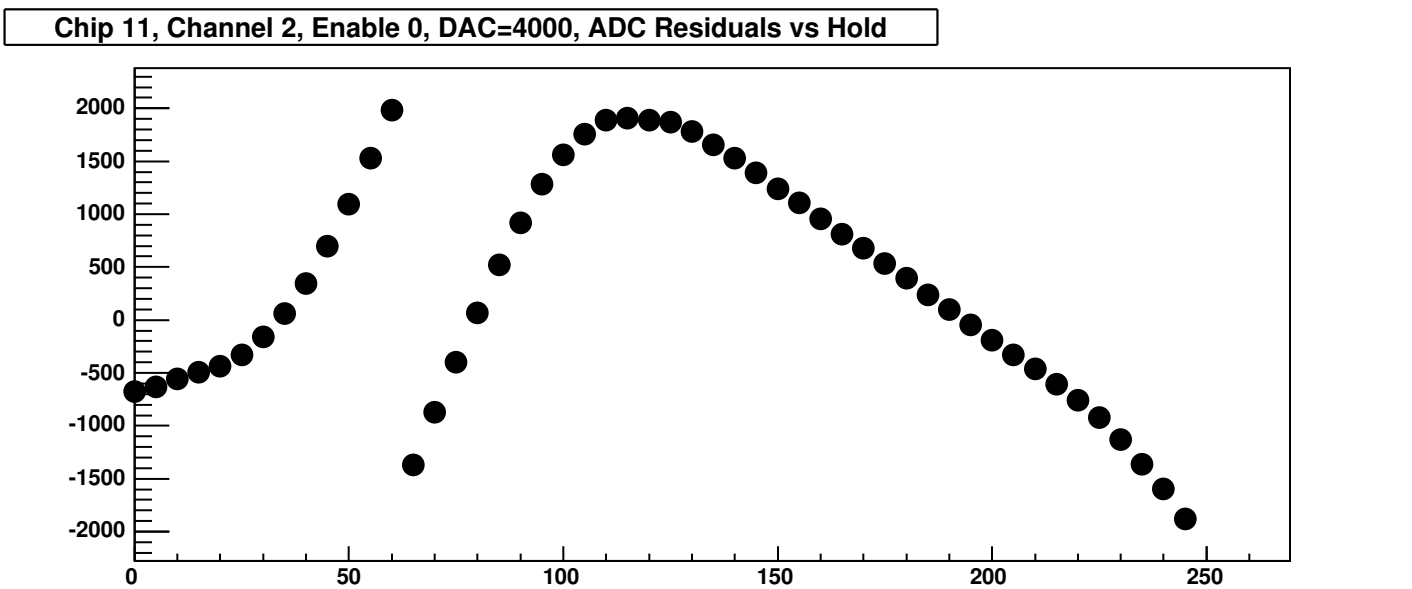


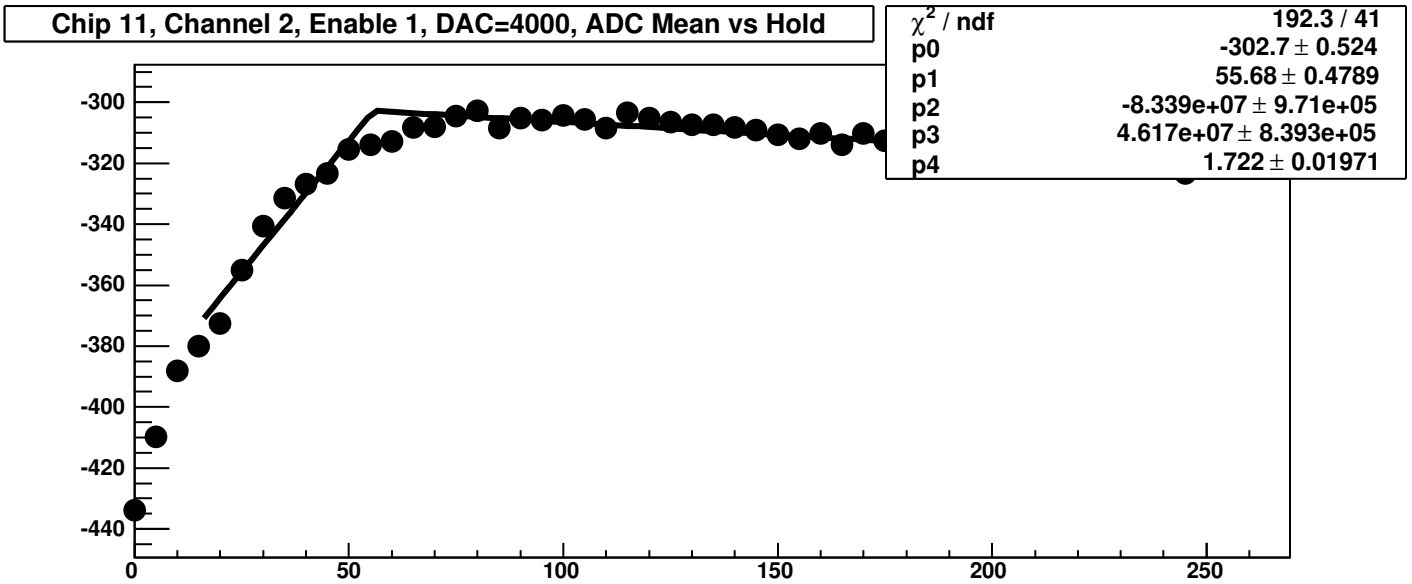
Chip 11, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold

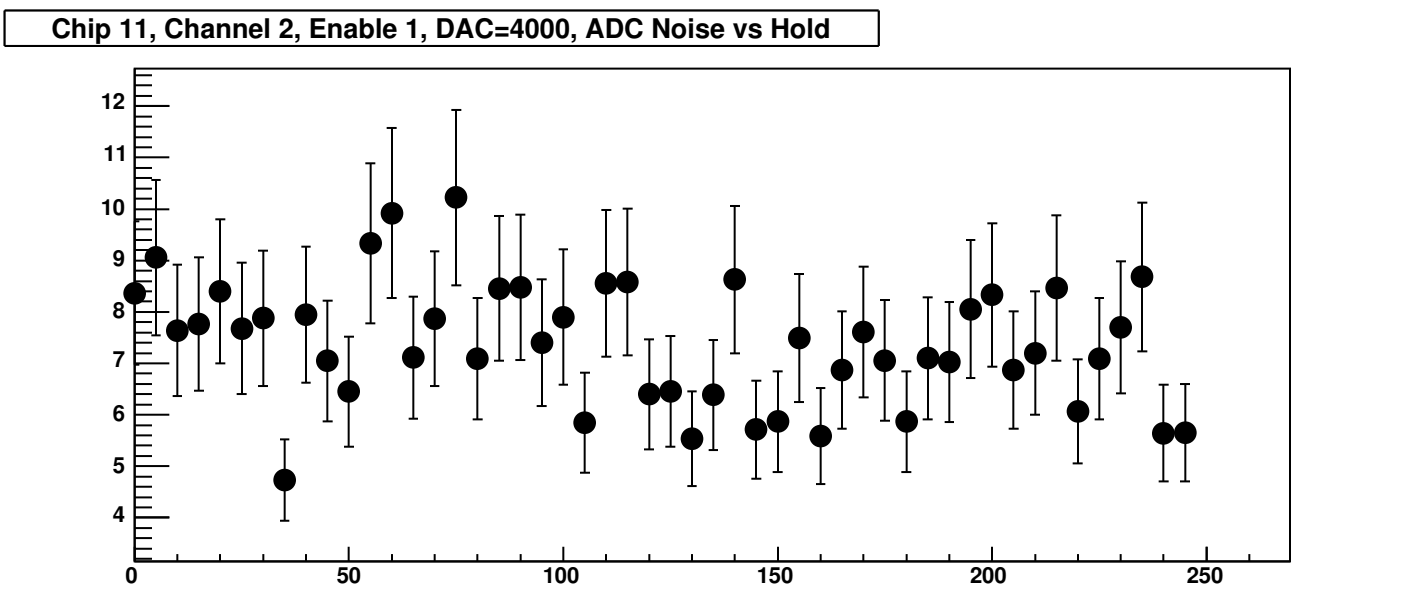


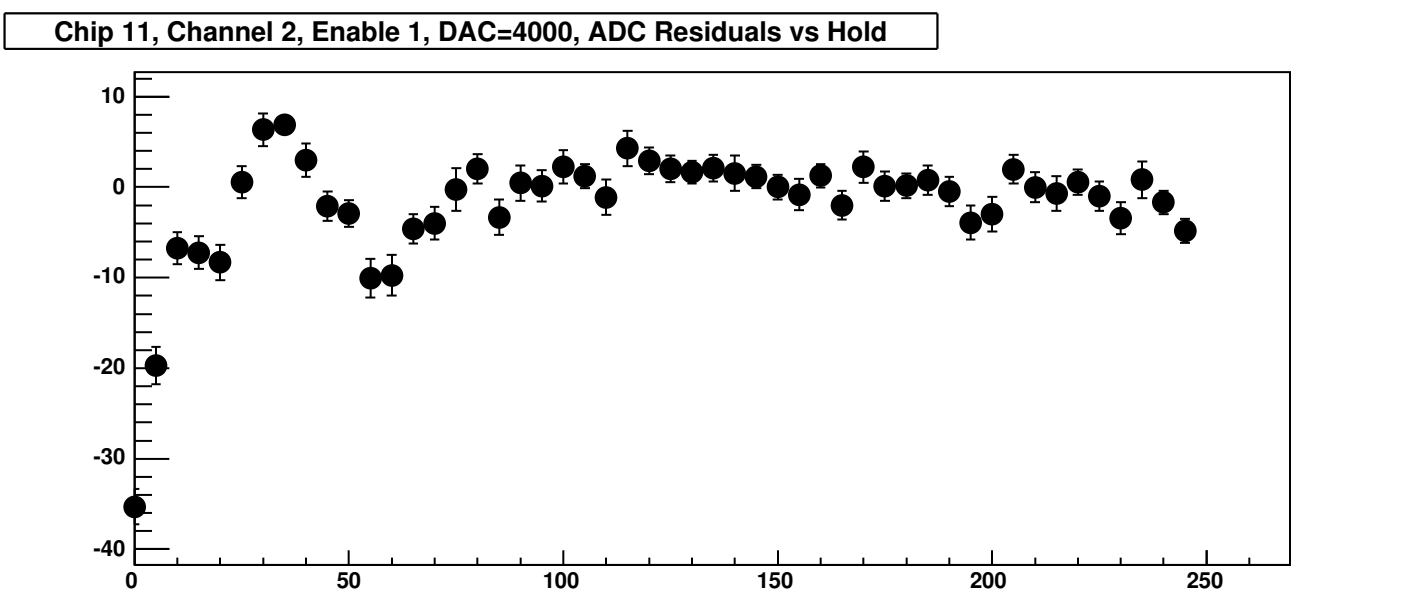

Chip 11, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 9.753e+05 / 41
p0 -106.8 ± 1.558
p1 64.99 ± 1.915e-05
p2 3804 ± 3.642
p3 -12.59 ± 0.01591
p4 -5.147 ± 0.02669
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 11, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250

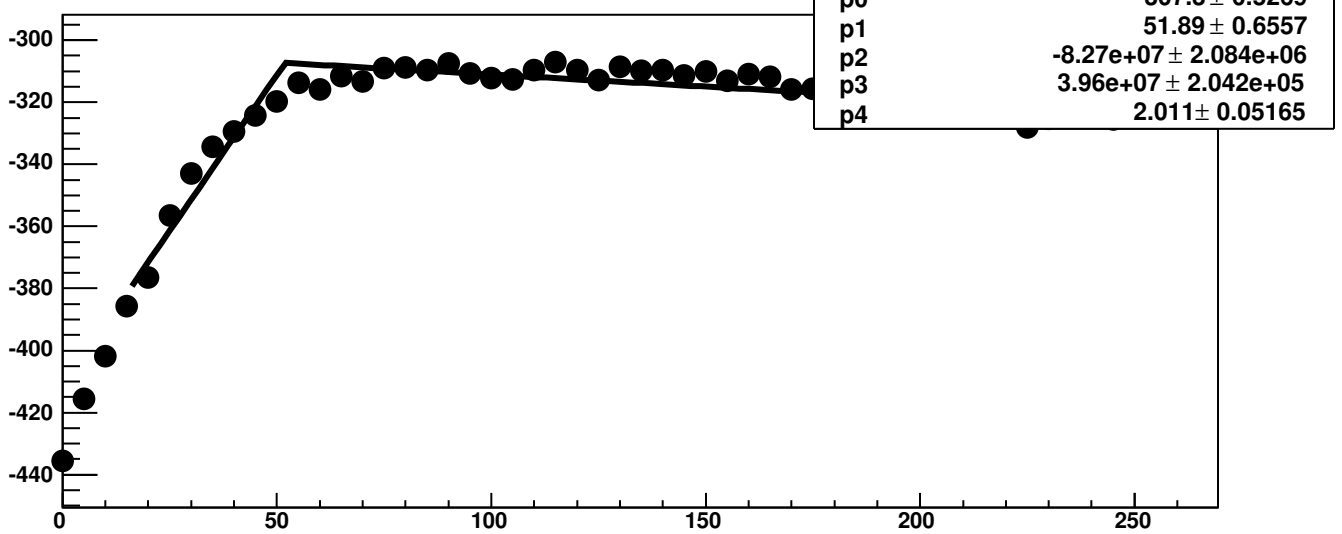

Chip 11, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250


Chip 11, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 192.3 / 41
p0 -302.7 ± 0.524
p1 55.68 ± 0.4789
p2 -8.339e+07 ± 9.71e+05
p3 4.617e+07 ± 8.393e+05
p4 1.722 ± 0.01971
-300
-320
-340
-360
-380
-400
-420
-440
0
50
100
150
200
250

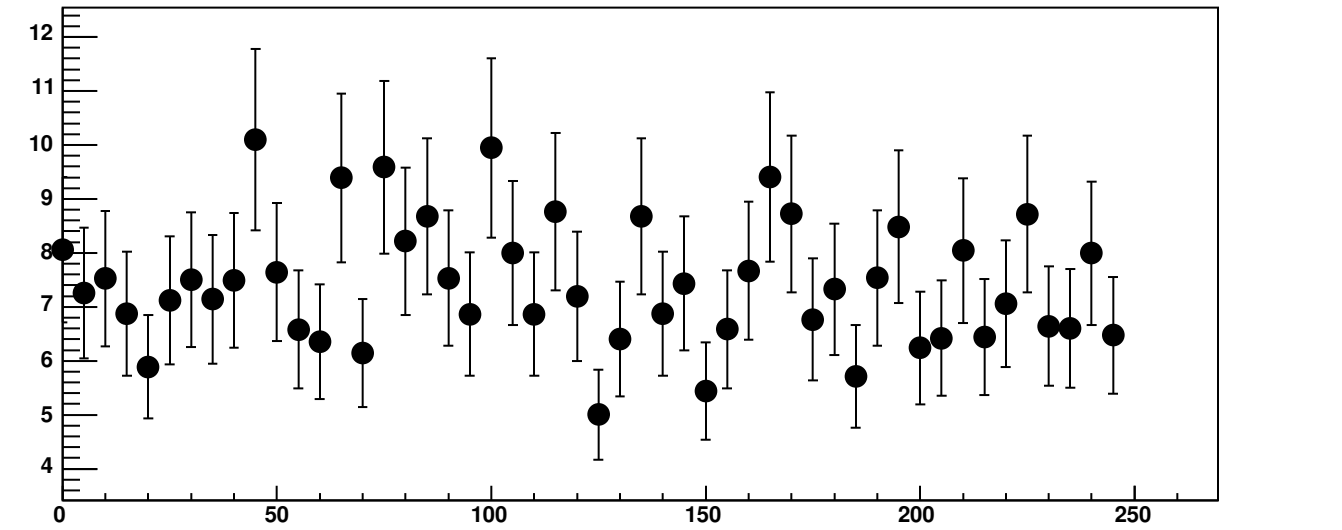

Chip 11, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 11, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold
10
0
-10
-20
-30
-40
0
50
100
150
200
250

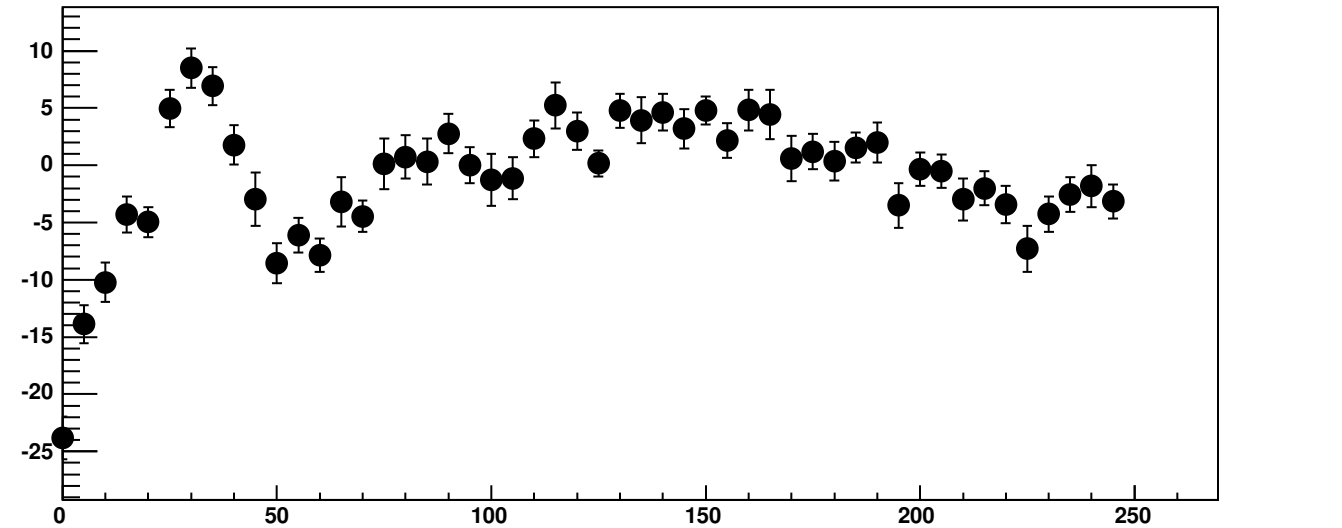
Chip 11, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

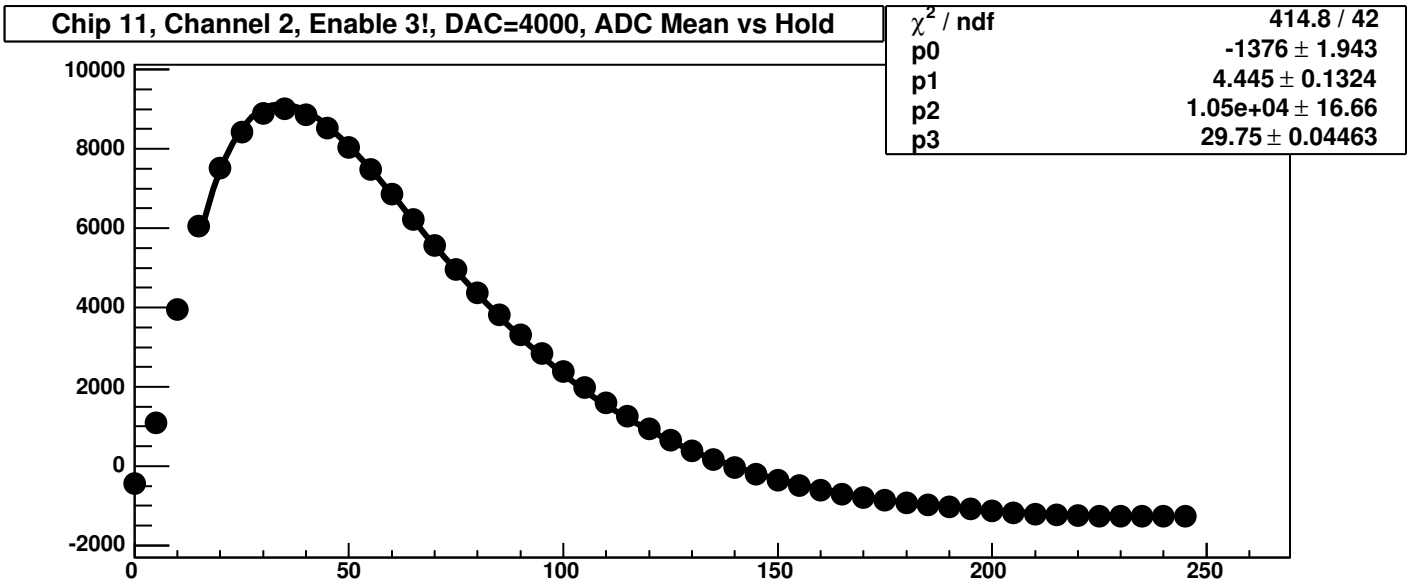


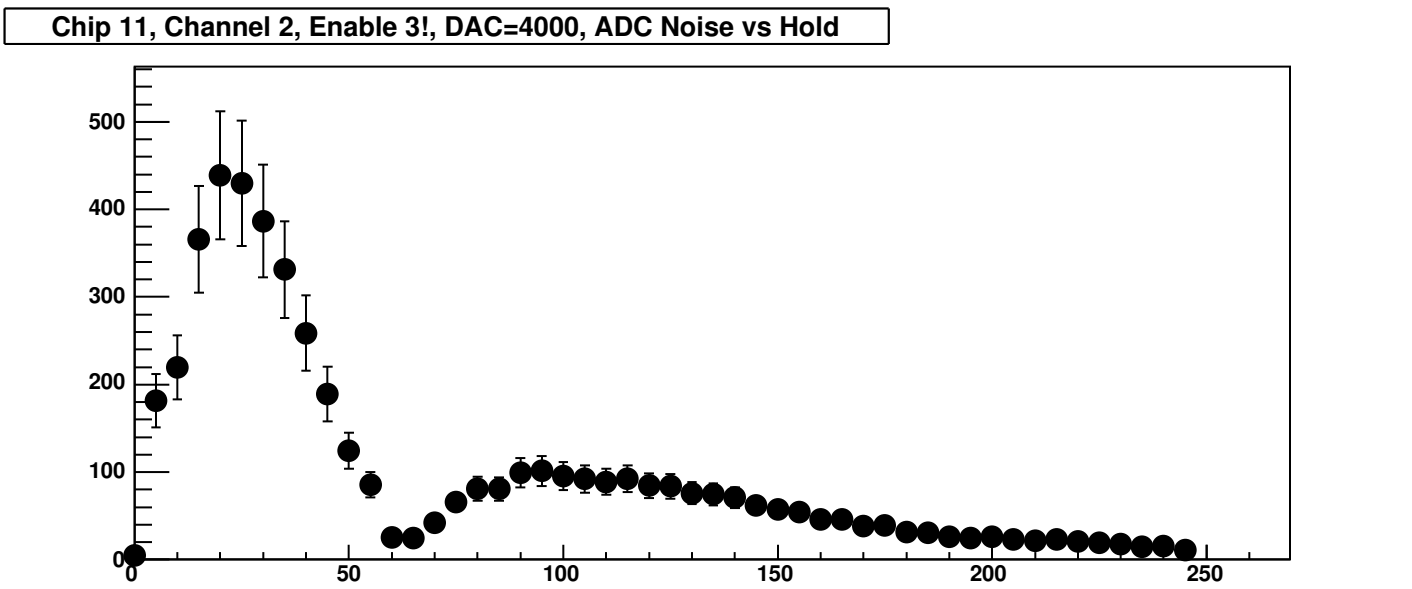
Chip 11, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold

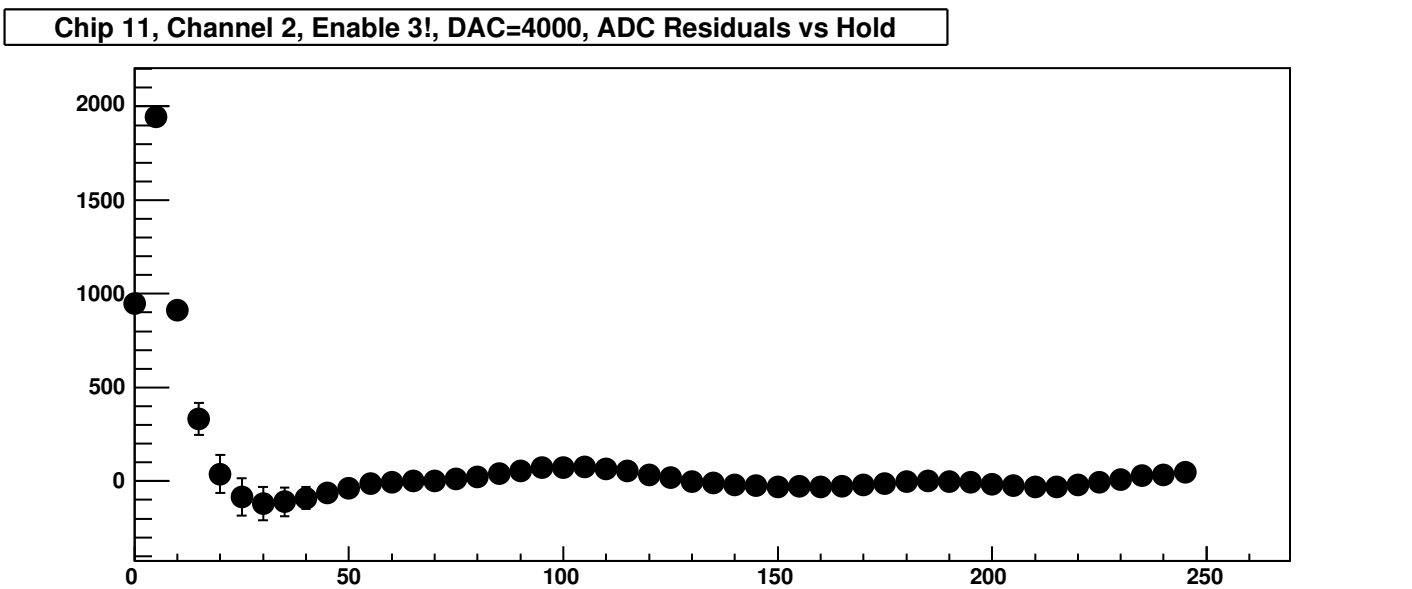


Chip 11, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

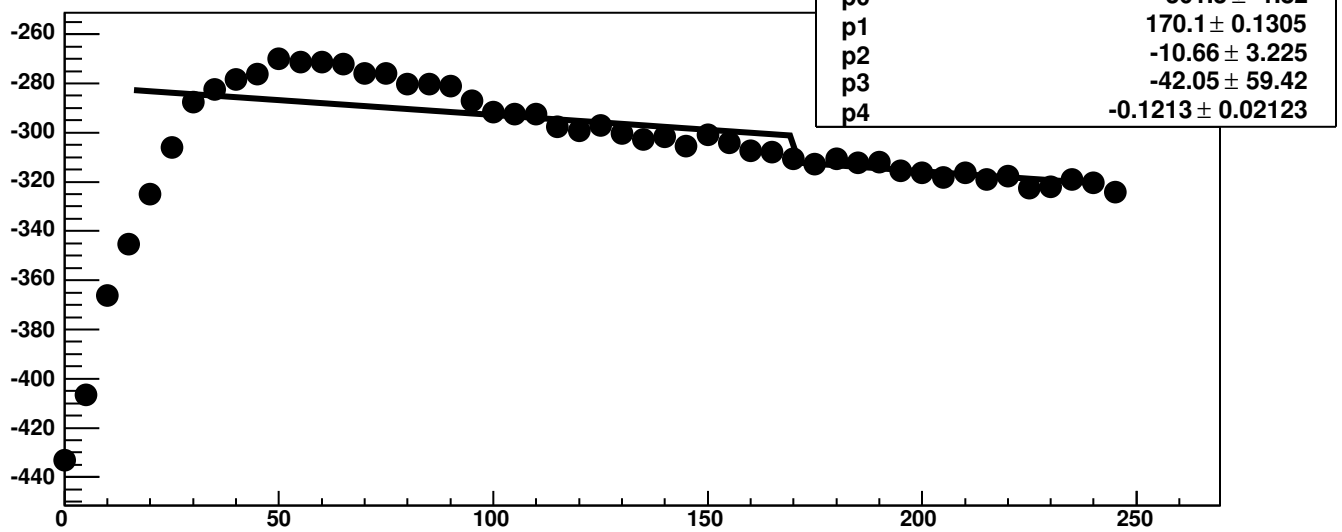



Chip 11, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 414.8 / 42
p0 -1376 ± 1.943
p1 4.445 ± 0.1324
p2 1.05e+04 ± 16.66
p3 29.75 ± 0.04463
10000
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

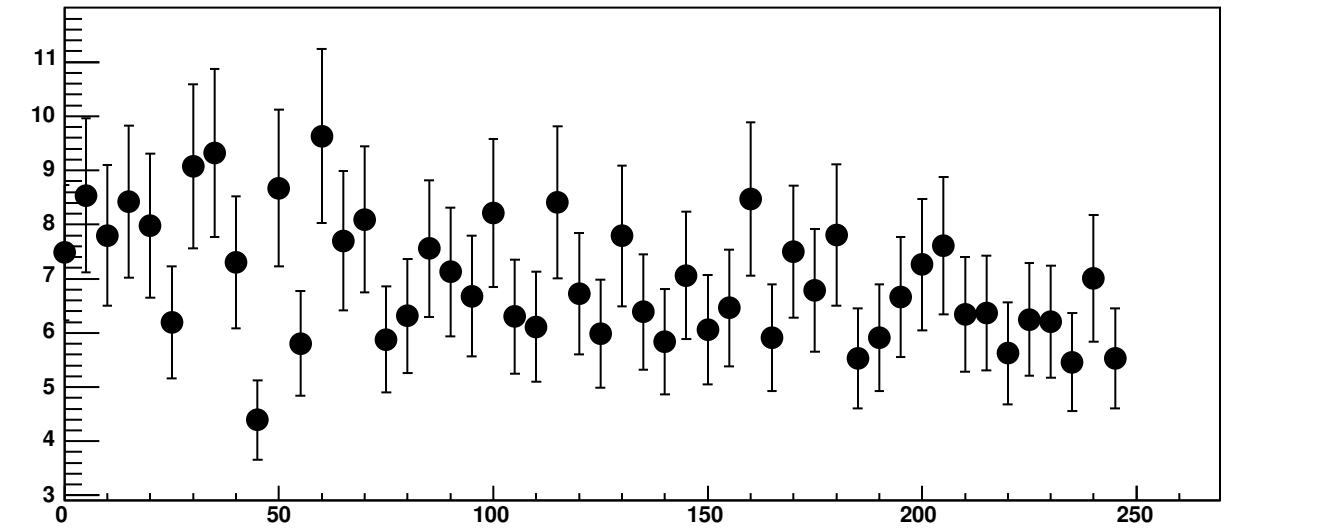

Chip 11, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold
500
400
300
200
100
0
0
50
100
150
200
250


Chip 11, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
0
50
100
150
200
250

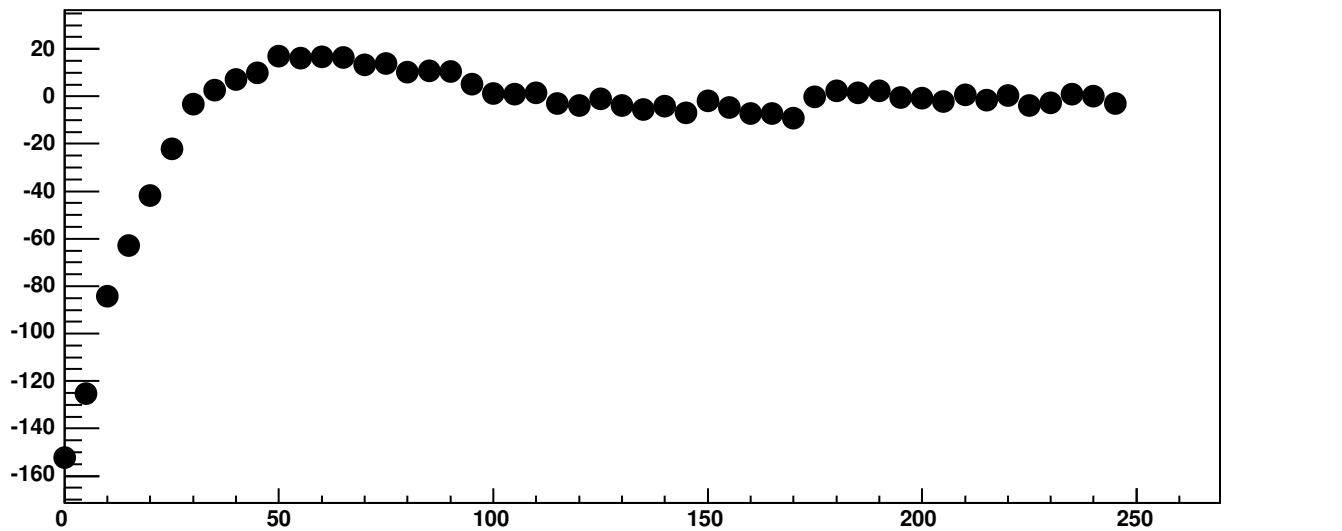
Chip 11, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold

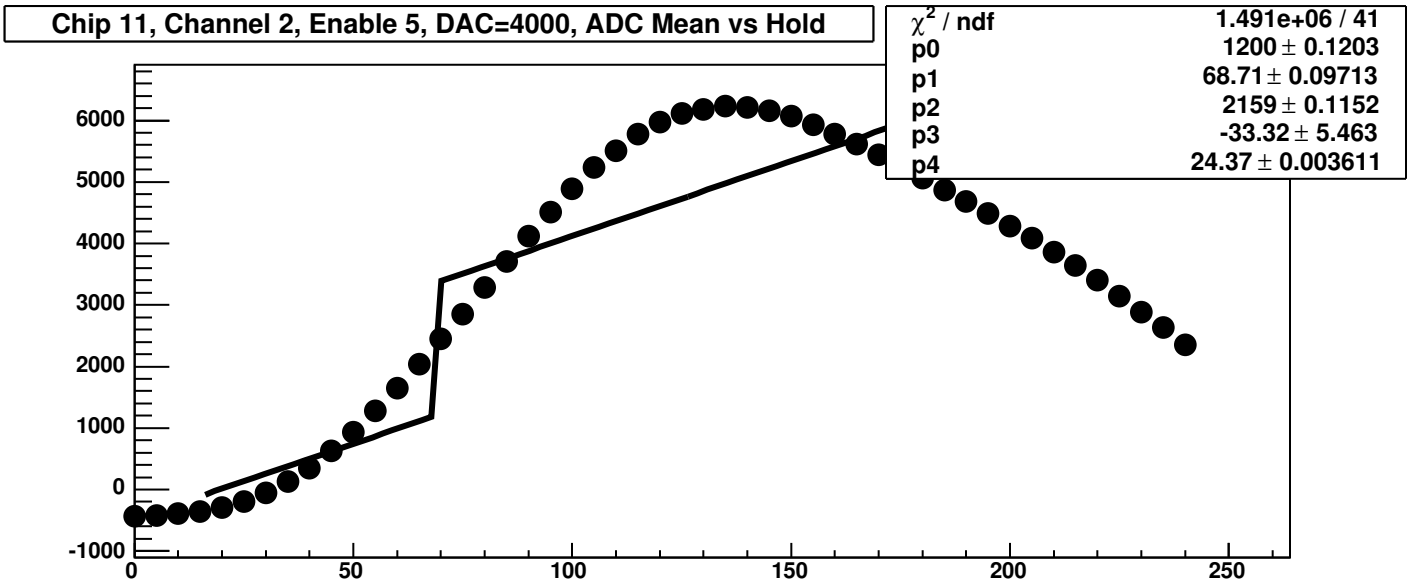


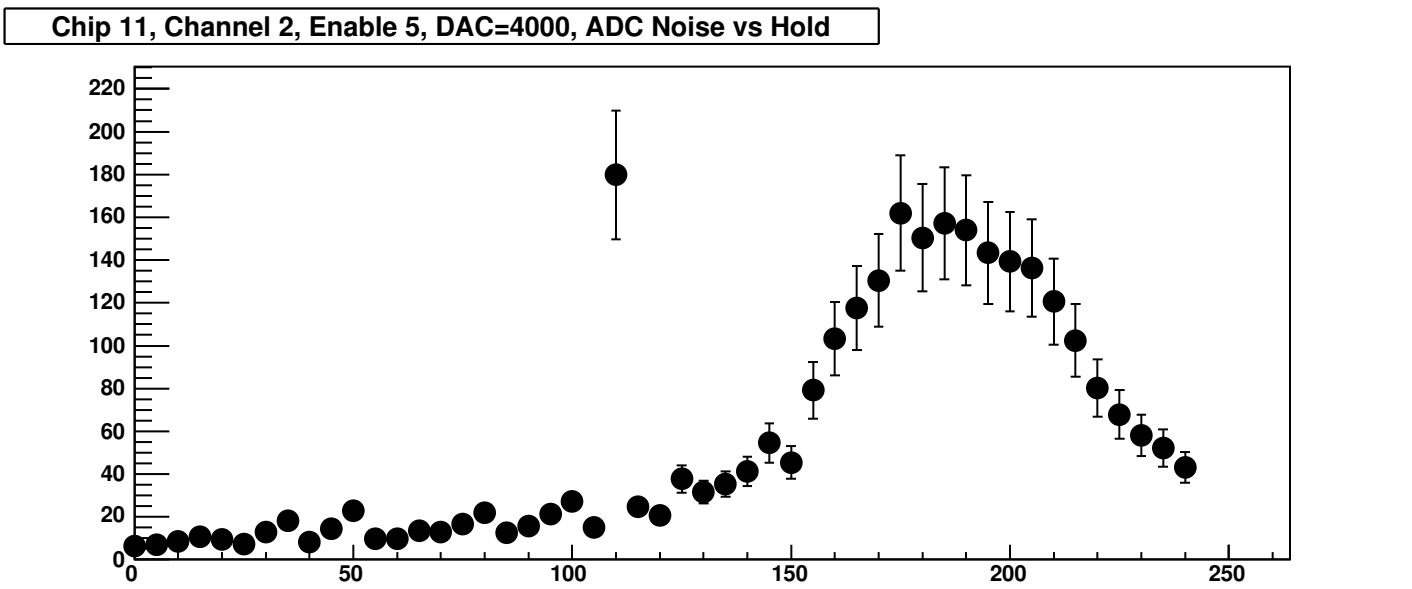
Chip 11, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold

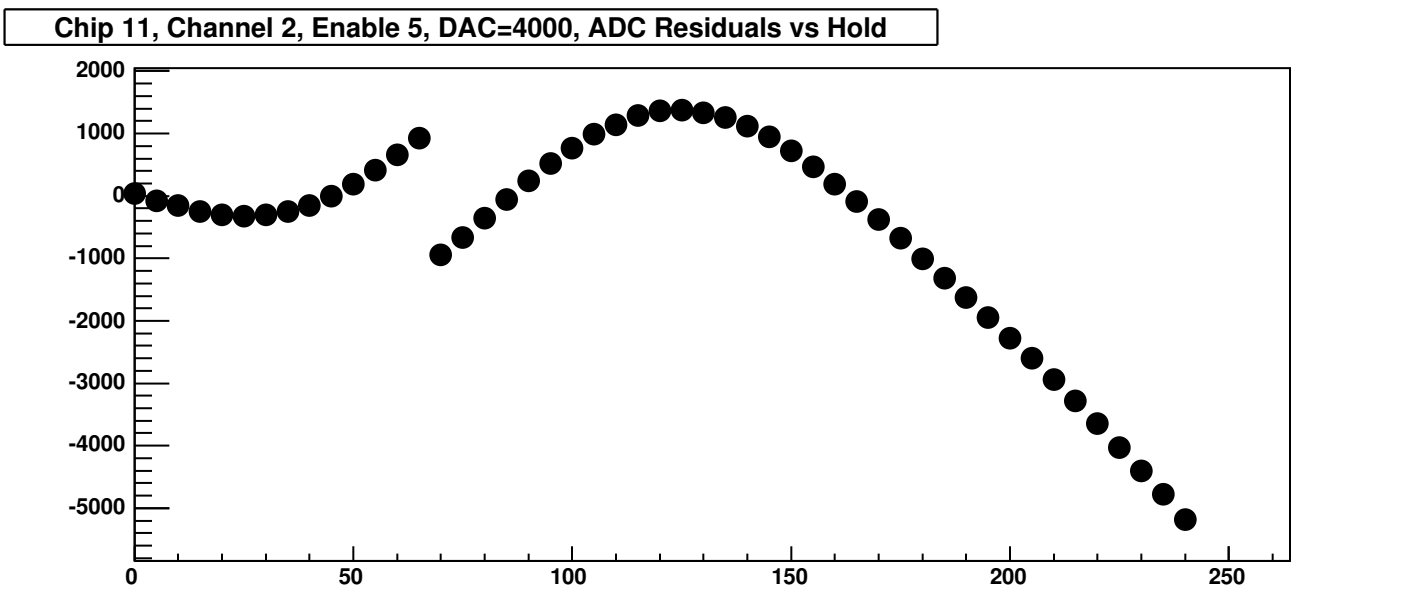


Chip 11, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

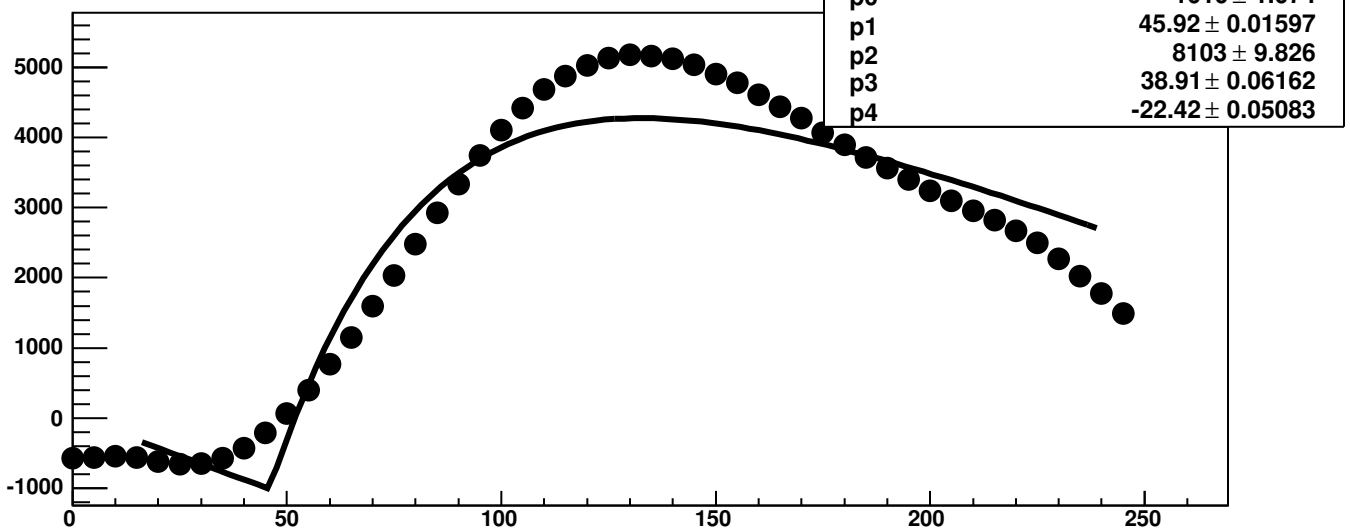



Chip 11, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.491e+06 / 41
p0 1200 ± 0.1203
p1 68.71 ± 0.09713
p2 2159 ± 0.1152
p3 -33.32 ± 5.463
p4 24.37 ± 0.003611
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

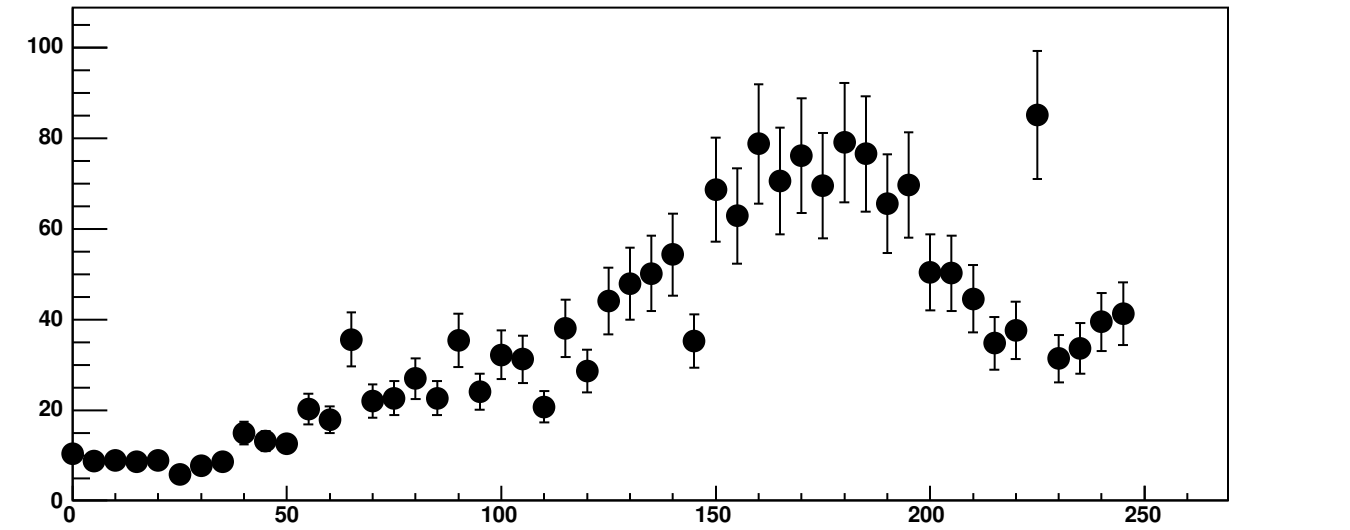

Chip 11, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 11, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
0
50
100
150
200
250

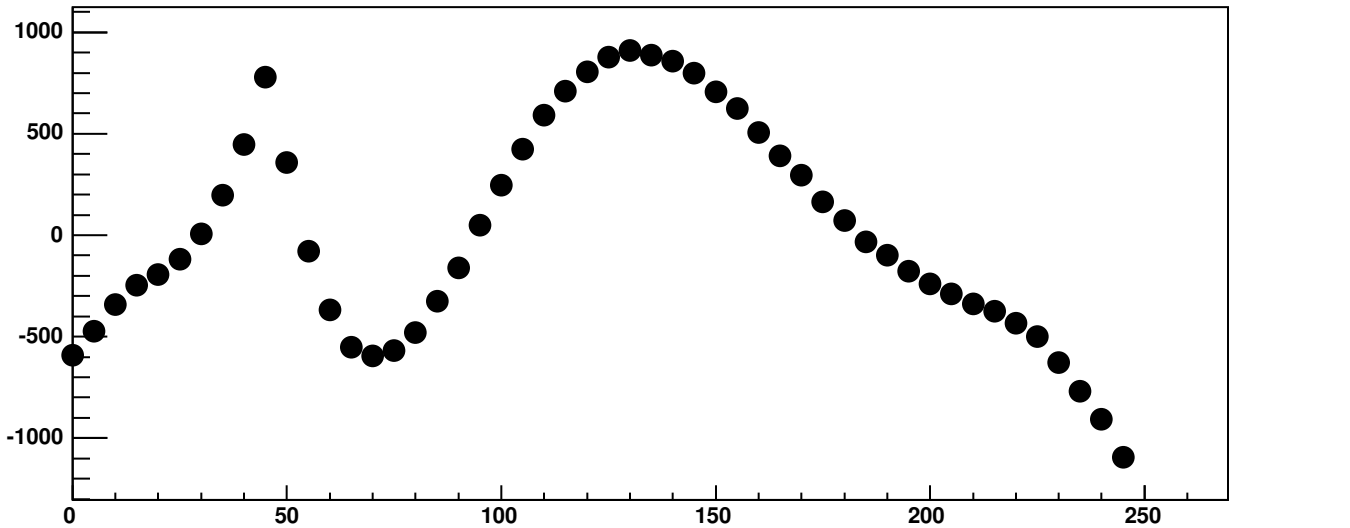
Chip 11, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold

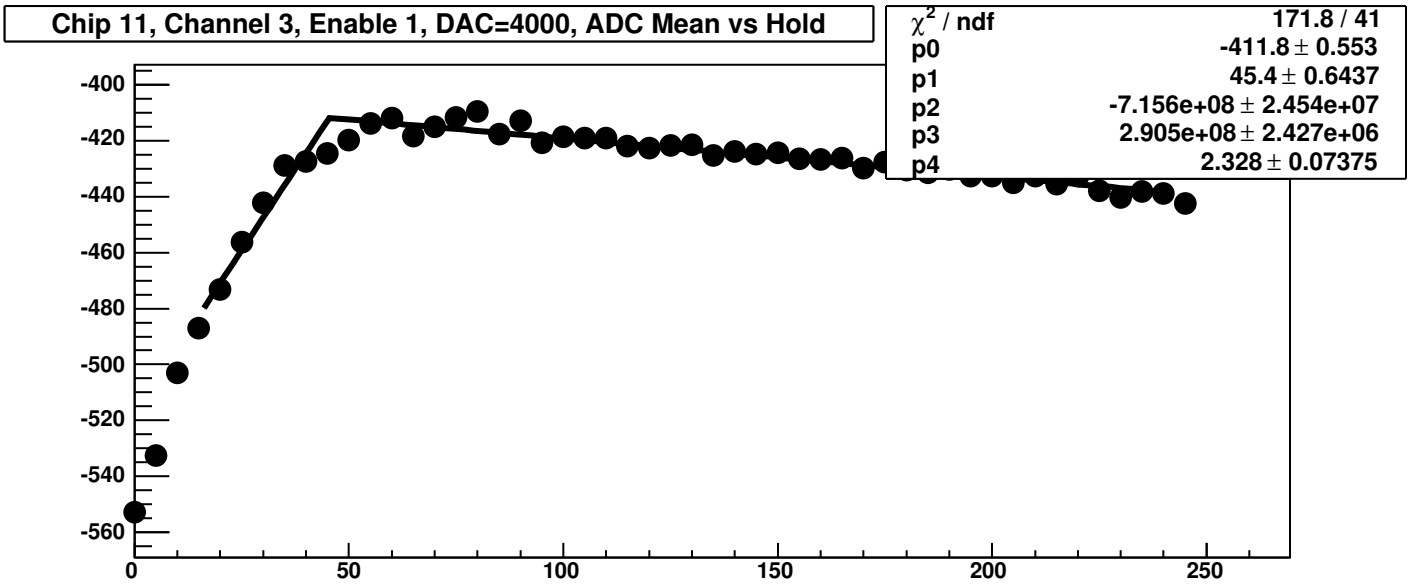


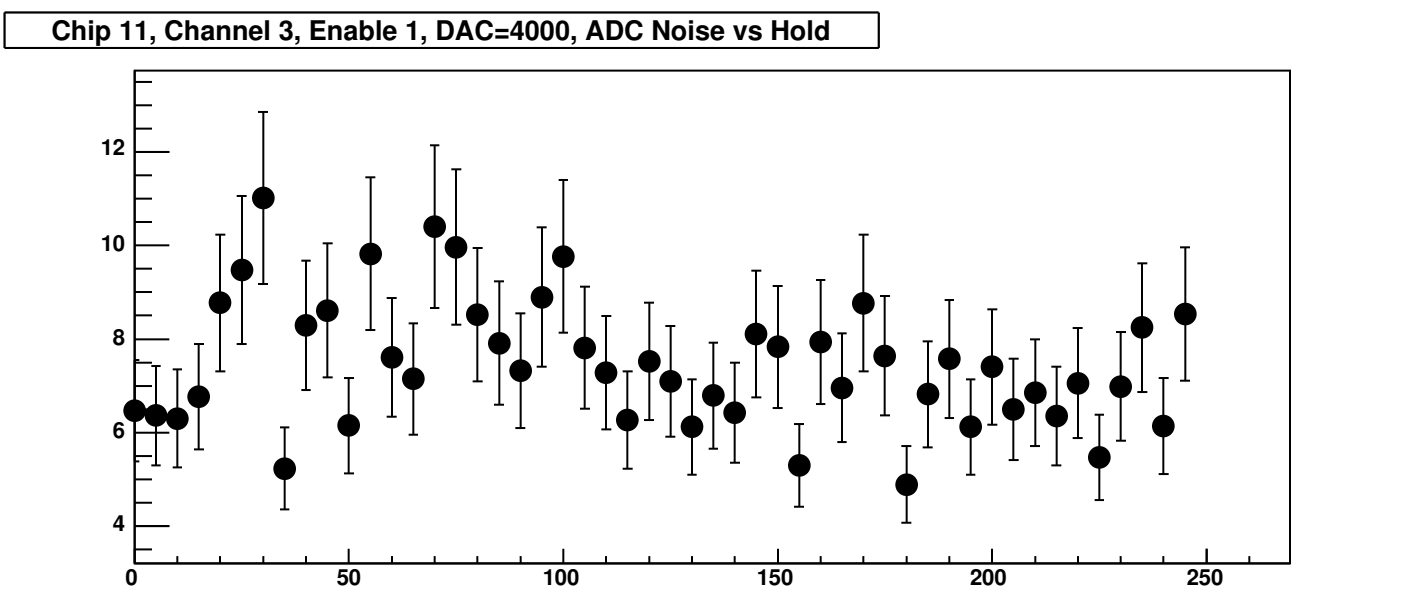
Chip 11, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold

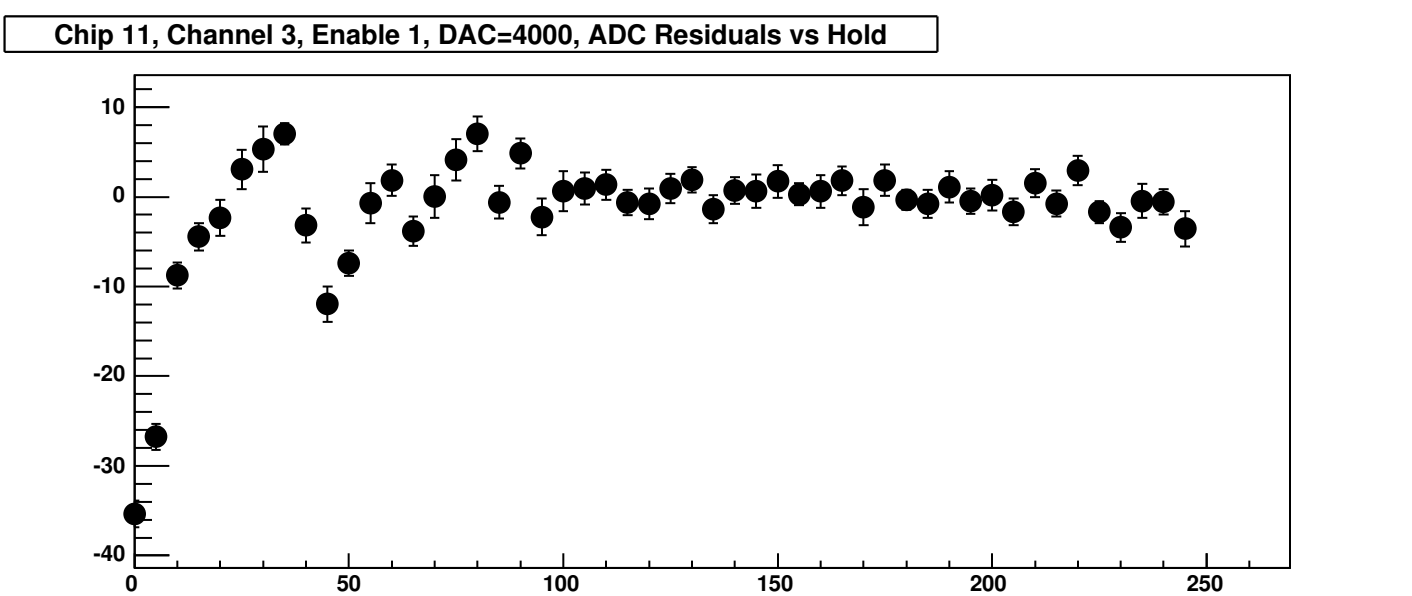


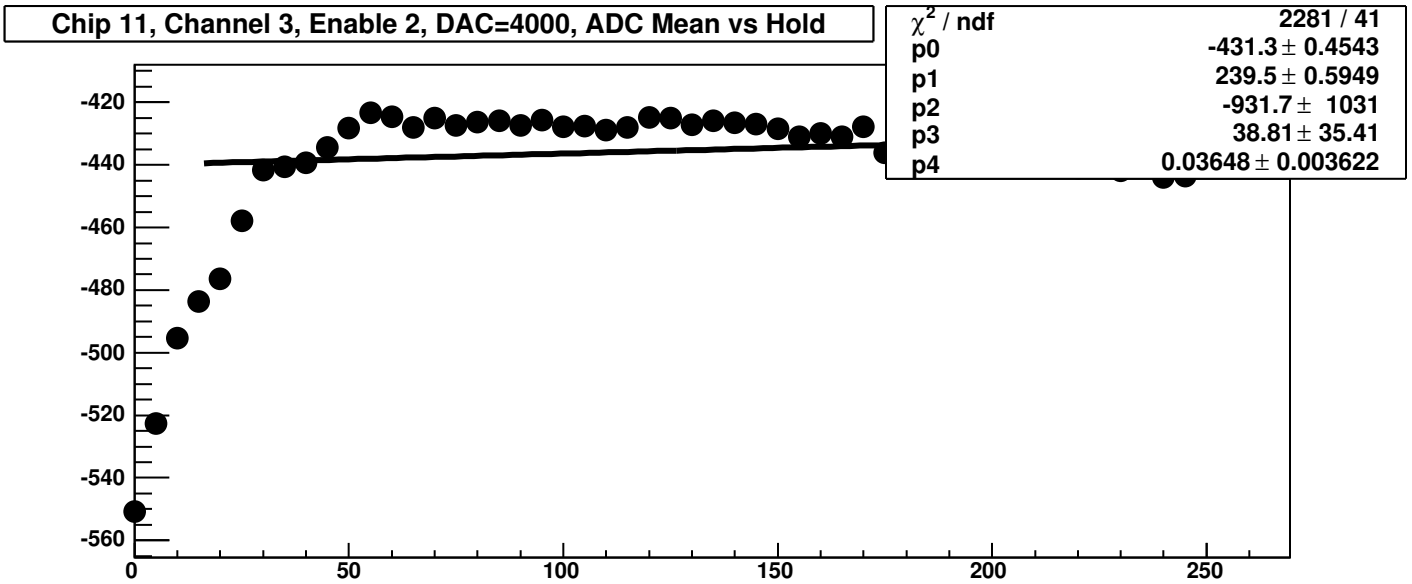
Chip 11, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

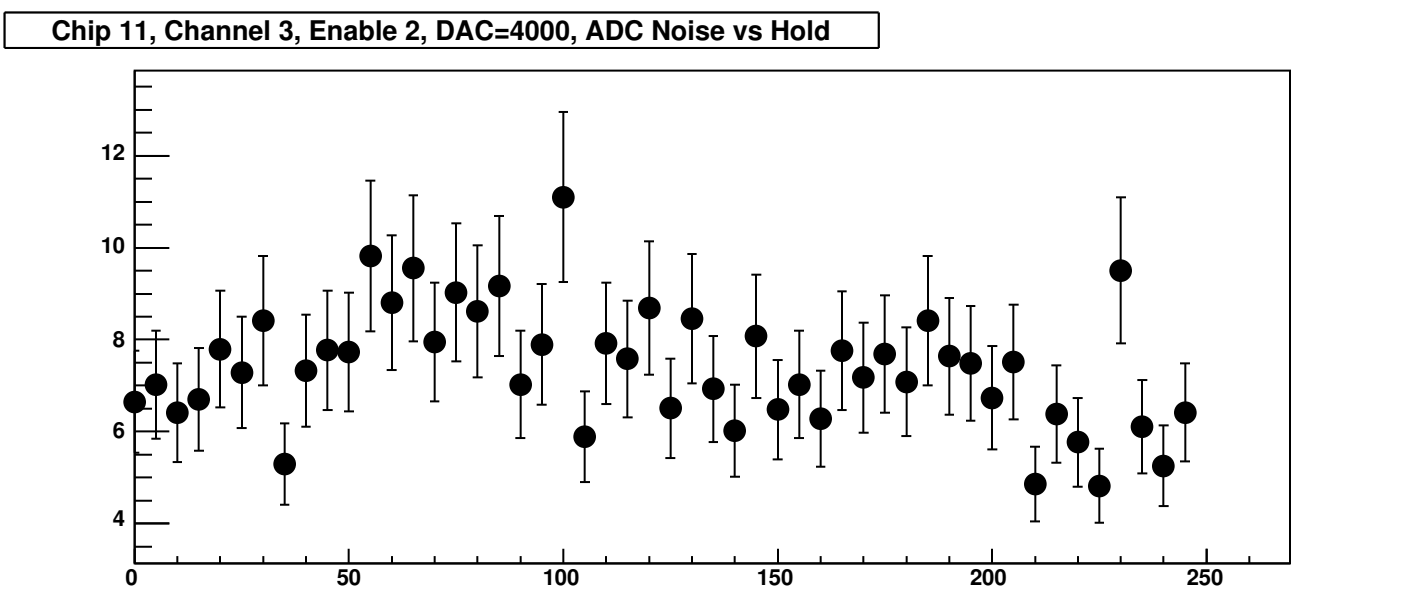


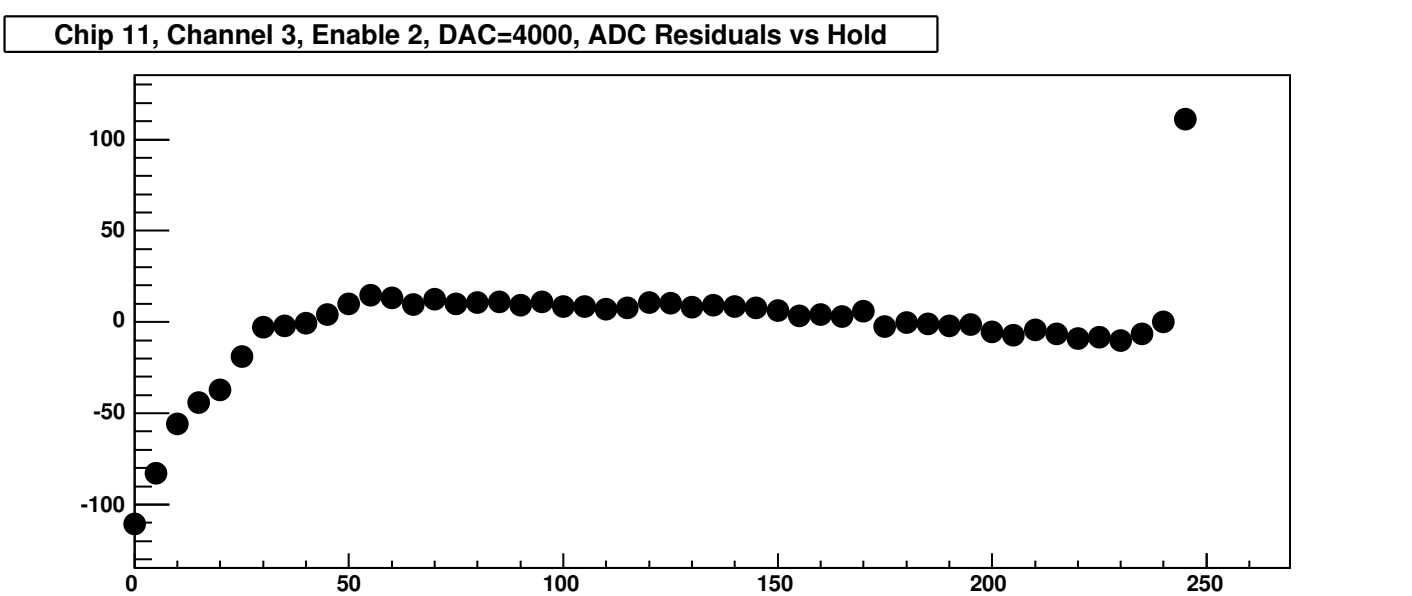

Chip 11, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 171.8 / 41
p0 -411.8 ± 0.553
p1 45.4 ± 0.6437
p2 -7.156e+08 ± 2.454e+07
p3 2.905e+08 ± 2.427e+06
p4 2.328 ± 0.07375
-400
-420
-440
-460
-480
-500
-520
-540
-560
0
50
100
150
200
250


Chip 11, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

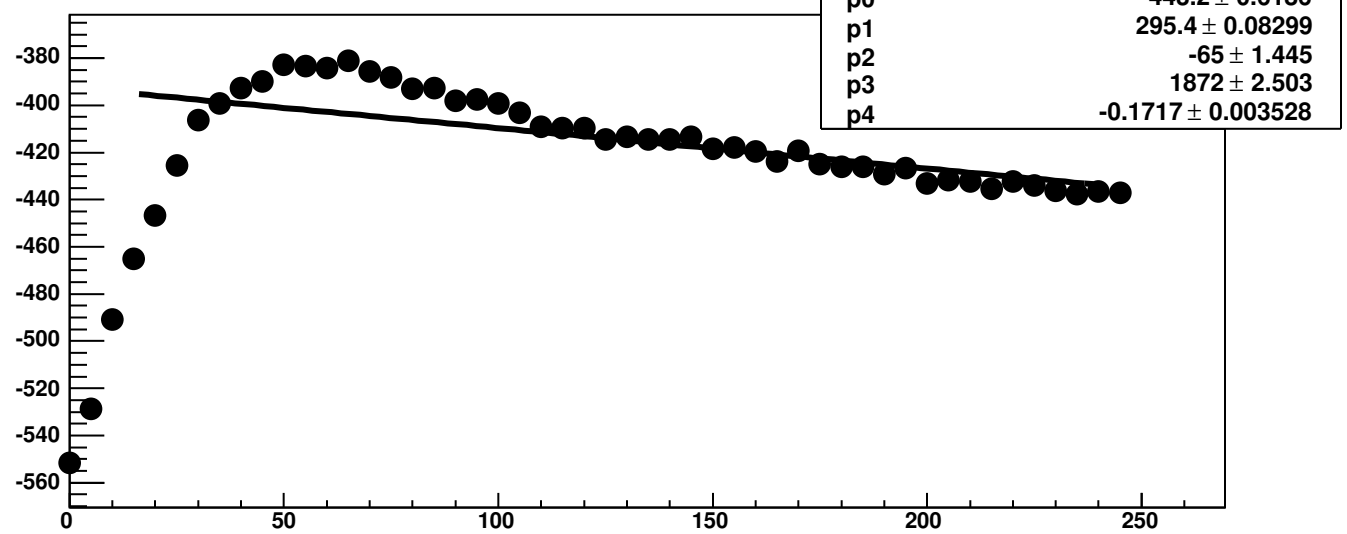

Chip 11, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold
10
0
-10
-20
-30
-40
0
50
100
150
200
250


Chip 11, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 2281 / 41
p0 -431.3 ± 0.4543
p1 239.5 ± 0.5949
p2 -931.7 ± 1031
p3 38.81 ± 35.41
p4 0.03648 ± 0.003622
-420
-440
-460
-480
-500
-520
-540
-560
0
50
100
150
200
250

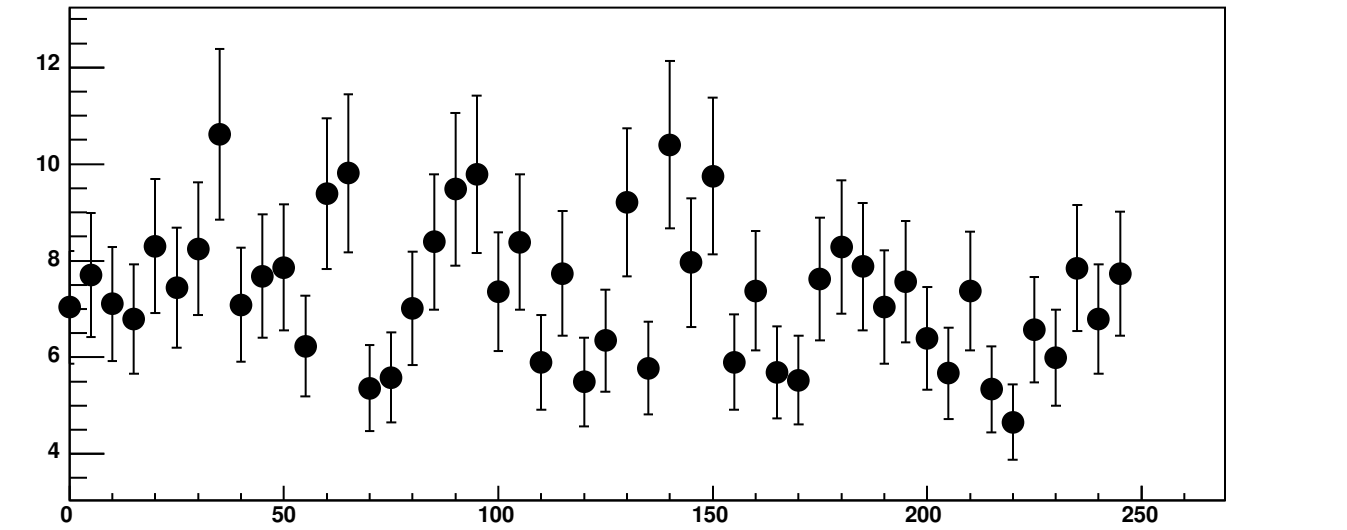

Chip 11, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 11, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold
100
50
0
-50
-100
0
50
100
150
200
250

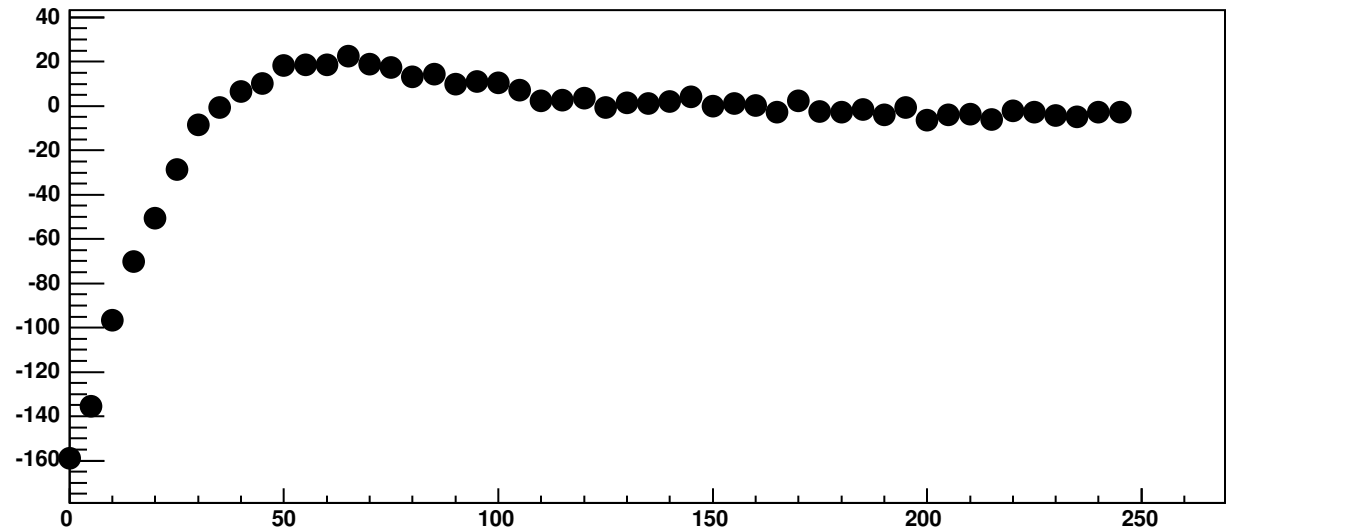
Chip 11, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold



Chip 11, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold

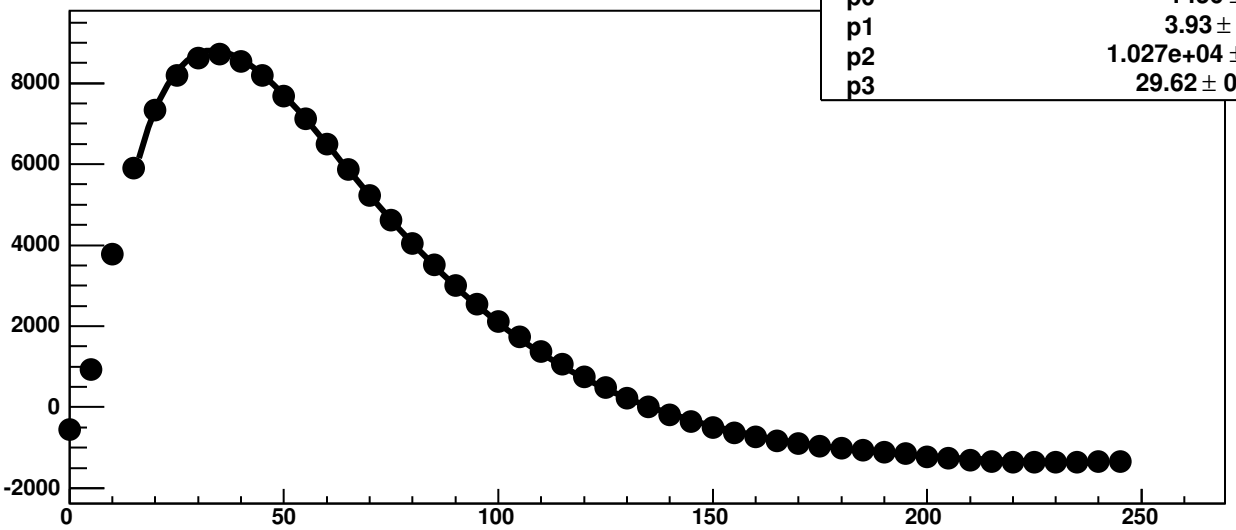


Chip 11, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

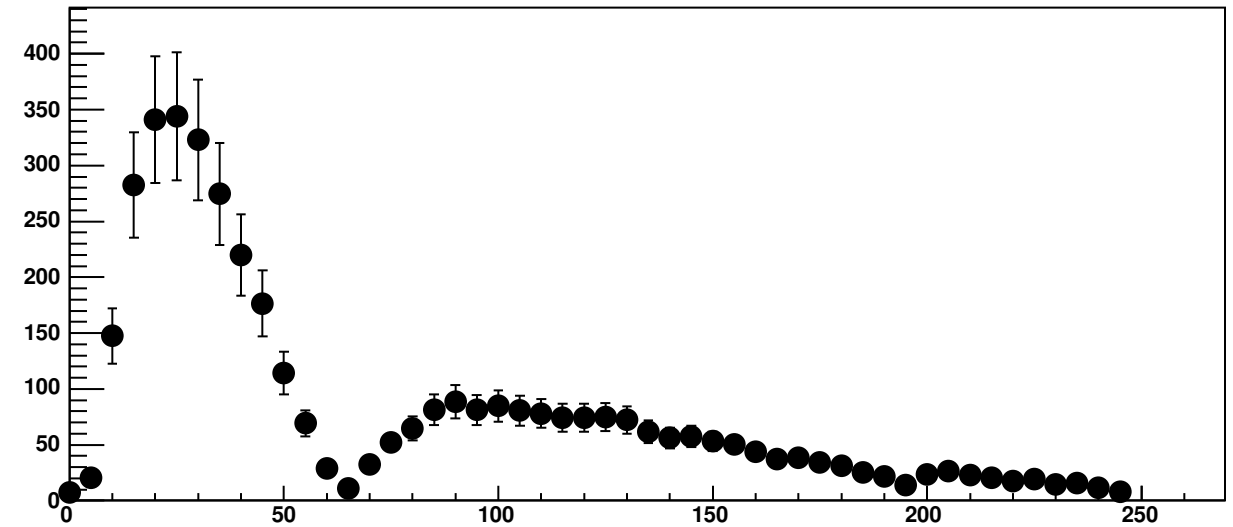


Chip 11, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold

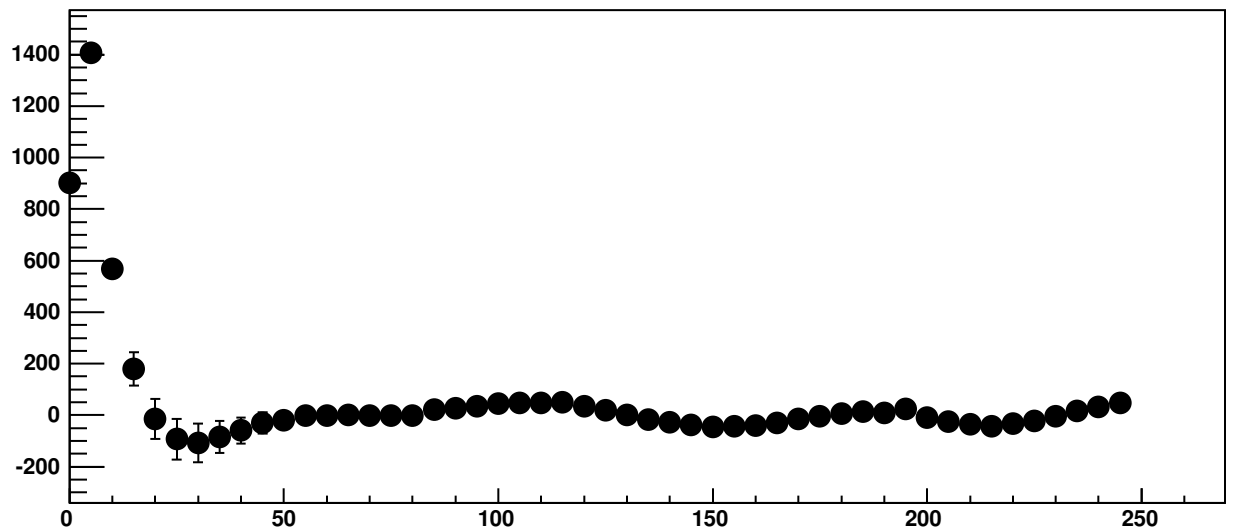
χ^2 / ndf	613 / 42
p0	-1456 ± 1.696
p1	3.93 ± 0.1109
p2	1.027e+04 ± 14.07
p3	29.62 ± 0.04018

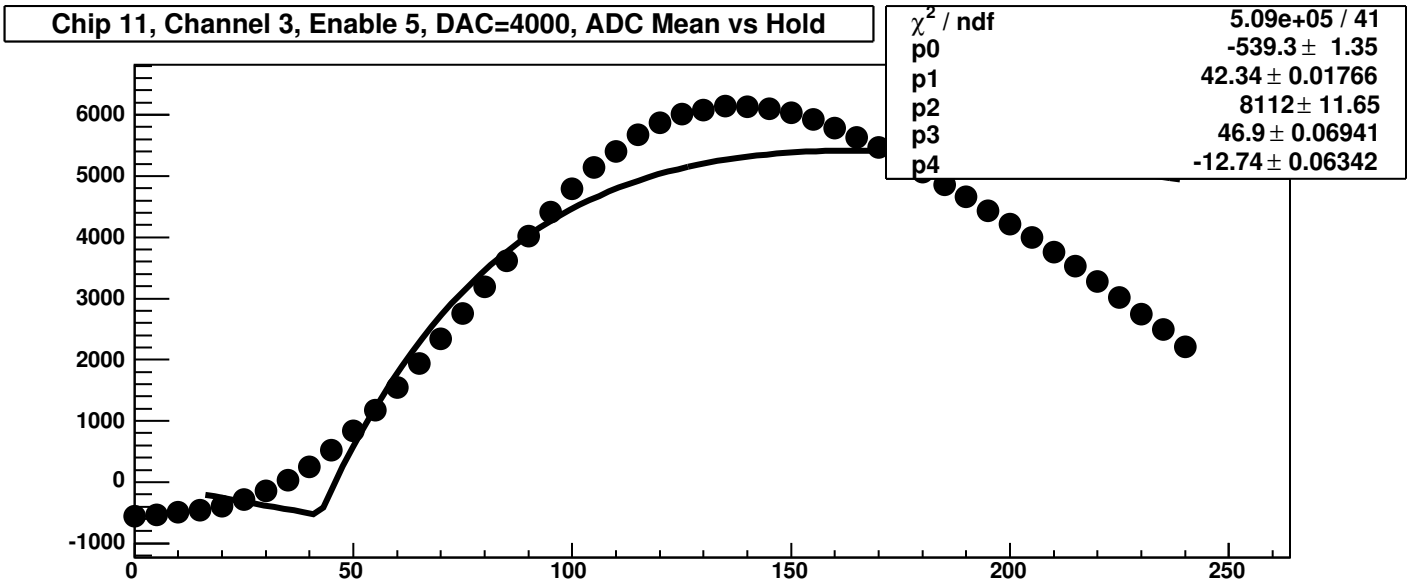


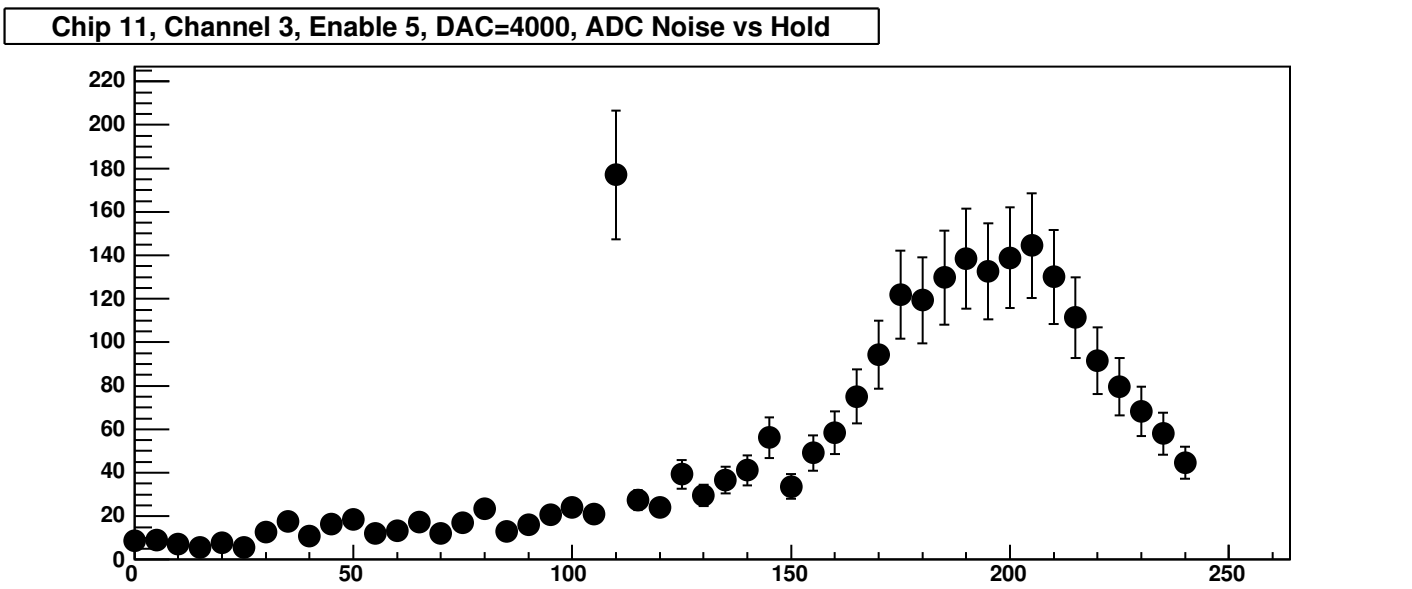
Chip 11, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold

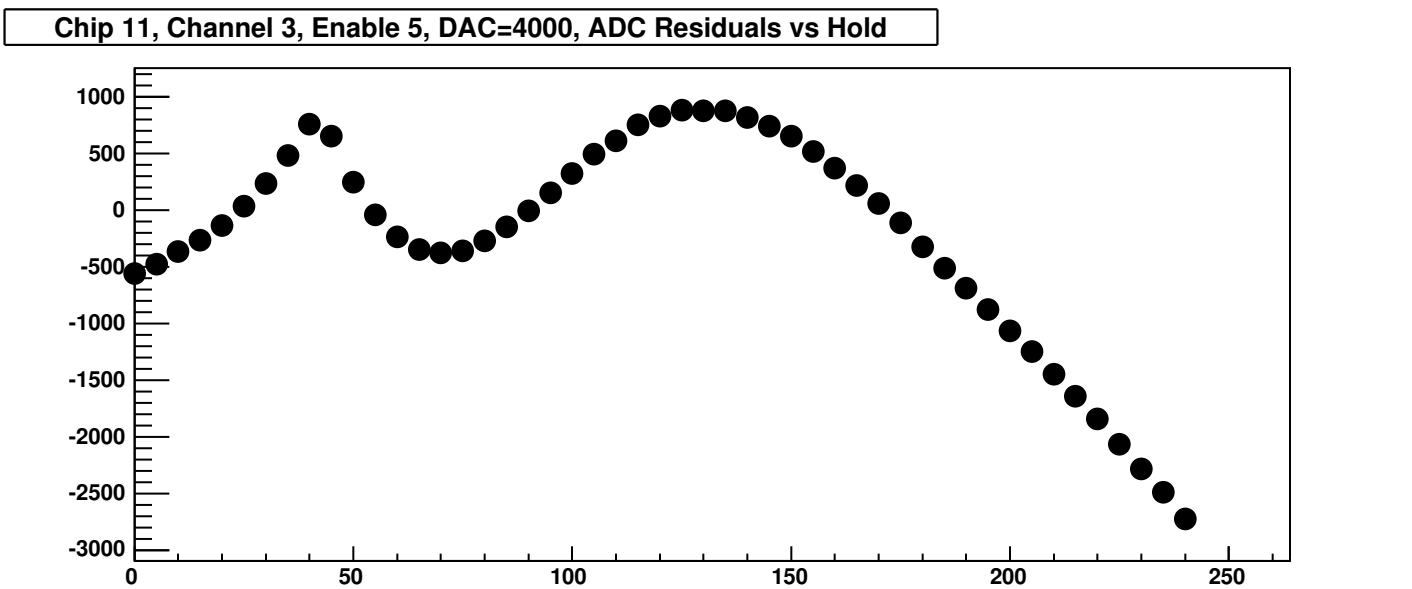


Chip 11, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold

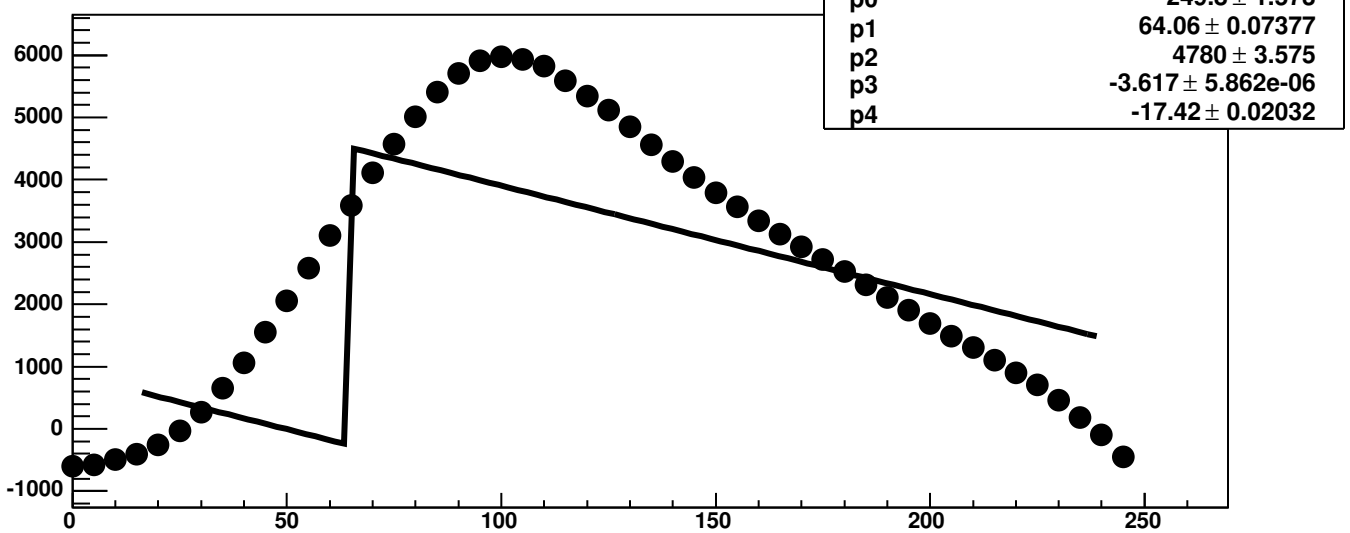



Chip 11, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.09e+05 / 41
p0 -539.3 ± 1.35
p1 42.34 ± 0.01766
p2 8112 ± 11.65
p3 46.9 ± 0.06941
p4 -12.74 ± 0.06342
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

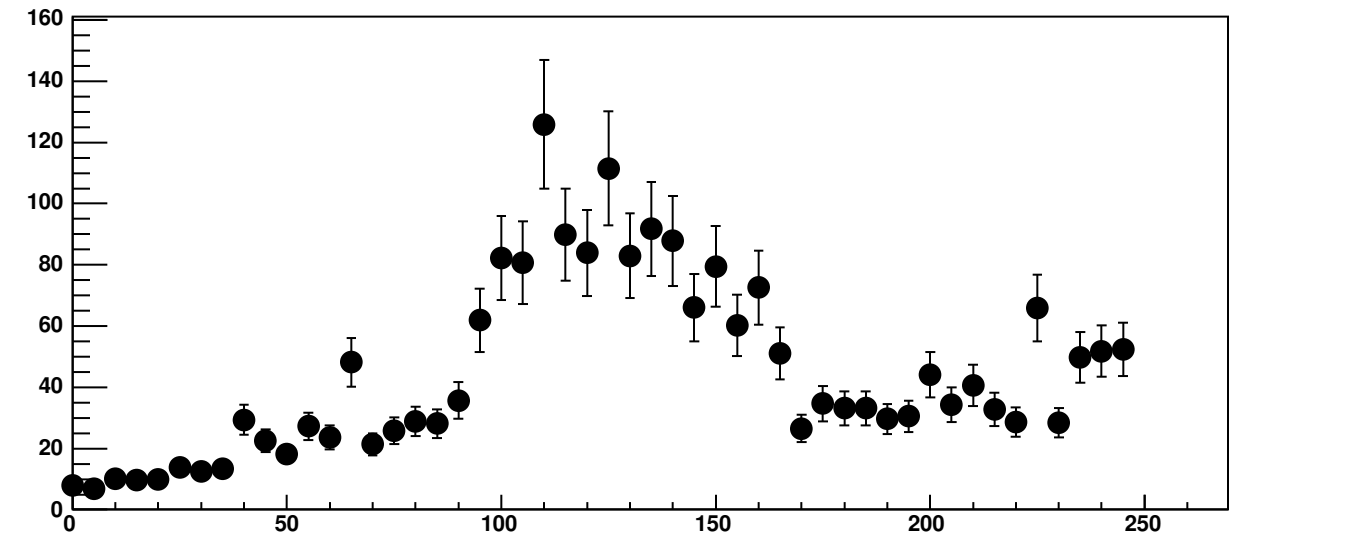

Chip 11, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 11, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250

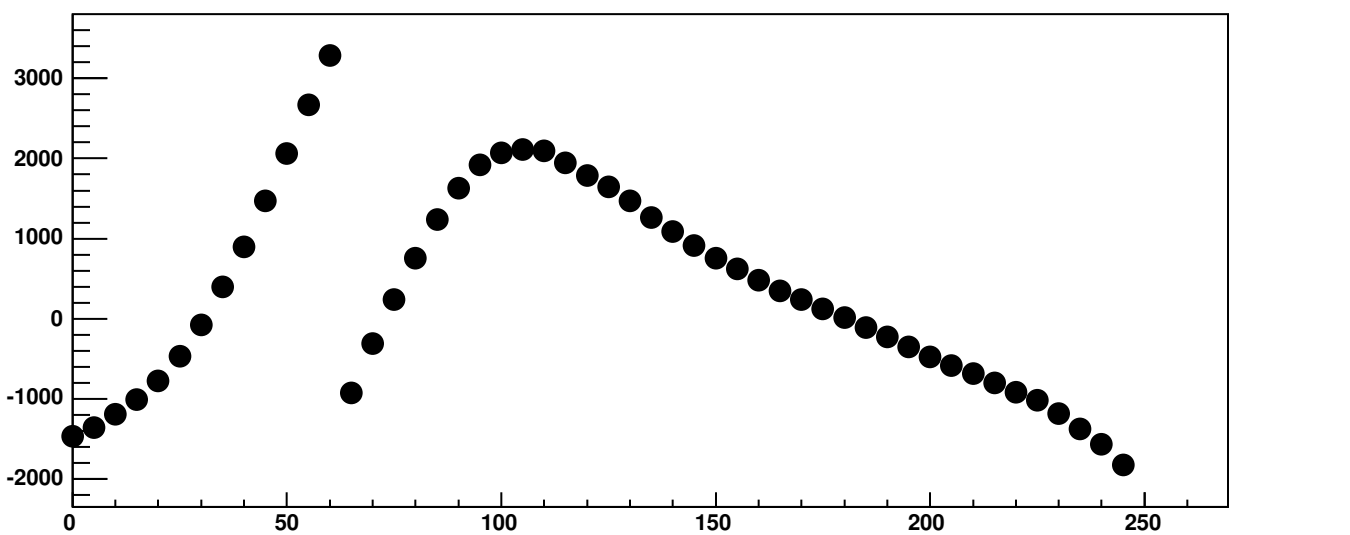
Chip 11, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold



Chip 11, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold

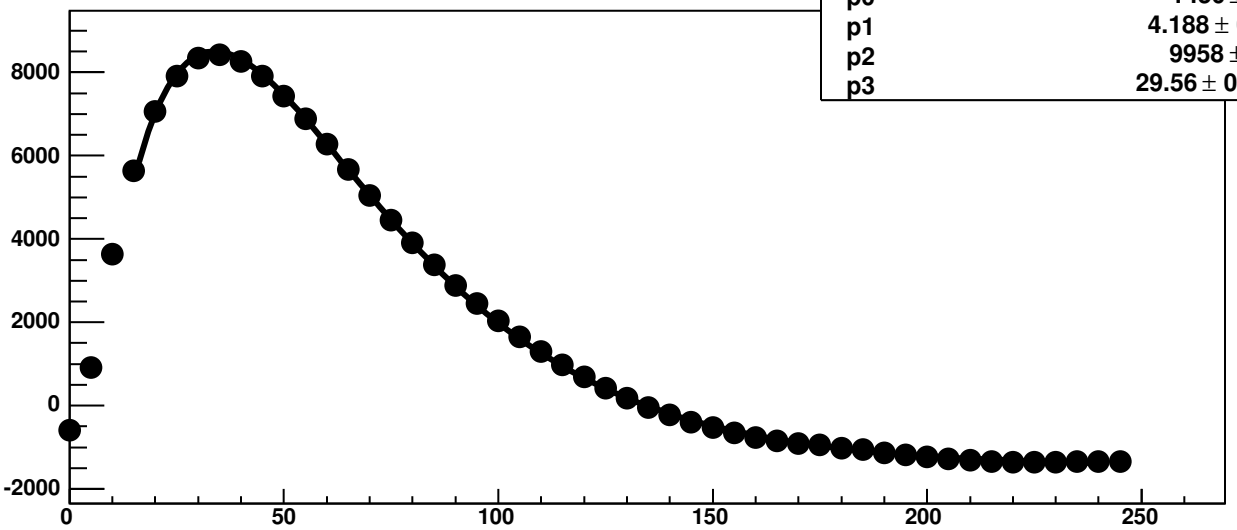


Chip 11, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

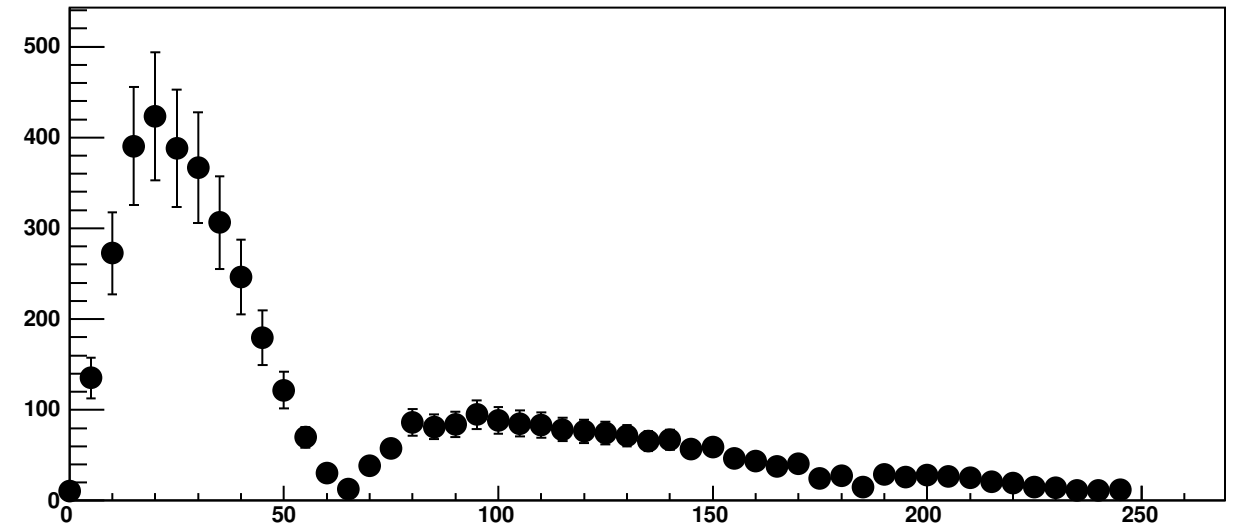


Chip 11, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold

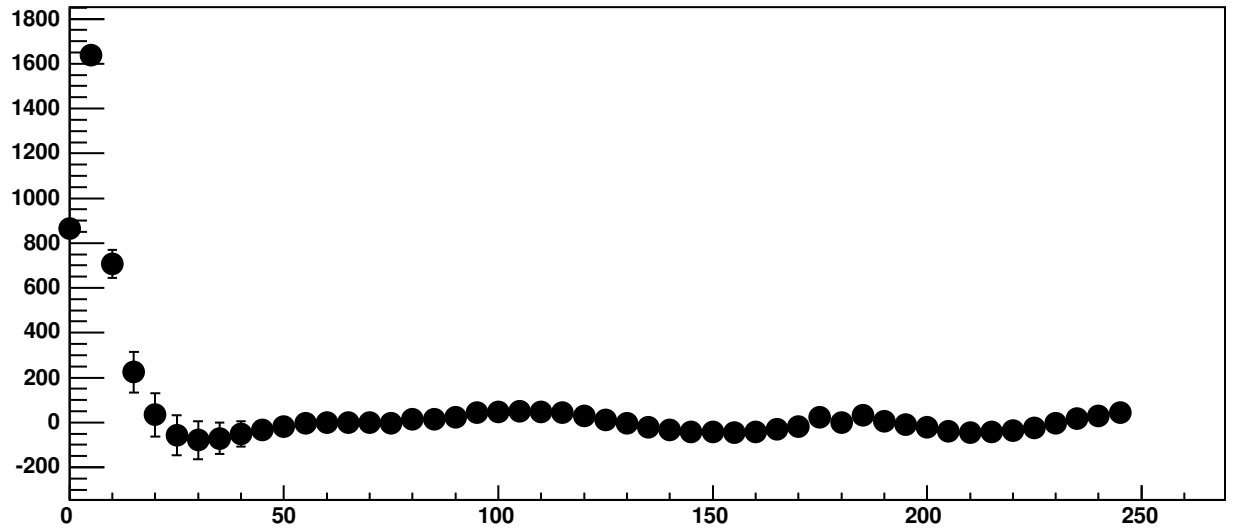
χ^2 / ndf	711.8 / 42
p0	-1450 ± 1.649
p1	4.188 ± 0.1394
p2	9958 ± 17.04
p3	29.56 ± 0.04334

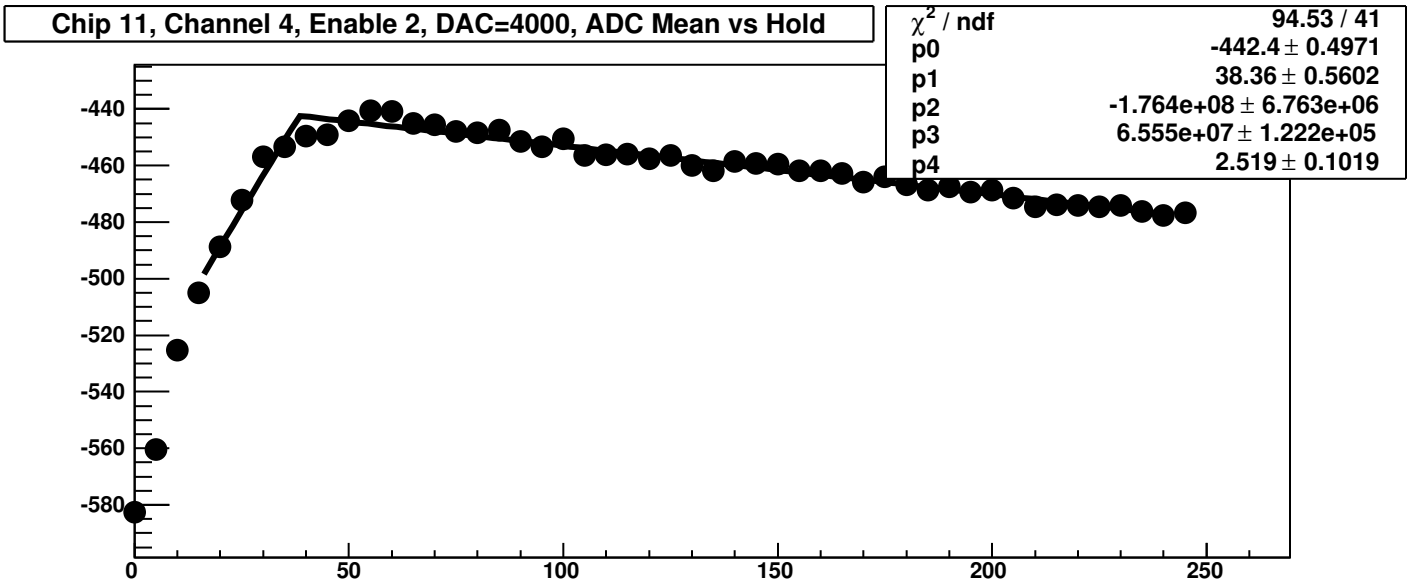


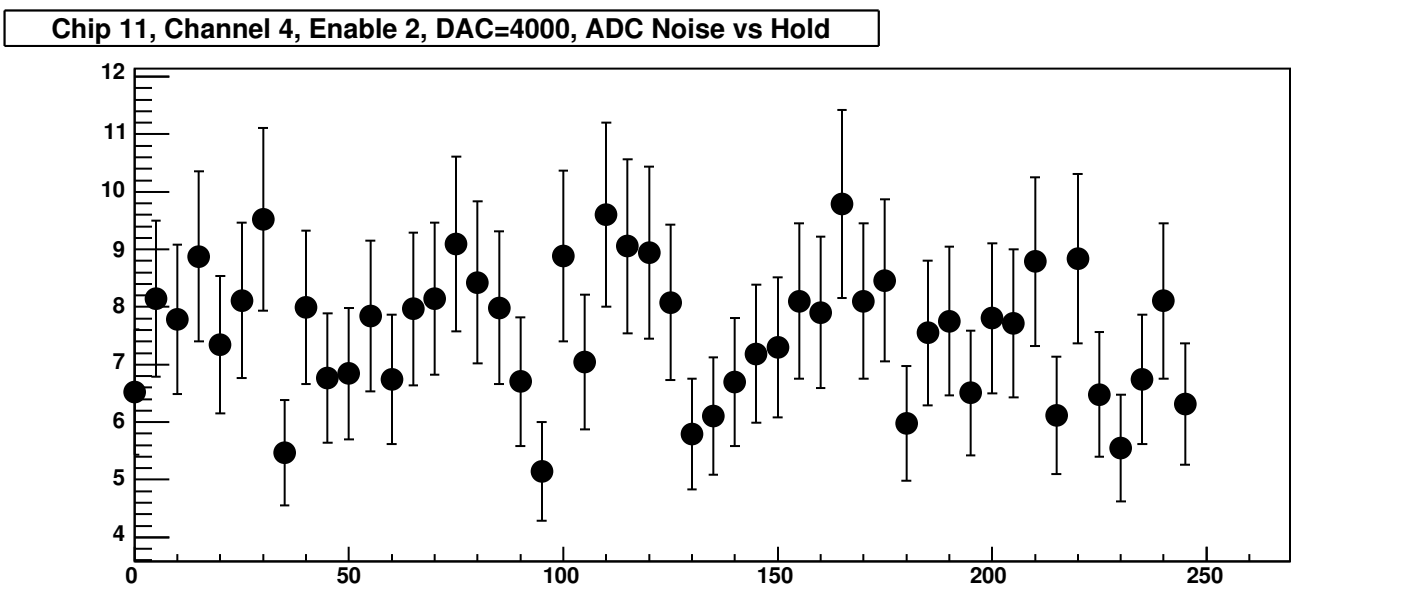
Chip 11, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold

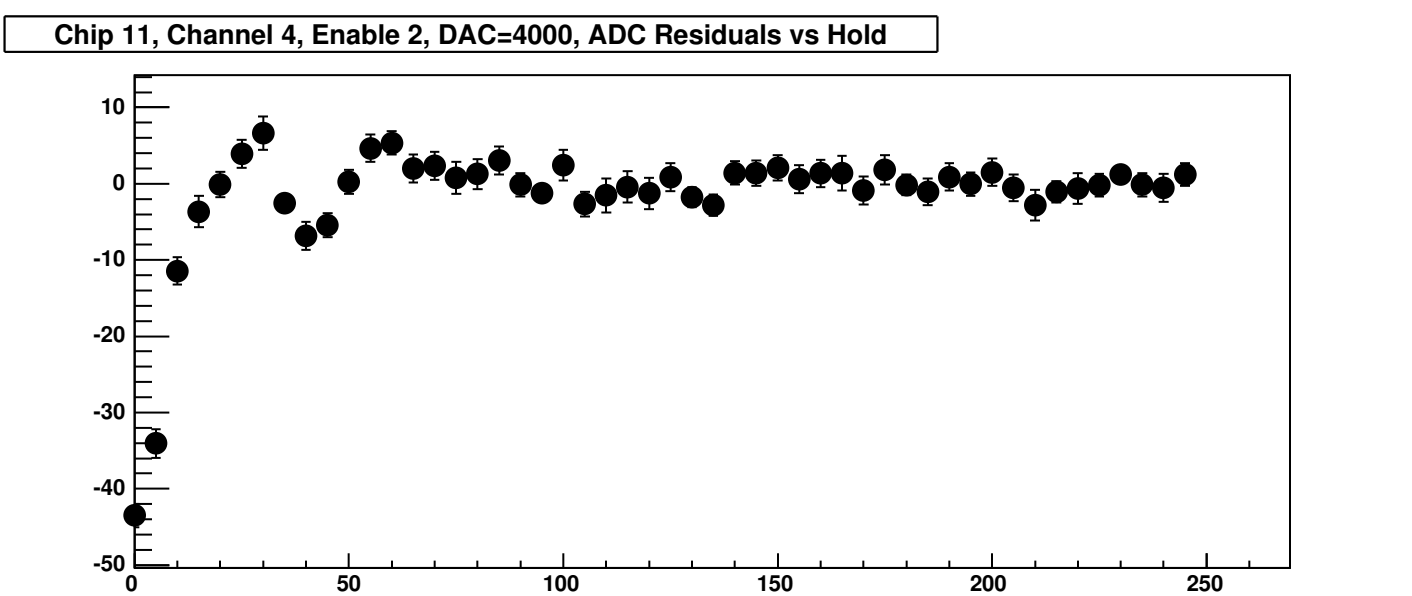


Chip 11, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold

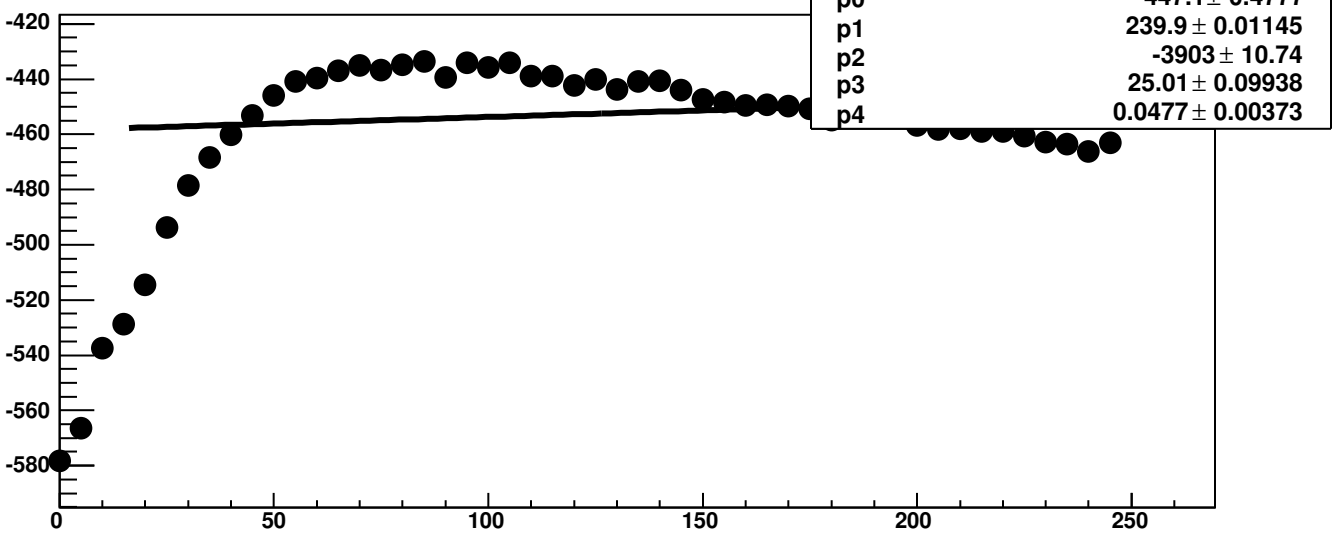



Chip 11, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 94.53 / 41
p0 -442.4 ± 0.4971
p1 38.36 ± 0.5602
p2 -1.764e+08 ± 6.763e+06
p3 6.555e+07 ± 1.222e+05
p4 2.519 ± 0.1019
-440
-460
-480
-500
-520
-540
-560
-580
0
50
100
150
200
250

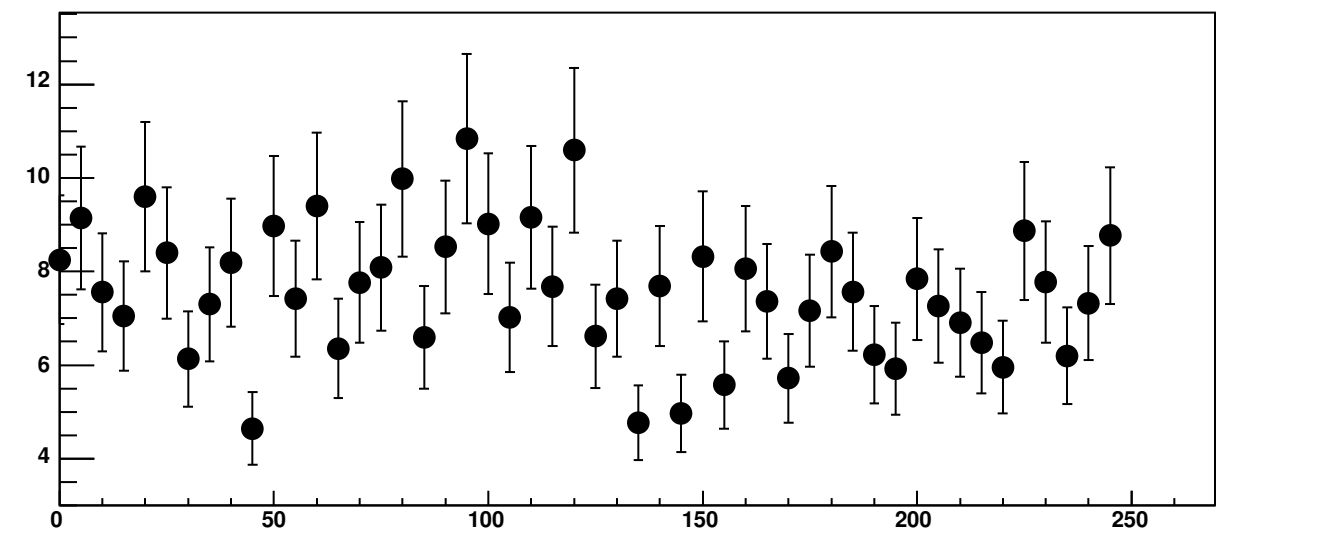

Chip 11, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 11, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold
10
0
-10
-20
-30
-40
-50
0
50
100
150
200
250

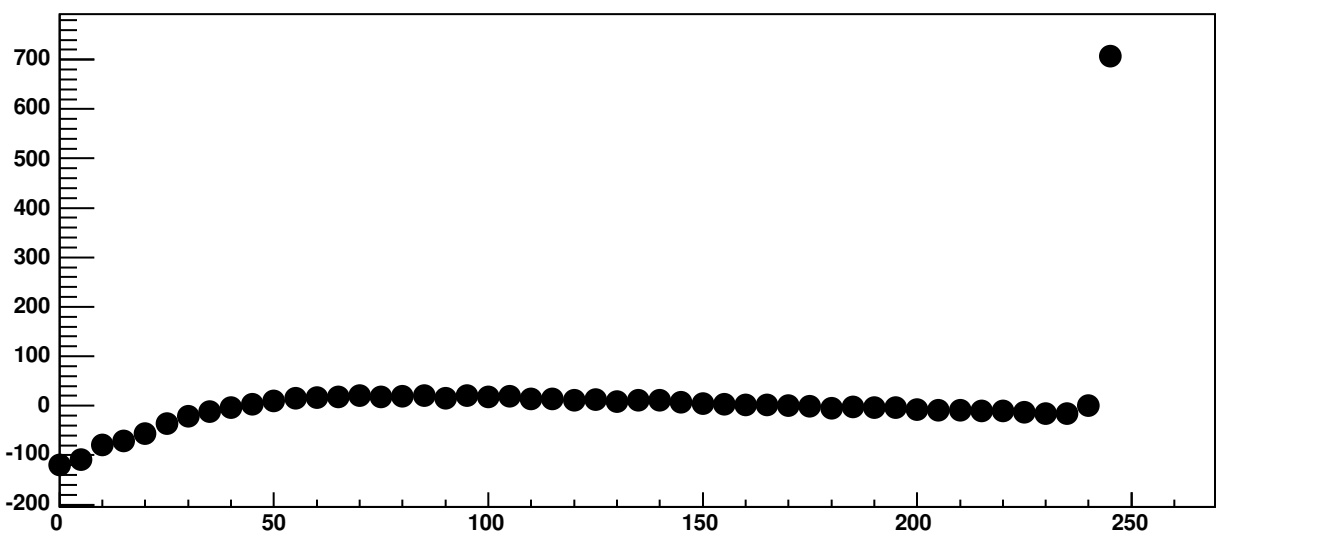
Chip 11, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold



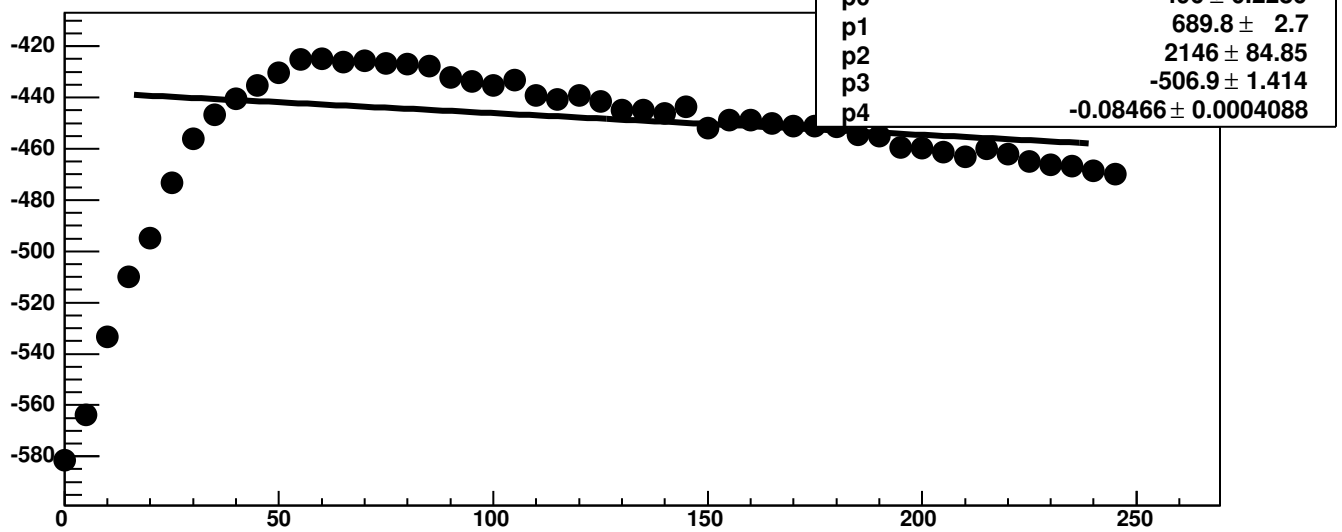
Chip 11, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



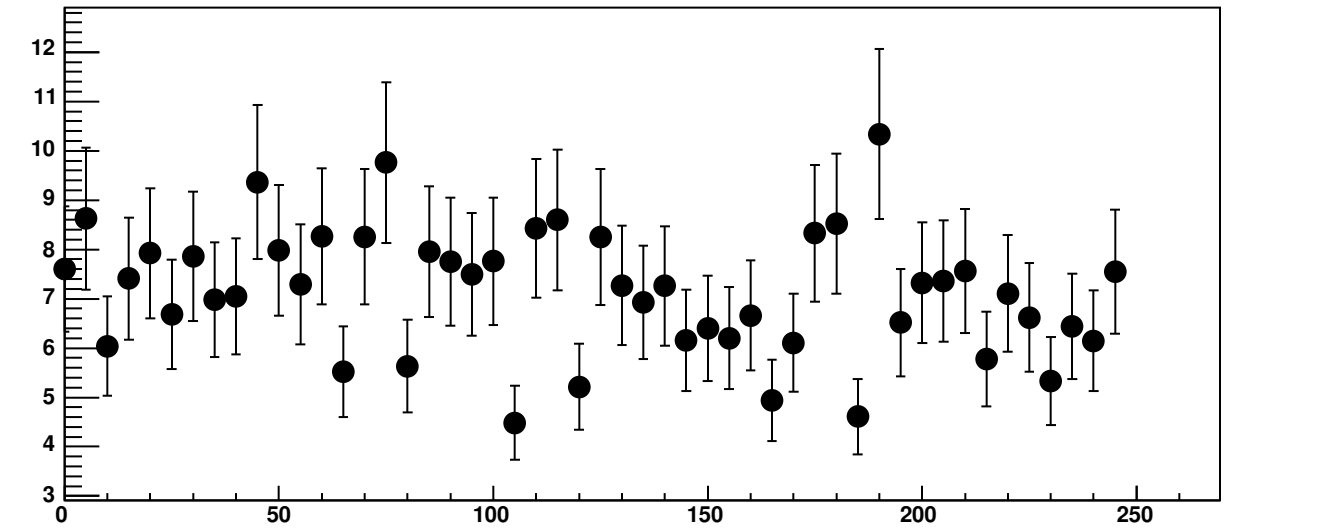
Chip 11, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold



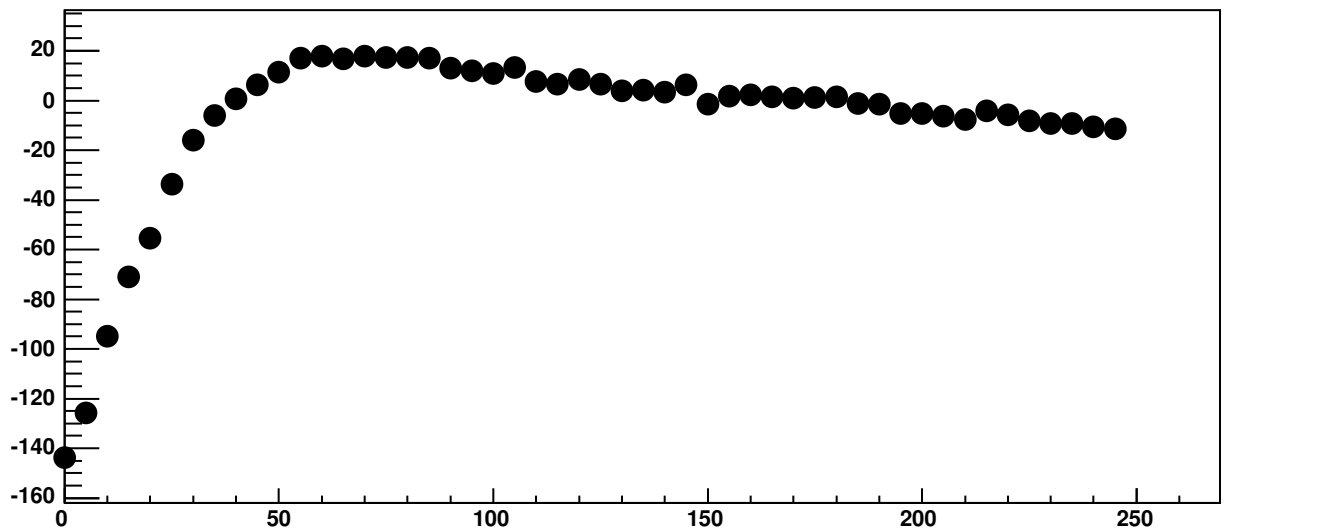
Chip 11, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold

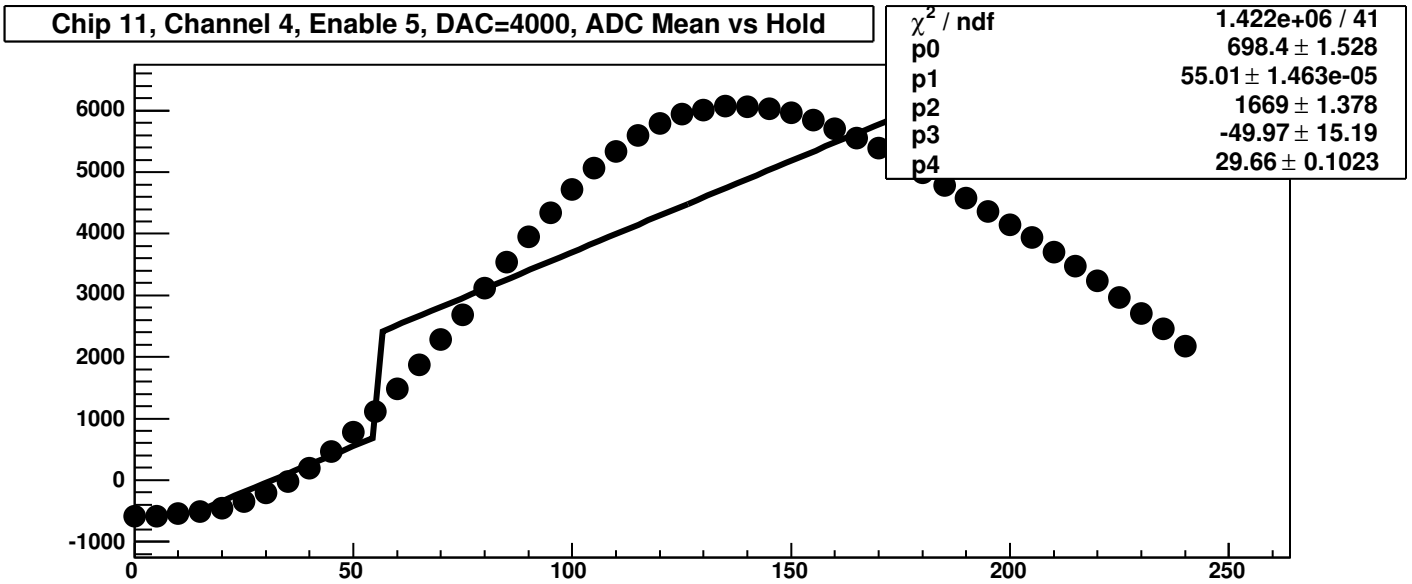


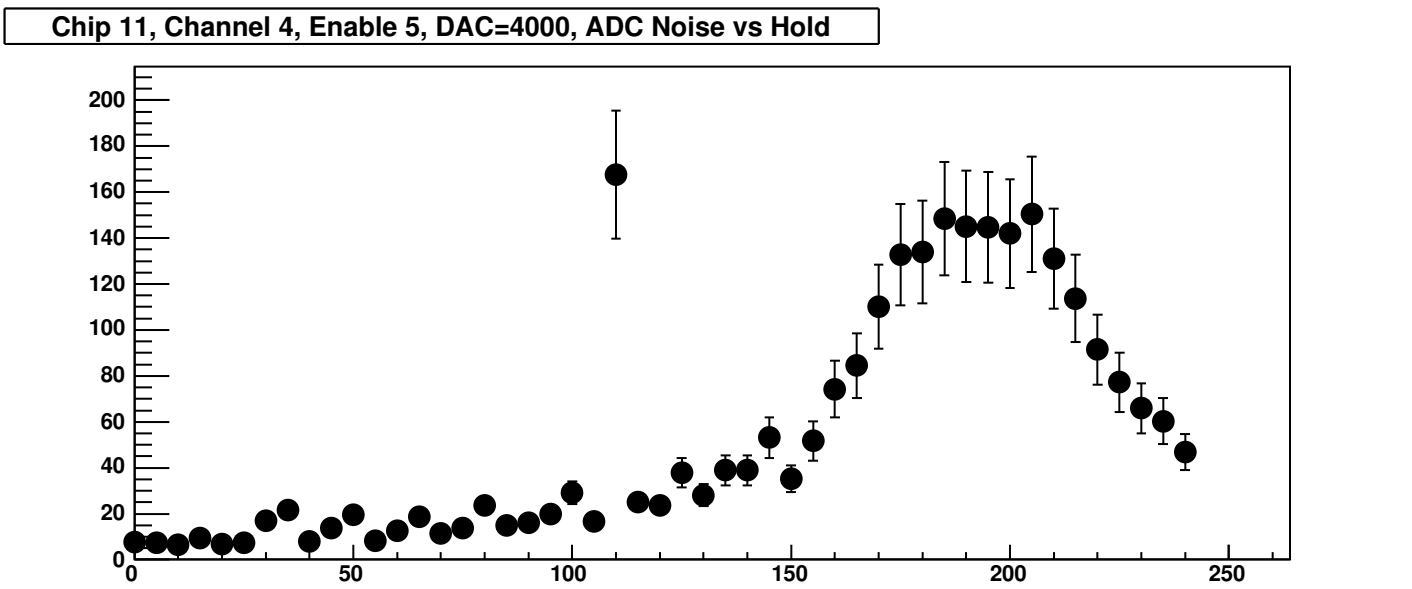
Chip 11, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold

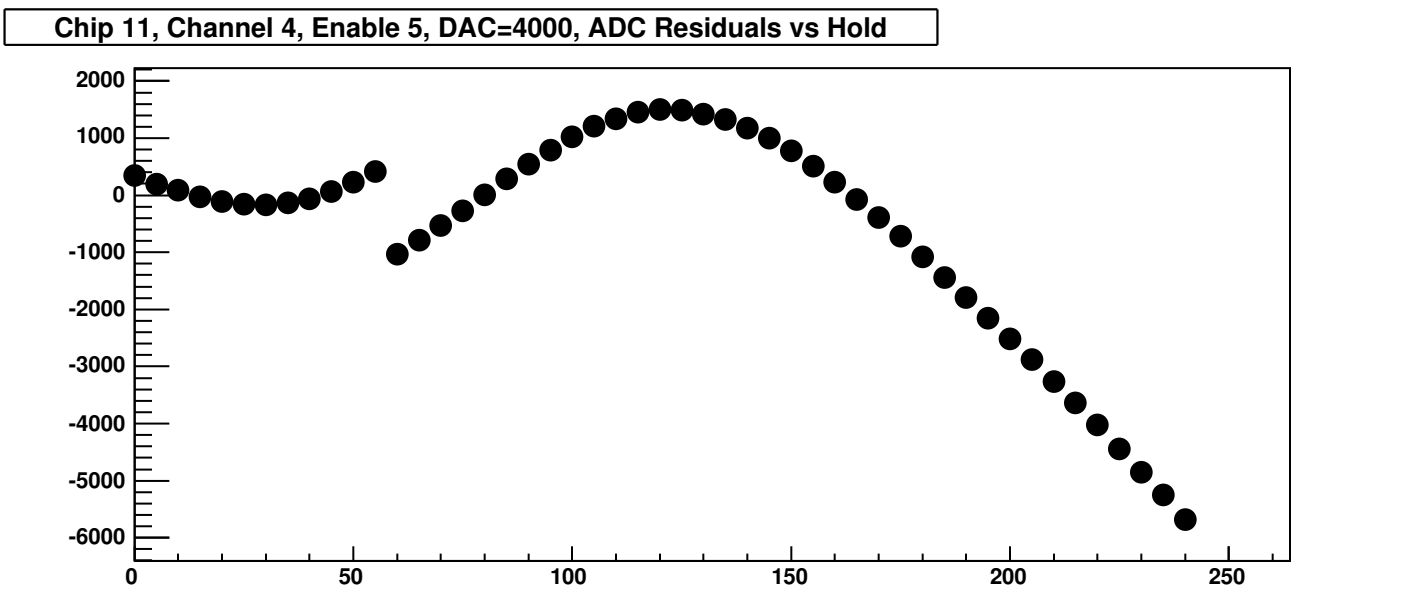


Chip 11, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold

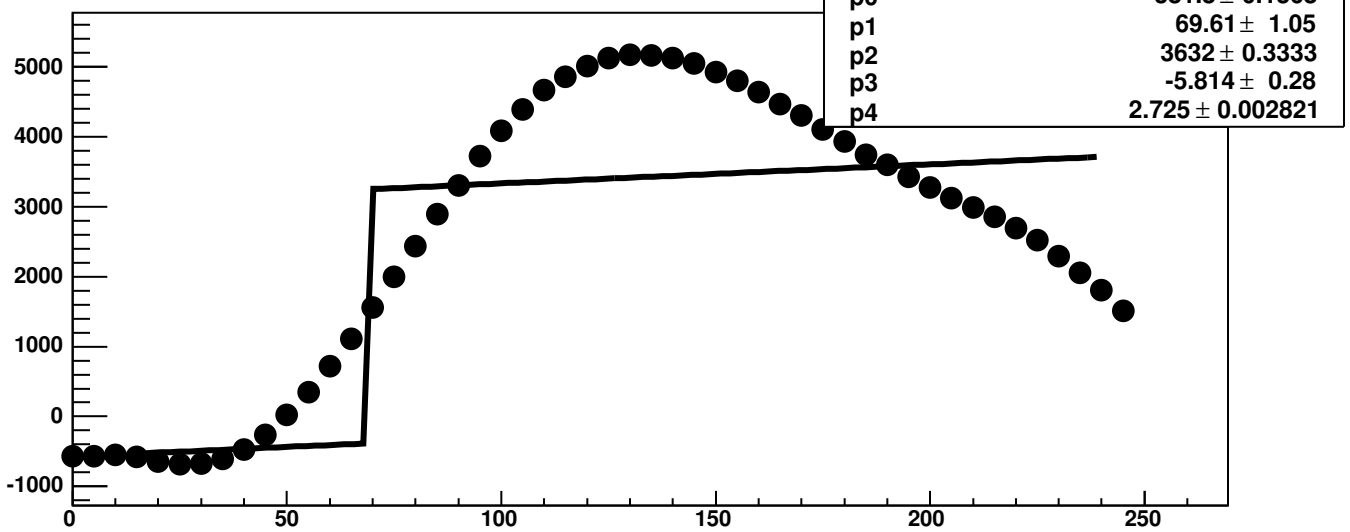



Chip 11, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.422e+06 / 41
p0 698.4 ± 1.528
p1 55.01 ± 1.463e-05
p2 1669 ± 1.378
p3 -49.97 ± 15.19
p4 29.66 ± 0.1023
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

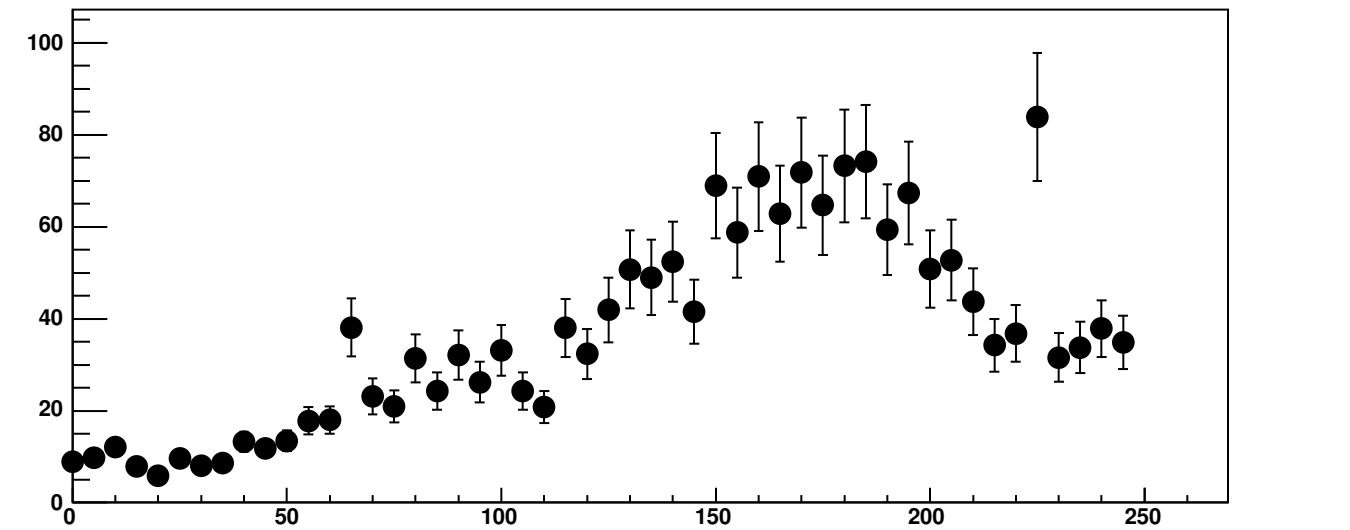

Chip 11, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 11, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
50
100
150
200
250

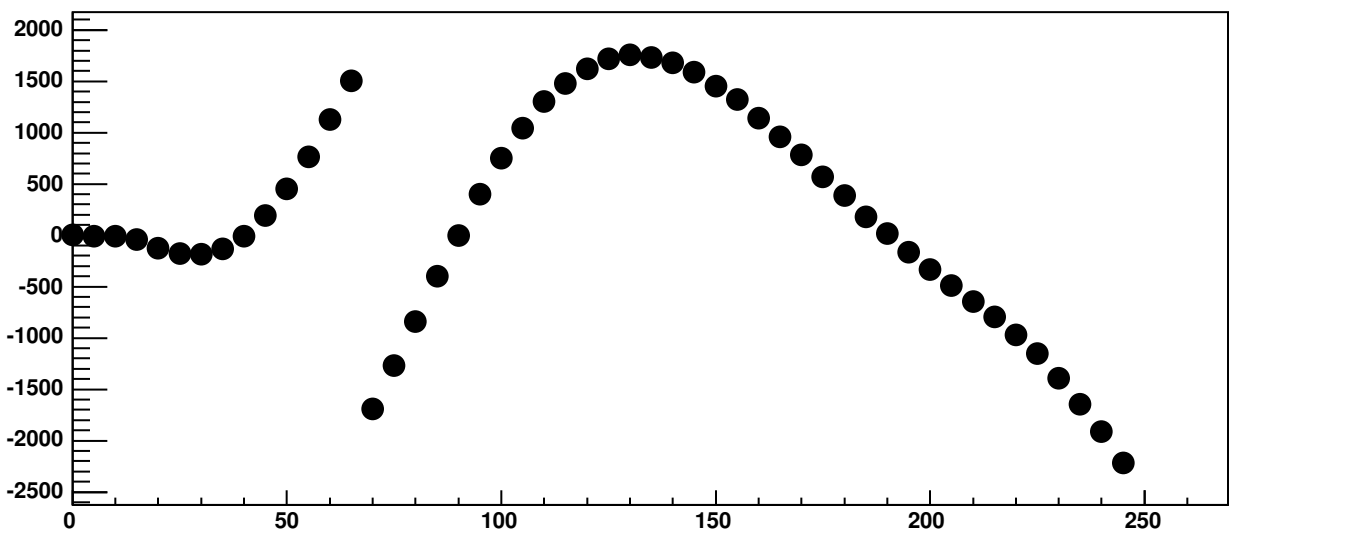
Chip 11, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold



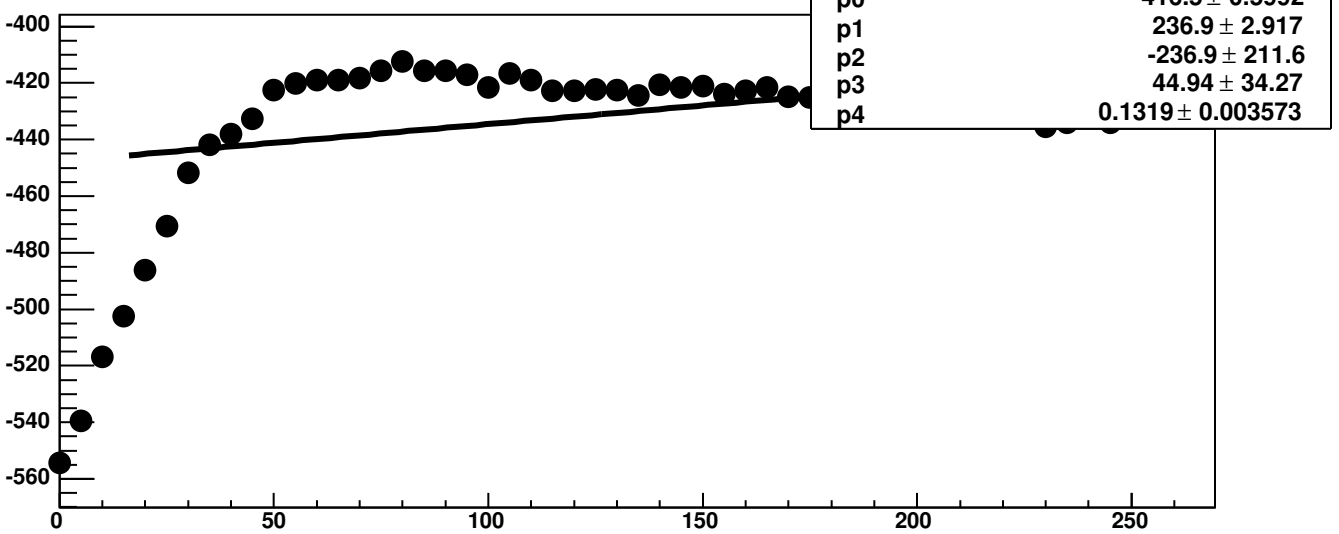
Chip 11, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



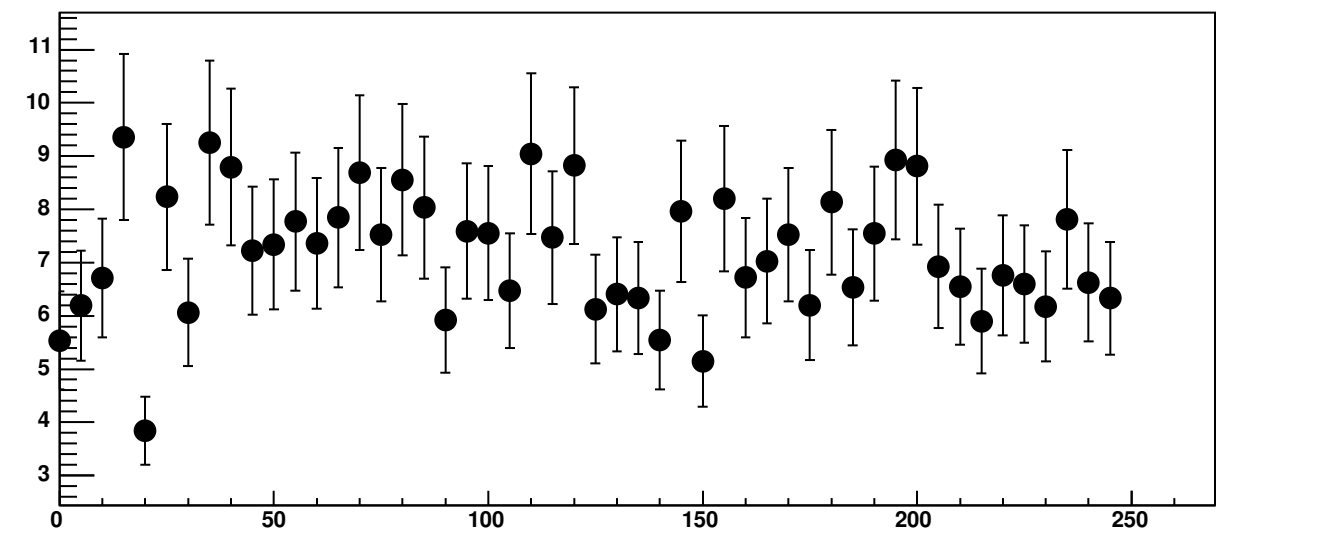
Chip 11, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold



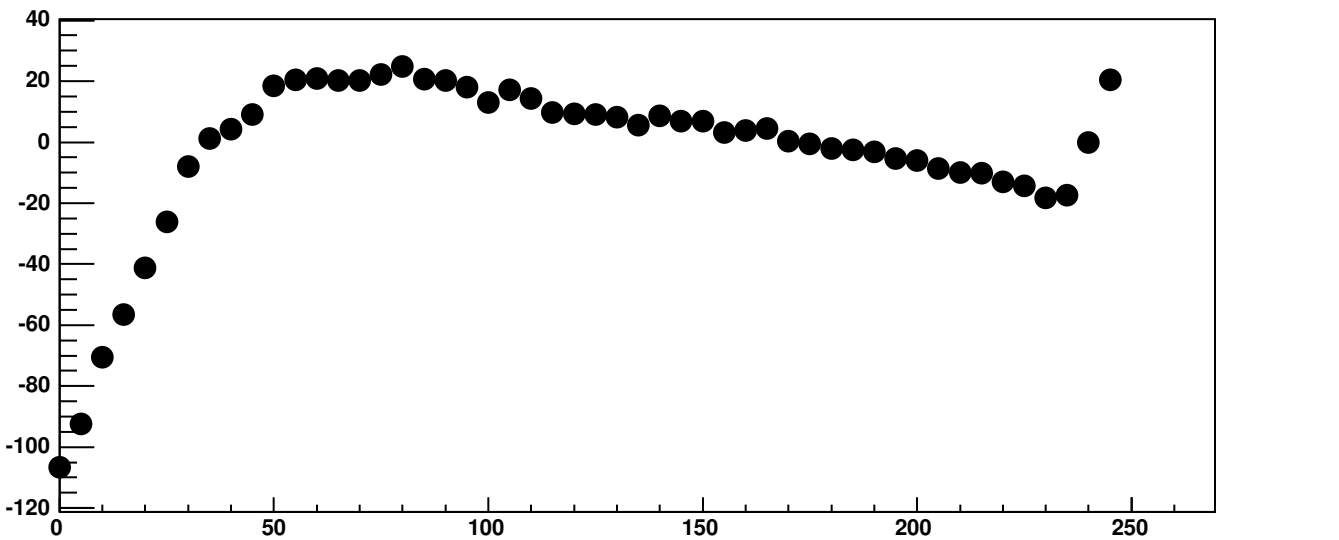
Chip 11, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold



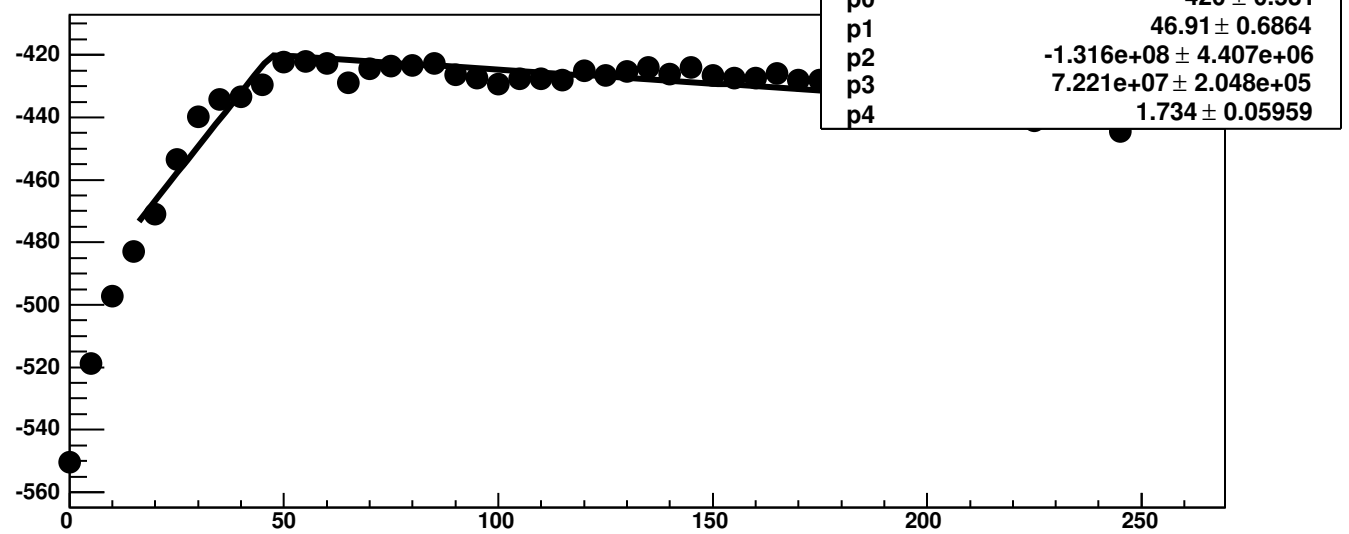
Chip 11, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold



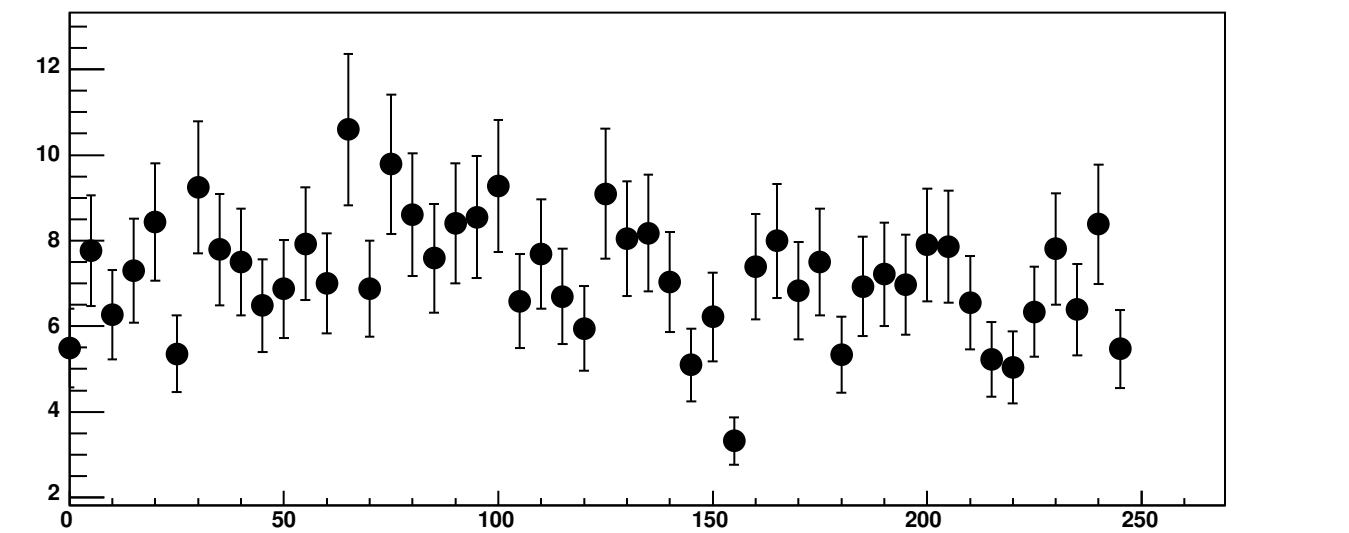
Chip 11, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold



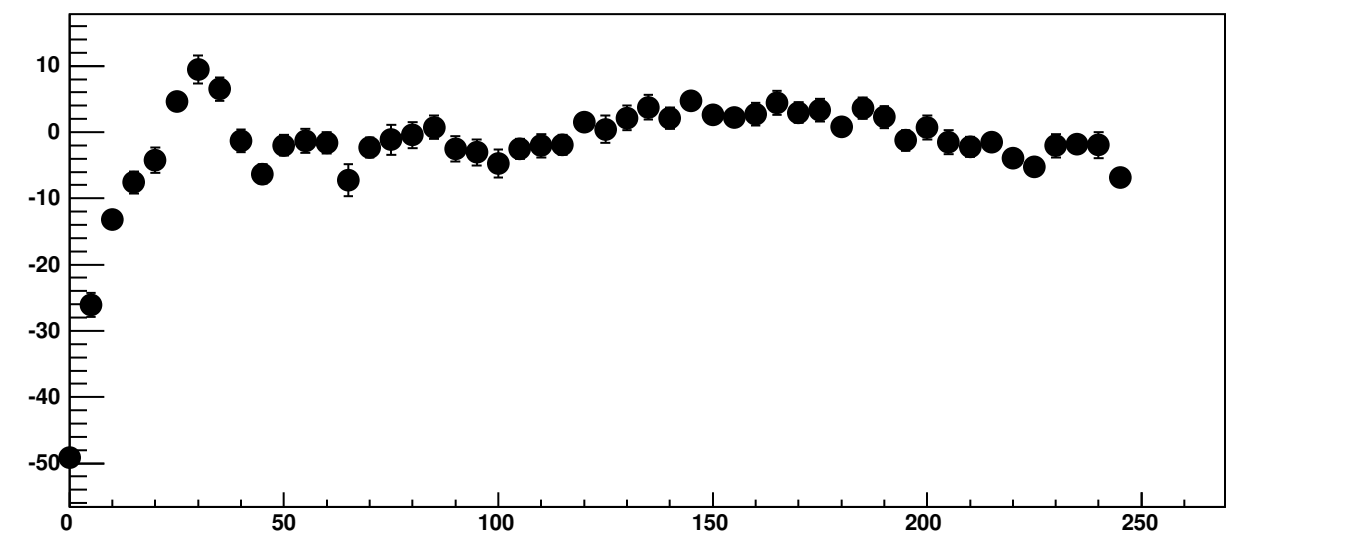
Chip 11, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

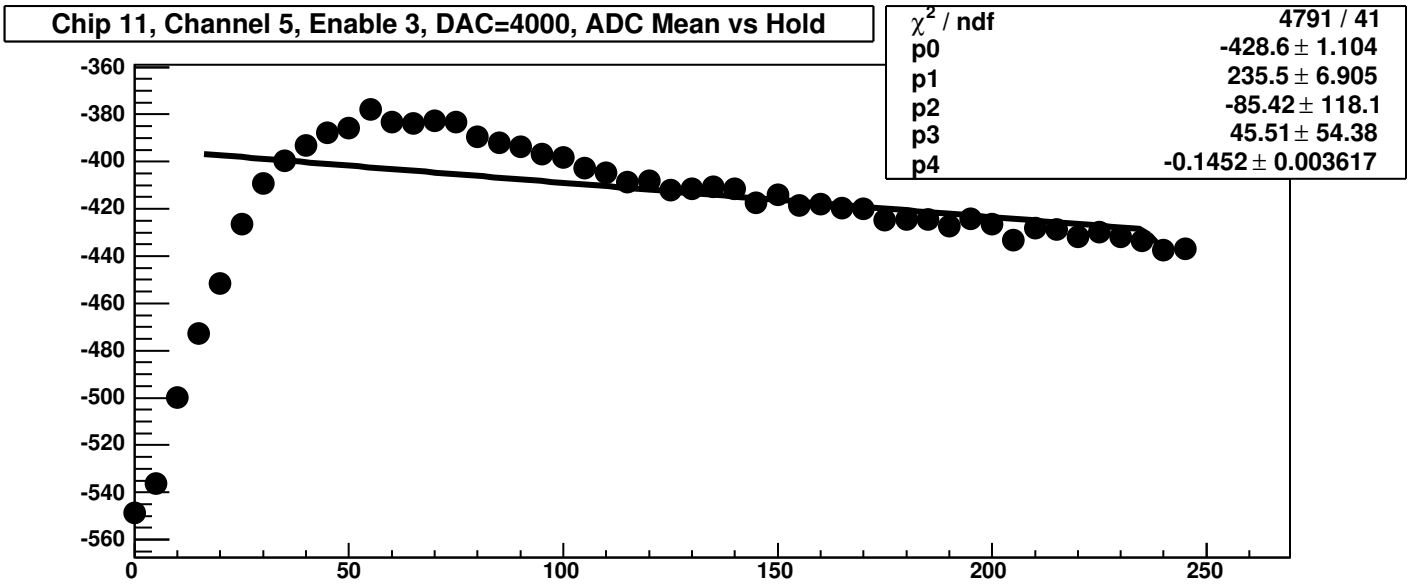


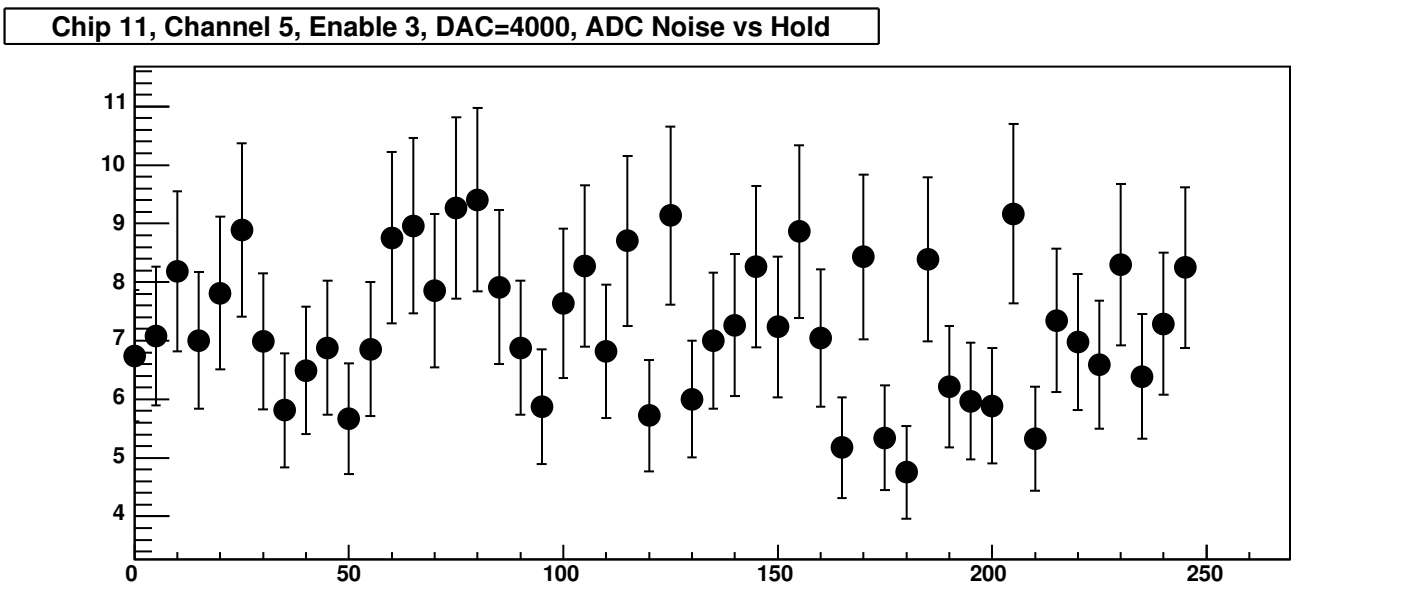
Chip 11, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold

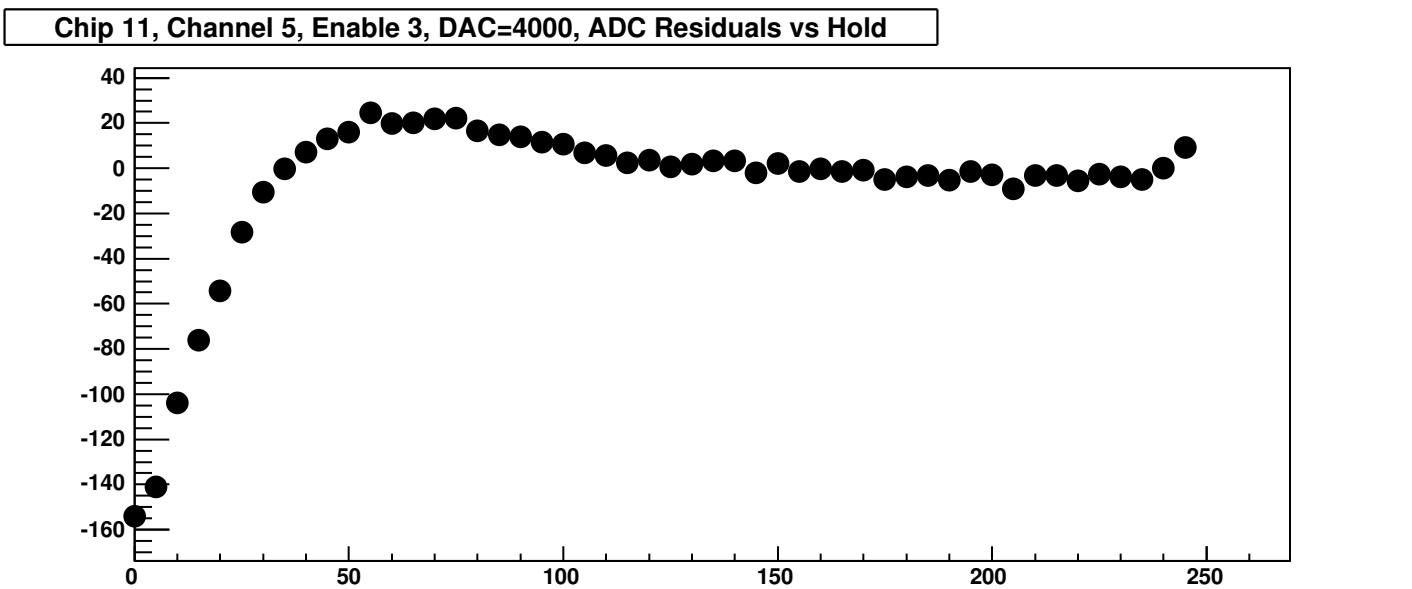


Chip 11, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

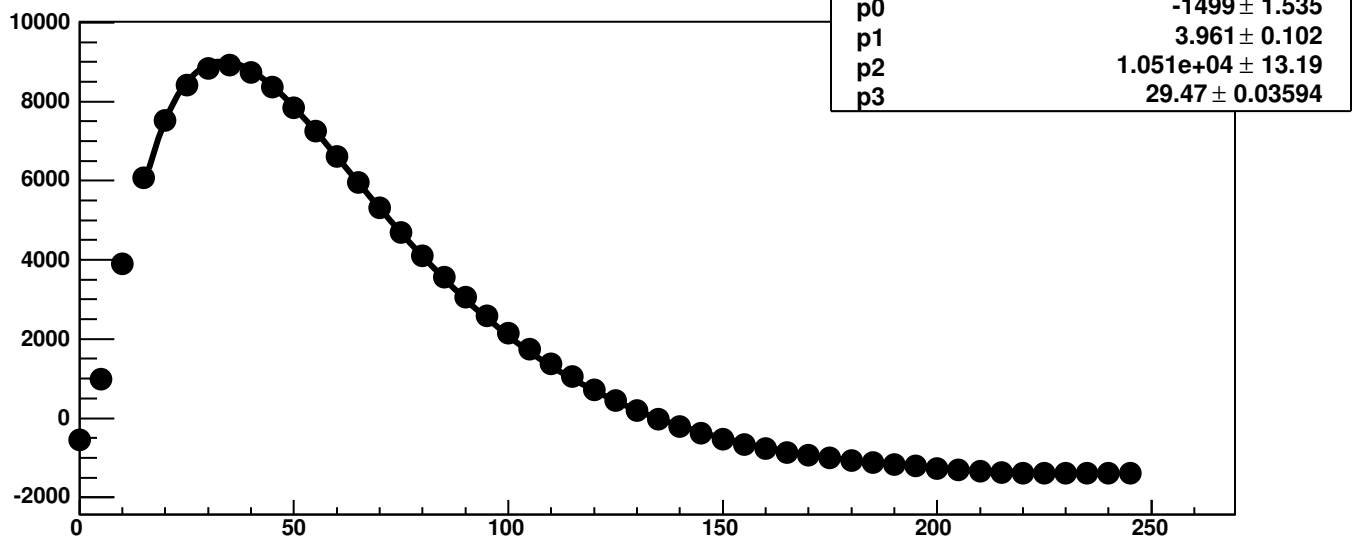



Chip 11, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 4791 / 41
p0 -428.6 ± 1.104
p1 235.5 ± 6.905
p2 -85.42 ± 118.1
p3 45.51 ± 54.38
p4 -0.1452 ± 0.003617
-360
-380
-400
-420
-440
-460
-480
-500
-520
-540
-560
0
50
100
150
200
250

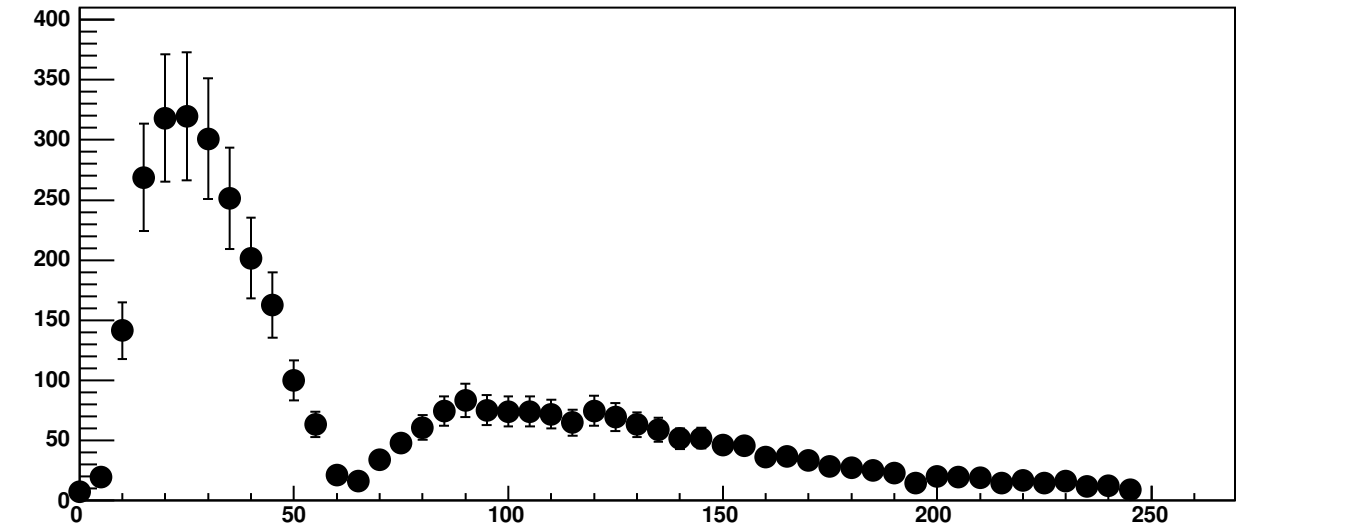

Chip 11, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 11, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
50
100
150
200
250

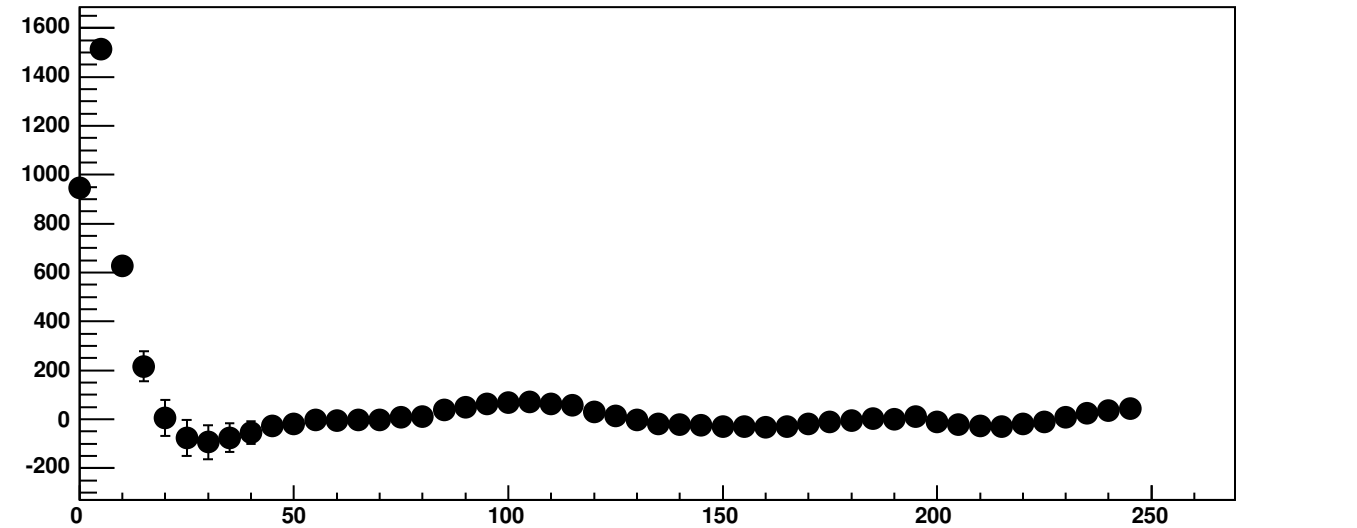
Chip 11, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold



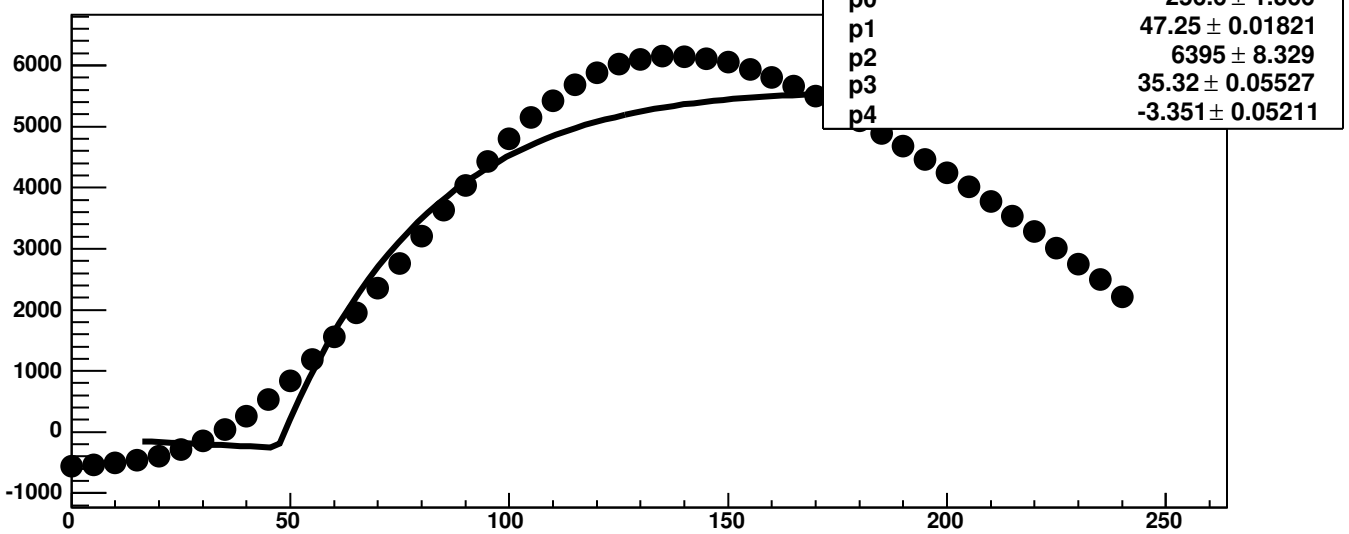
Chip 11, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold



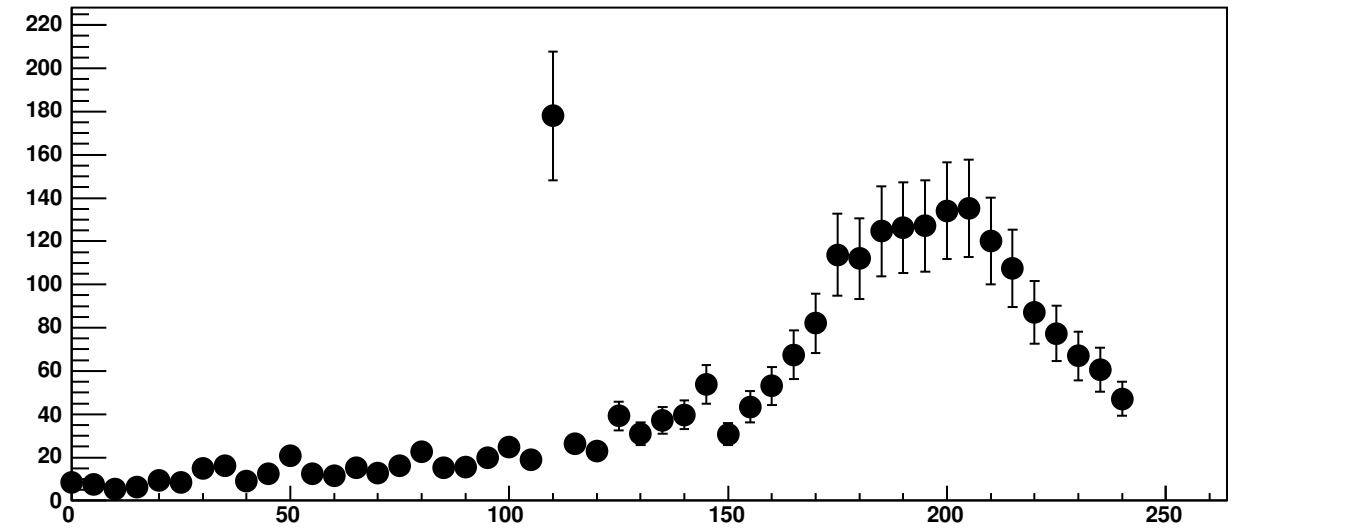
Chip 11, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold



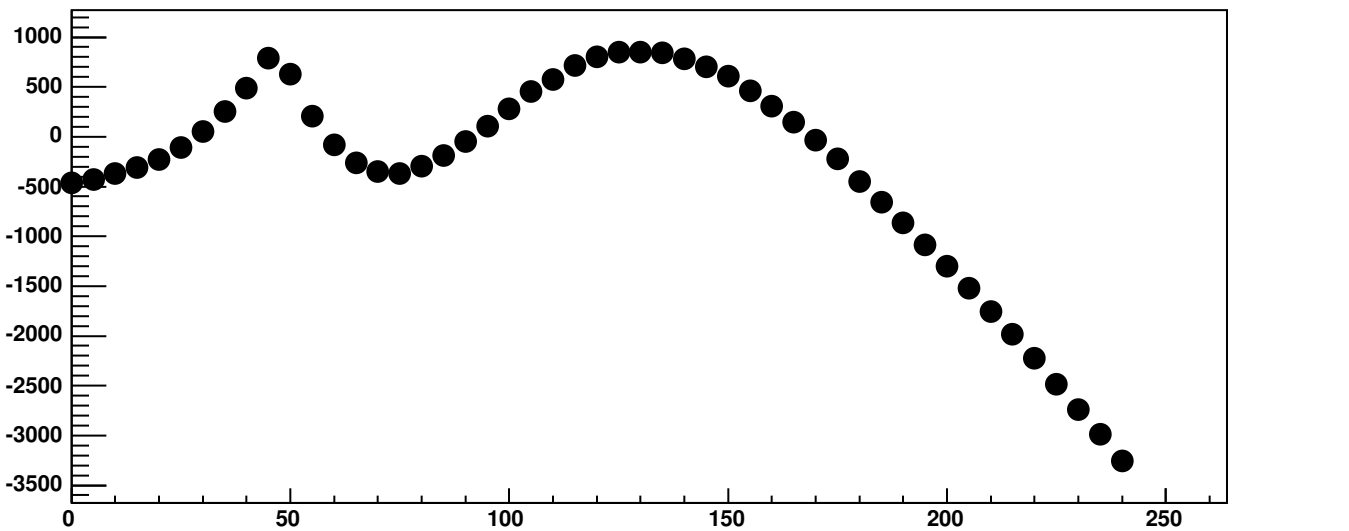
Chip 11, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

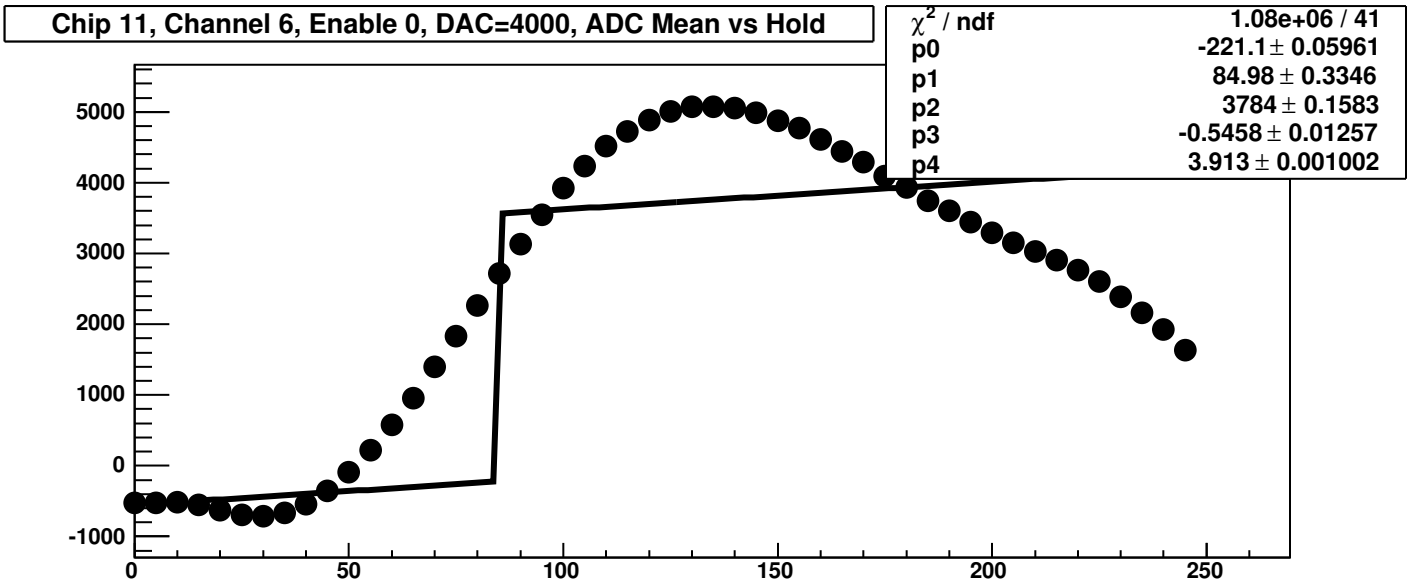


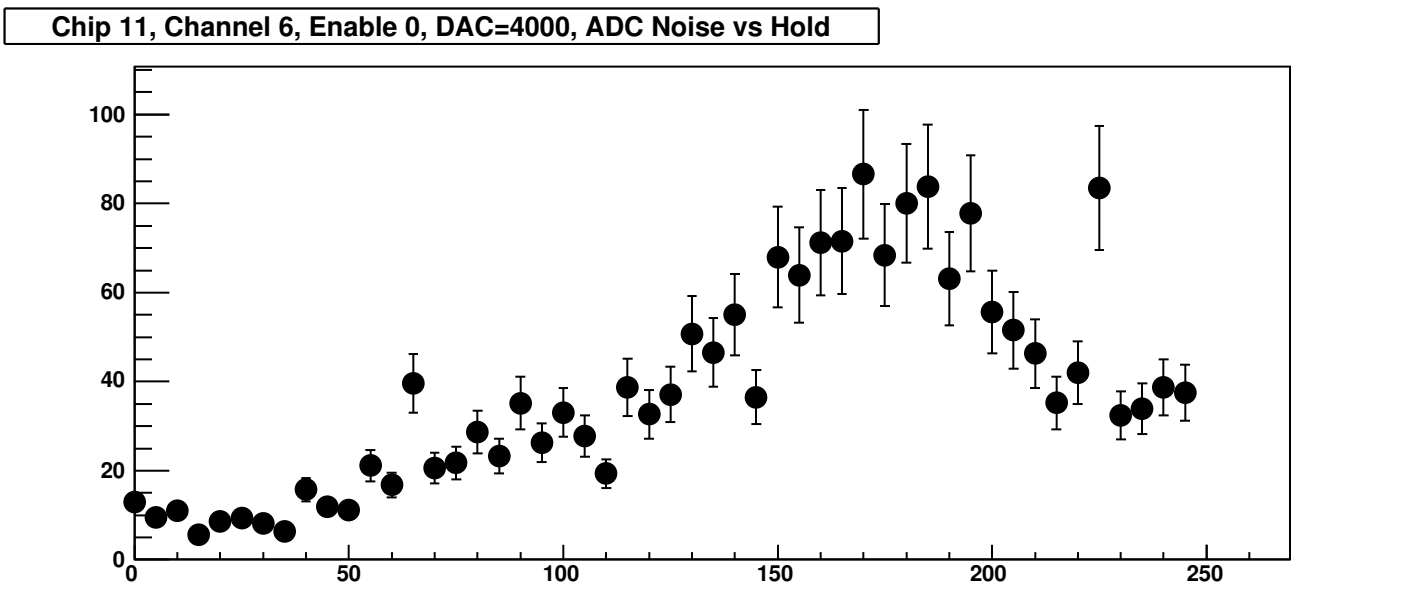
Chip 11, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold

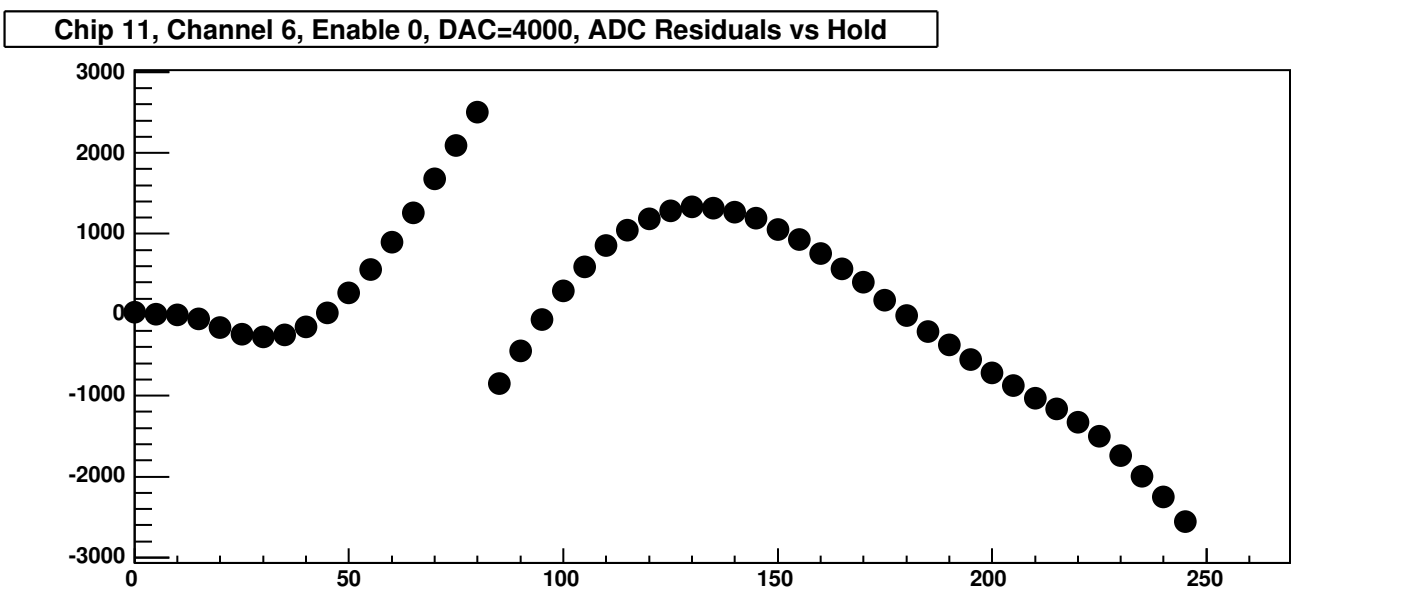


Chip 11, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

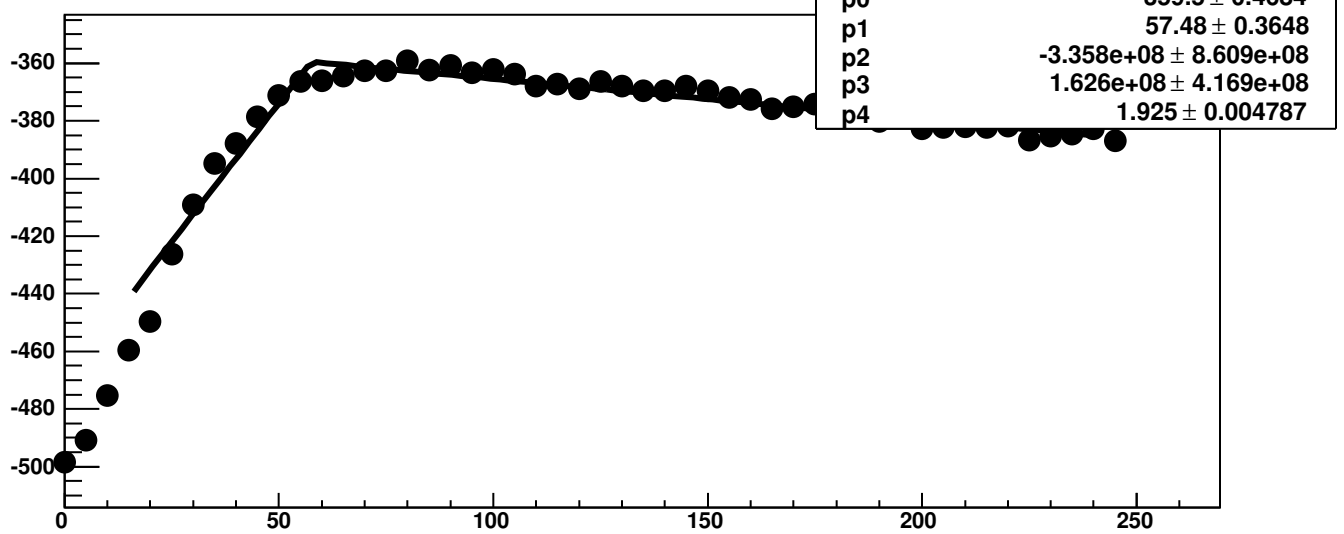



Chip 11, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.08e+06 / 41
p0 -221.1 ± 0.05961
p1 84.98 ± 0.3346
p2 3784 ± 0.1583
p3 -0.5458 ± 0.01257
p4 3.913 ± 0.001002
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

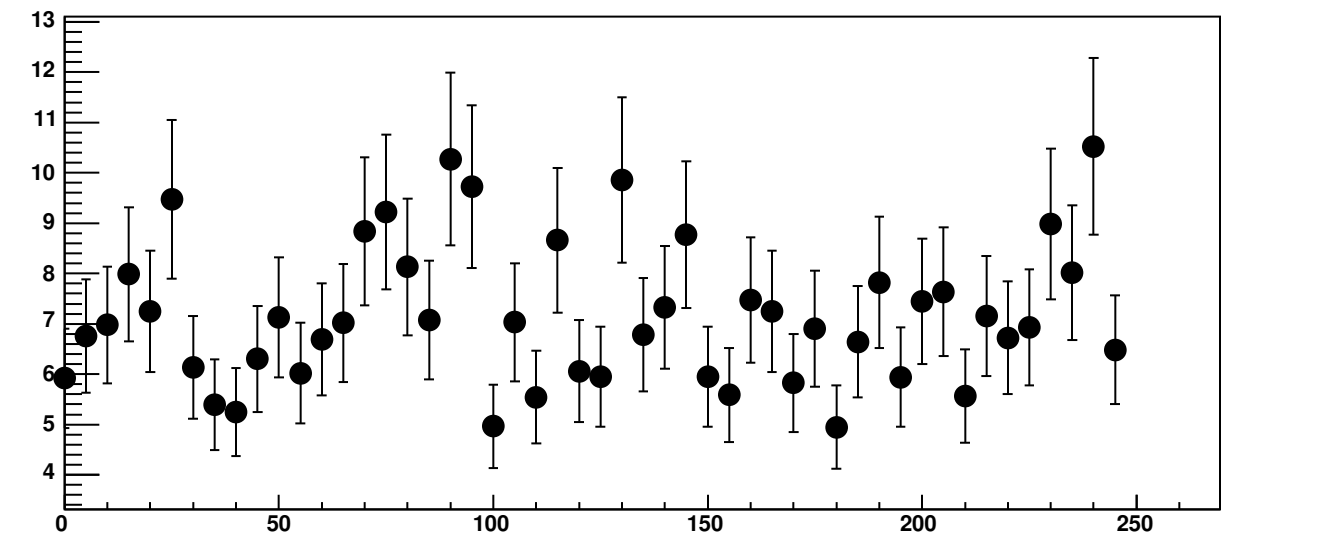

Chip 11, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250


Chip 11, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

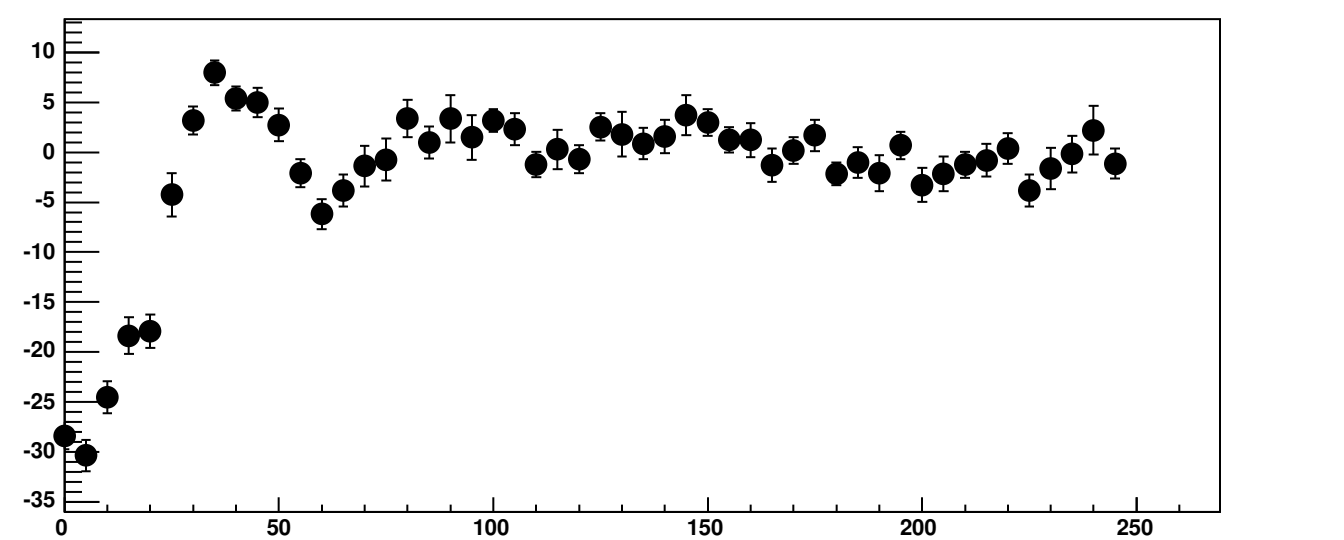
Chip 11, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold



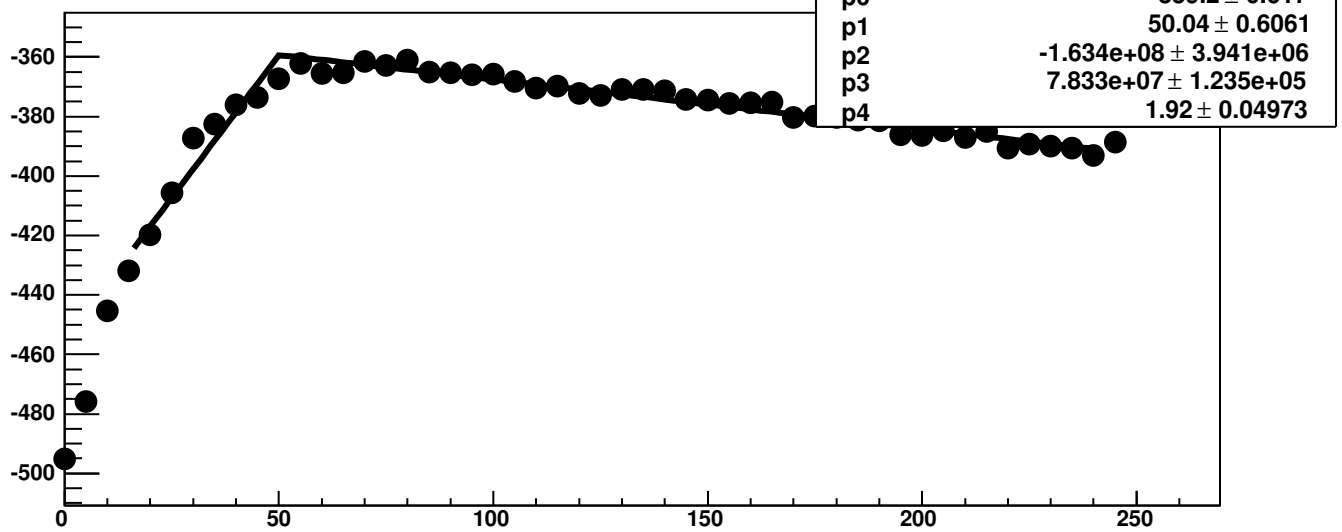
Chip 11, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



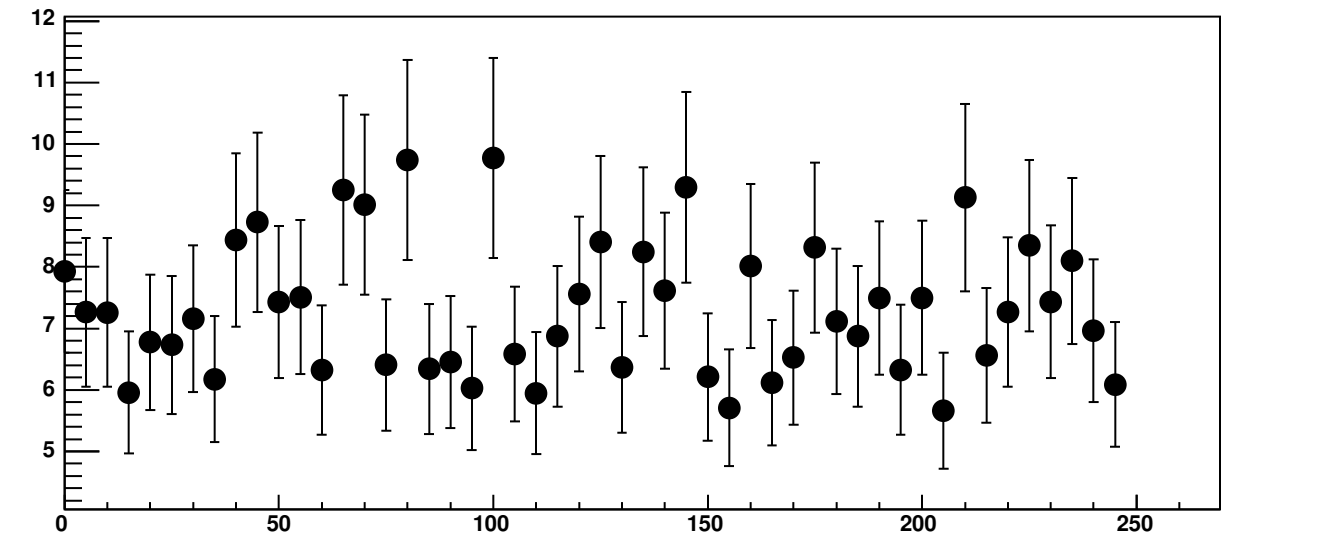
Chip 11, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold



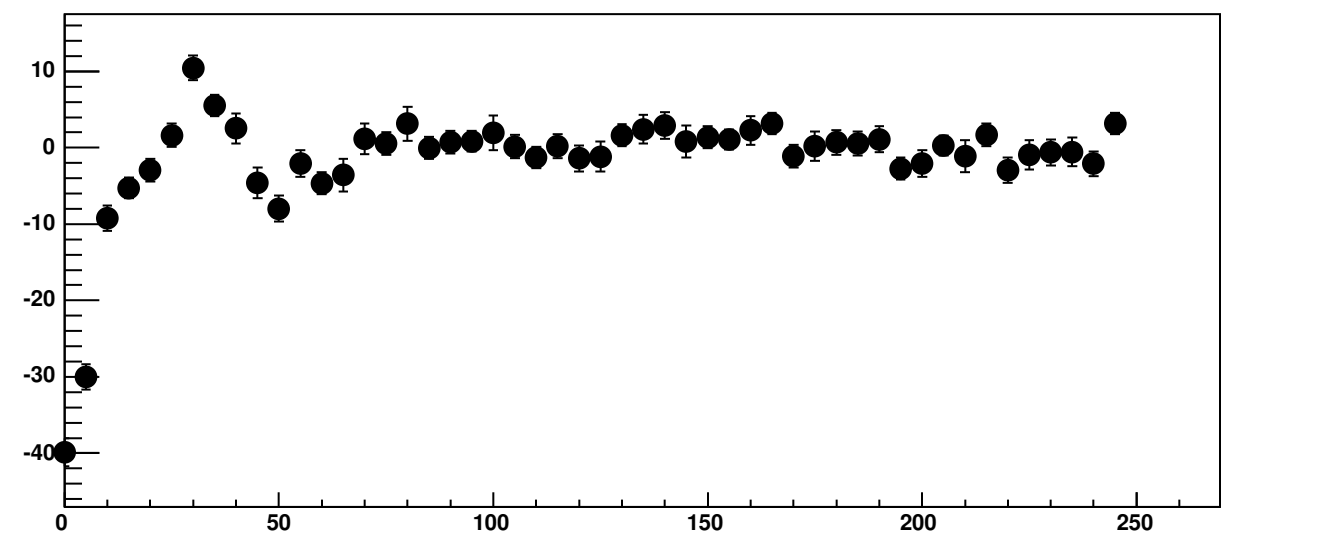
Chip 11, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold



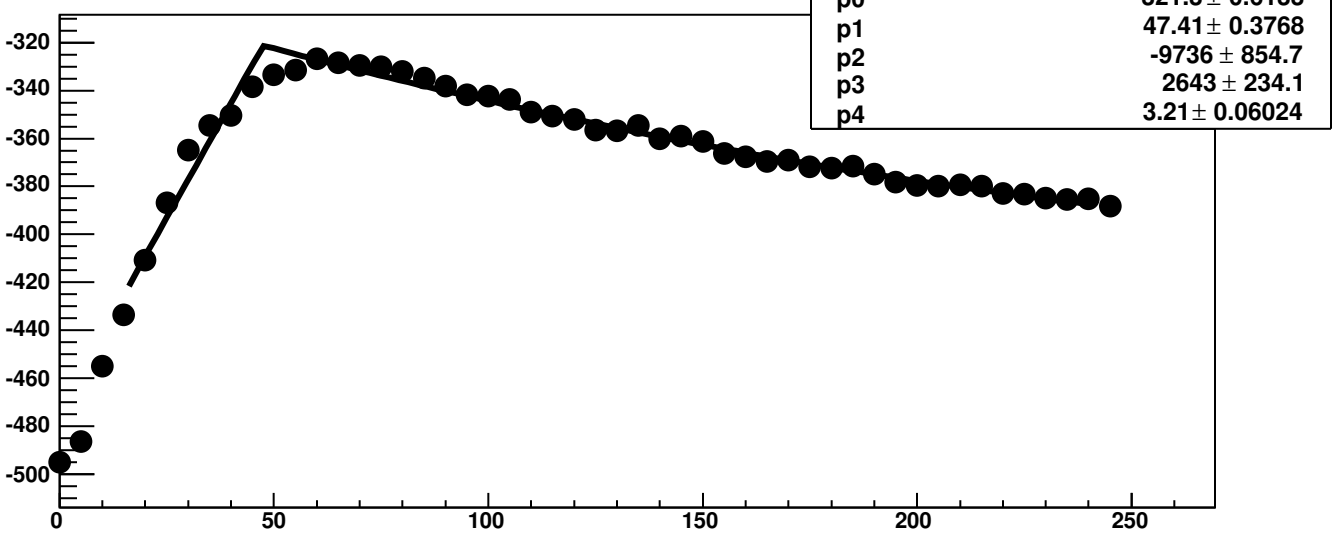
Chip 11, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold



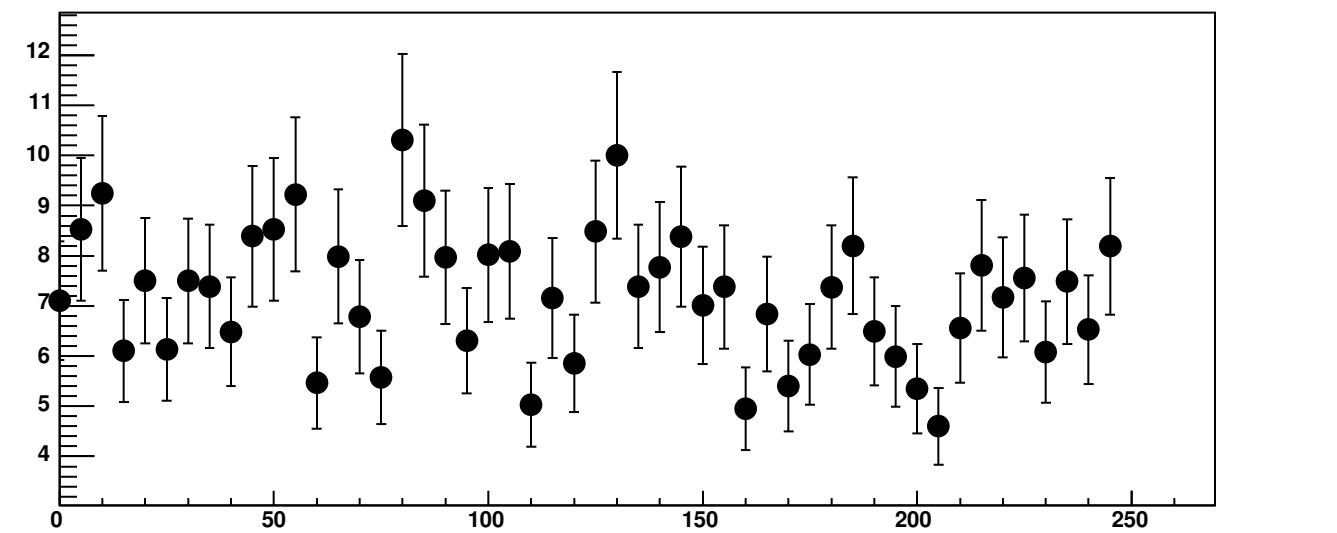
Chip 11, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold



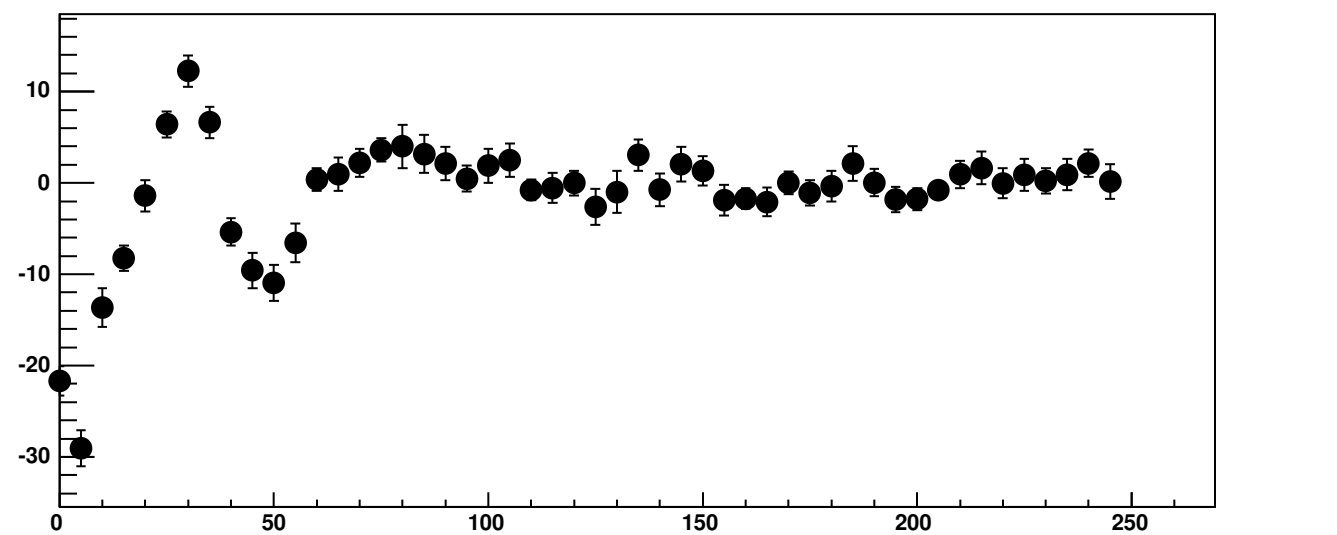
Chip 11, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold



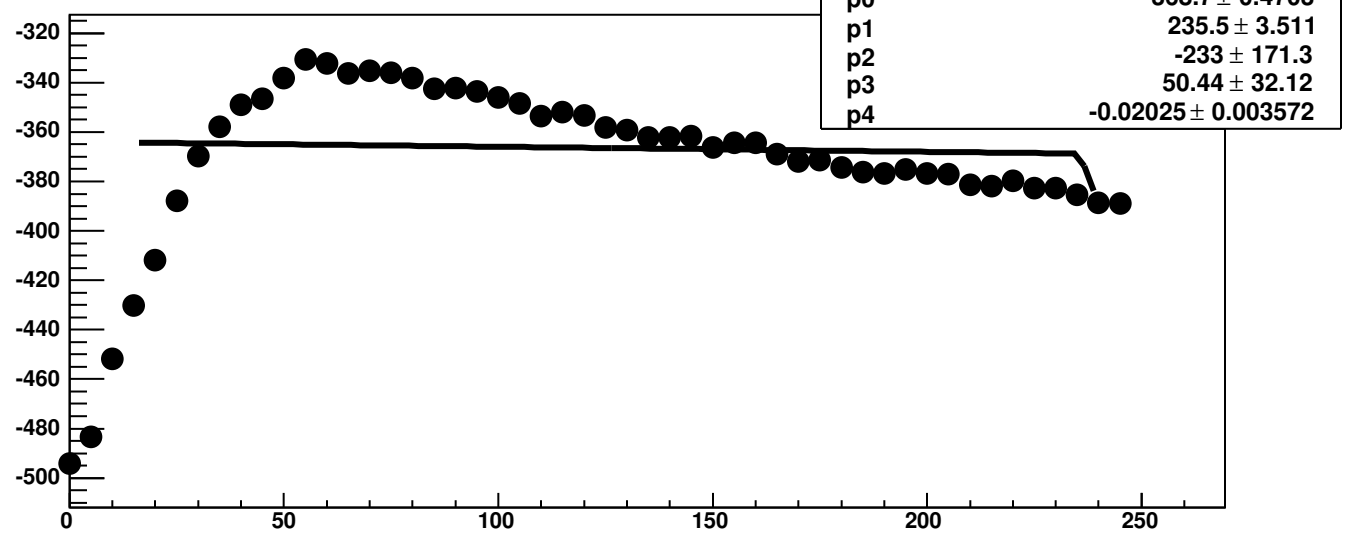
Chip 11, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



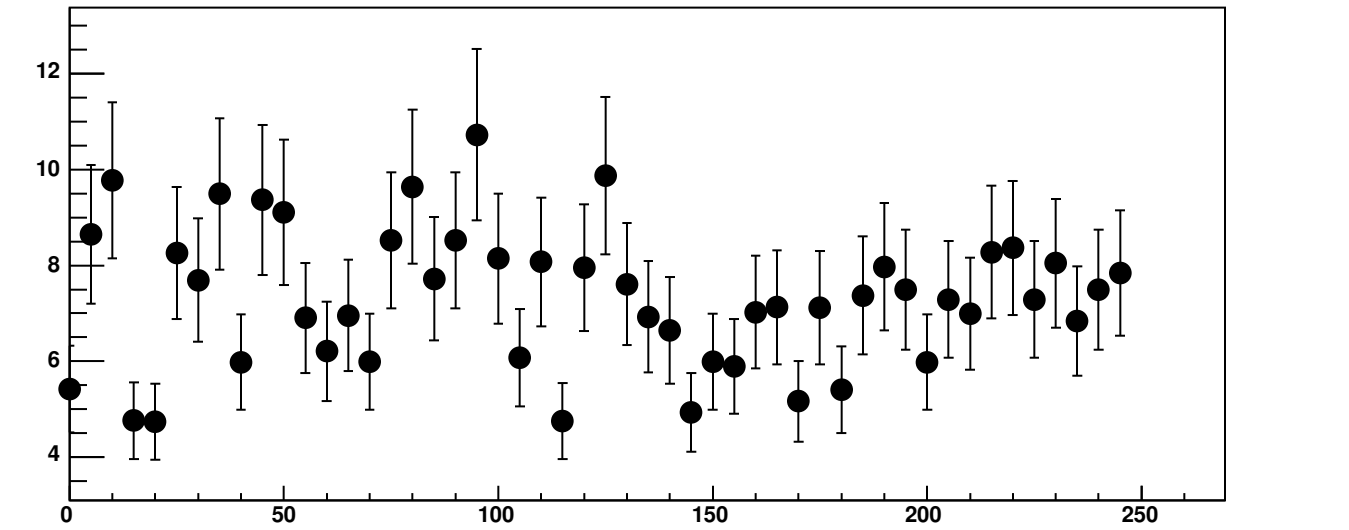
Chip 11, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold



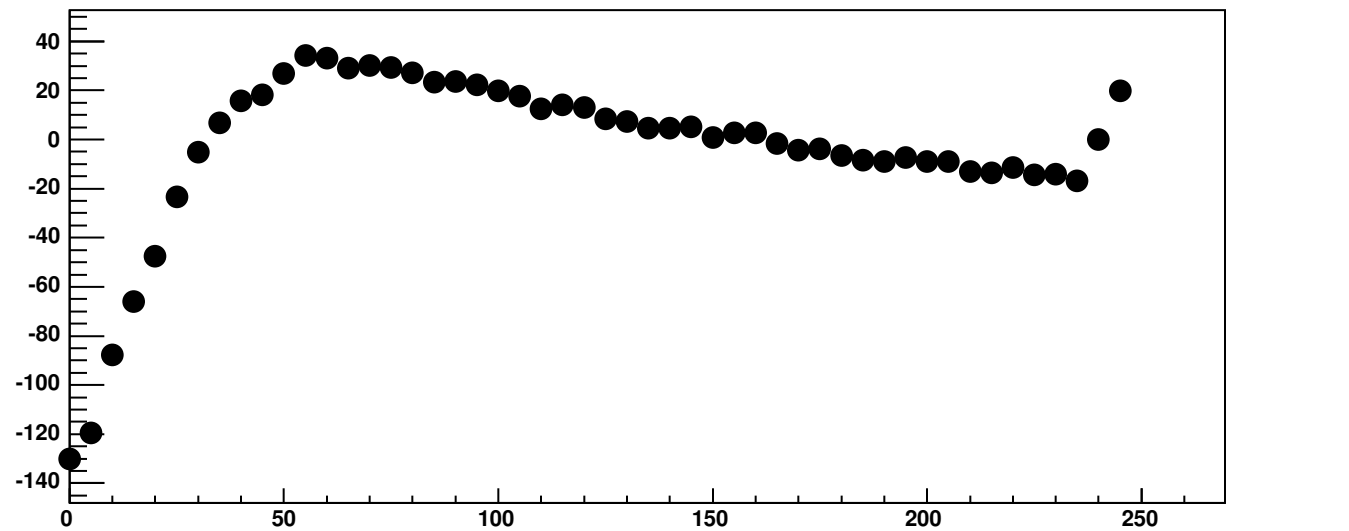
Chip 11, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold



Chip 11, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold

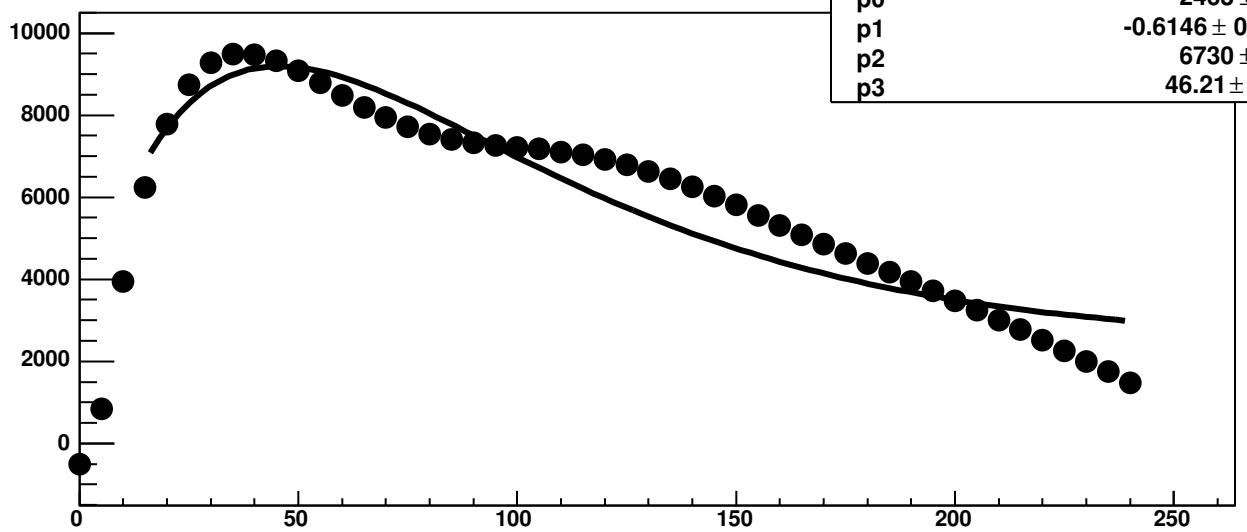


Chip 11, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

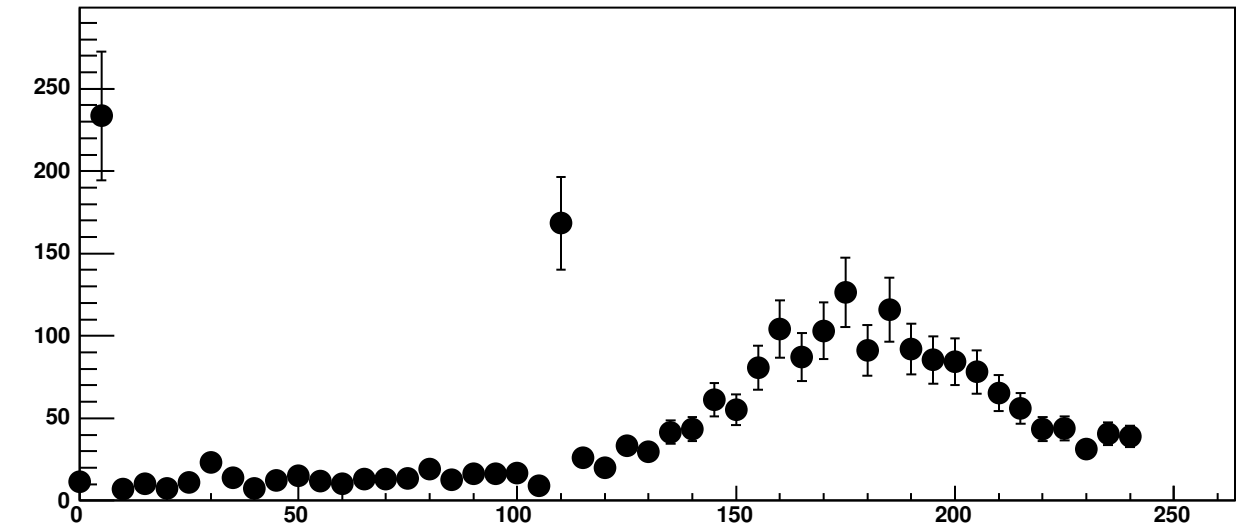


Chip 11, Channel 6, Enable 5!, DAC=4000, ADC Mean vs Hold

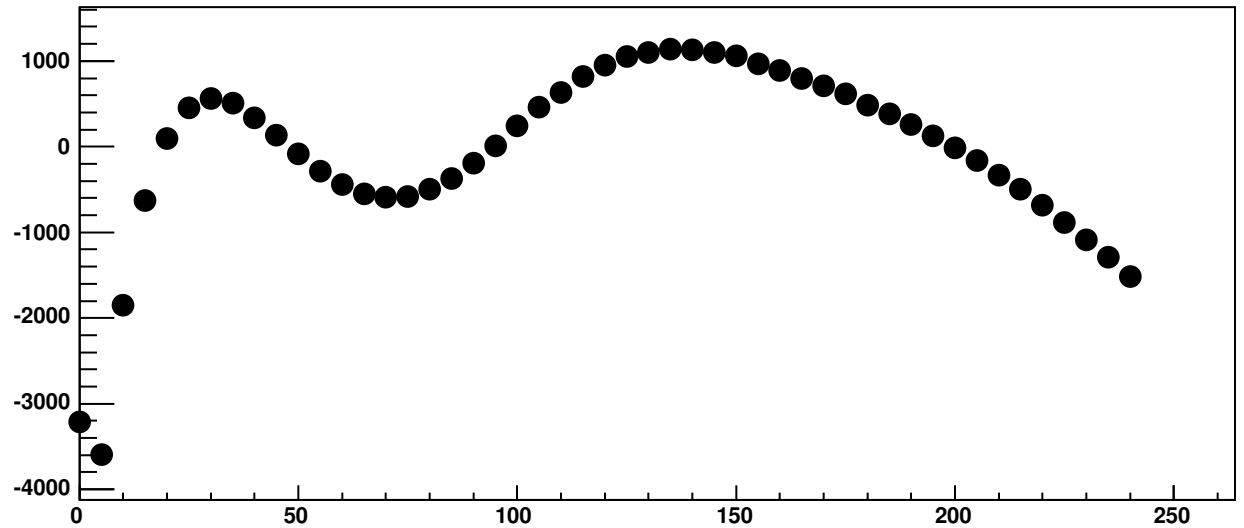
χ^2 / ndf	6.645e+05 / 42
p0	2463 ± 5.922
p1	-0.6146 ± 0.03146
p2	6730 ± 5.638
p3	46.21 ± 0.0401



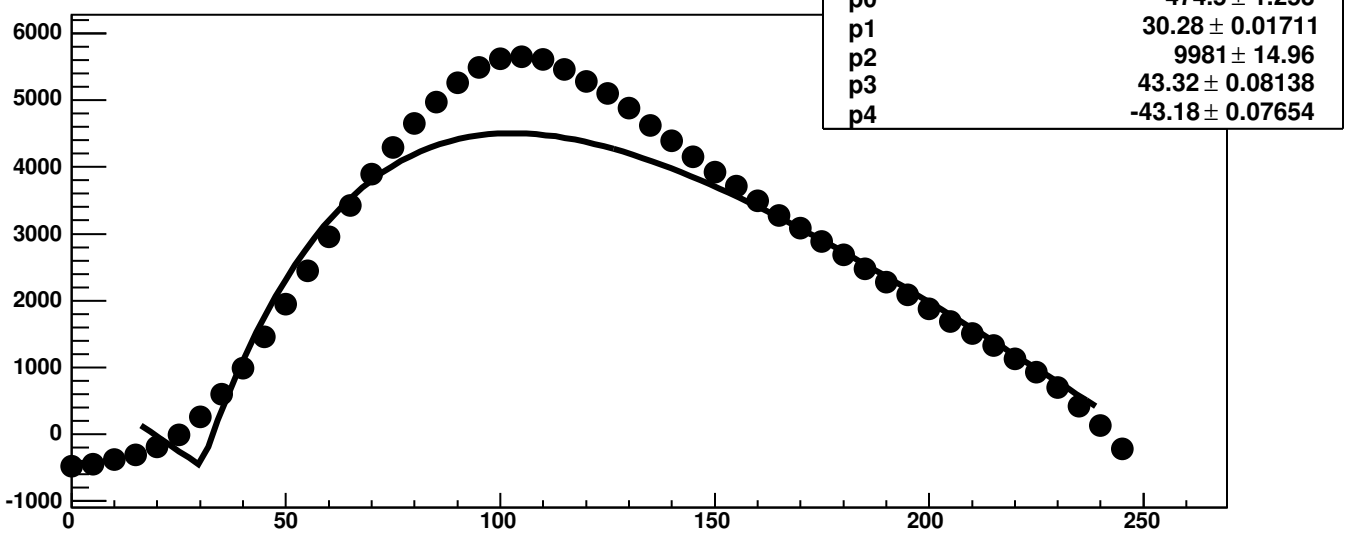
Chip 11, Channel 6, Enable 5!, DAC=4000, ADC Noise vs Hold



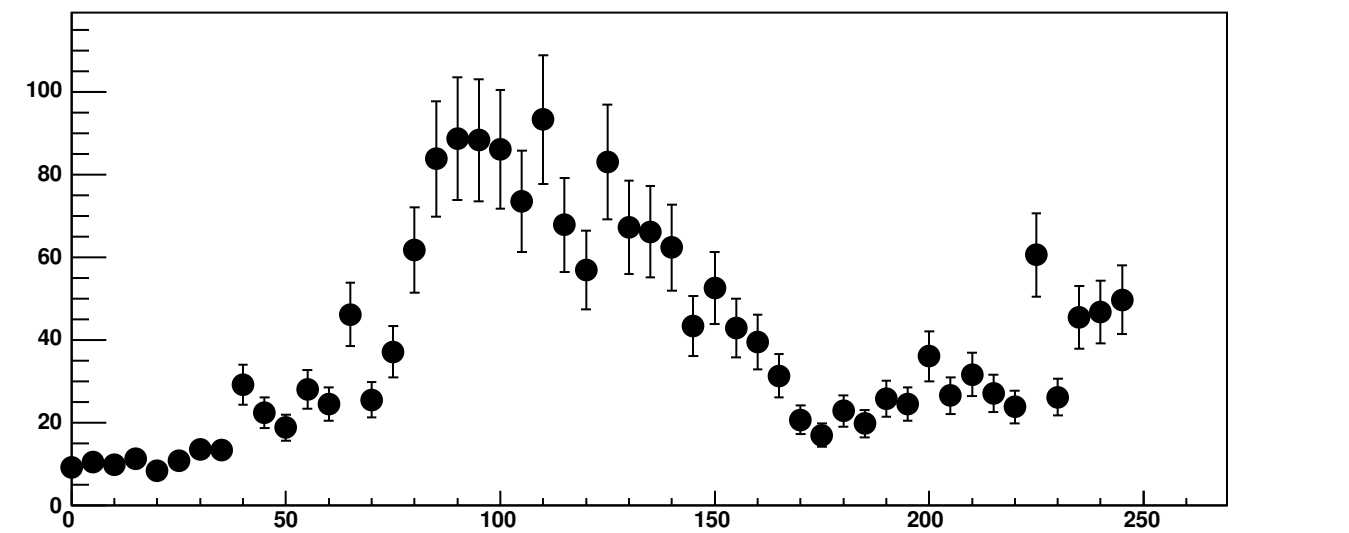
Chip 11, Channel 6, Enable 5!, DAC=4000, ADC Residuals vs Hold



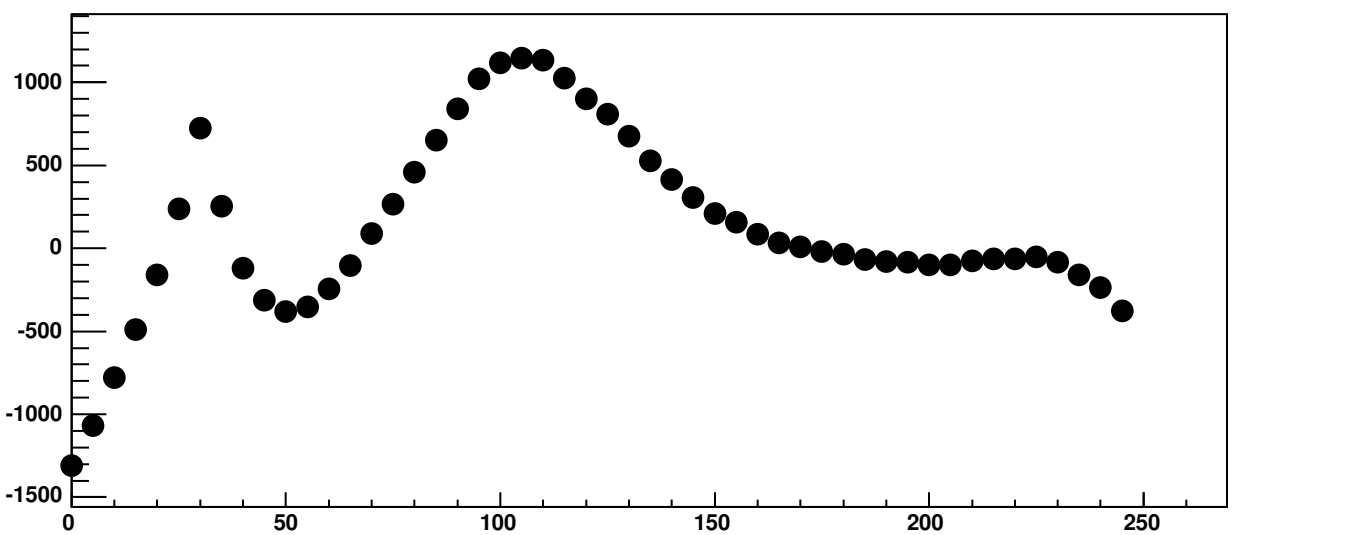
Chip 11, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

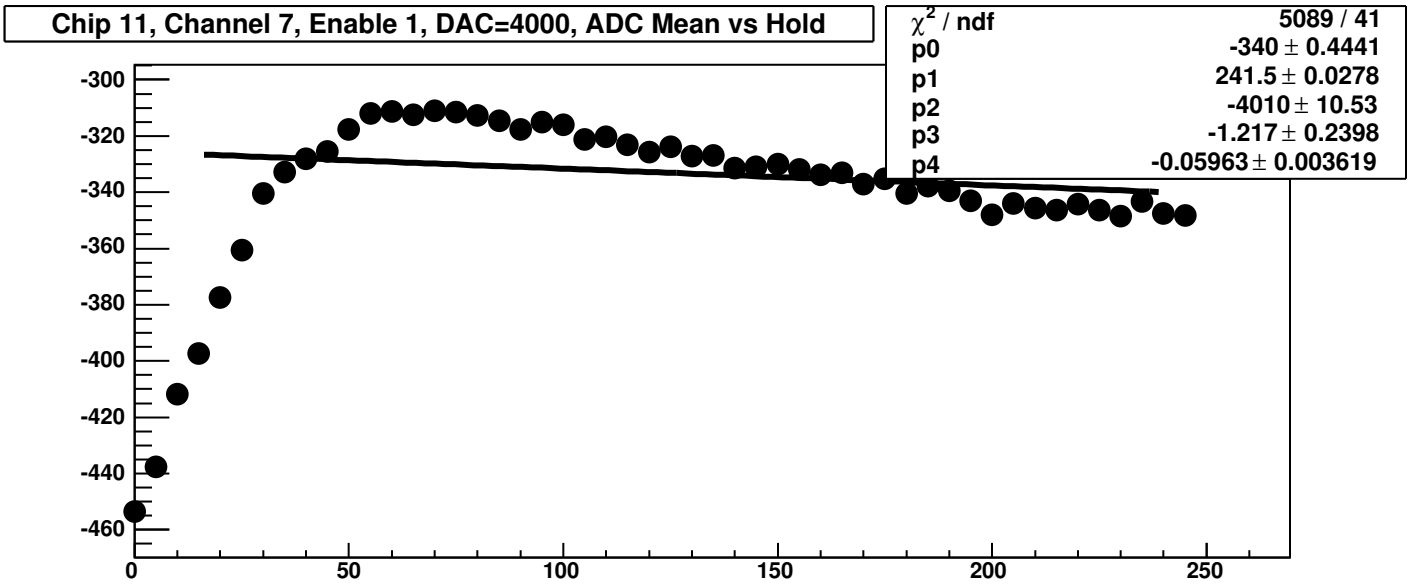


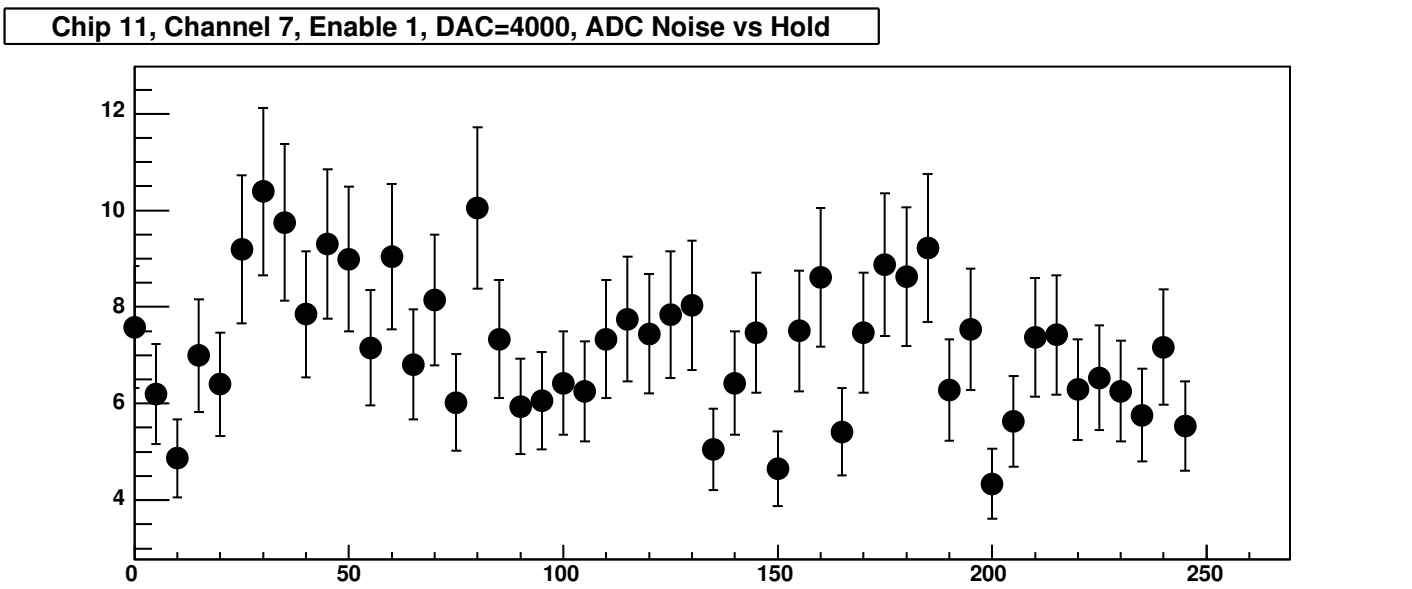
Chip 11, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold

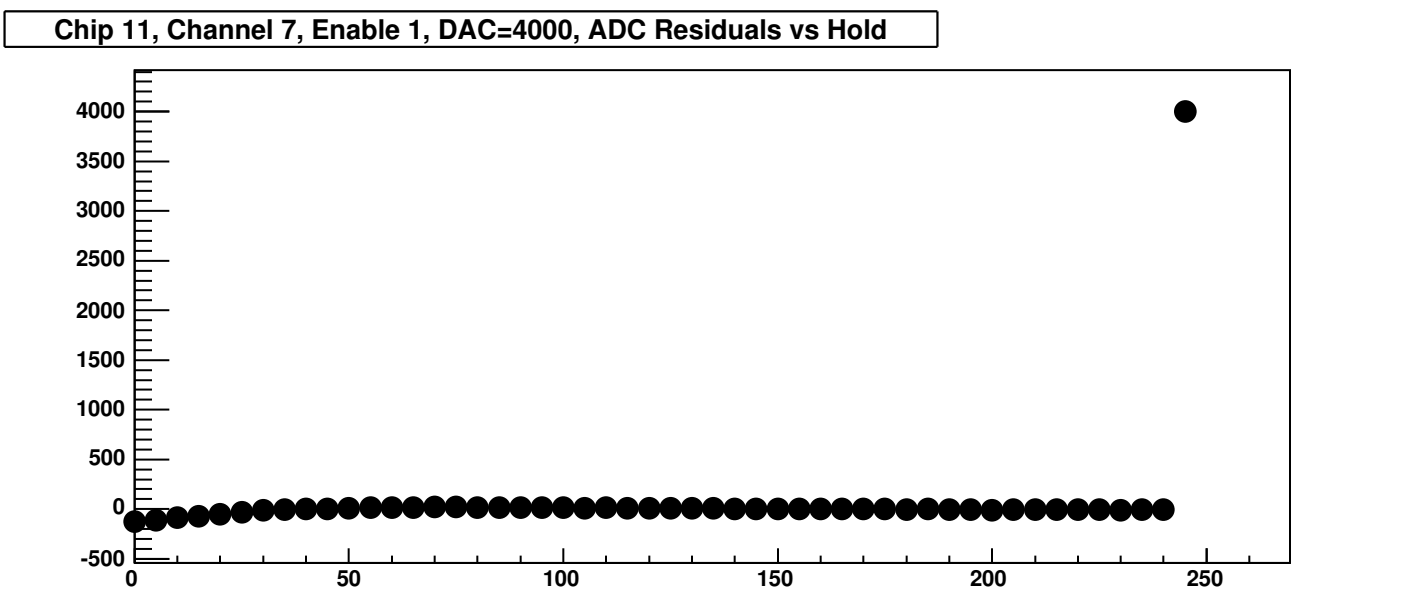


Chip 11, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold



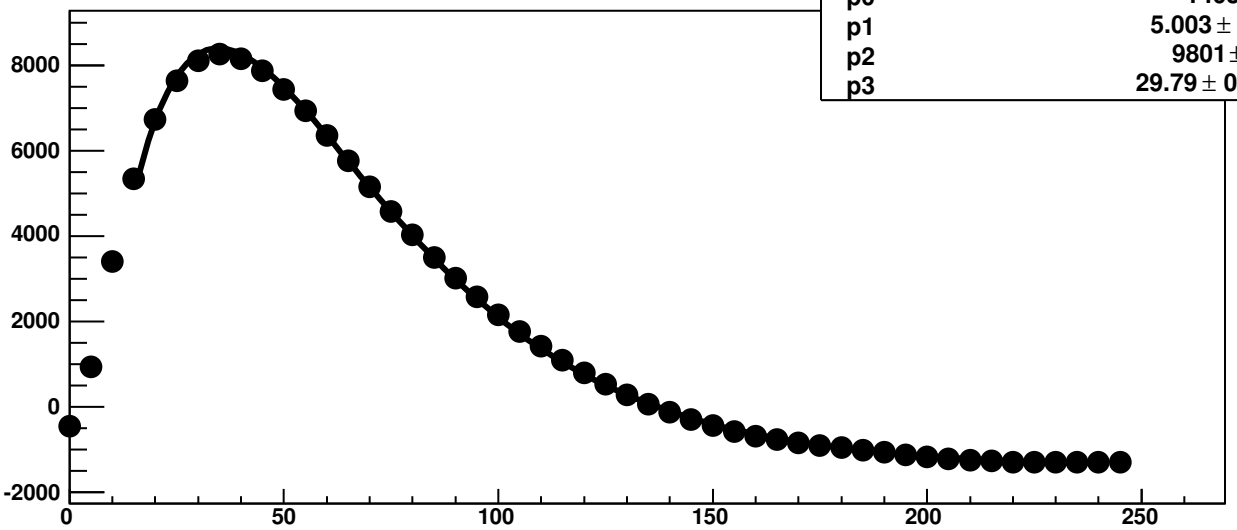

Chip 11, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 5089 / 41
p0 -340 ± 0.4441
p1 241.5 ± 0.0278
p2 -4010 ± 10.53
p3 -1.217 ± 0.2398
p4 -0.05963 ± 0.003619
-300
-320
-340
-360
-380
-400
-420
-440
-460
0
50
100
150
200
250


Chip 11, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

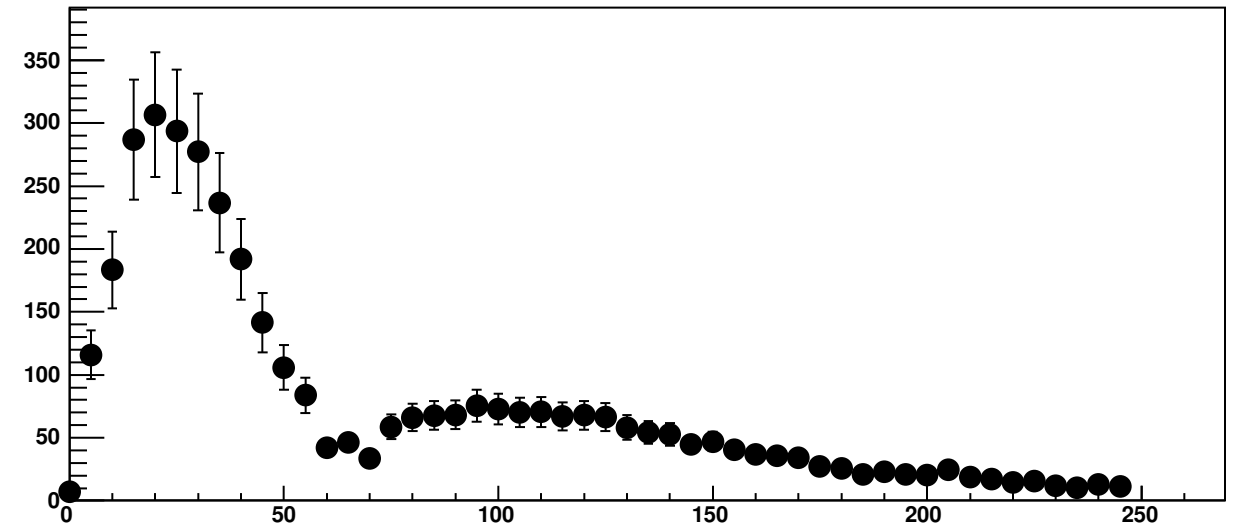

Chip 11, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold
4000
3500
3000
2500
2000
1500
1000
500
0
-500
0
50
100
150
200
250

Chip 11, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold

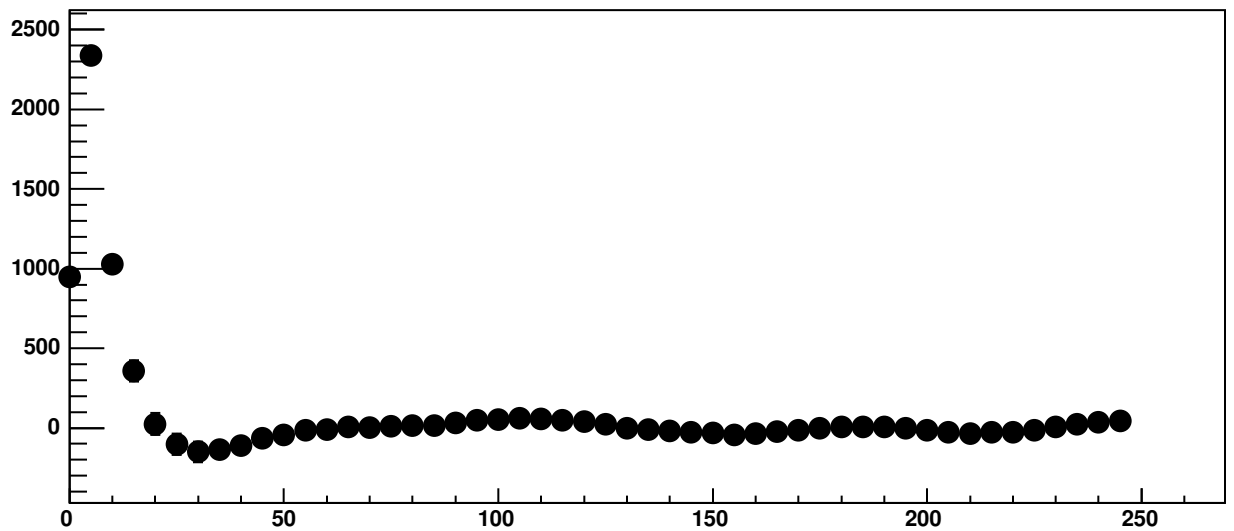
χ^2 / ndf	666 / 42
p0	-1403 ± 1.5
p1	5.003 ± 0.1048
p2	9801 ± 12.86
p3	29.79 ± 0.03808

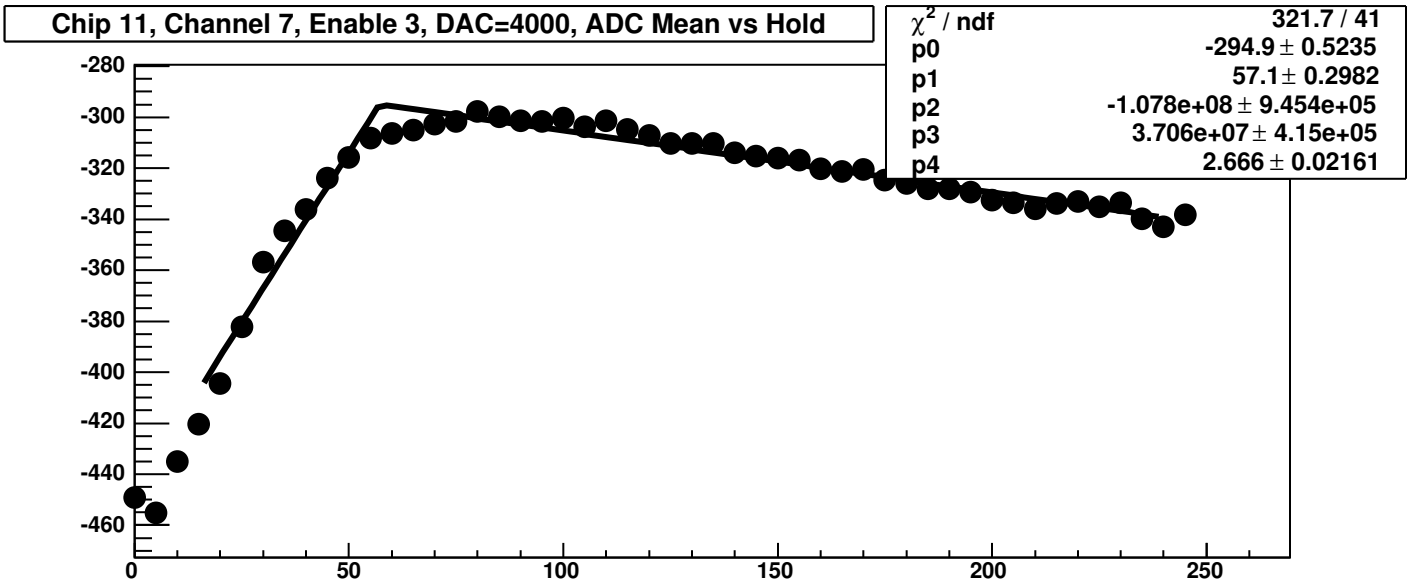


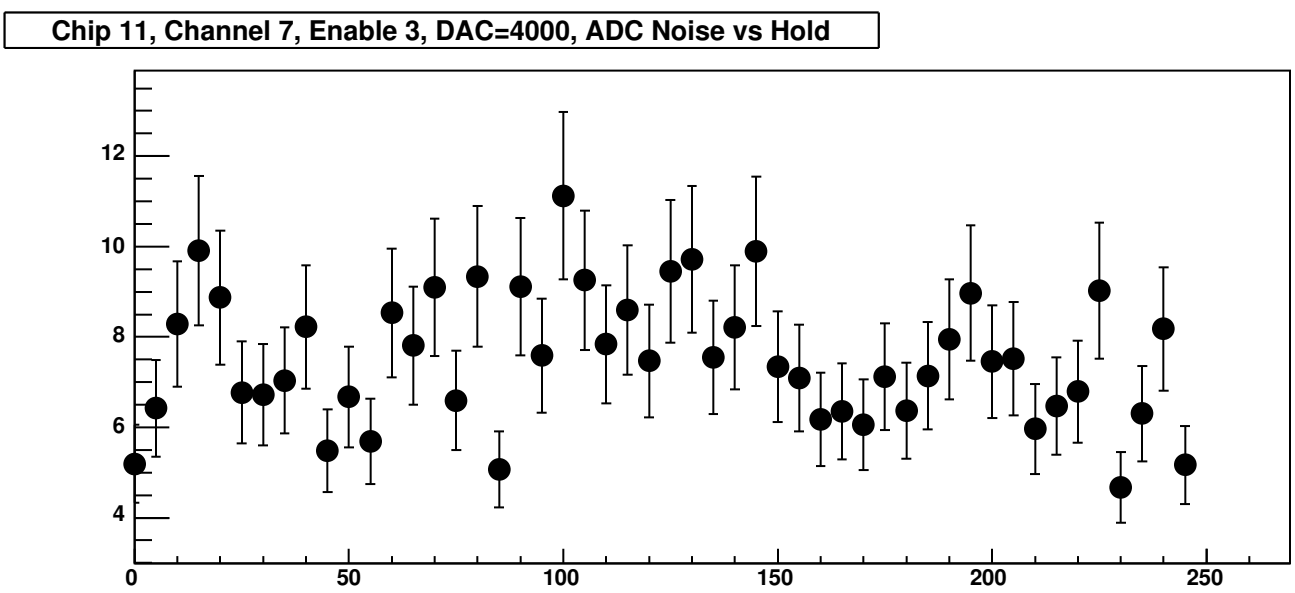
Chip 11, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold

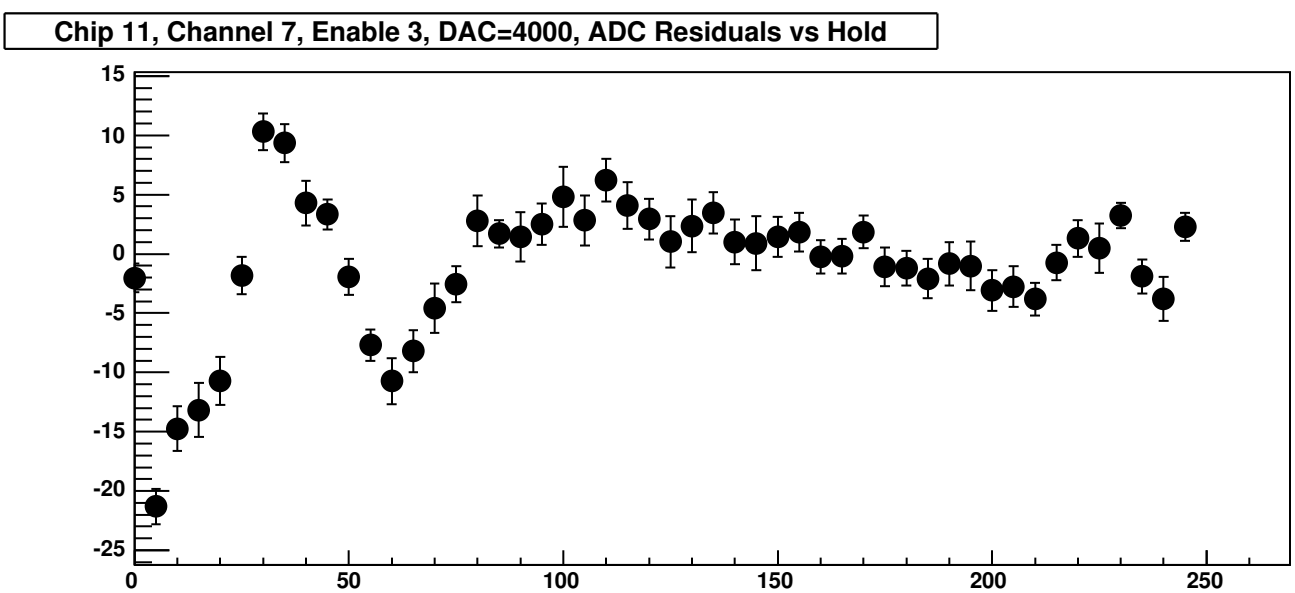


Chip 11, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold

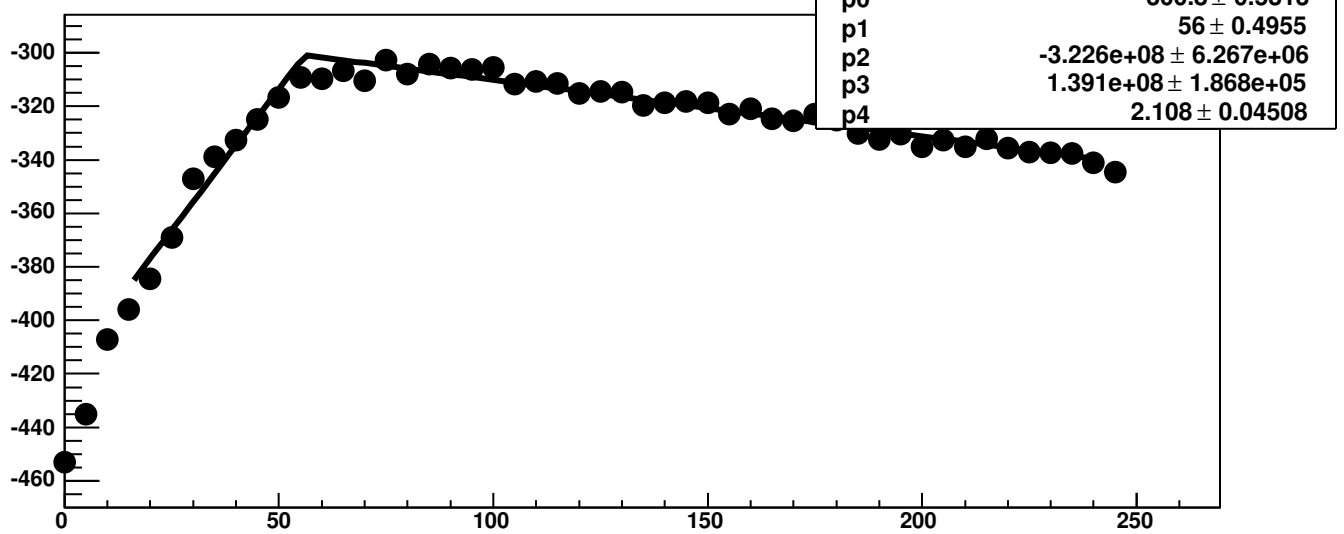



Chip 11, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 321.7 / 41
p0 -294.9 ± 0.5235
p1 57.1 ± 0.2982
p2 -1.078e+08 ± 9.454e+05
p3 3.706e+07 ± 4.15e+05
p4 2.666 ± 0.02161
-280
-300
-320
-340
-360
-380
-400
-420
-440
-460
0
50
100
150
200
250

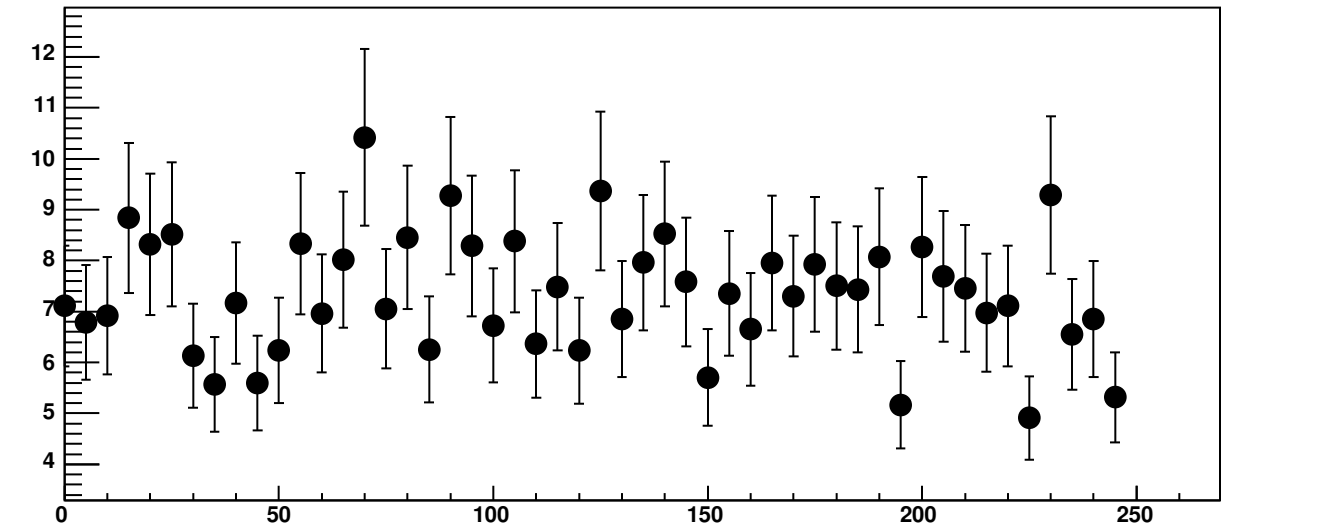

Chip 11, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 11, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
-15
-20
-25
0
50
100
150
200
250

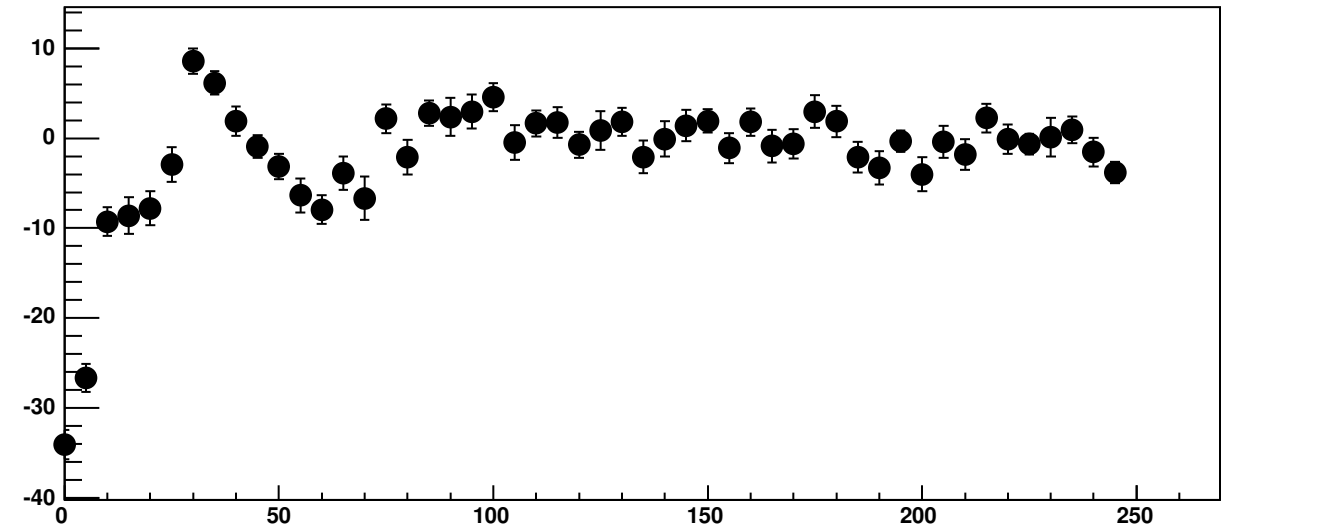
Chip 11, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold



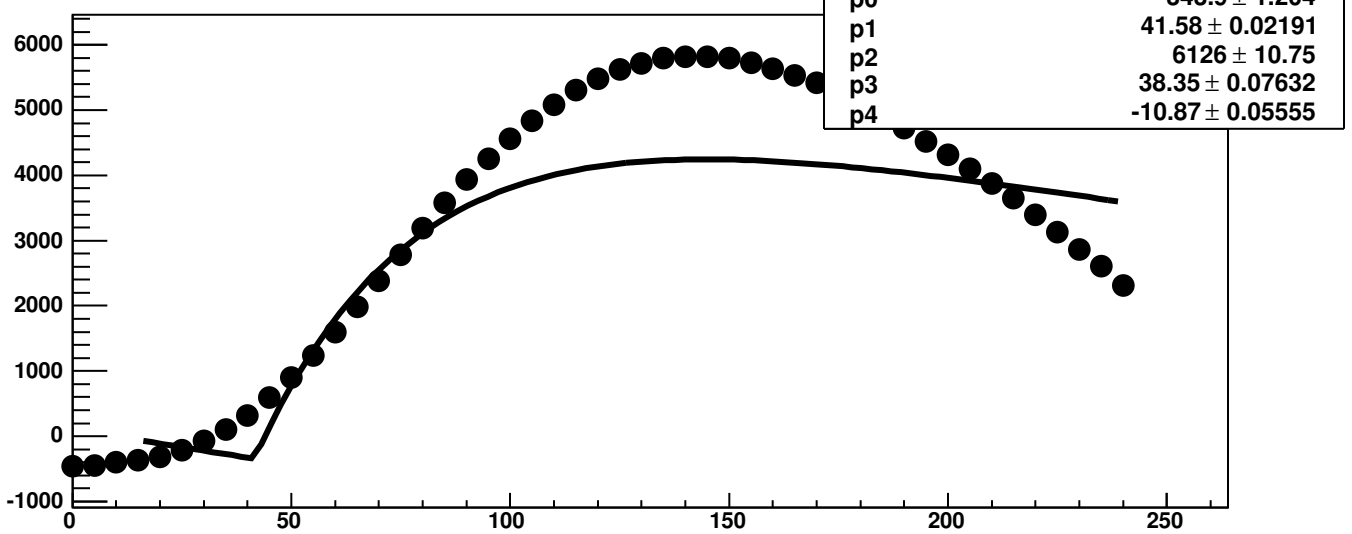
Chip 11, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



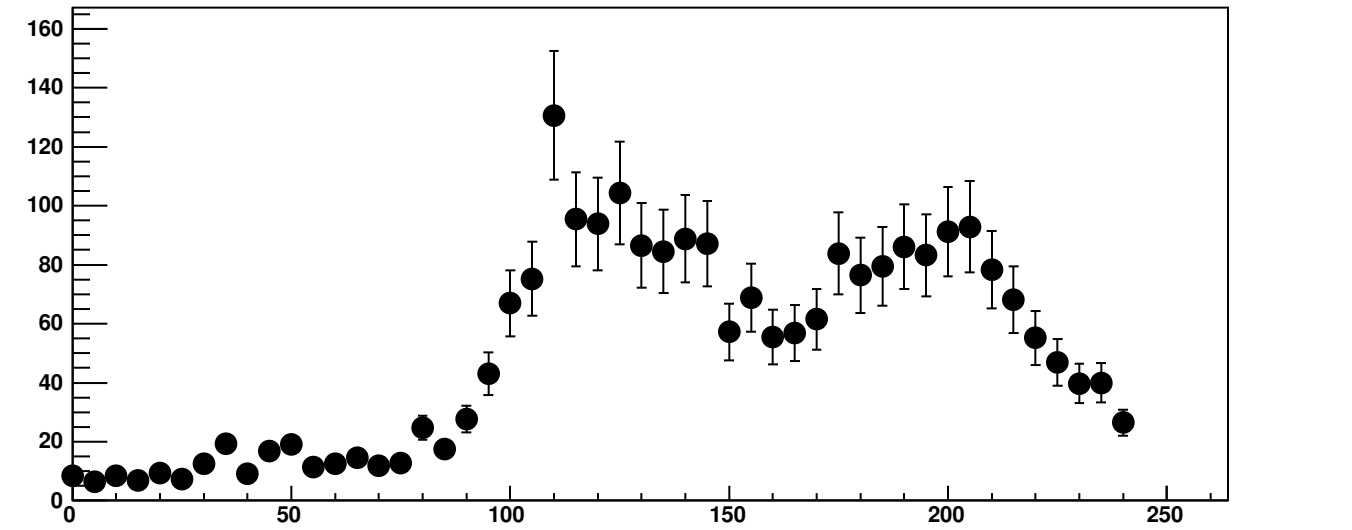
Chip 11, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold



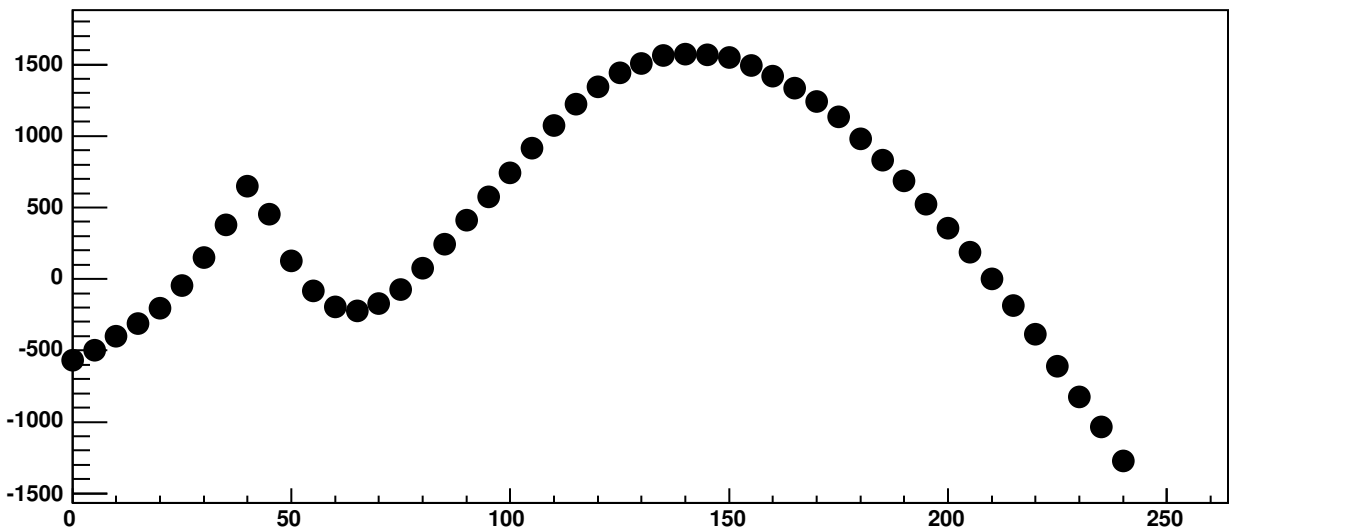
Chip 11, Channel 7, Enable 5, DAC=4000, ADC Mean vs Hold

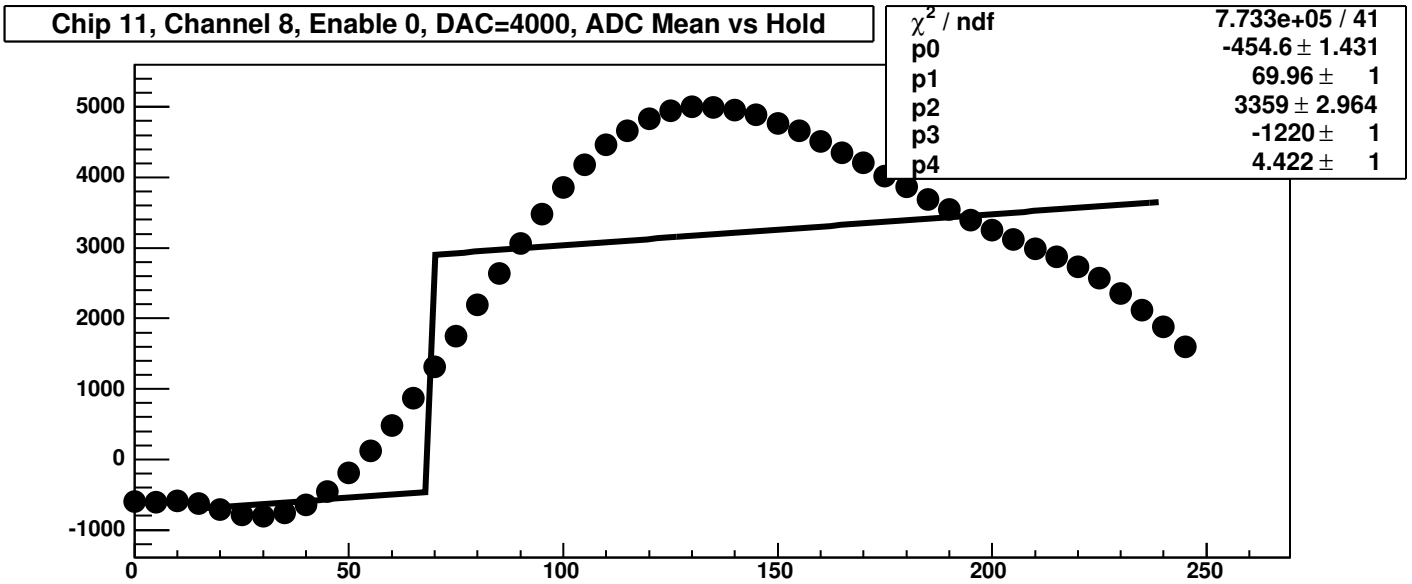


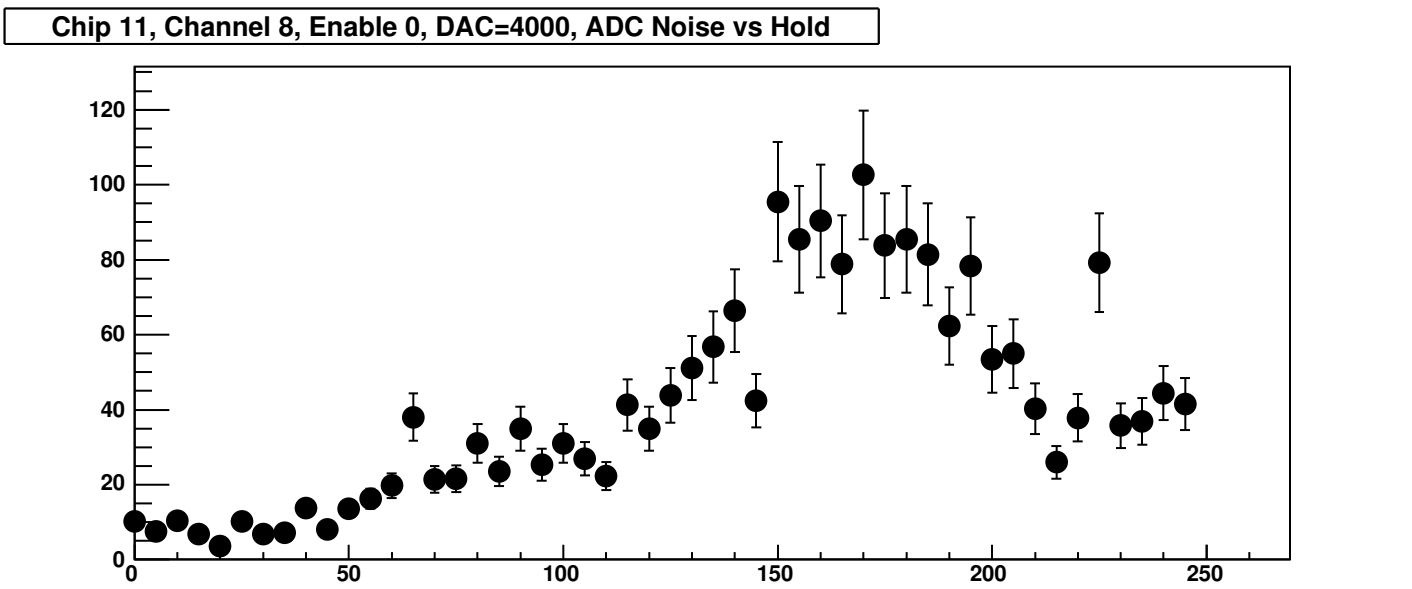
Chip 11, Channel 7, Enable 5, DAC=4000, ADC Noise vs Hold

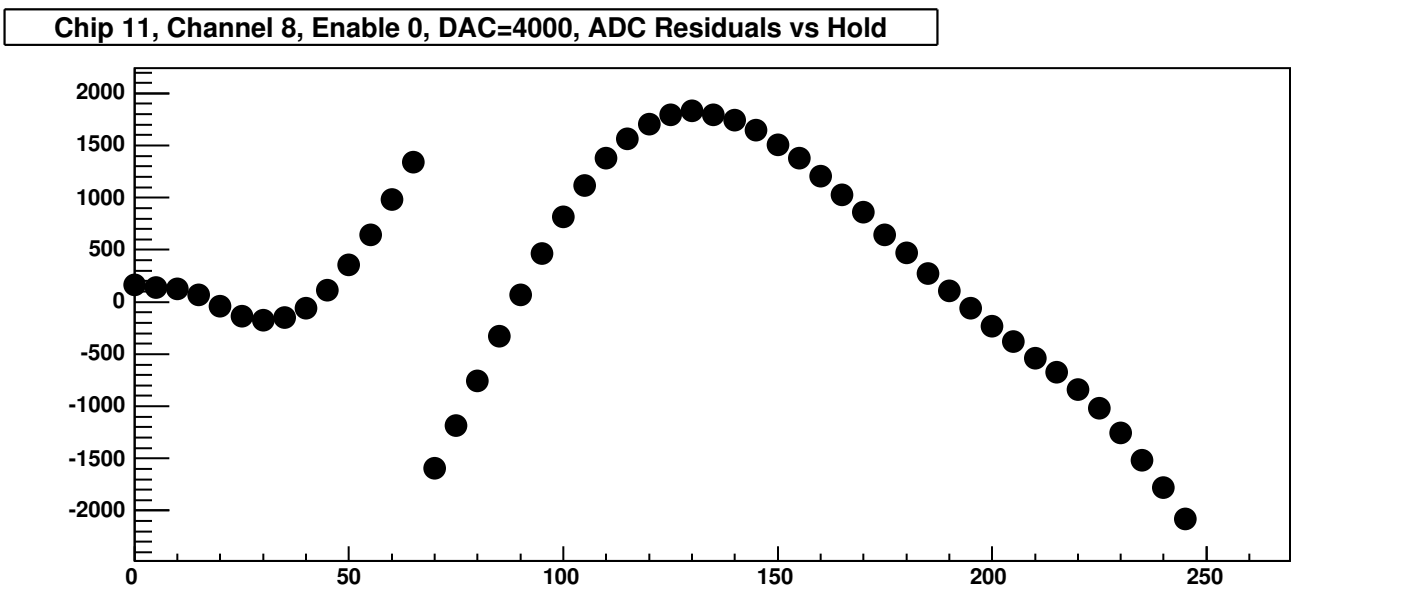


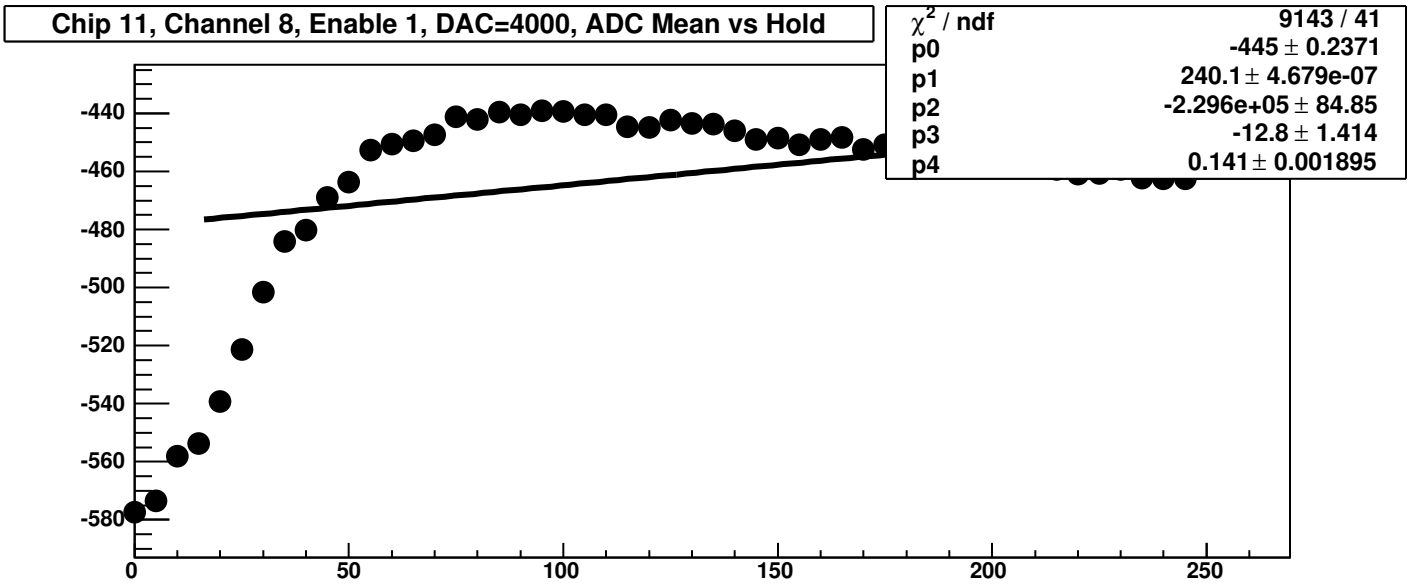
Chip 11, Channel 7, Enable 5, DAC=4000, ADC Residuals vs Hold

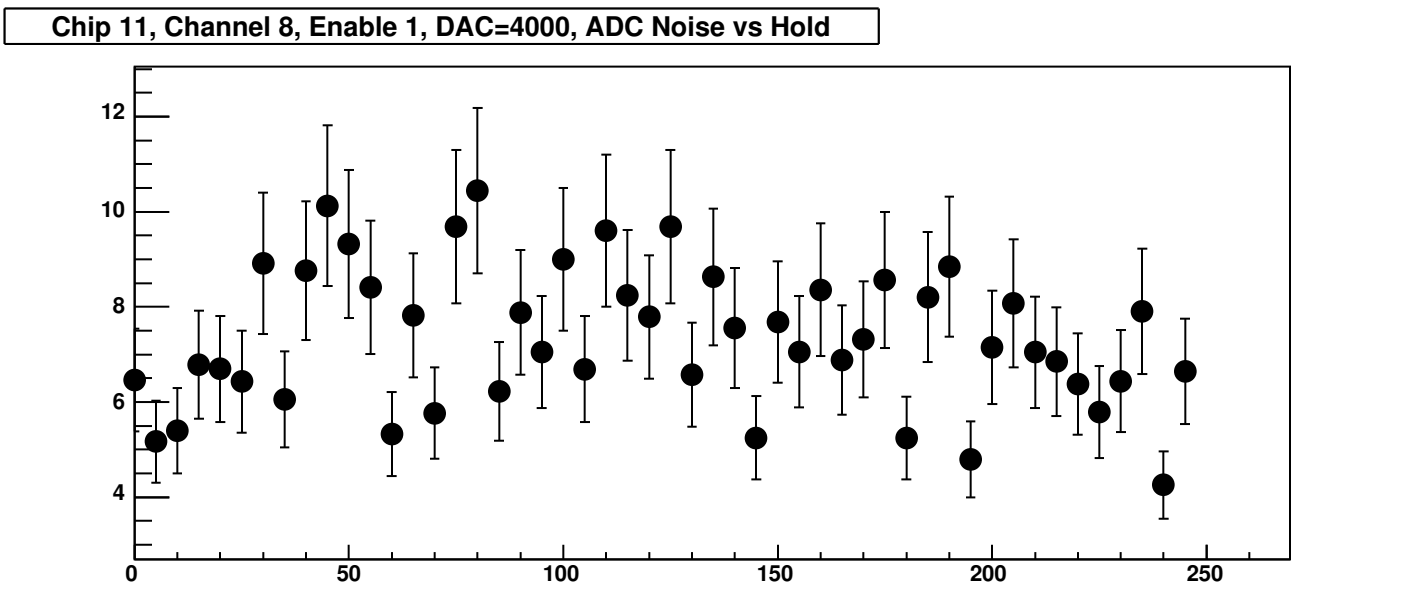


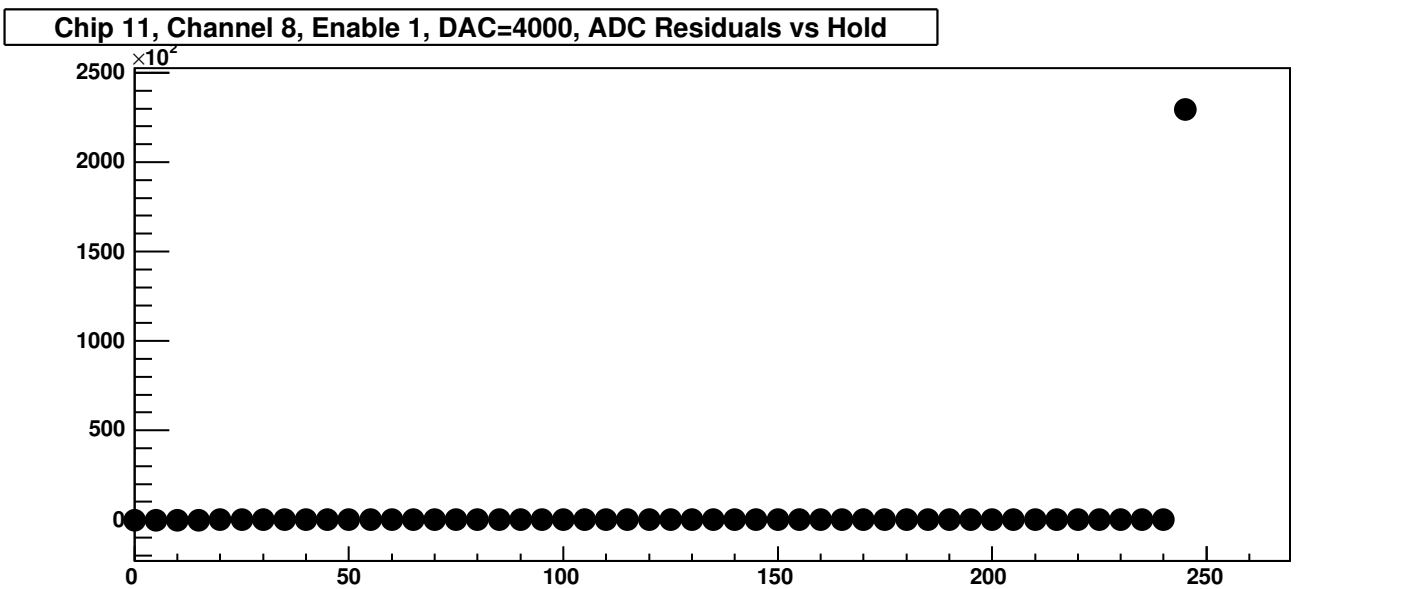

Chip 11, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 7.733e+05 / 41
p0 -454.6 ± 1.431
p1 69.96 ± 1
p2 3359 ± 2.964
p3 -1220 ± 1
p4 4.422 ± 1
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 11, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250

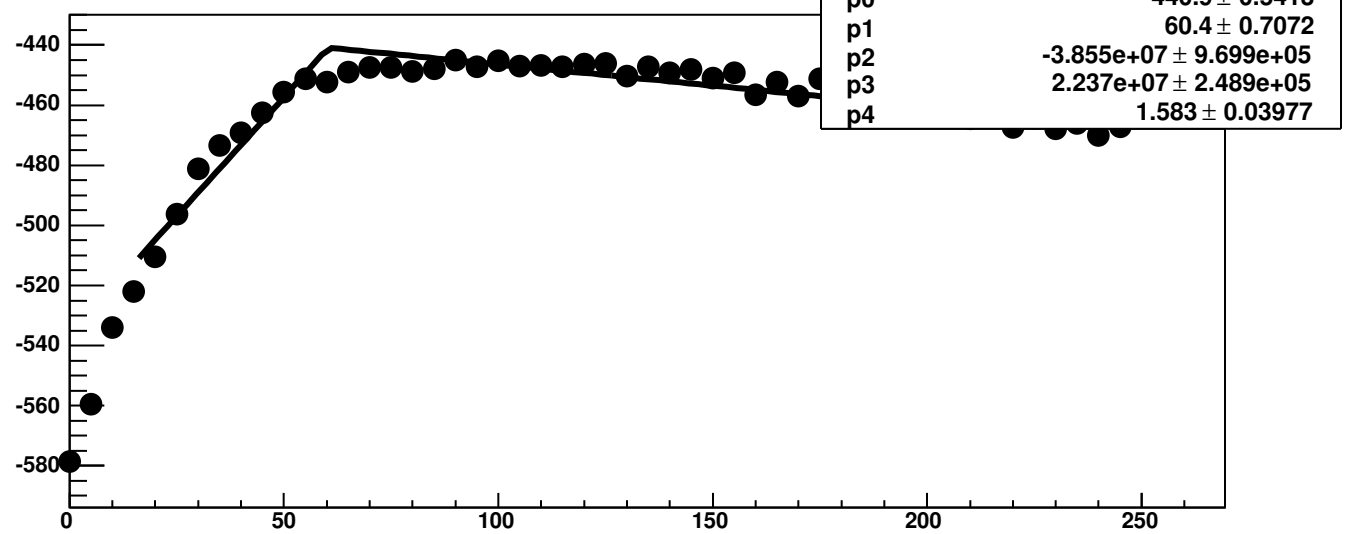

Chip 11, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250


Chip 11, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 9143 / 41
p0 -445 ± 0.2371
p1 240.1 ± 4.679e-07
p2 -2.296e+05 ± 84.85
p3 -12.8 ± 1.414
p4 0.141 ± 0.001895
-440
-460
-480
-500
-520
-540
-560
-580
0
50
100
150
200
250

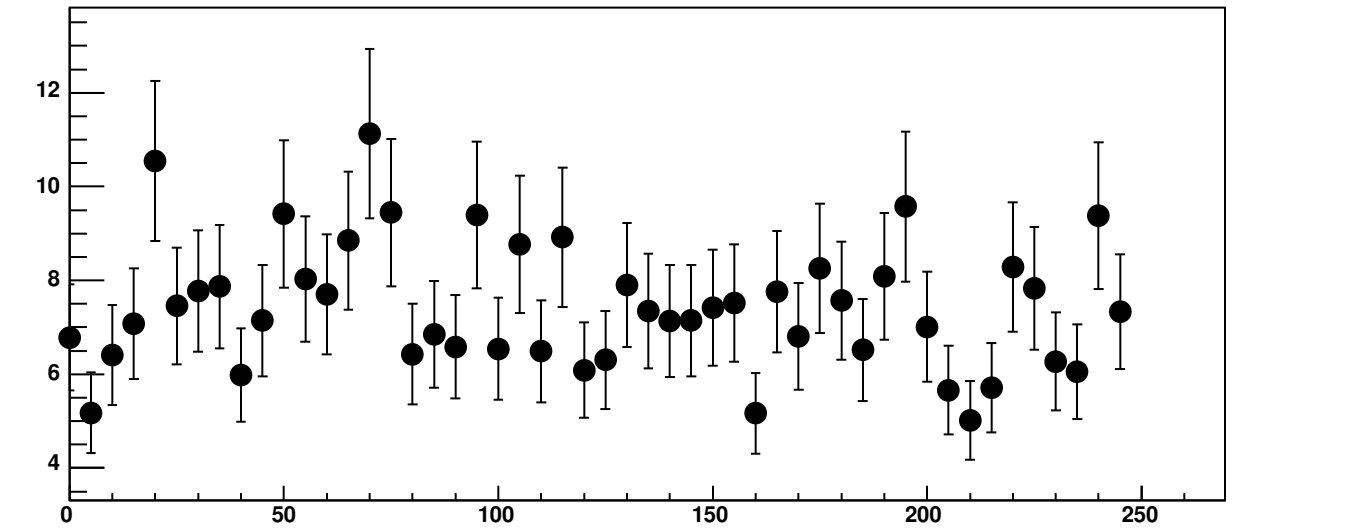

Chip 11, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 11, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold
×10²
2500
2000
1500
1000
500
0
0
50
100
150
200
250

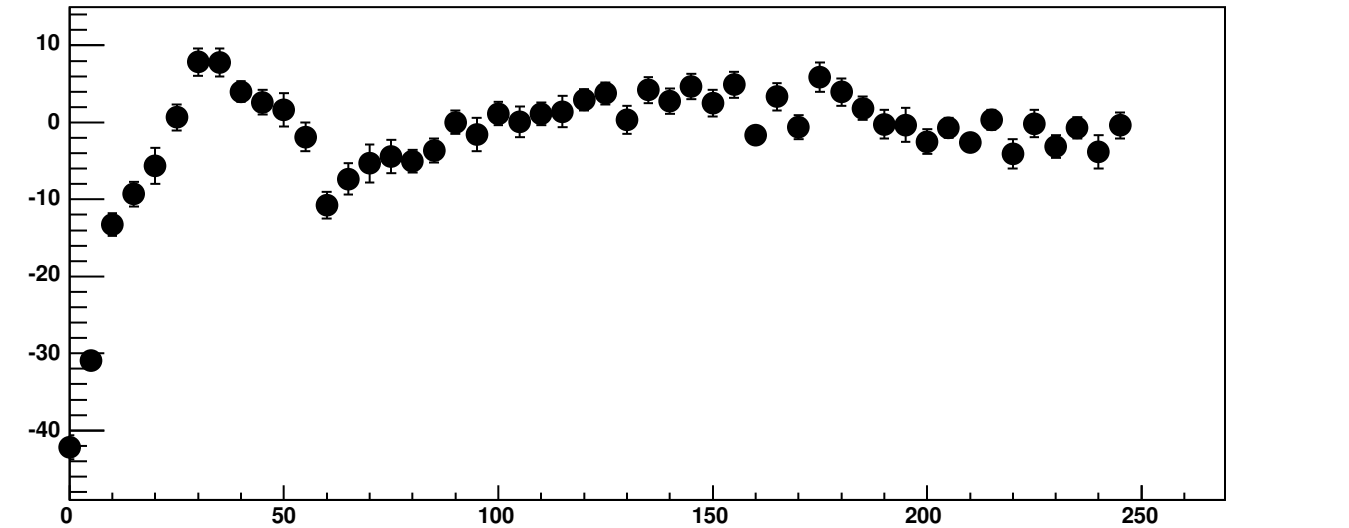
Chip 11, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold



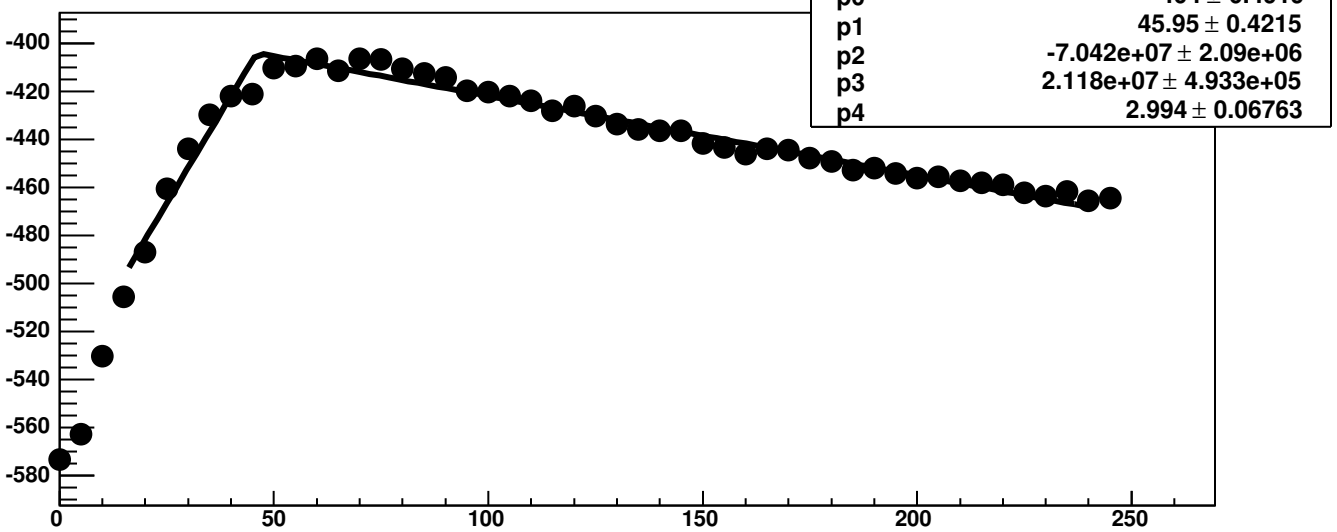
Chip 11, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold



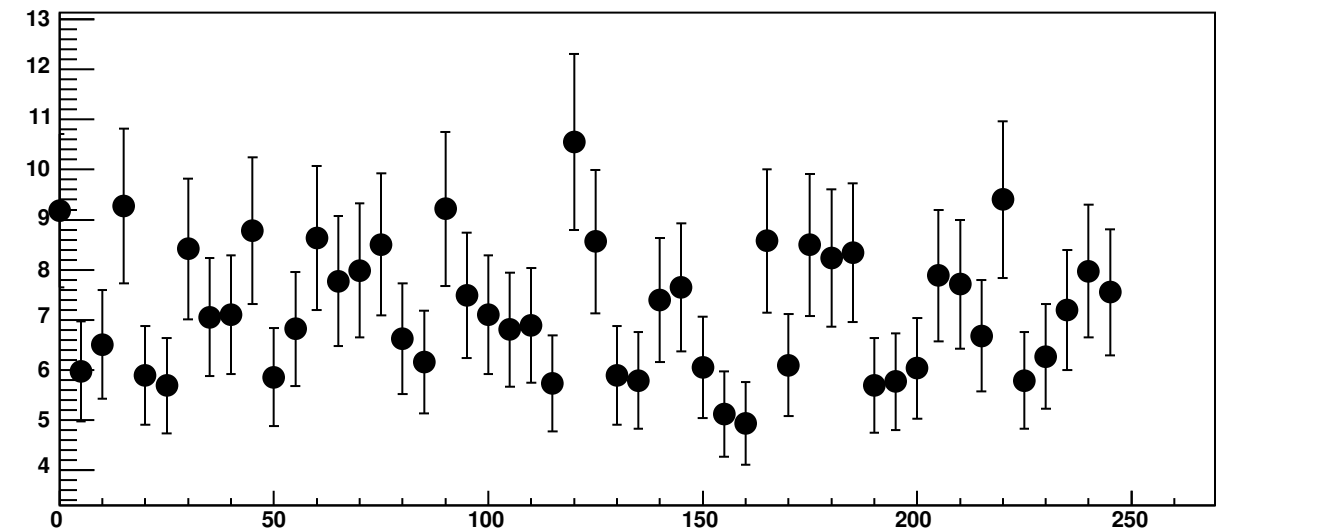
Chip 11, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold



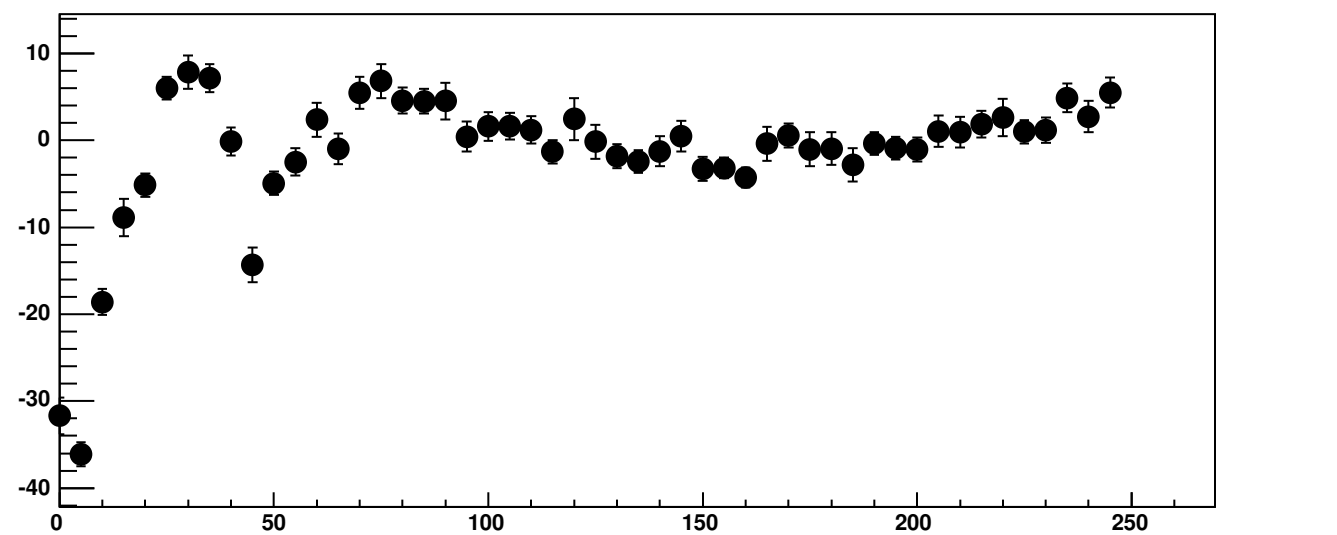
Chip 11, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold



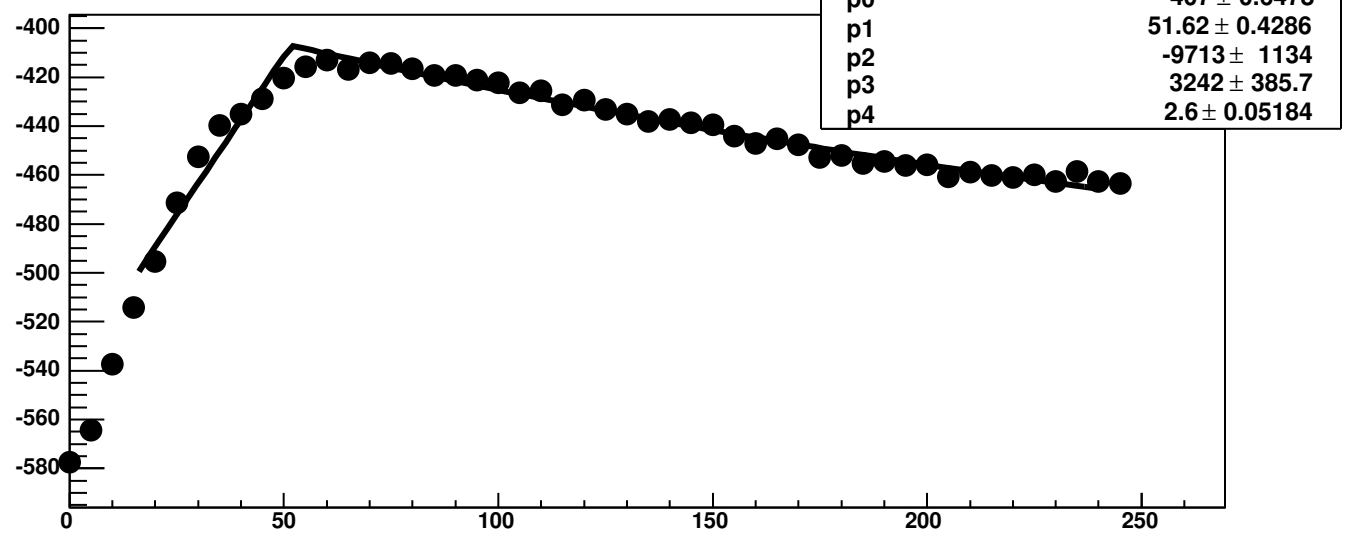
Chip 11, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



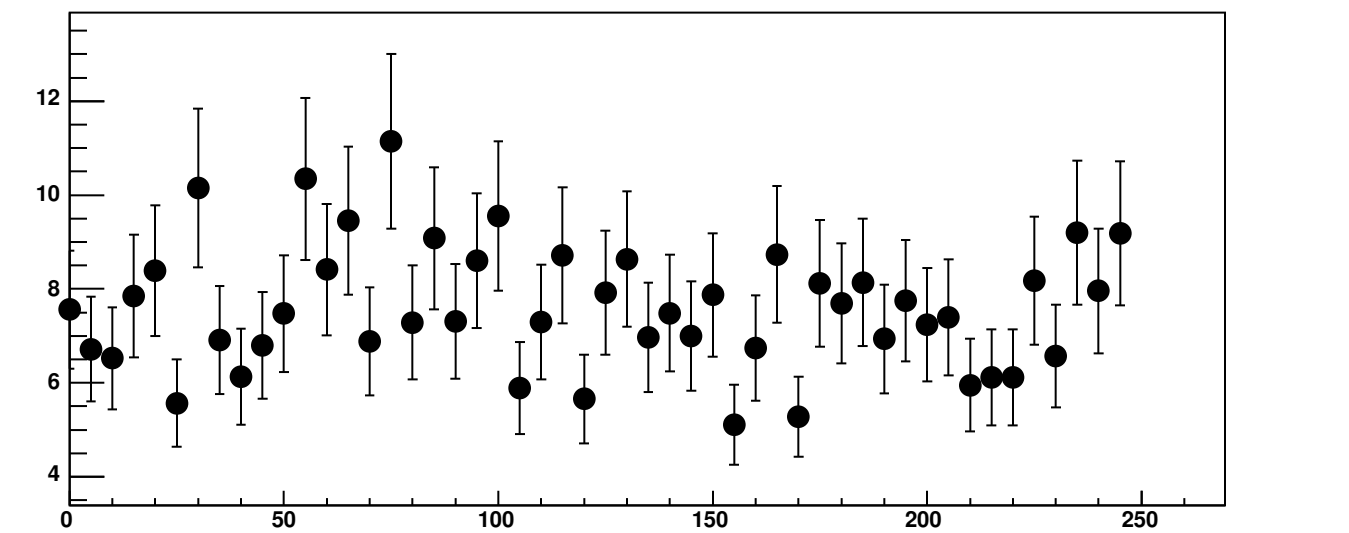
Chip 11, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold



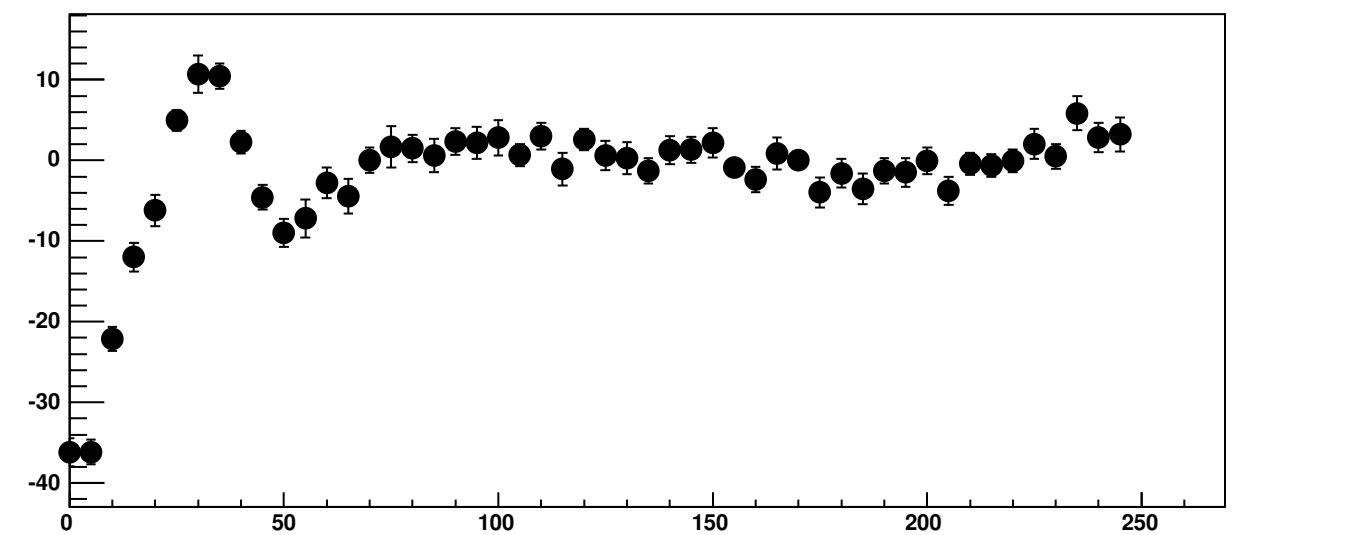
Chip 11, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

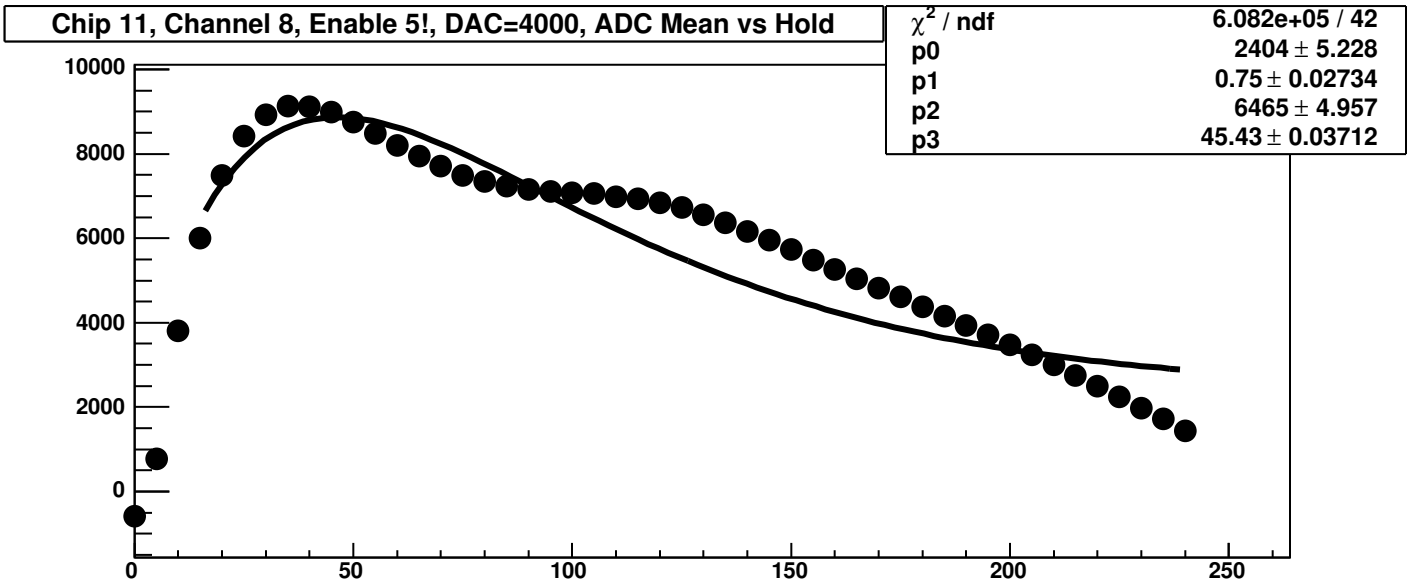


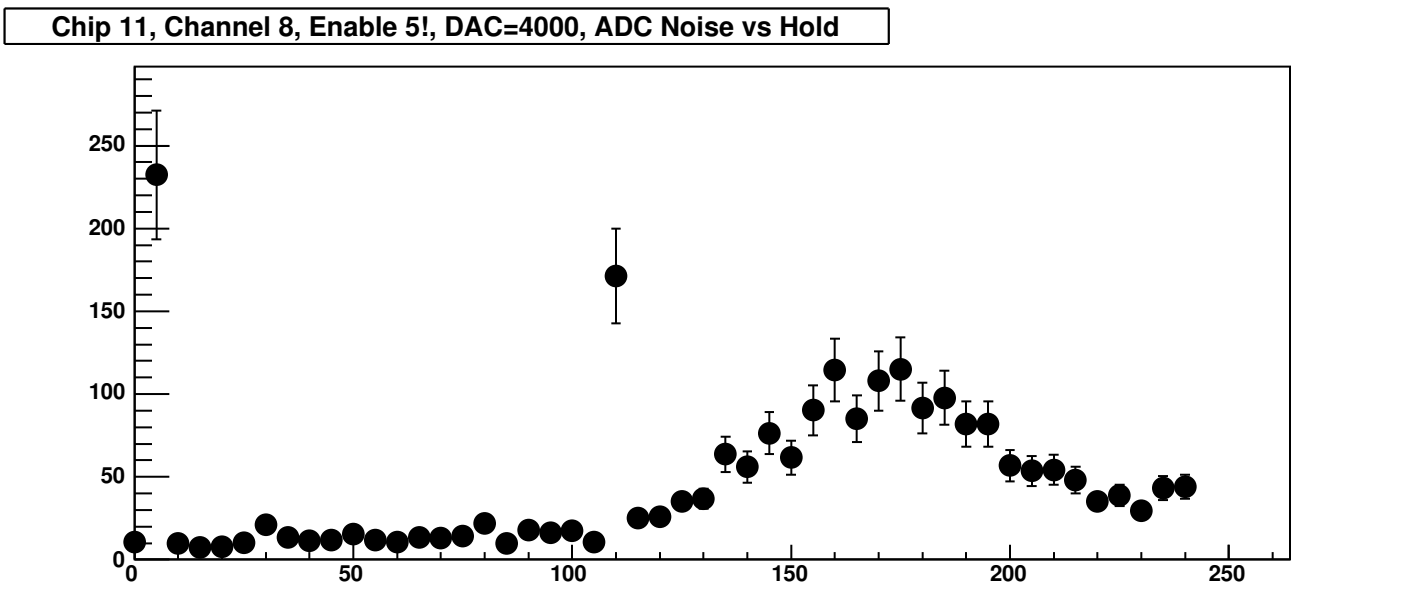
Chip 11, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold

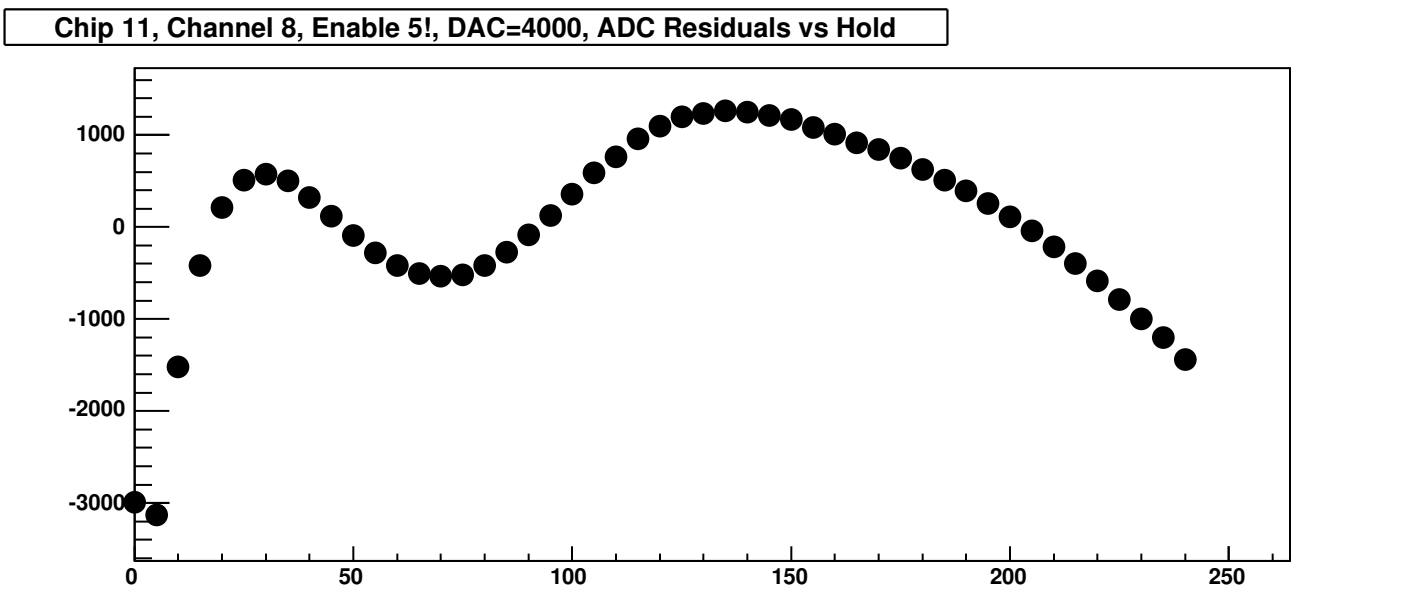


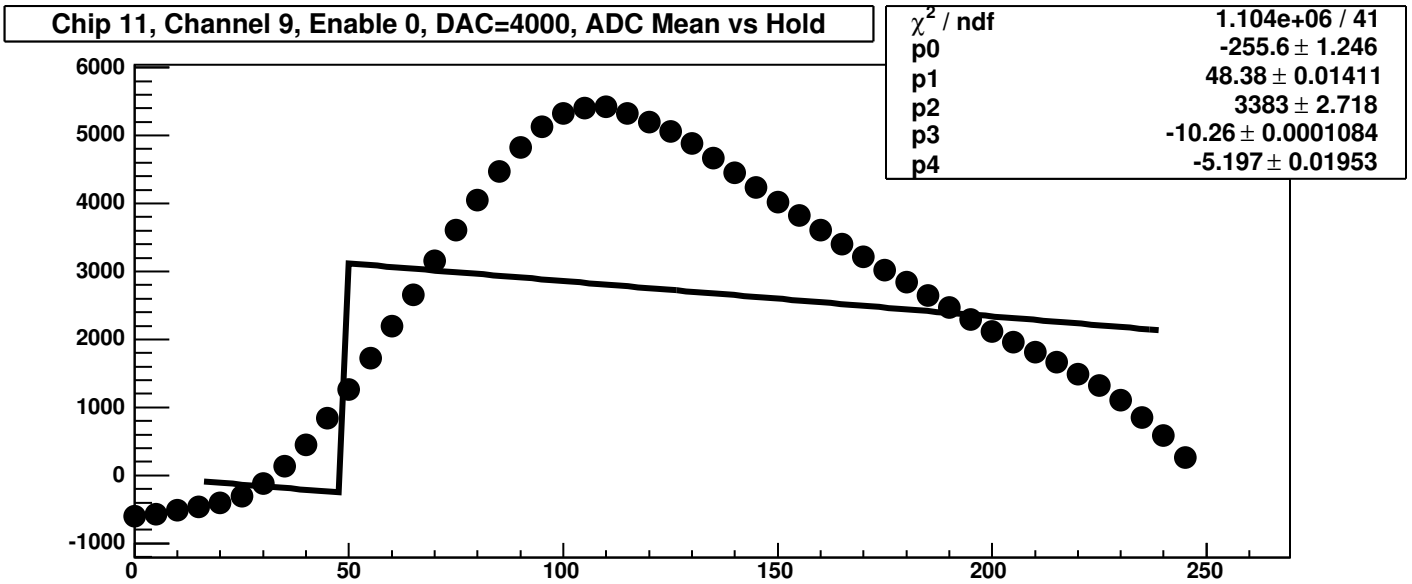
Chip 11, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

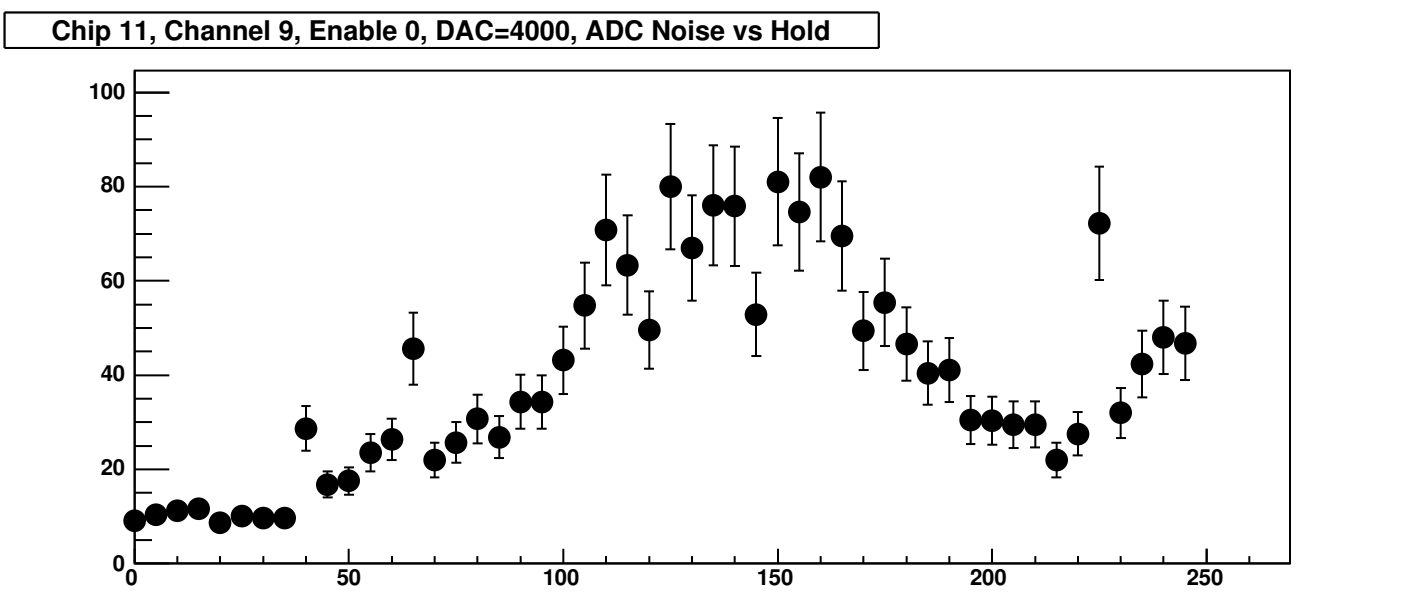


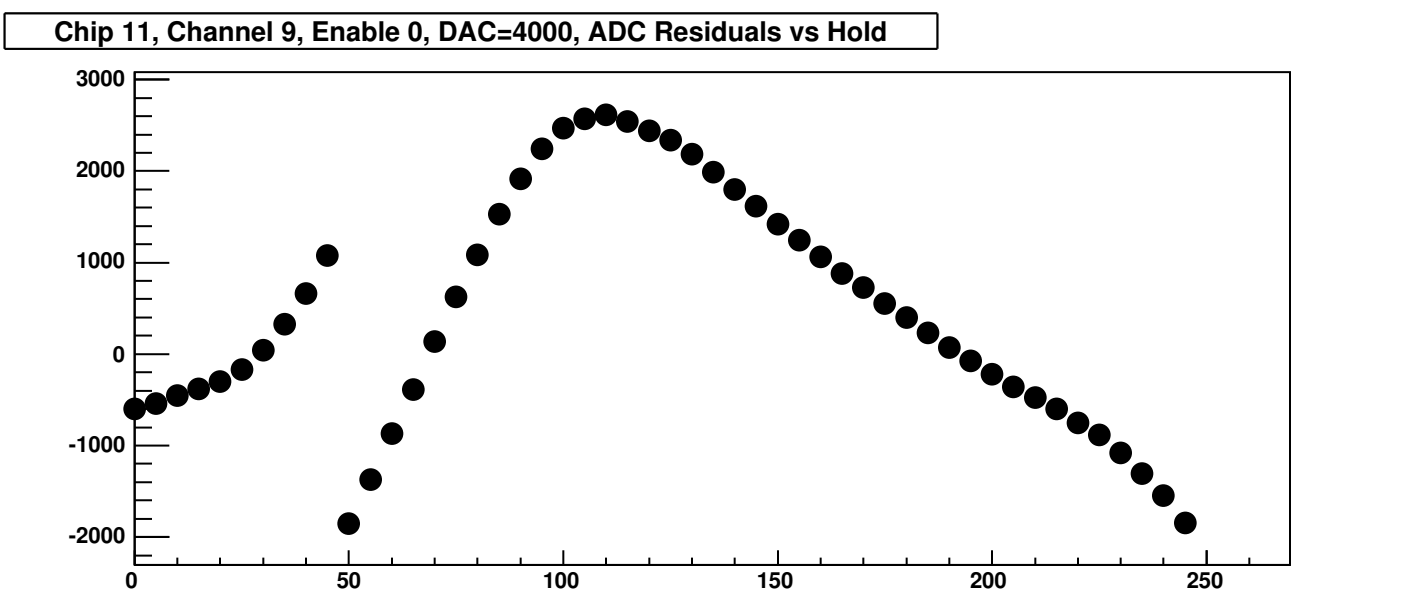

Chip 11, Channel 8, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 6.082e+05 / 42
p0 2404 ± 5.228
p1 0.75 ± 0.02734
p2 6465 ± 4.957
p3 45.43 ± 0.03712
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 11, Channel 8, Enable 5!, DAC=4000, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250

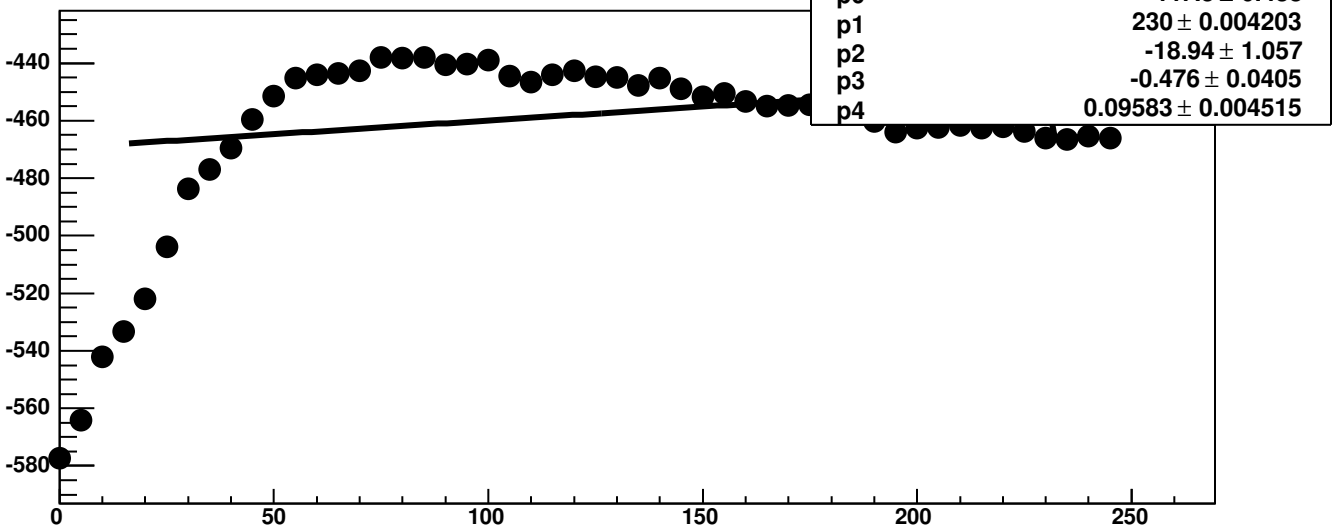

Chip 11, Channel 8, Enable 5!, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
0
50
100
150
200
250


Chip 11, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.104e+06 / 41
p0 -255.6 ± 1.246
p1 48.38 ± 0.01411
p2 3383 ± 2.718
p3 -10.26 ± 0.0001084
p4 -5.197 ± 0.01953
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

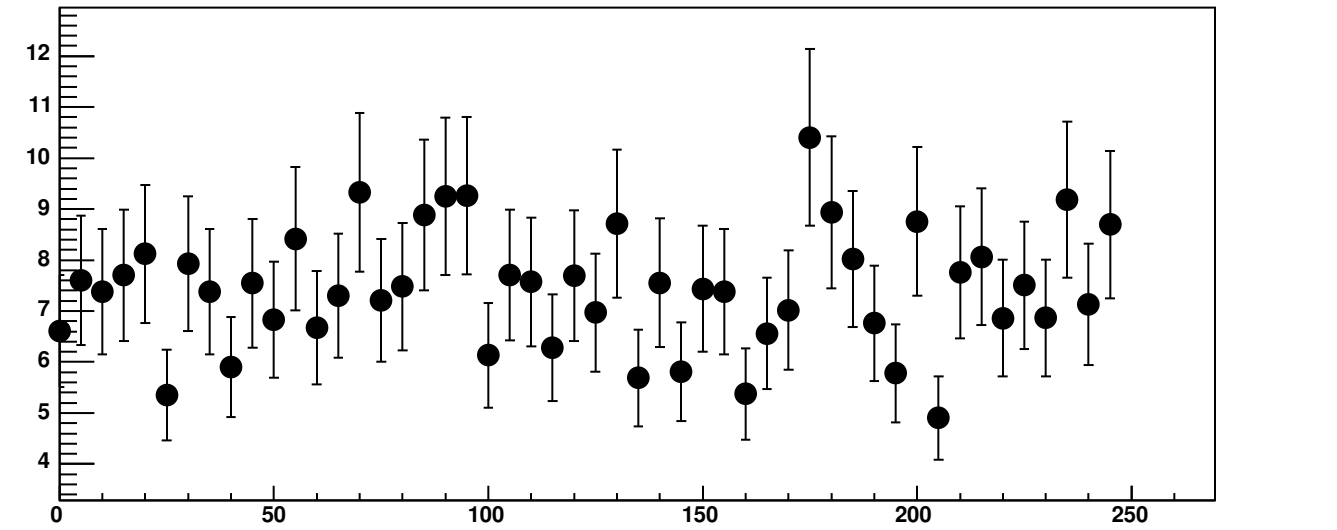

Chip 11, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250


Chip 11, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
0
50
100
150
200
250

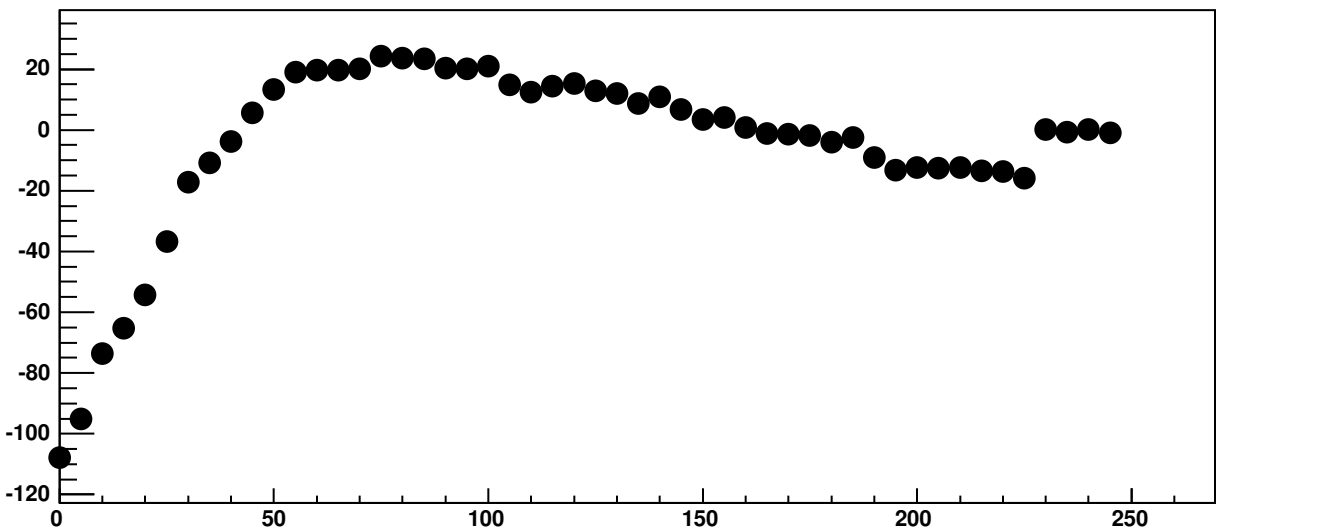
Chip 11, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

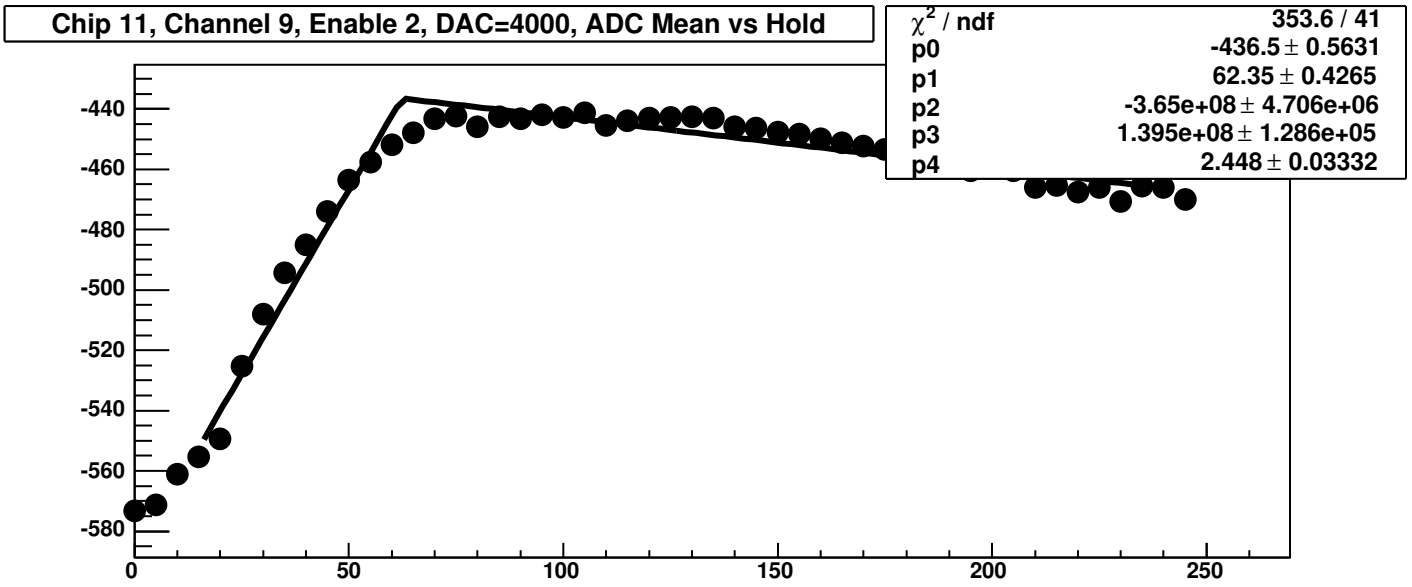


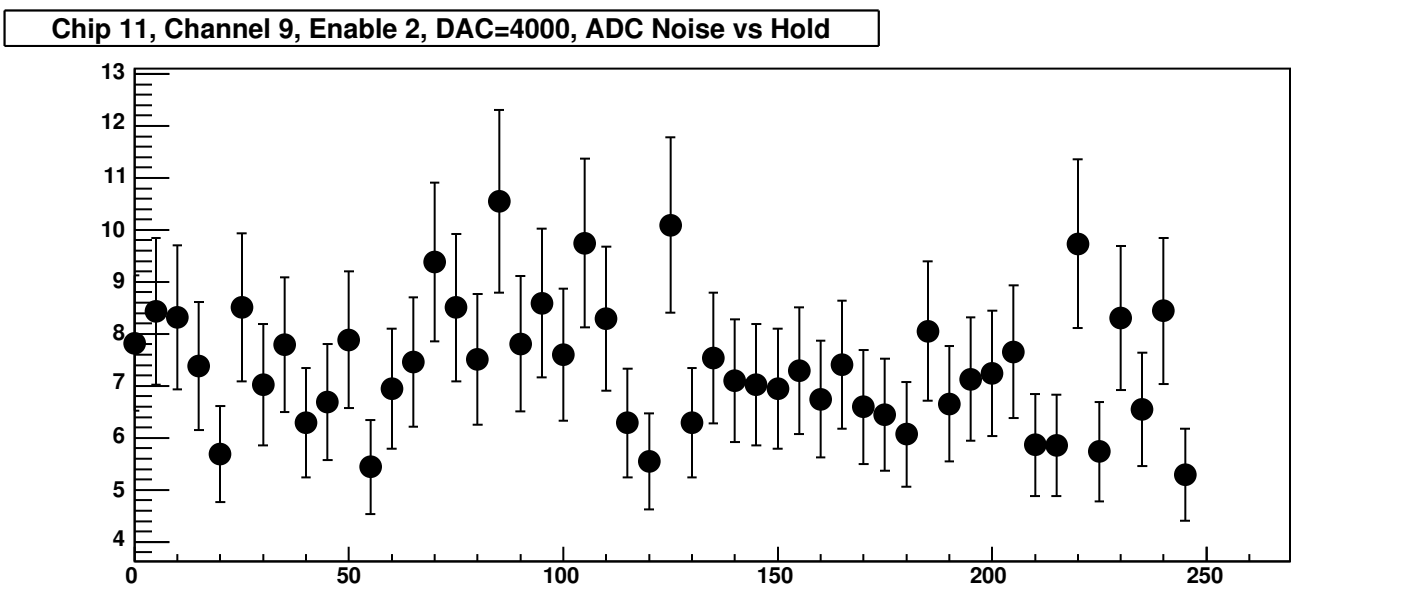
Chip 11, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold

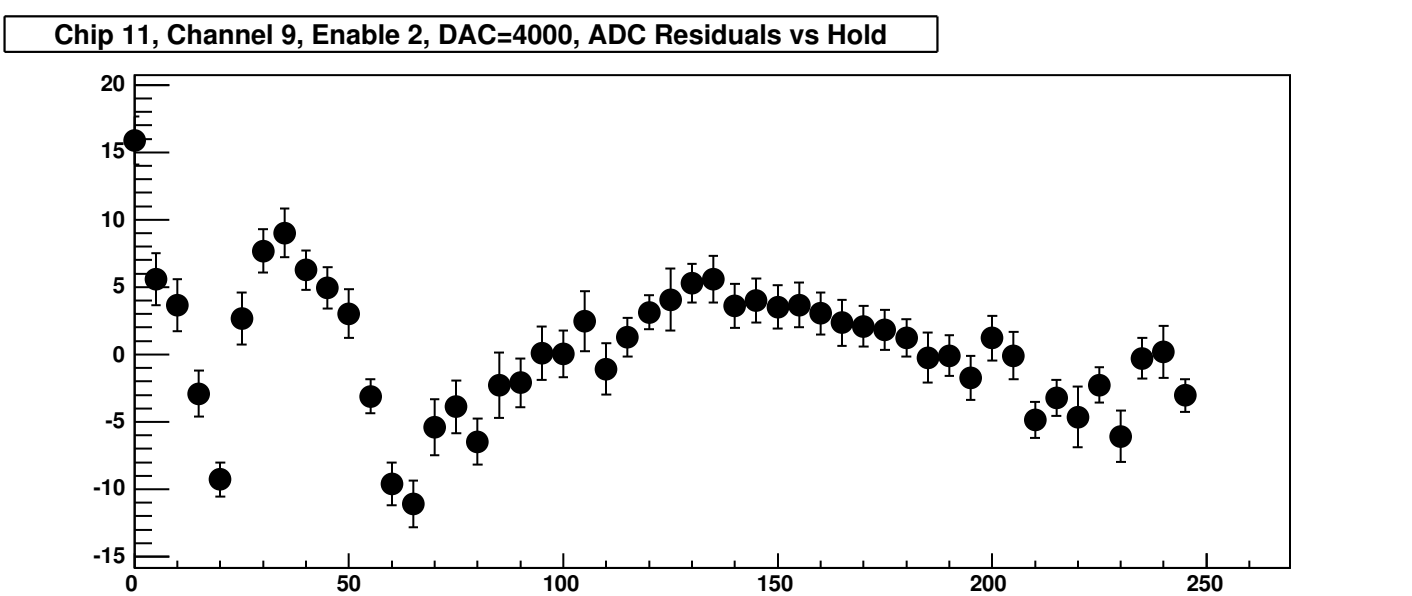


Chip 11, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

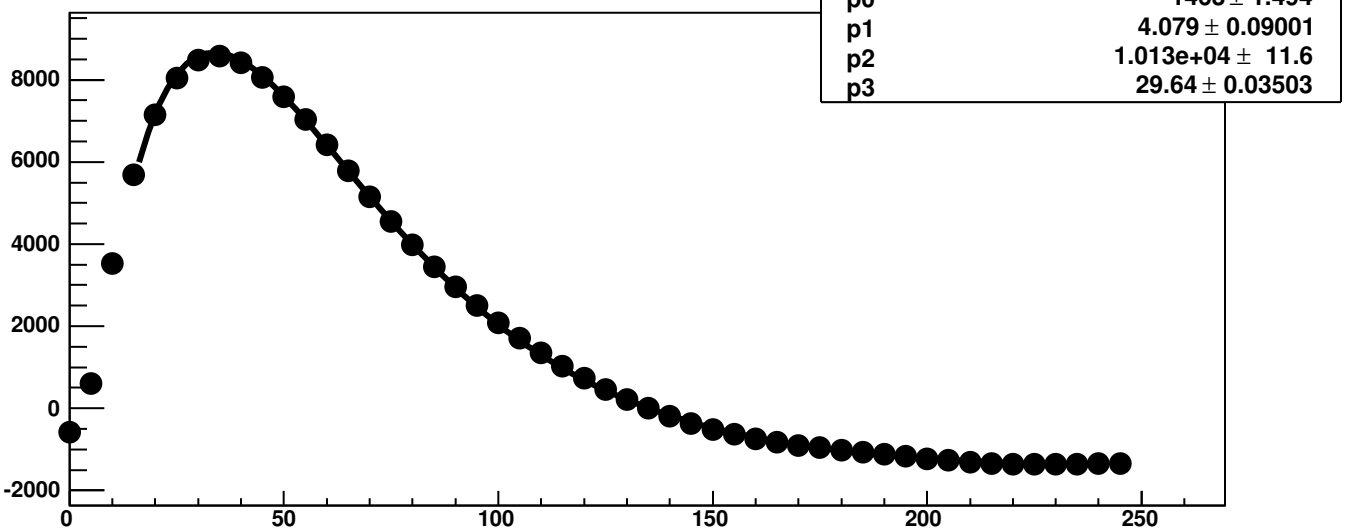



Chip 11, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 353.6 / 41
p0 -436.5 ± 0.5631
p1 62.35 ± 0.4265
p2 -3.65e+08 ± 4.706e+06
p3 1.395e+08 ± 1.286e+05
p4 2.448 ± 0.03332
-440
-460
-480
-500
-520
-540
-560
-580
0
50
100
150
200
250

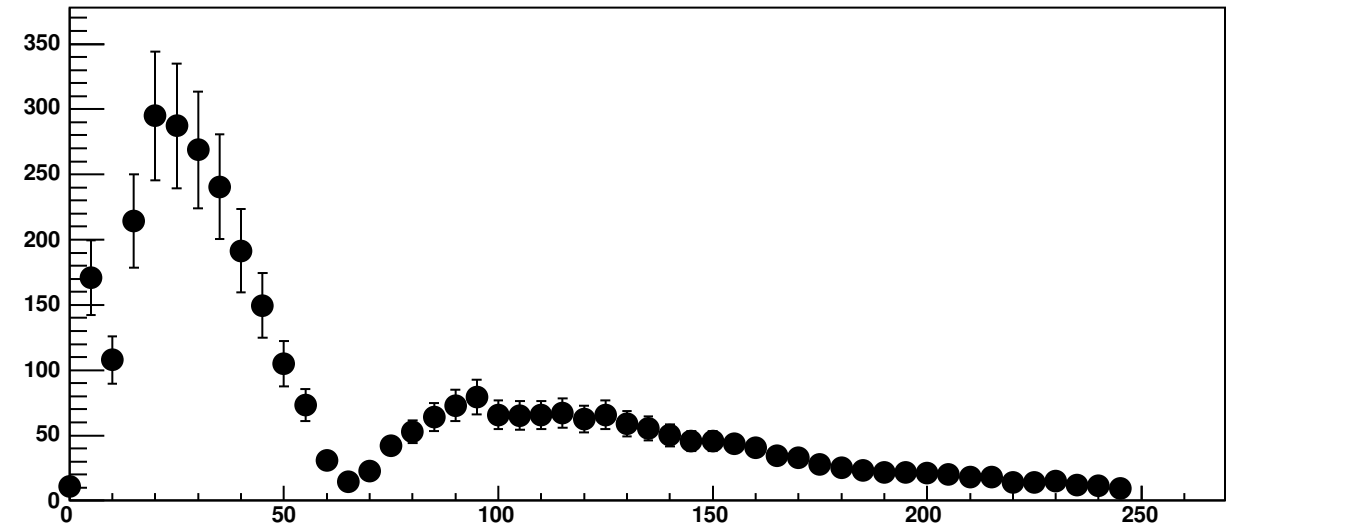

Chip 11, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 11, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

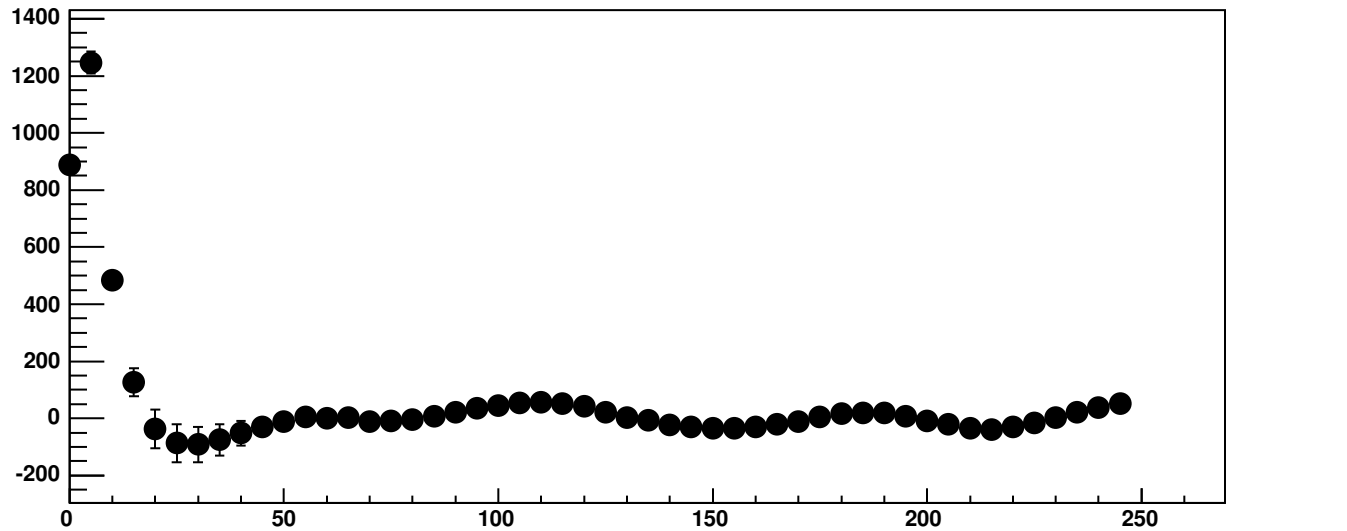
Chip 11, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold



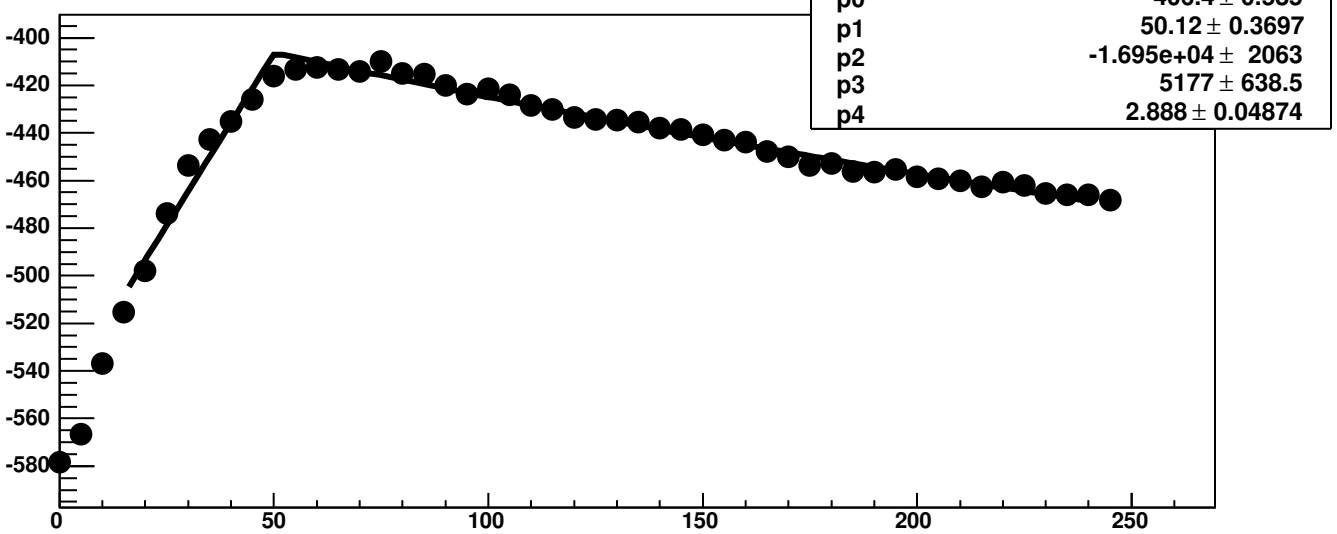
Chip 11, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold



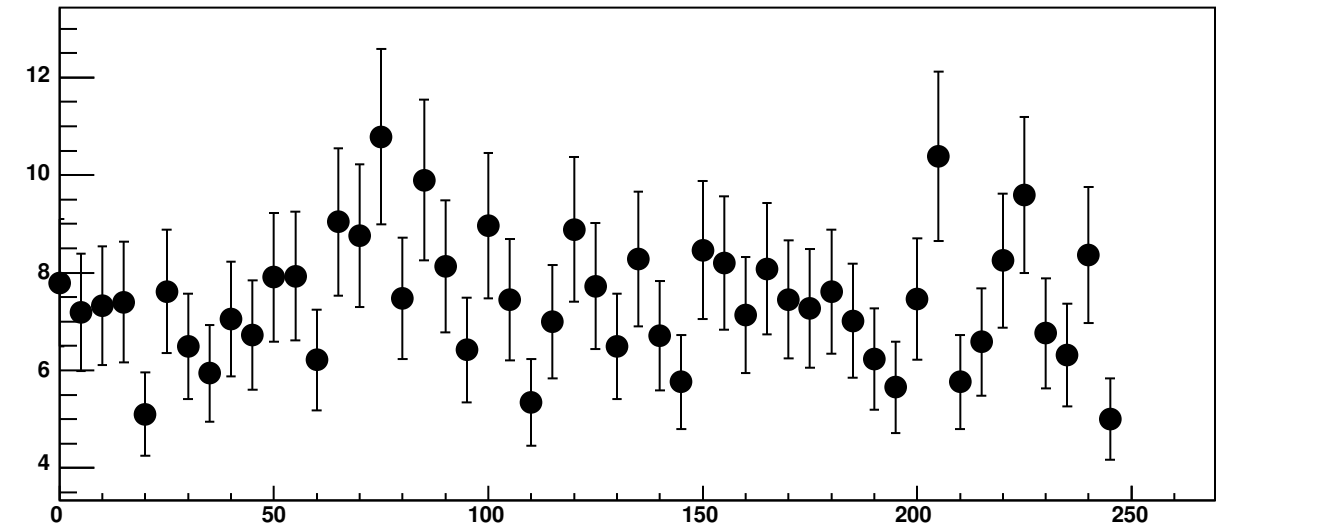
Chip 11, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold



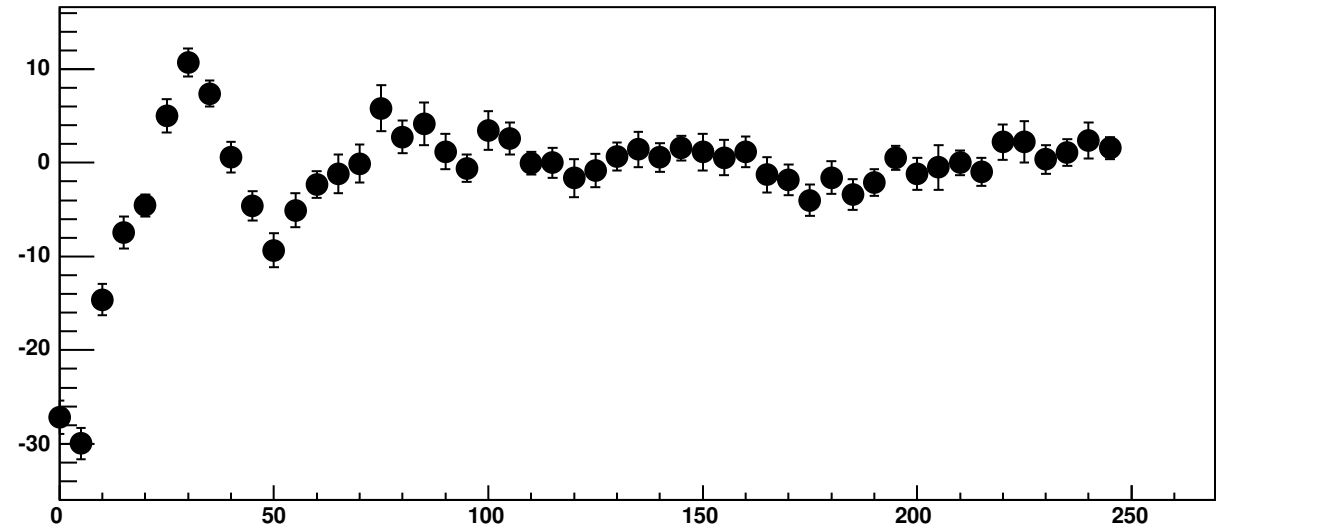
Chip 11, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

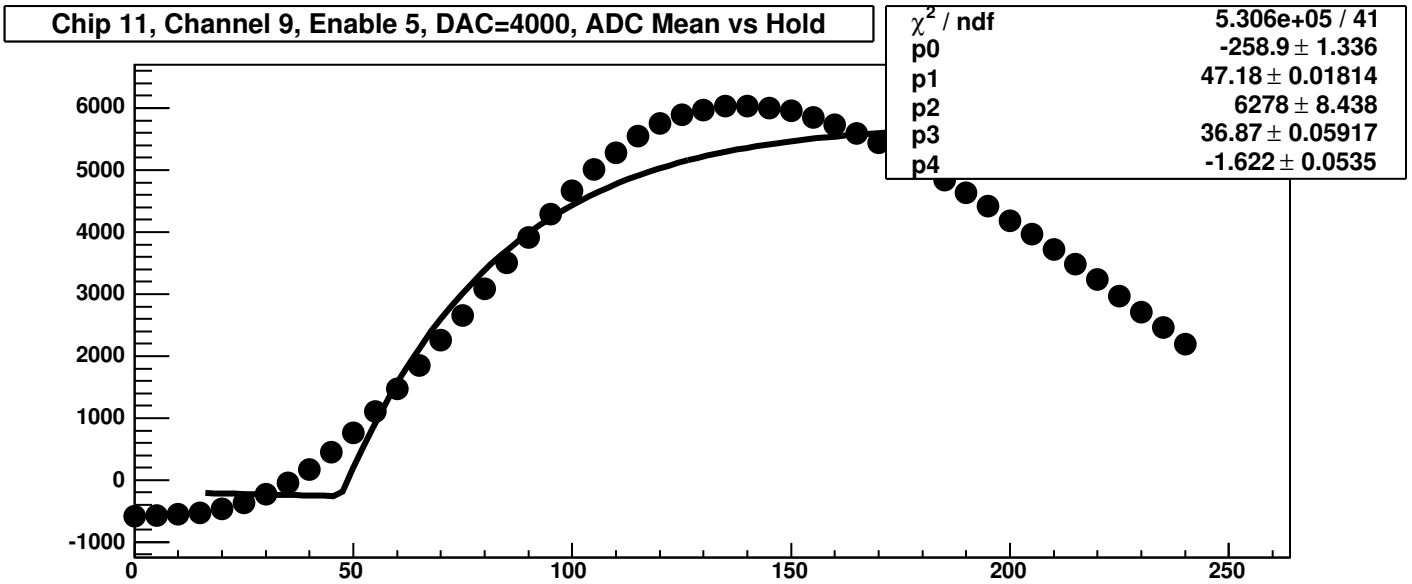


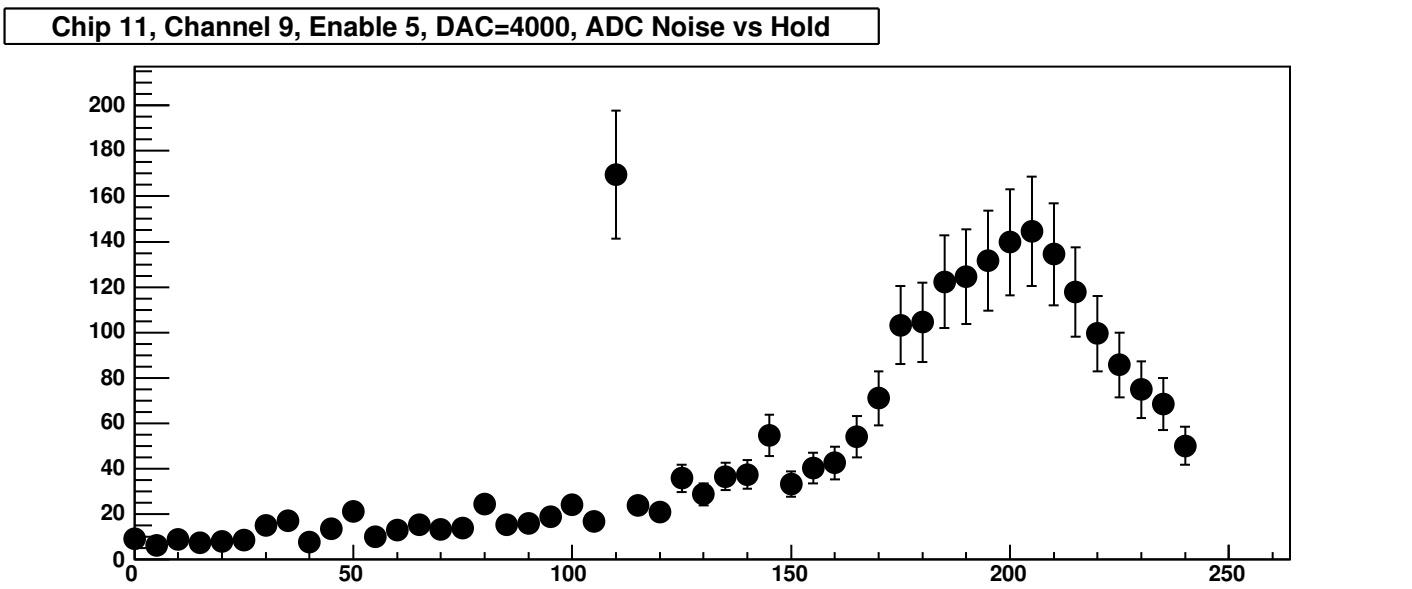
Chip 11, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold

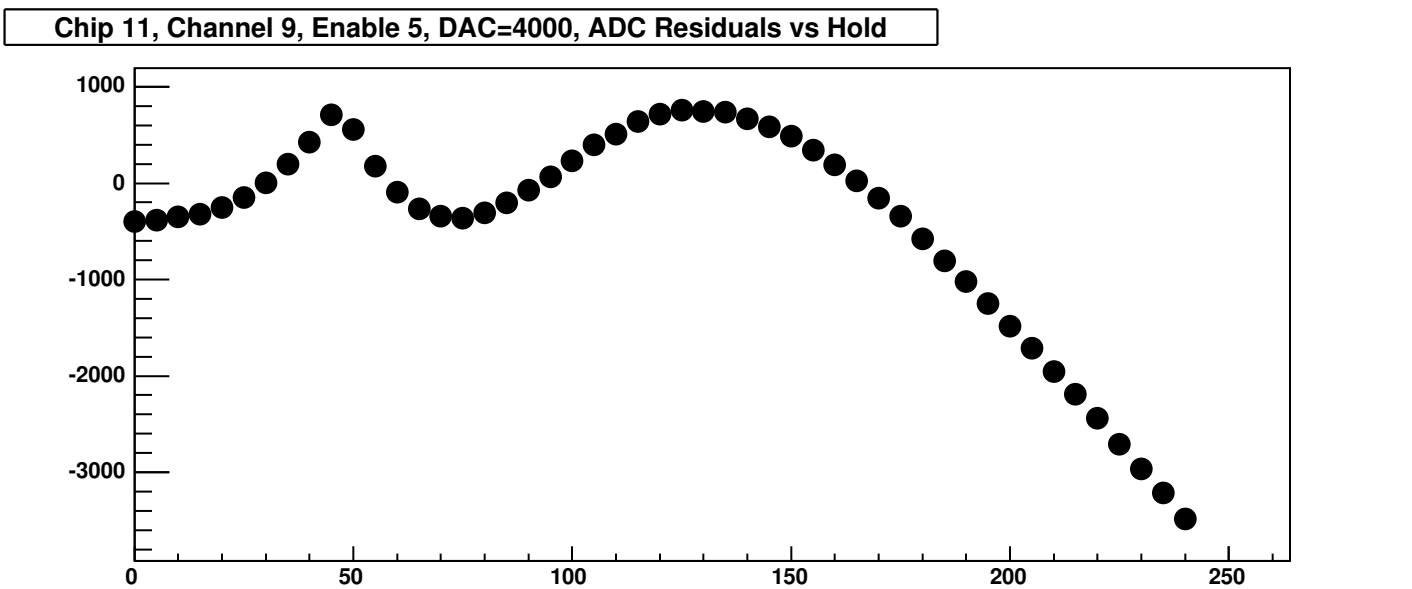


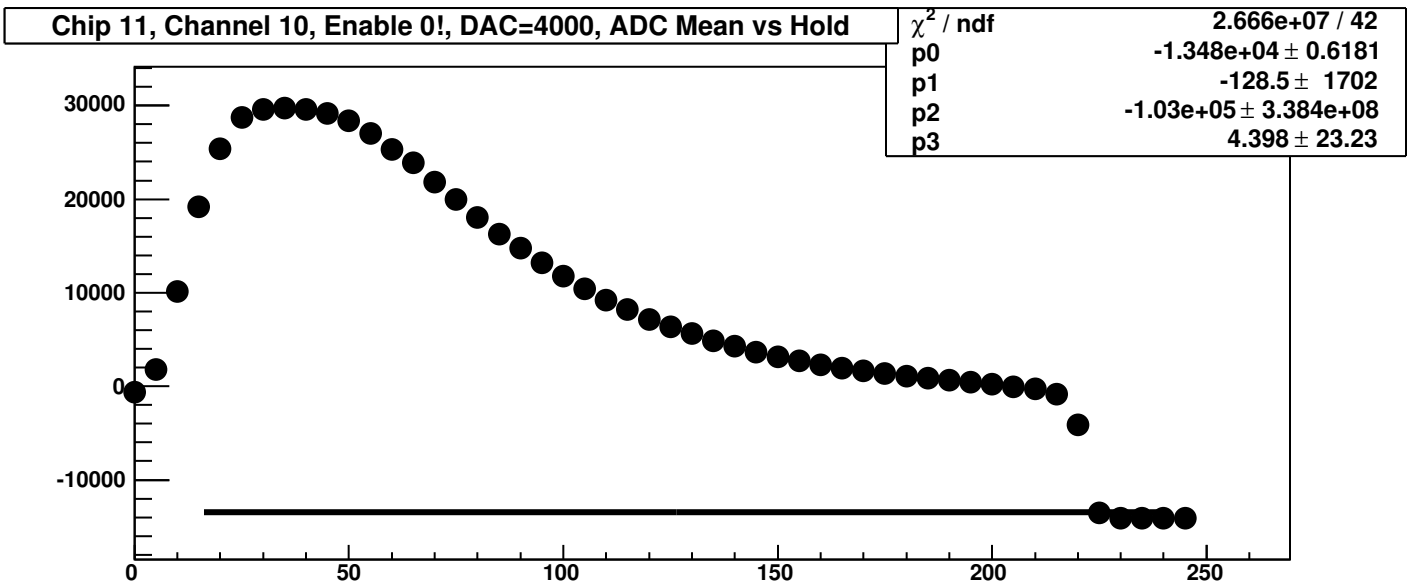
Chip 11, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

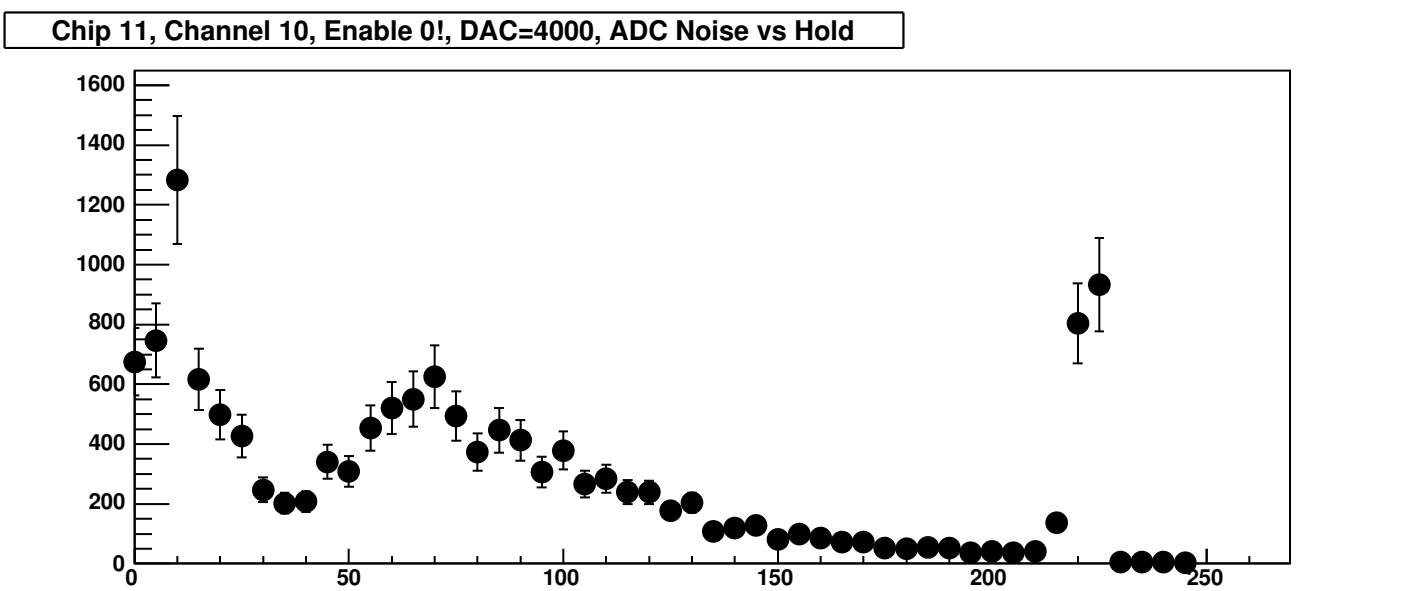


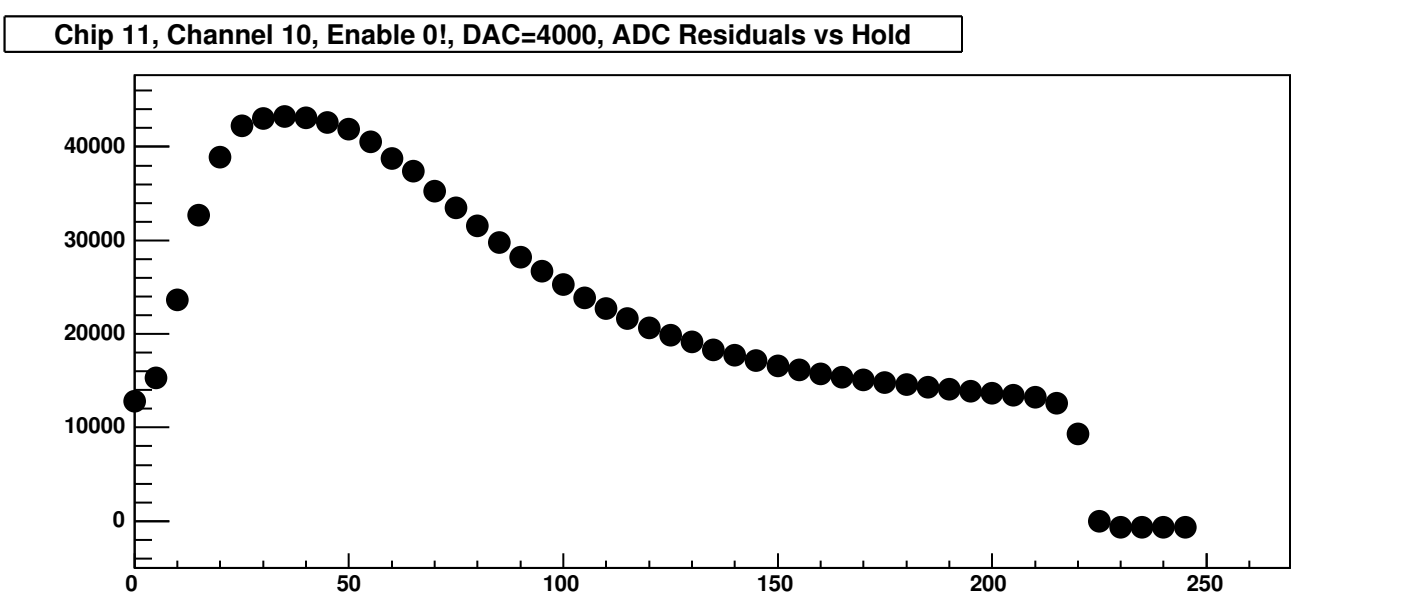

Chip 11, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.306e+05 / 41
p0 -258.9 ± 1.336
p1 47.18 ± 0.01814
p2 6278 ± 8.438
p3 36.87 ± 0.05917
p4 -1.622 ± 0.0535
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

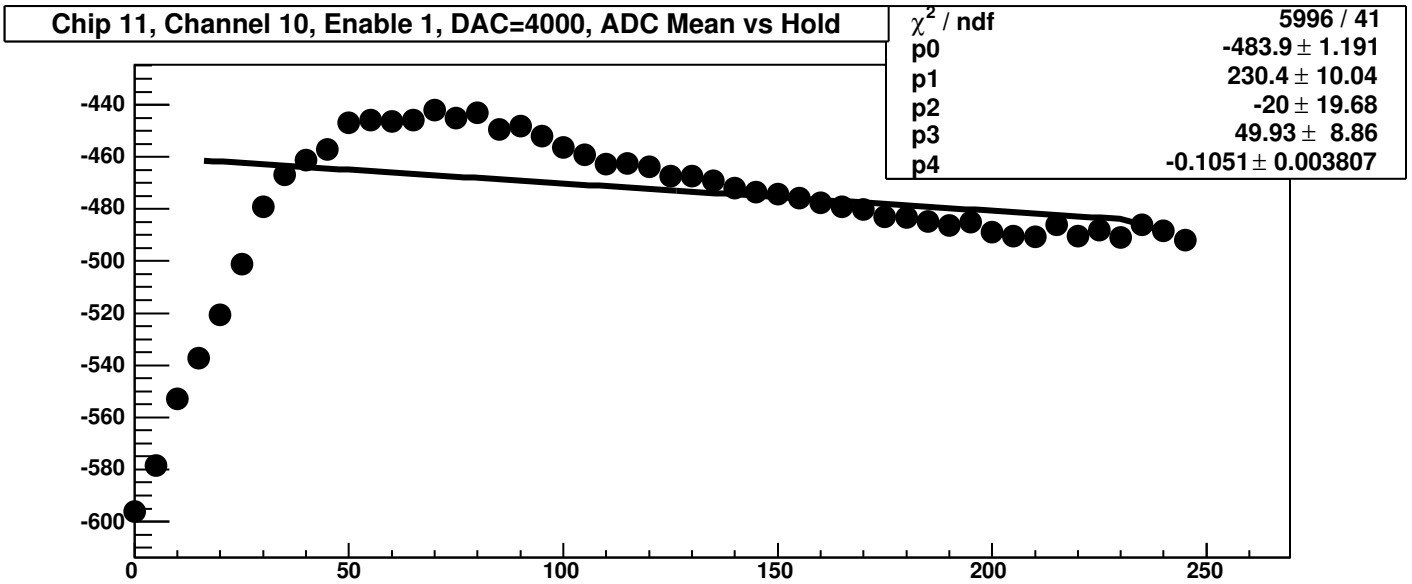

Chip 11, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

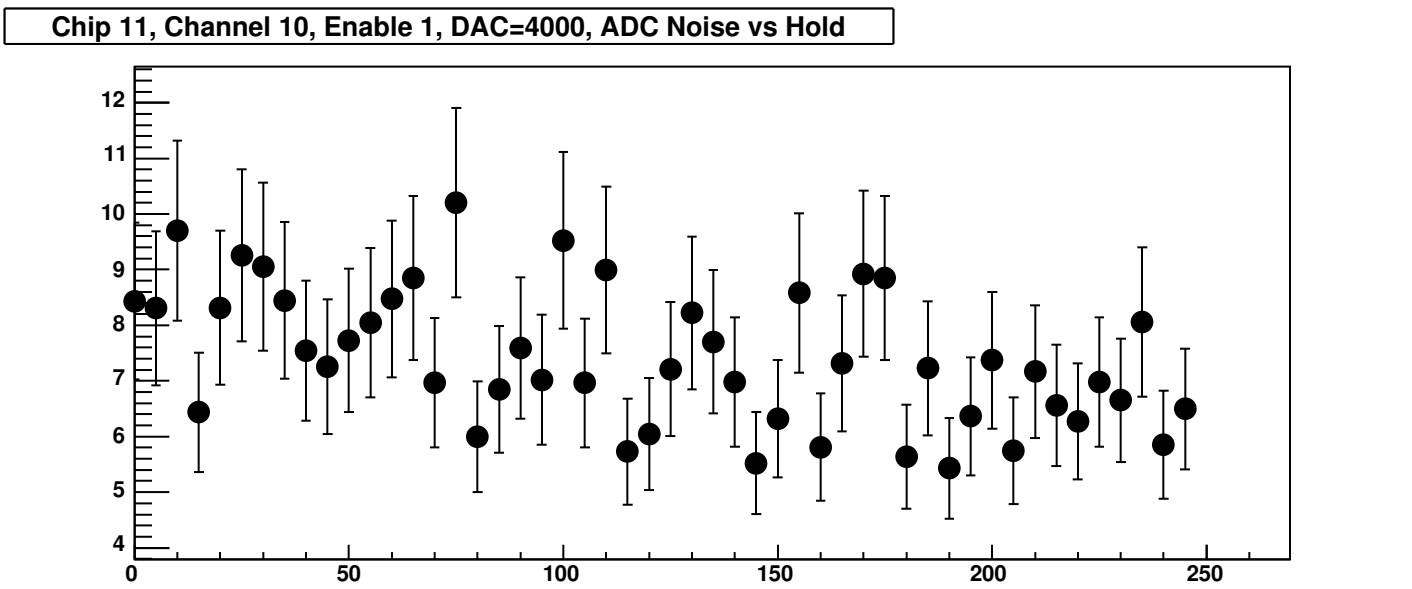

Chip 11, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

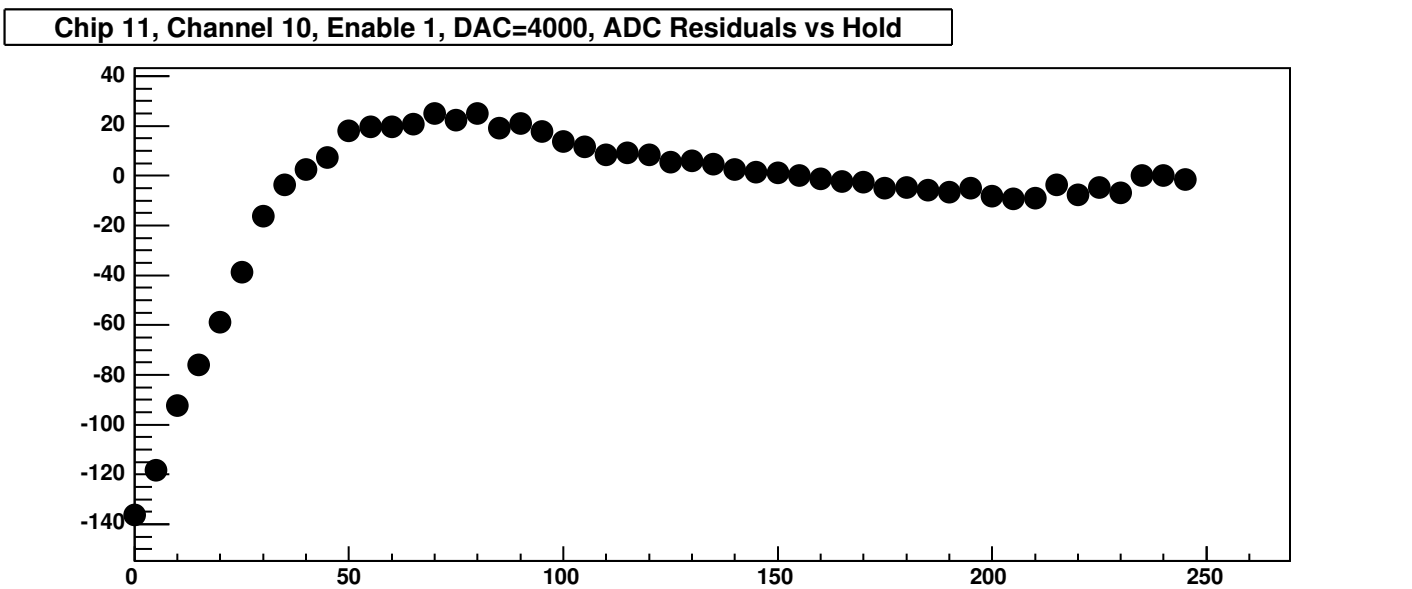

Chip 11, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.666e+07 / 42
p0 -1.348e+04 ± 0.6181
p1 -128.5 ± 1702
p2 -1.03e+05 ± 3.384e+08
p3 4.398 ± 23.23
30000
20000
10000
0
-10000
0
50
100
150
200
250

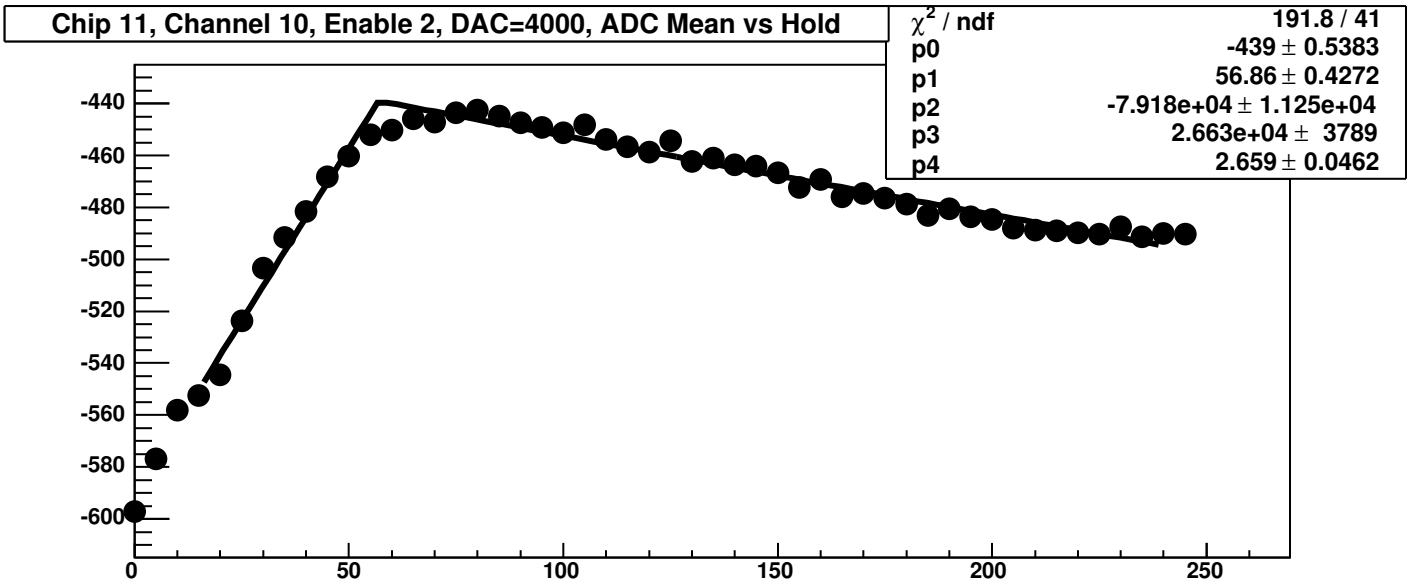

Chip 11, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

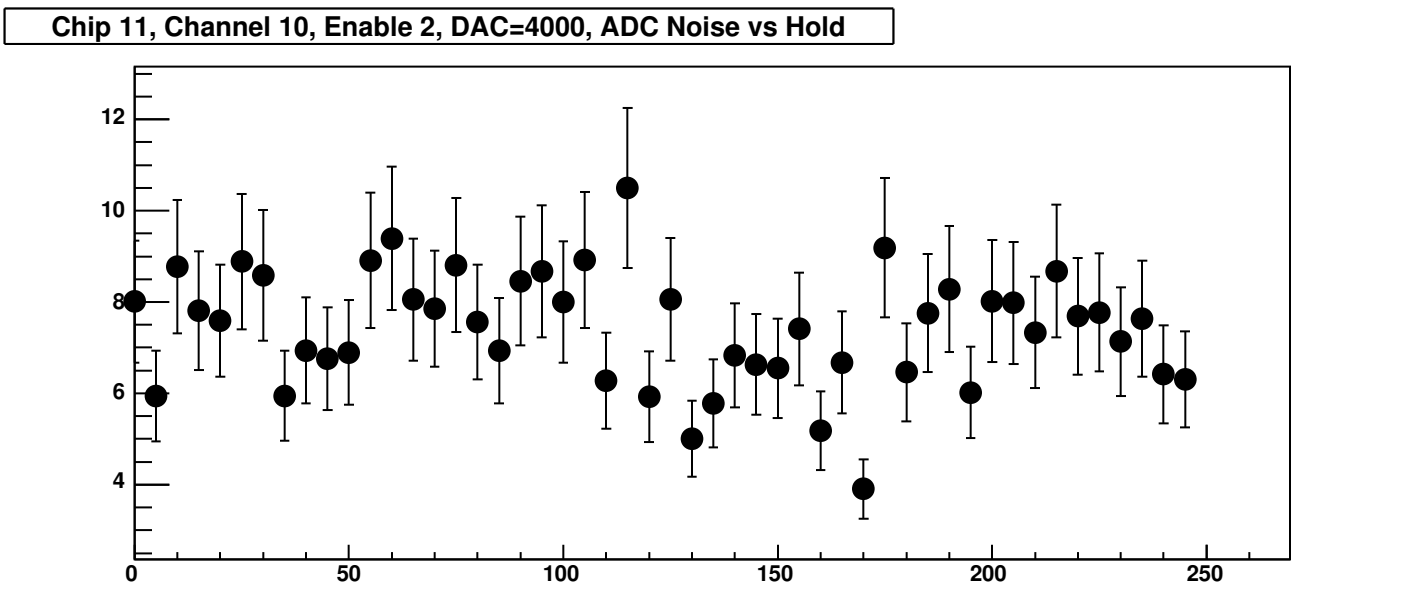

Chip 11, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold
40000
30000
20000
10000
0
0
50
100
150
200
250

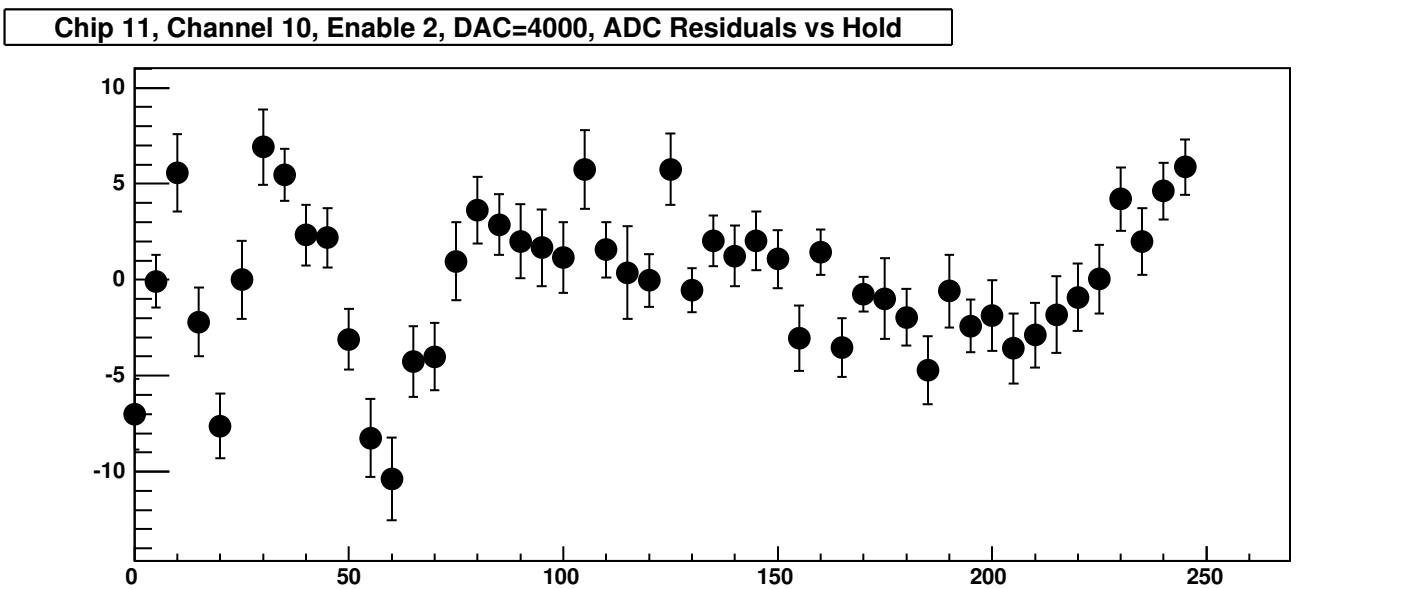

Chip 11, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 5996 / 41
p0 -483.9 ± 1.191
p1 230.4 ± 10.04
p2 -20 ± 19.68
p3 49.93 ± 8.86
p4 -0.1051 ± 0.003807
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
50
100
150
200
250


Chip 11, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

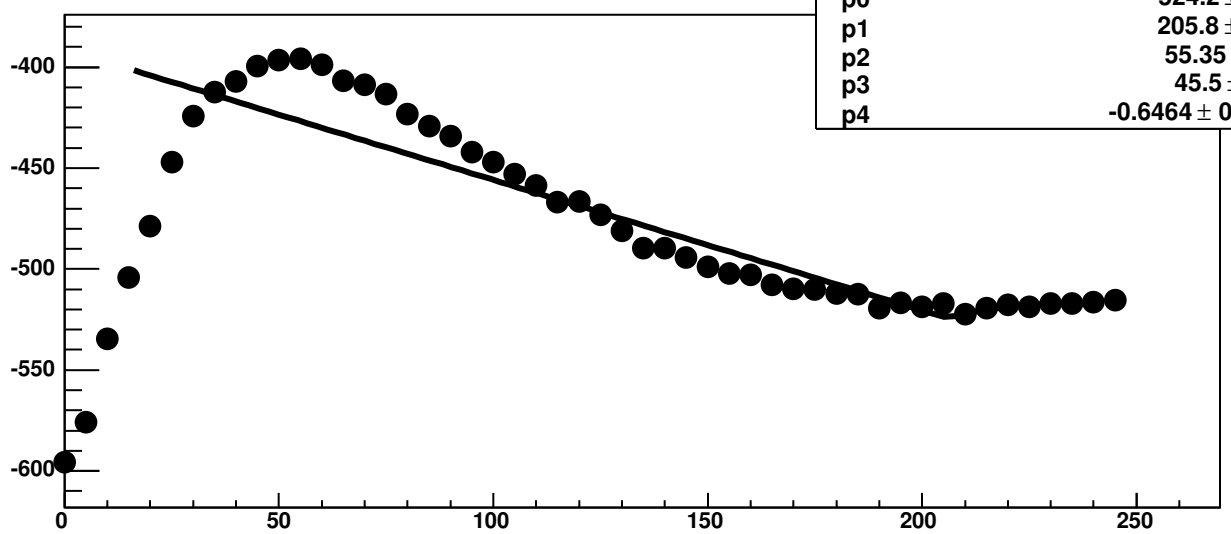

Chip 11, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
-100
-120
-140
0
50
100
150
200
250


Chip 11, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 191.8 / 41
p0 -439 ± 0.5383
p1 56.86 ± 0.4272
p2 -7.918e+04 ± 1.125e+04
p3 2.663e+04 ± 3789
p4 2.659 ± 0.0462
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
50
100
150
200
250

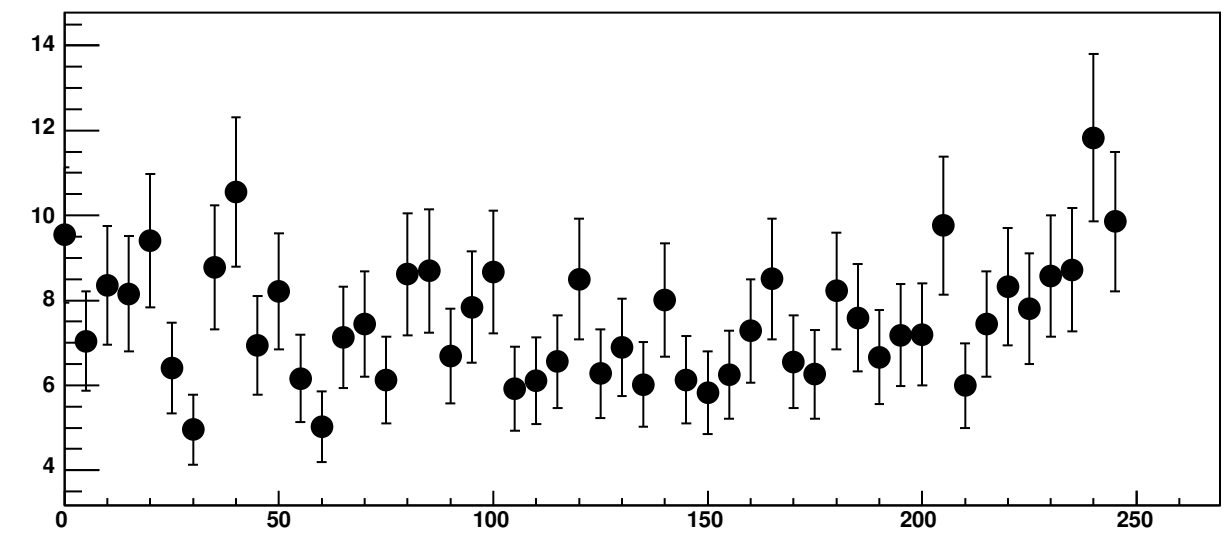

Chip 11, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 11, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250

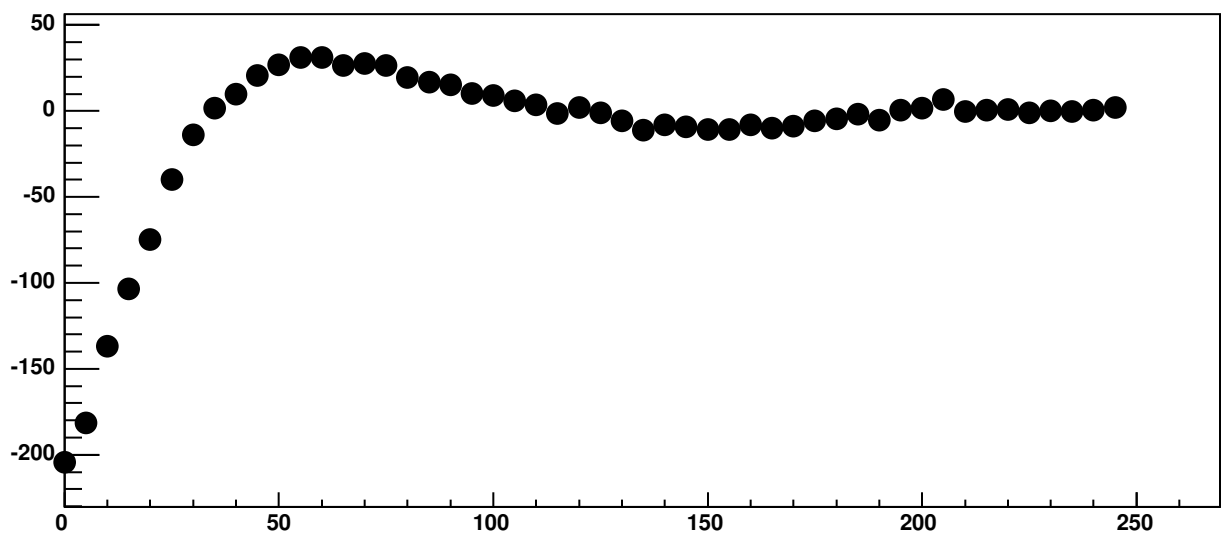
Chip 11, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold

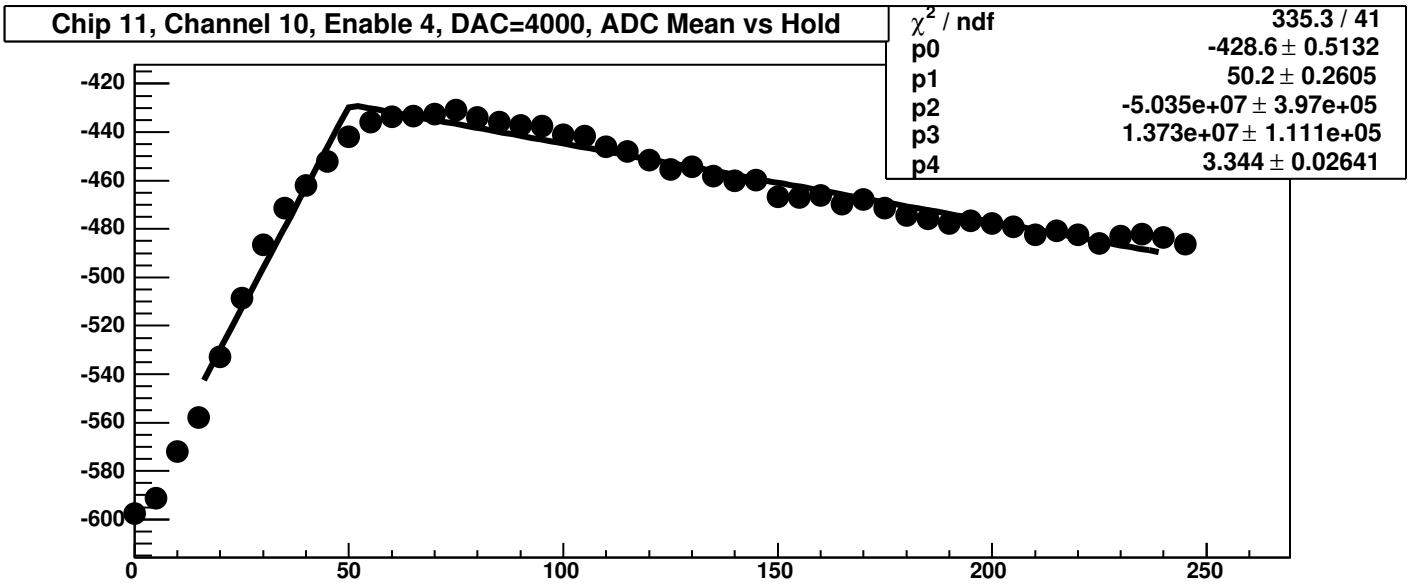


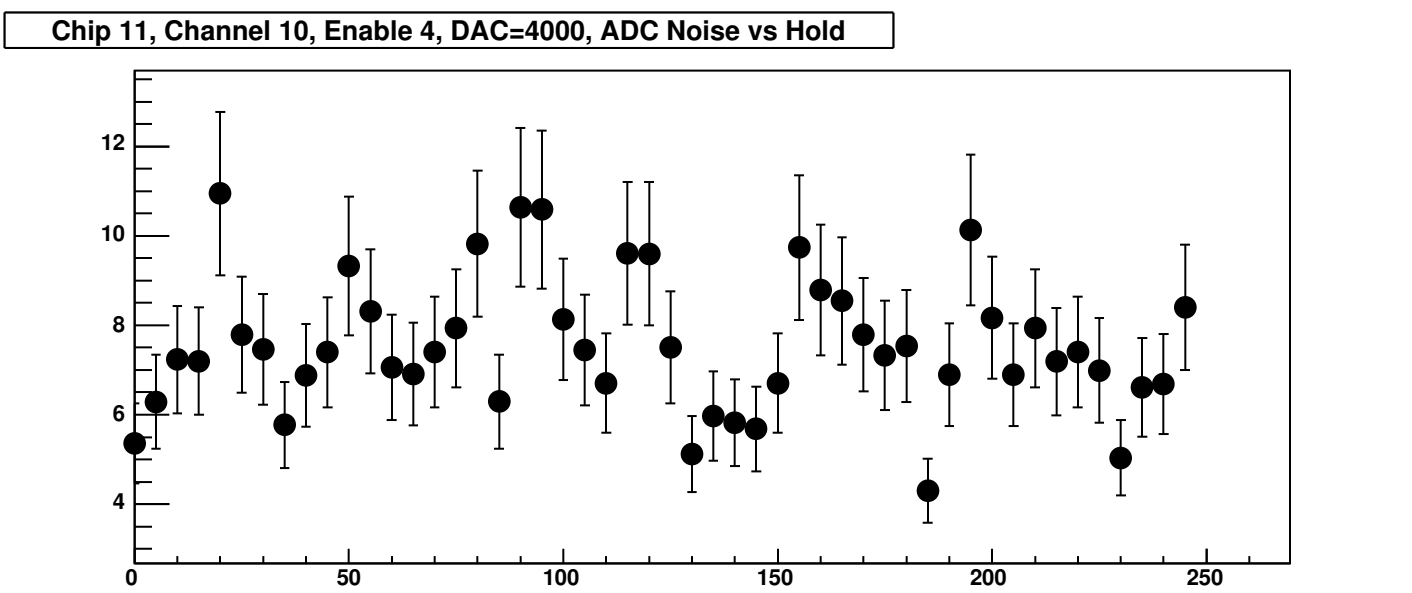
Chip 11, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold

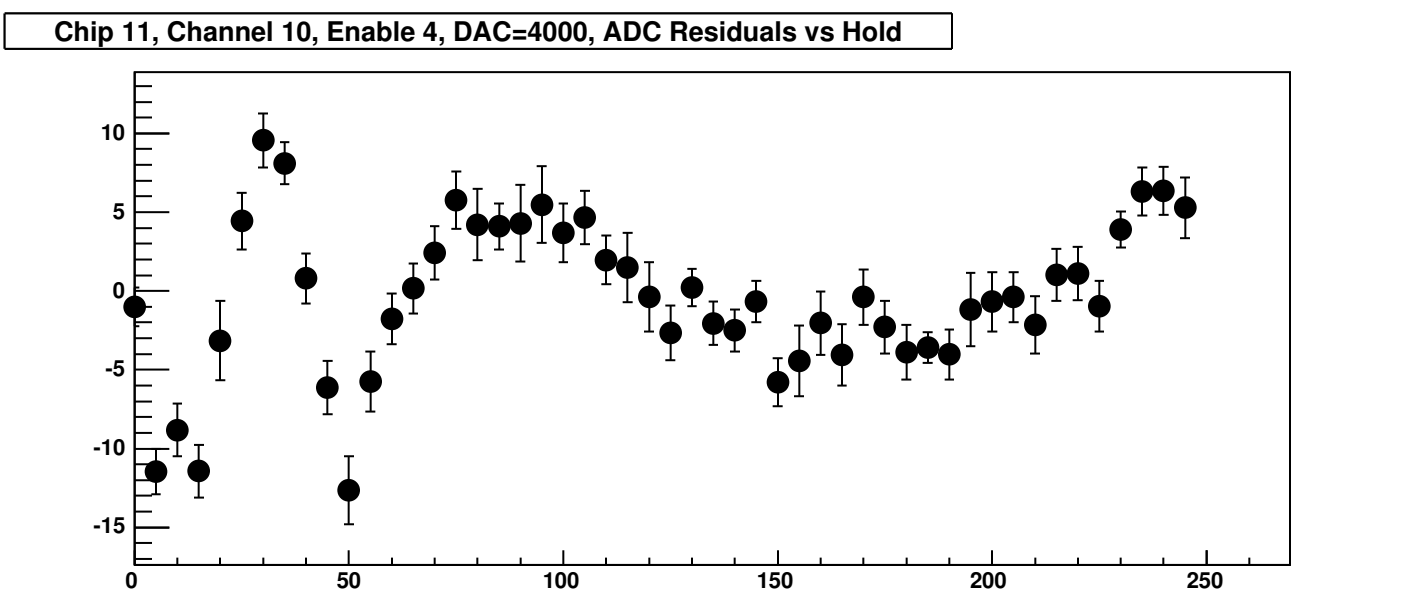


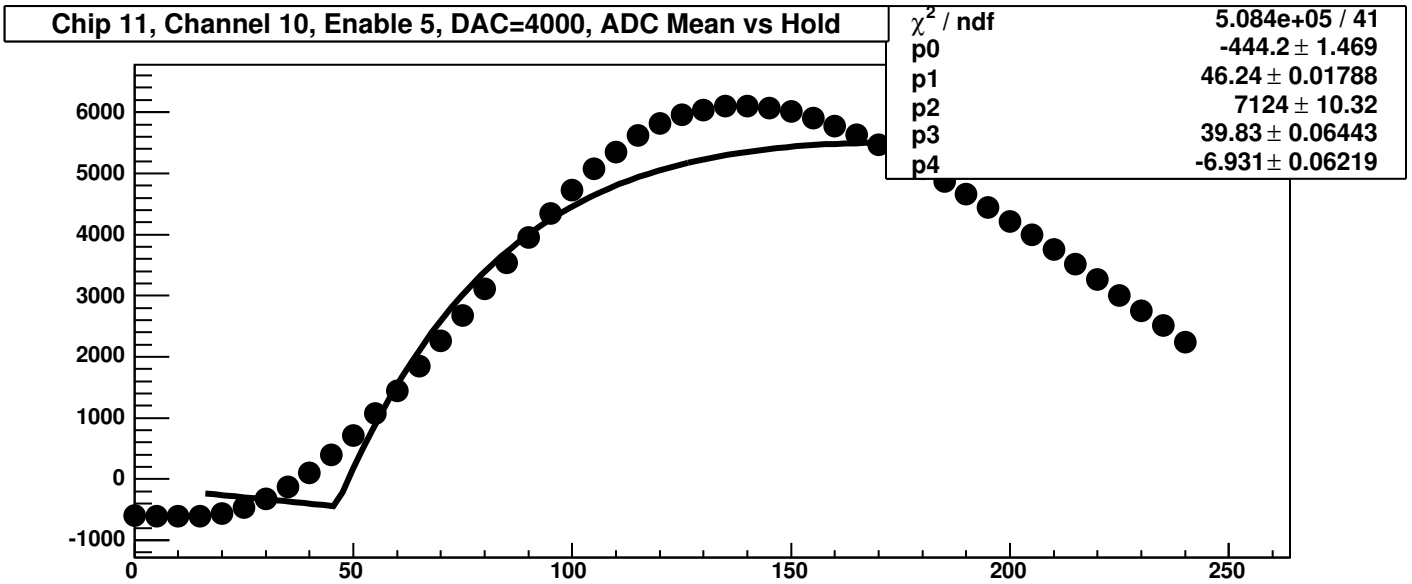
Chip 11, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold

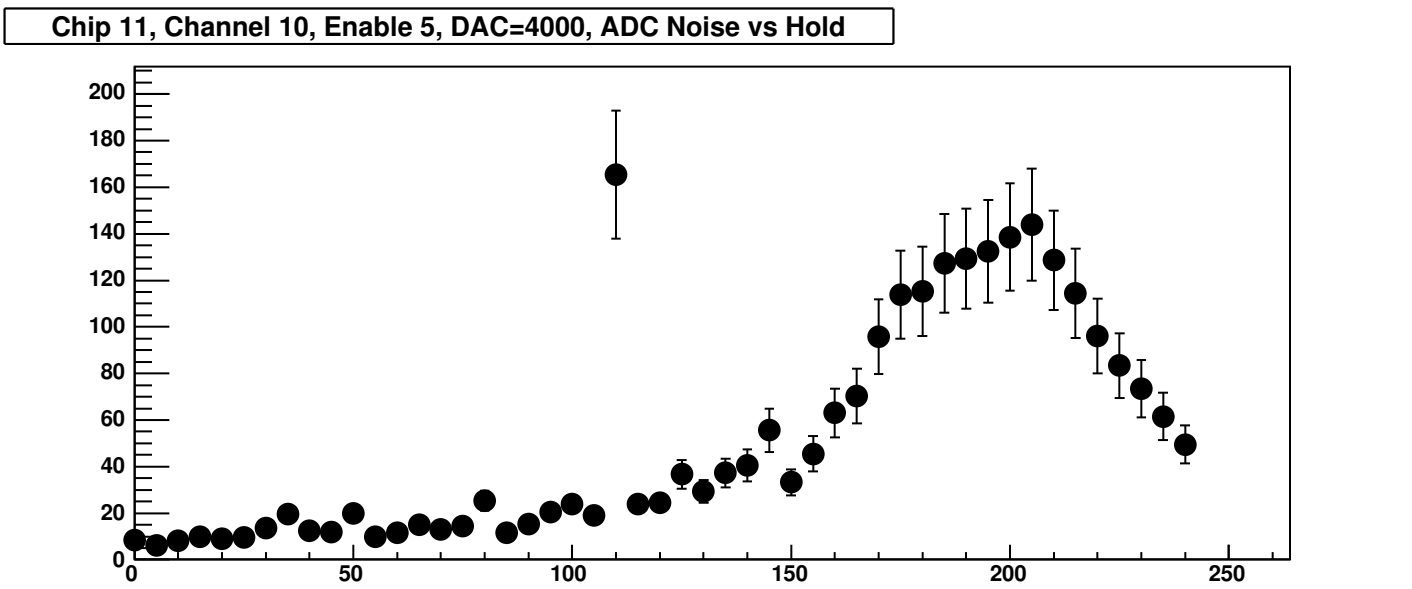


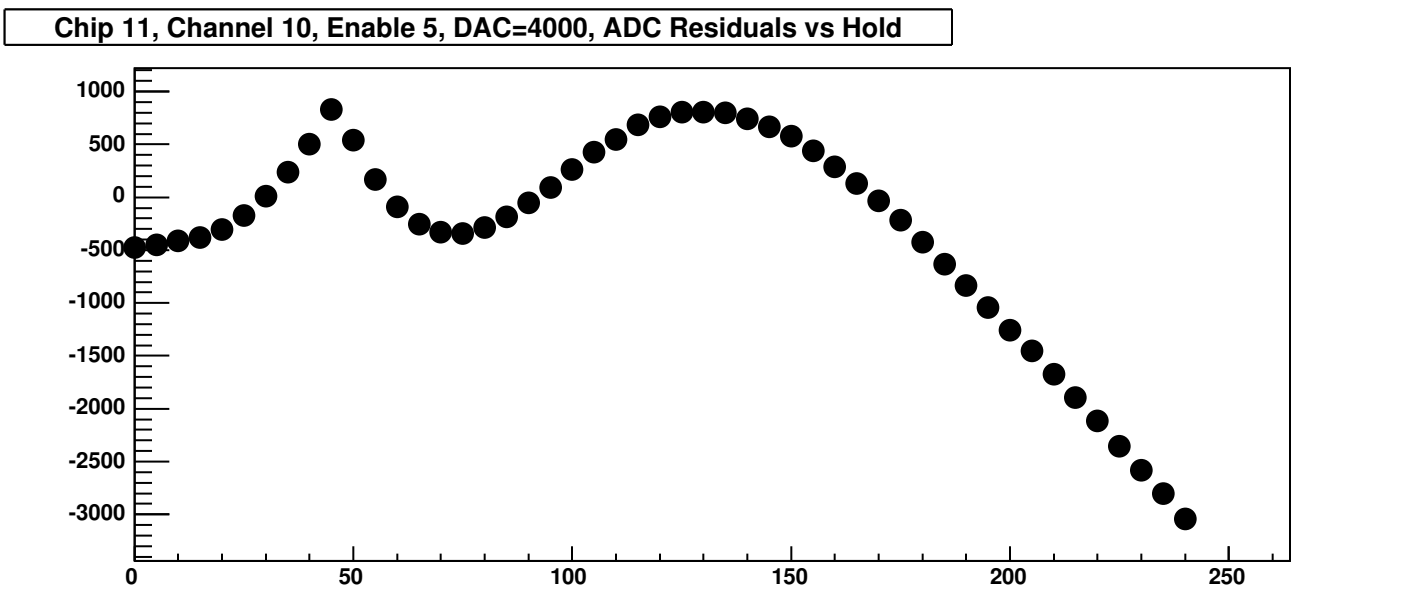

Chip 11, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 335.3 / 41
p0 -428.6 ± 0.5132
p1 50.2 ± 0.2605
p2 -5.035e+07 ± 3.97e+05
p3 1.373e+07 ± 1.111e+05
p4 3.344 ± 0.02641
-420
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
50
100
150
200
250

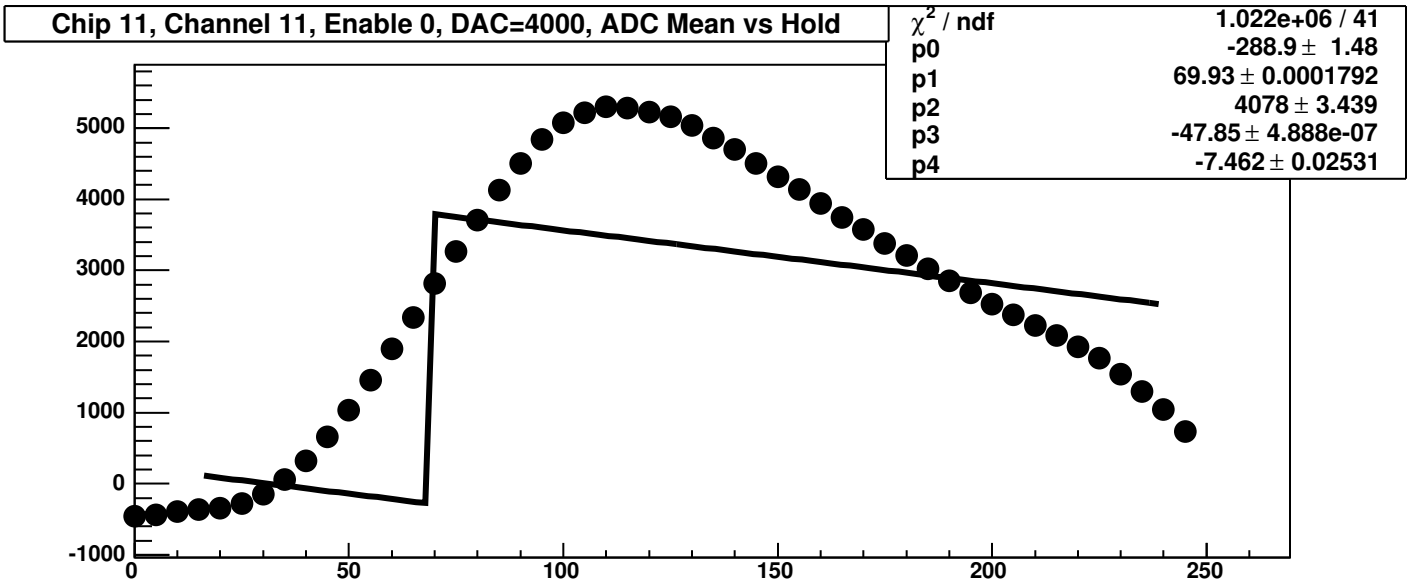

Chip 11, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

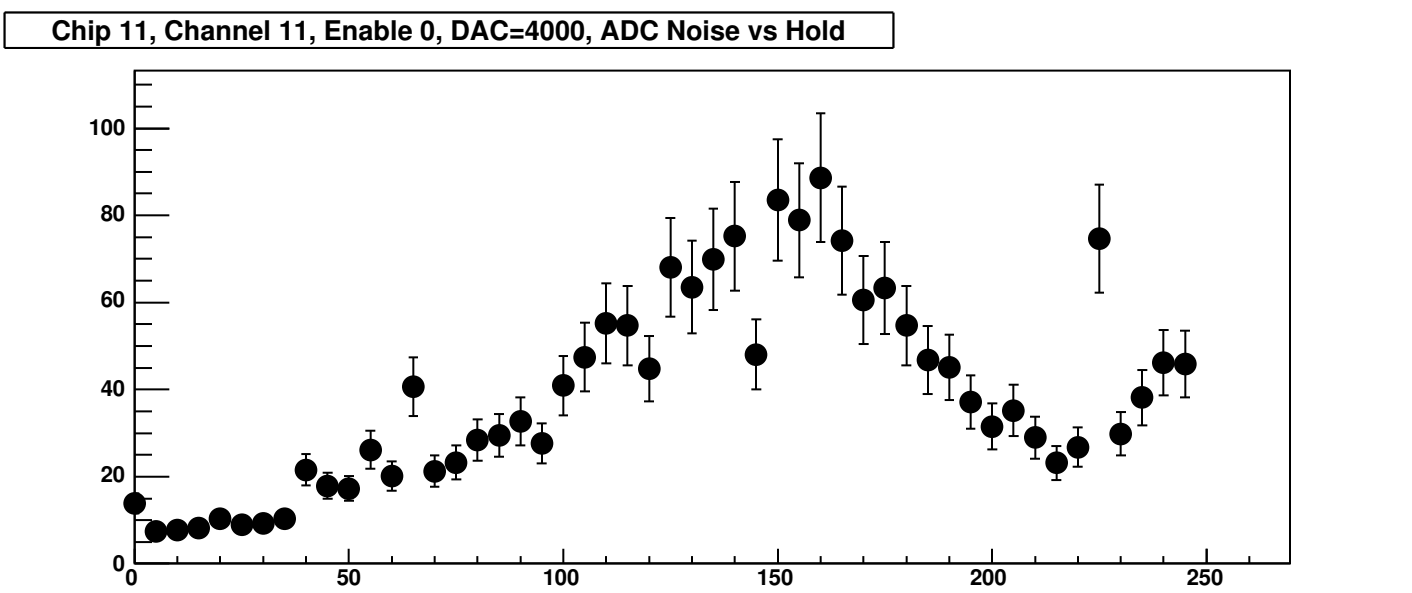

Chip 11, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
0
50
100
150
200
250

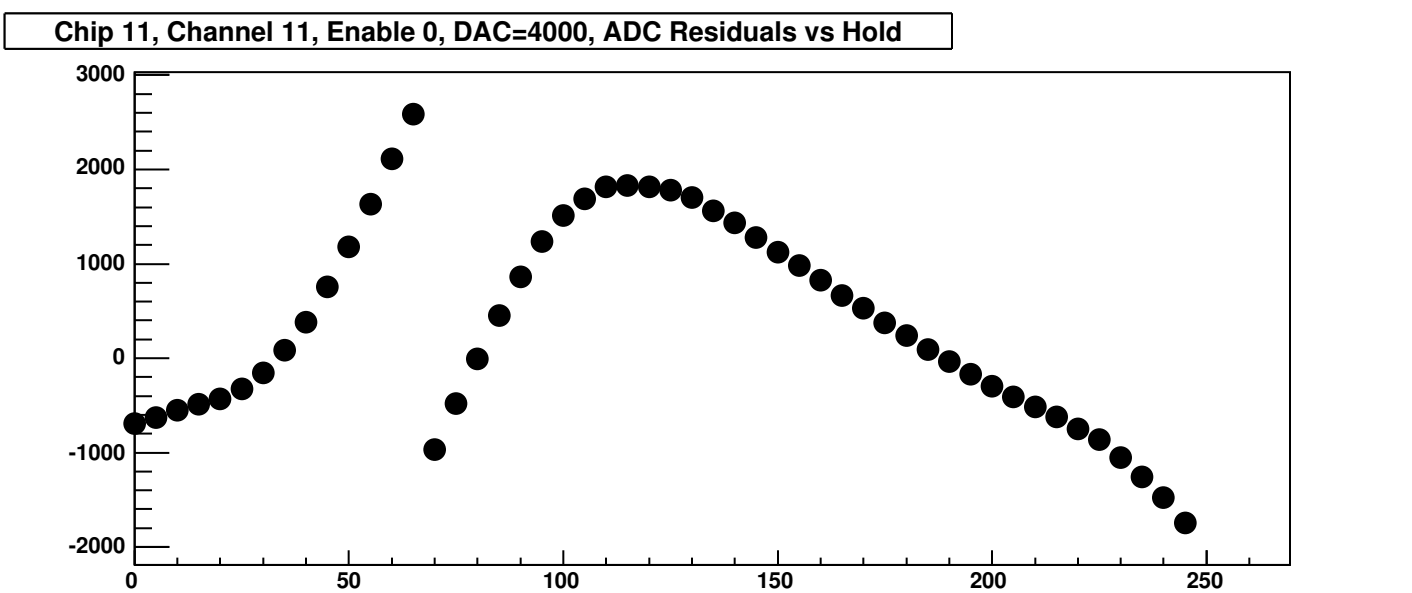

Chip 11, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.084e+05 / 41
p0 -444.2 ± 1.469
p1 46.24 ± 0.01788
p2 7124 ± 10.32
p3 39.83 ± 0.06443
p4 -6.931 ± 0.06219
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

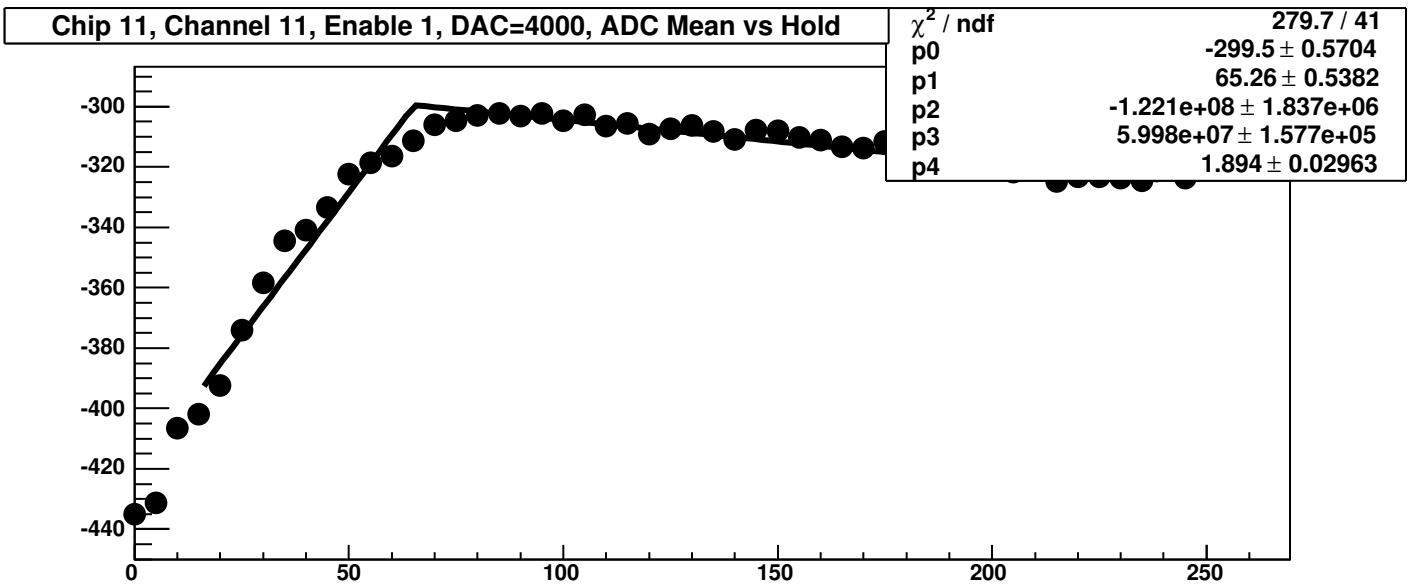

Chip 11, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

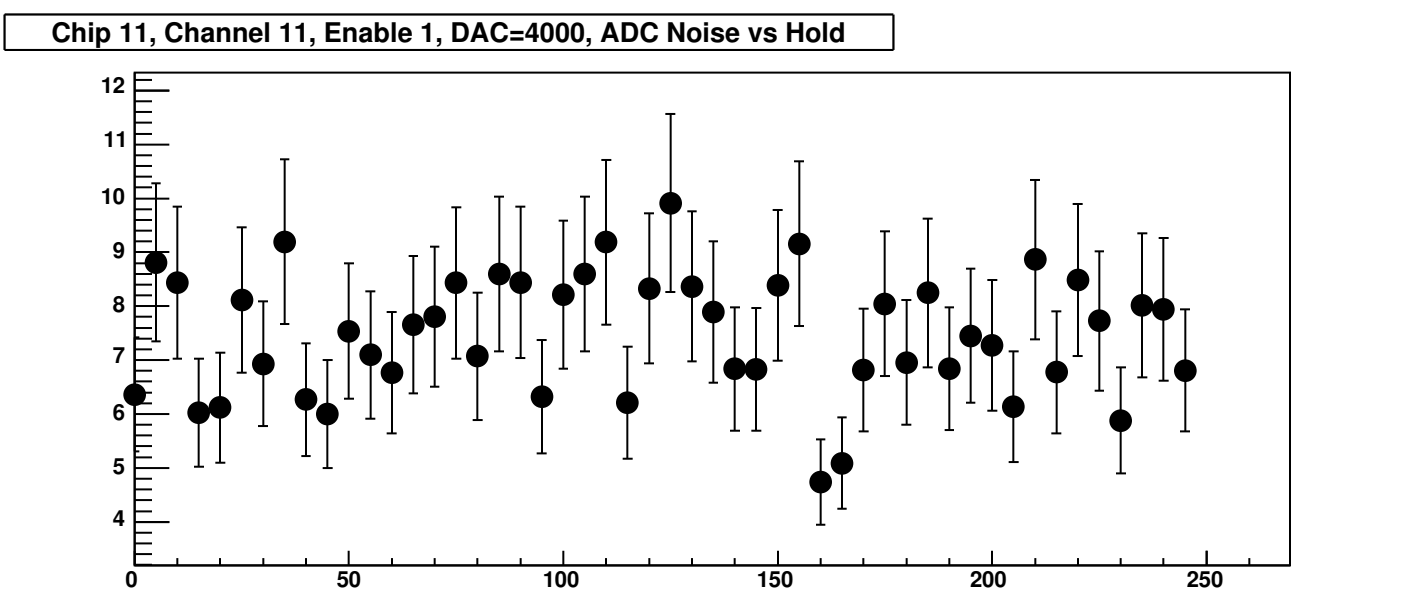

Chip 11, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250

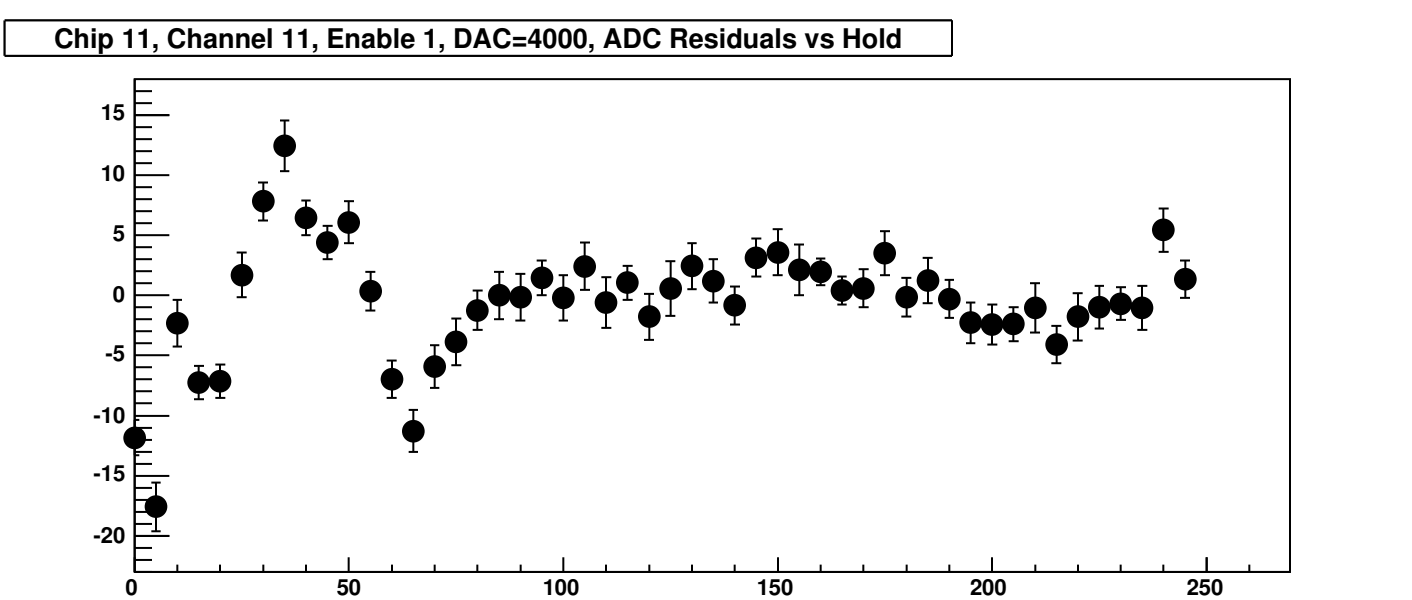

Chip 11, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.022e+06 / 41
p0 -288.9 ± 1.48
p1 69.93 ± 0.0001792
p2 4078 ± 3.439
p3 -47.85 ± 4.888e-07
p4 -7.462 ± 0.02531
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

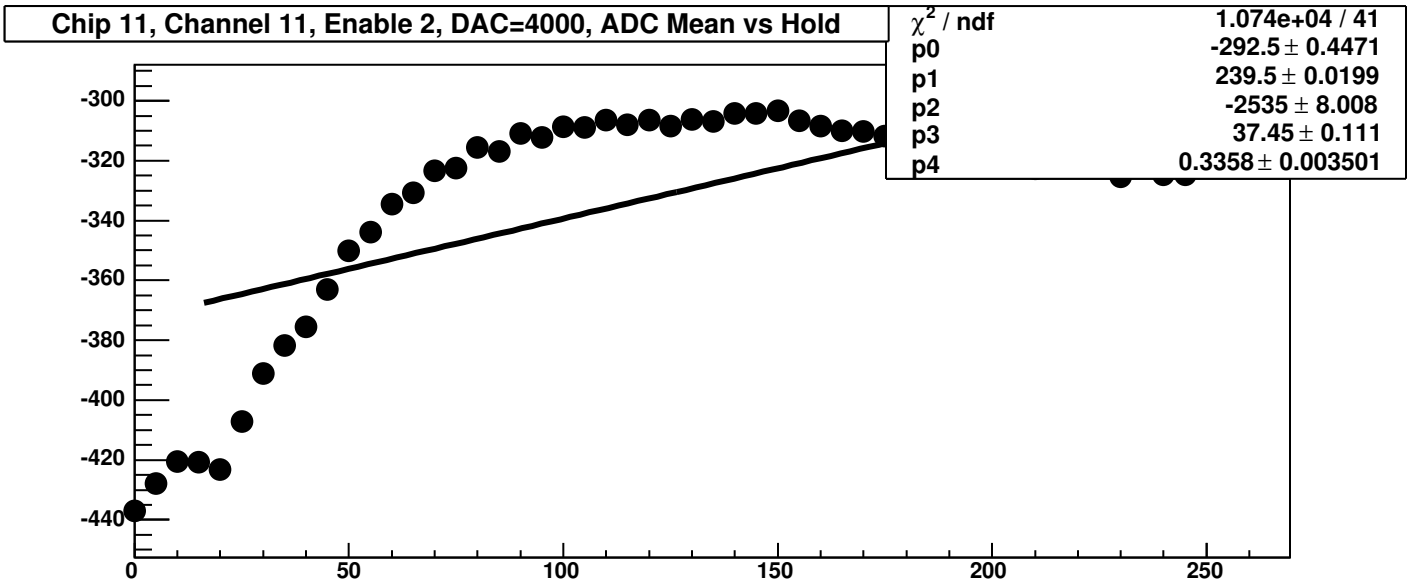

Chip 11, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250

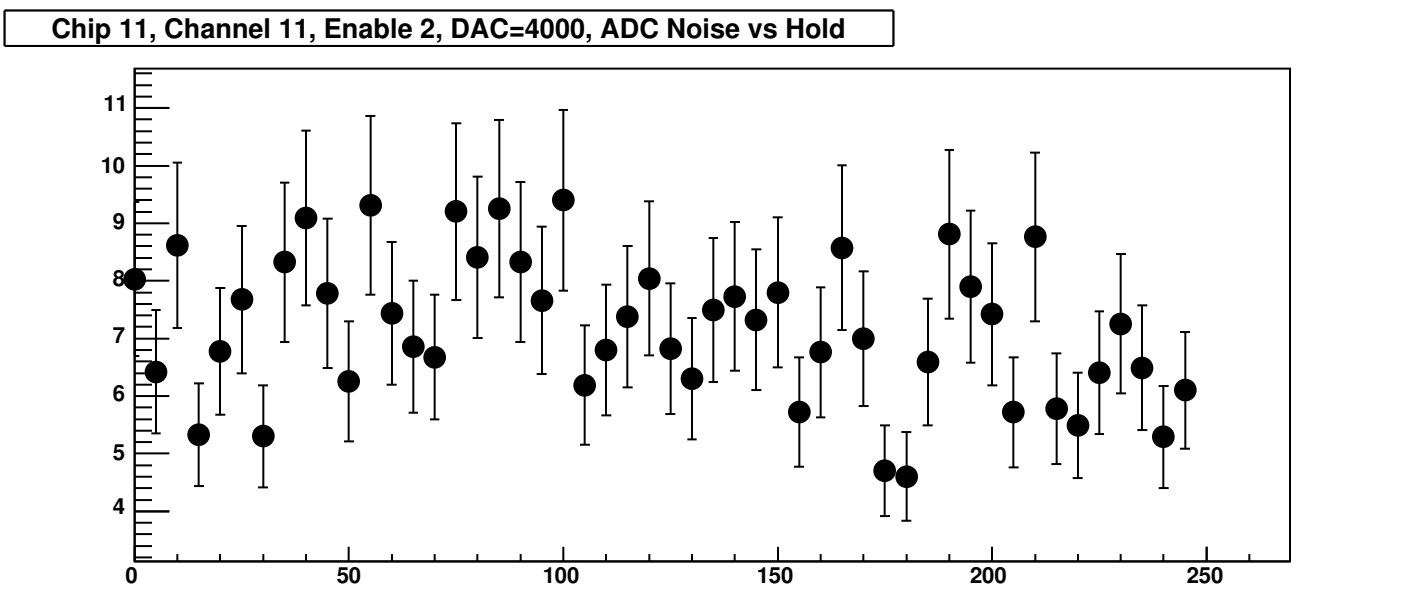

Chip 11, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold
3000
2000
1000
0
-1000
-2000
0
50
100
150
200
250

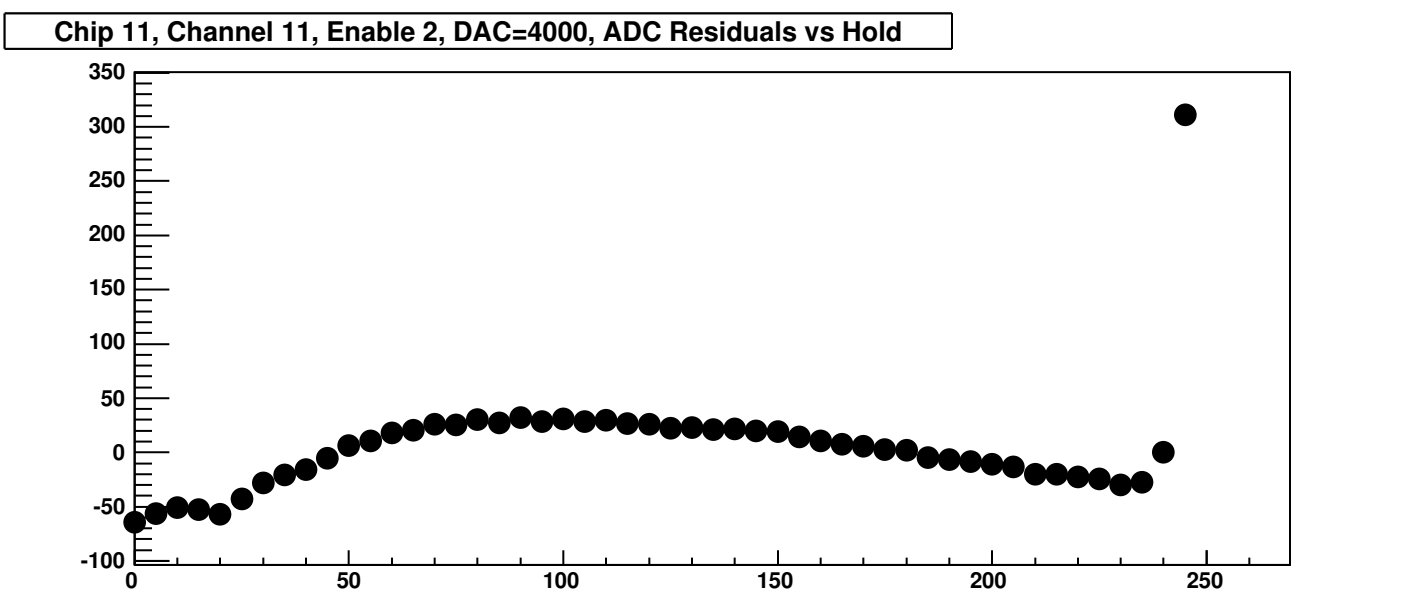

Chip 11, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 279.7 / 41
p0 -299.5 ± 0.5704
p1 65.26 ± 0.5382
p2 -1.221e+08 ± 1.837e+06
p3 5.998e+07 ± 1.577e+05
p4 1.894 ± 0.02963
-300
-320
-340
-360
-380
-400
-420
-440
0
50
100
150
200
250

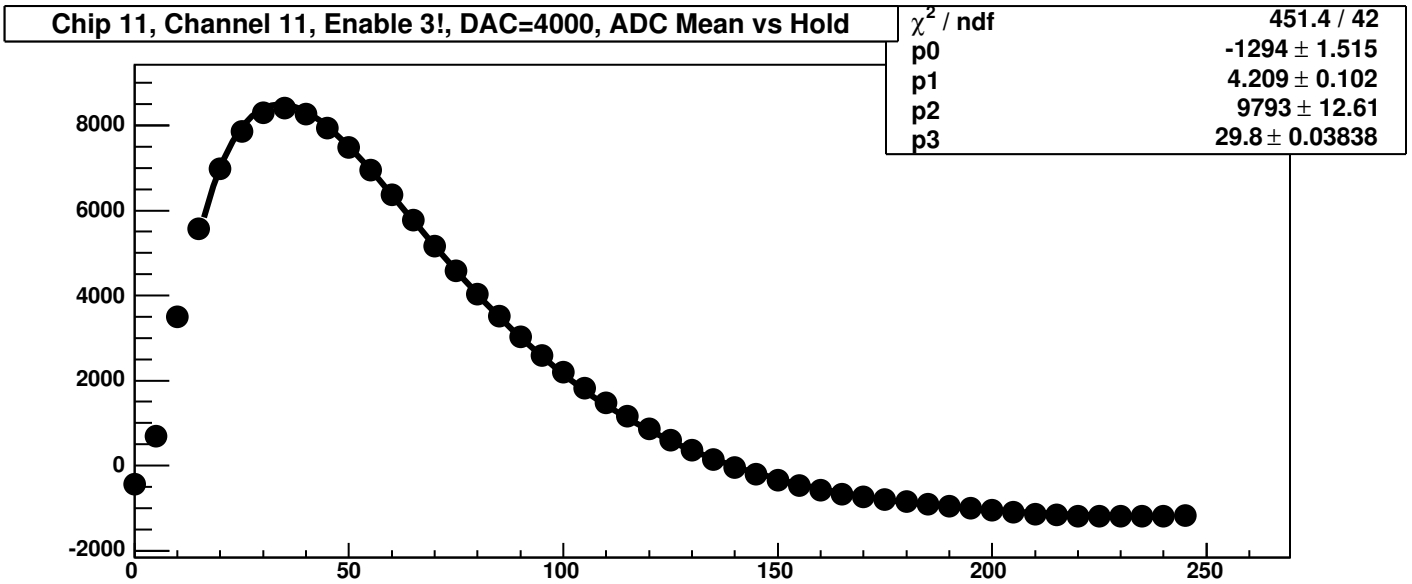

Chip 11, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

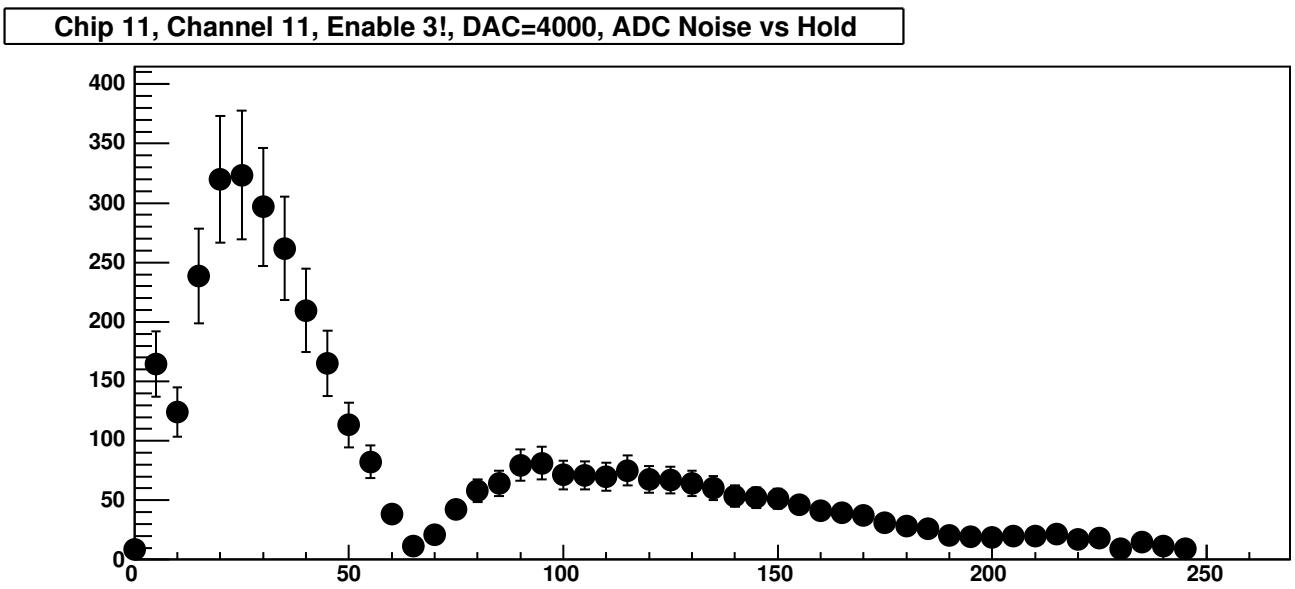

Chip 11, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
-15
-20
0
50
100
150
200
250

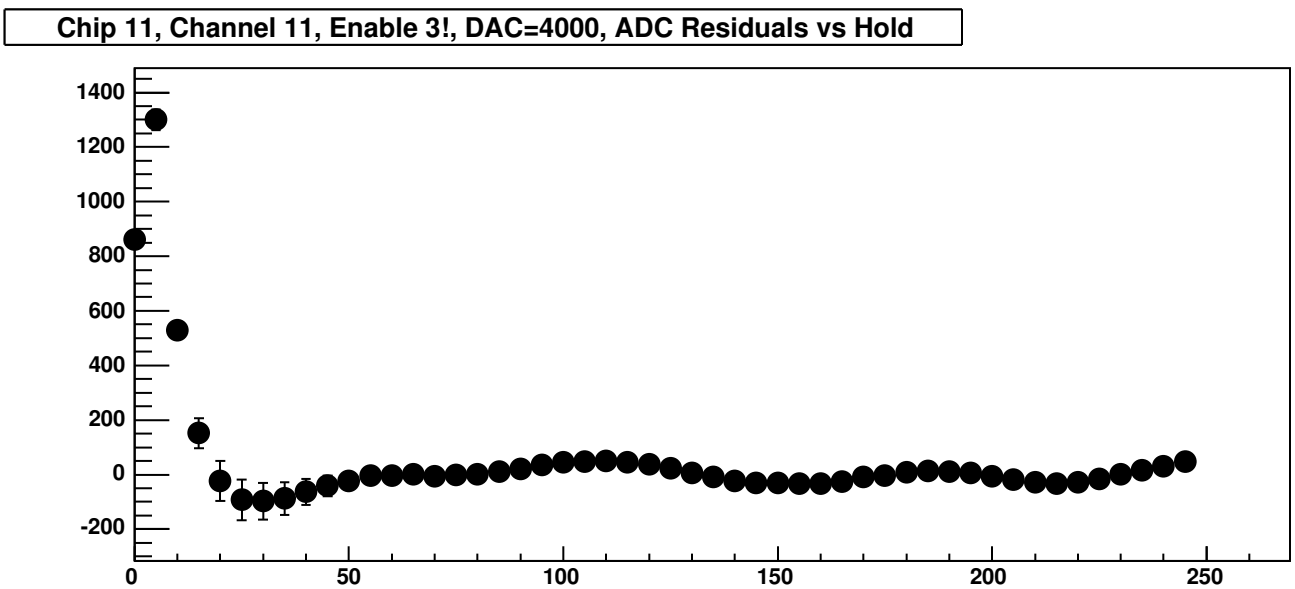

Chip 11, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.074e+04 / 41
p0 -292.5 ± 0.4471
p1 239.5 ± 0.0199
p2 -2535 ± 8.008
p3 37.45 ± 0.111
p4 0.3358 ± 0.003501
-300
-320
-340
-360
-380
-400
-420
-440
0
50
100
150
200
250


Chip 11, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250

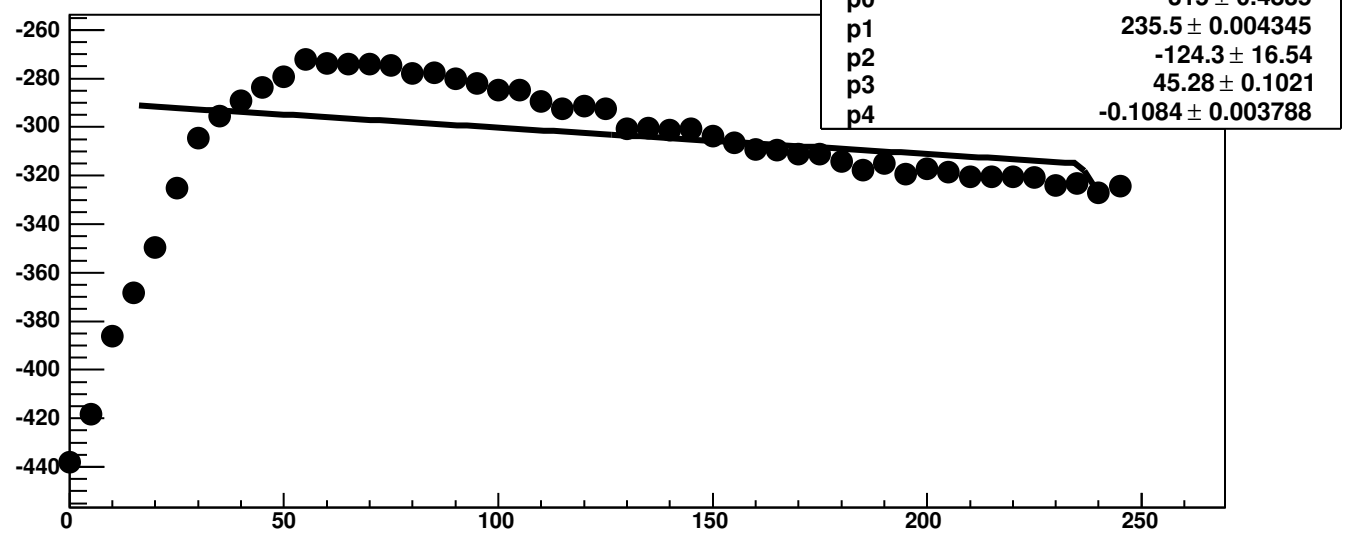

Chip 11, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold
350
300
250
200
150
100
50
0
-50
-100
0
50
100
150
200
250


Chip 11, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 451.4 / 42
p0 -1294 ± 1.515
p1 4.209 ± 0.102
p2 9793 ± 12.61
p3 29.8 ± 0.03838
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

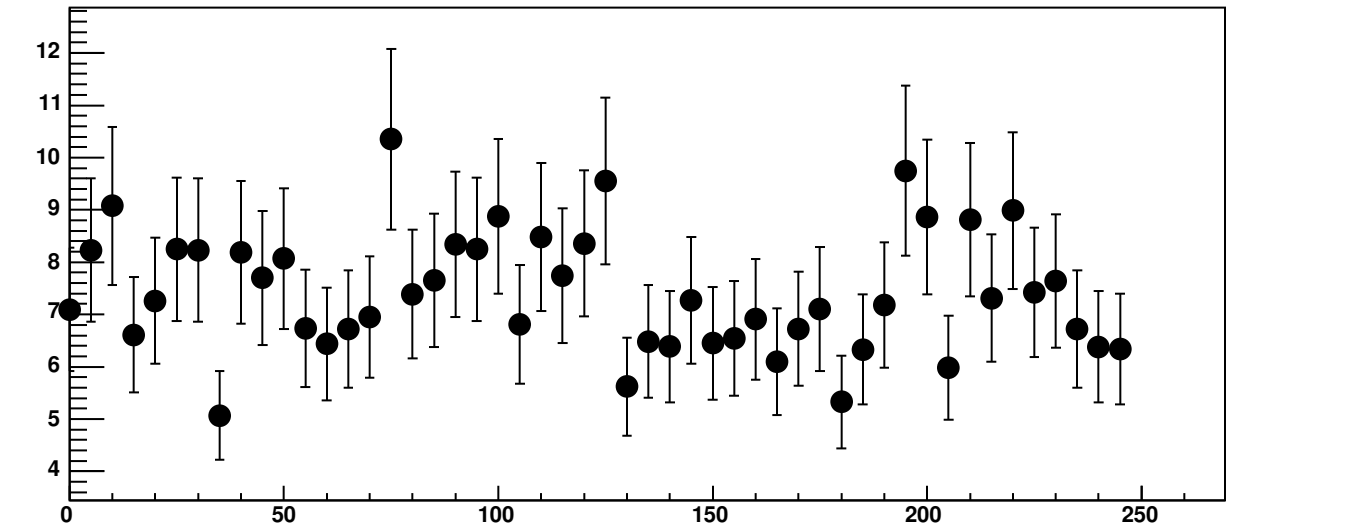

Chip 11, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold
400
350
300
250
200
150
100
50
0
0
50
100
150
200
250


Chip 11, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

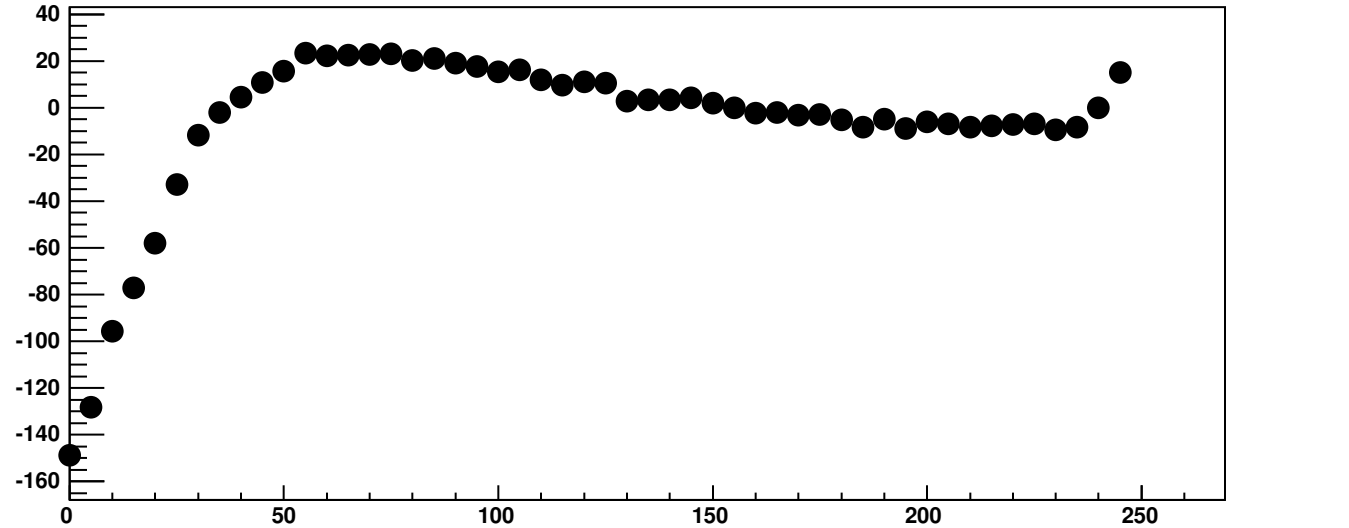
Chip 11, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

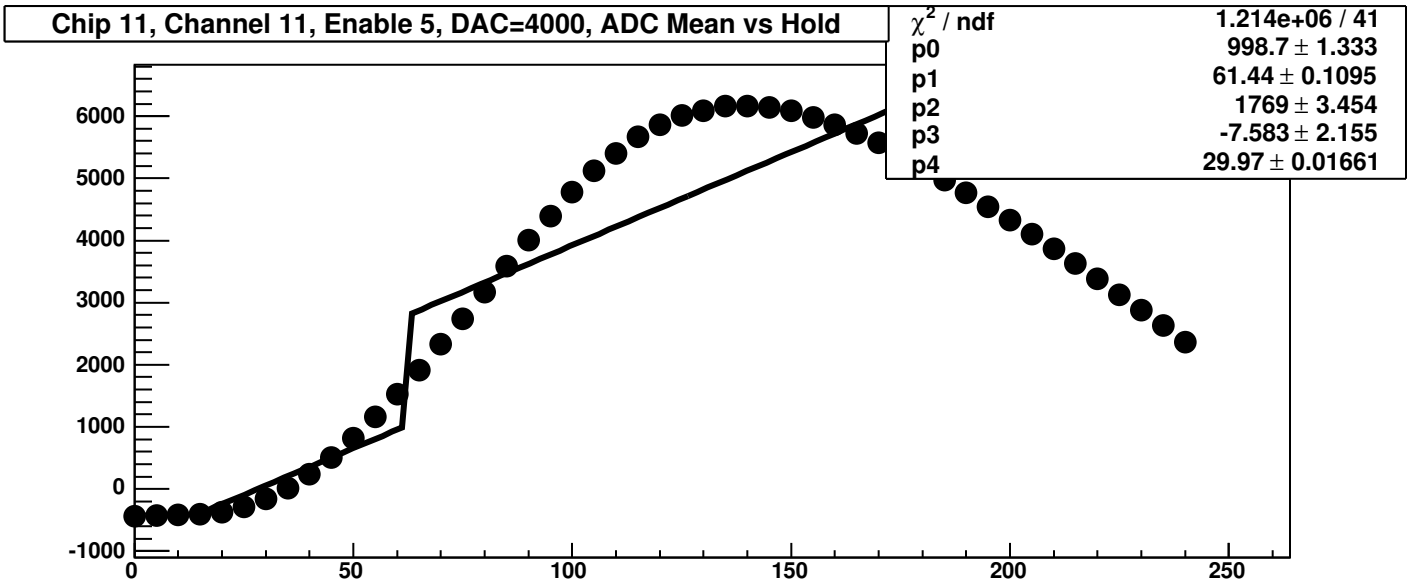


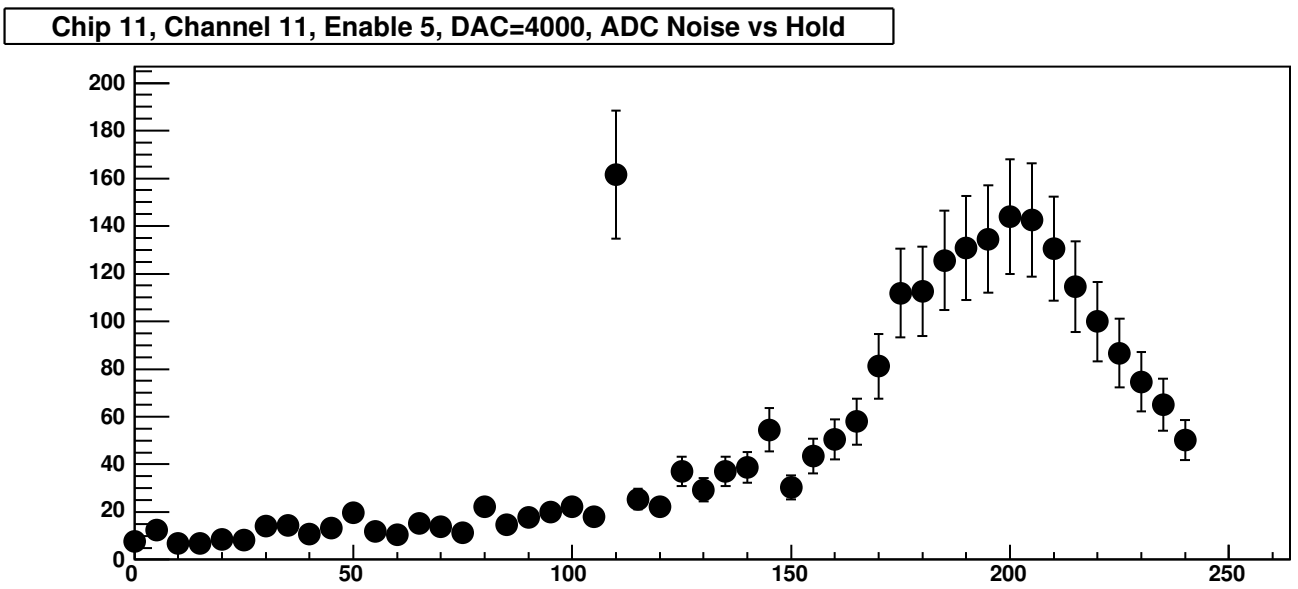
Chip 11, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold

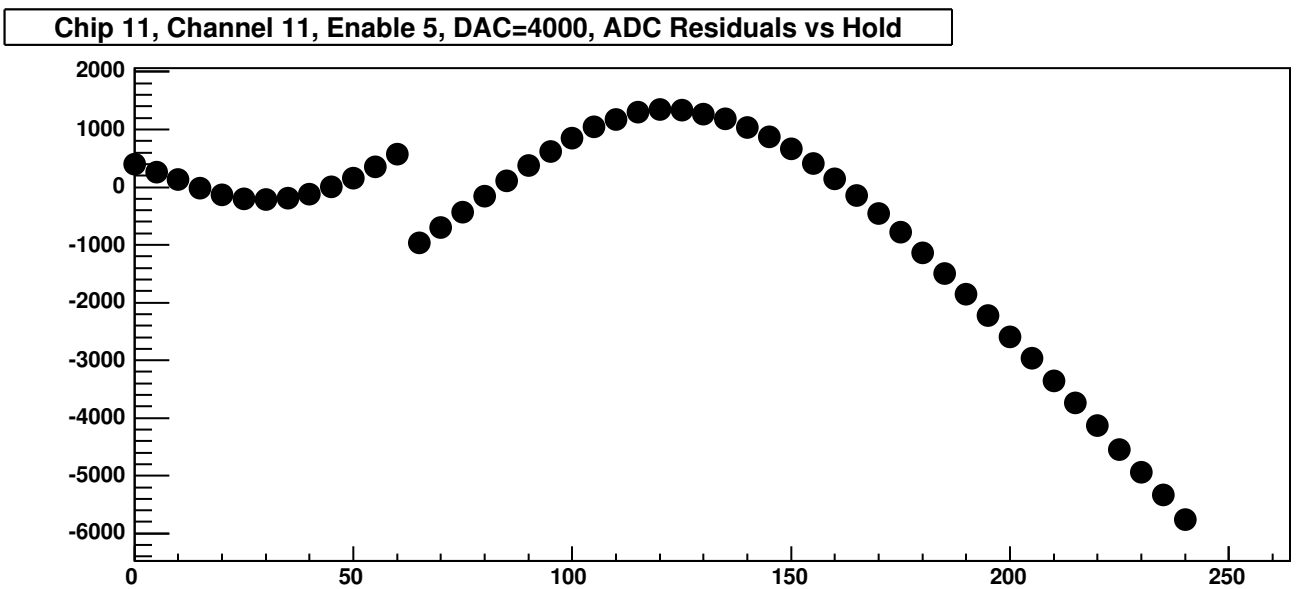


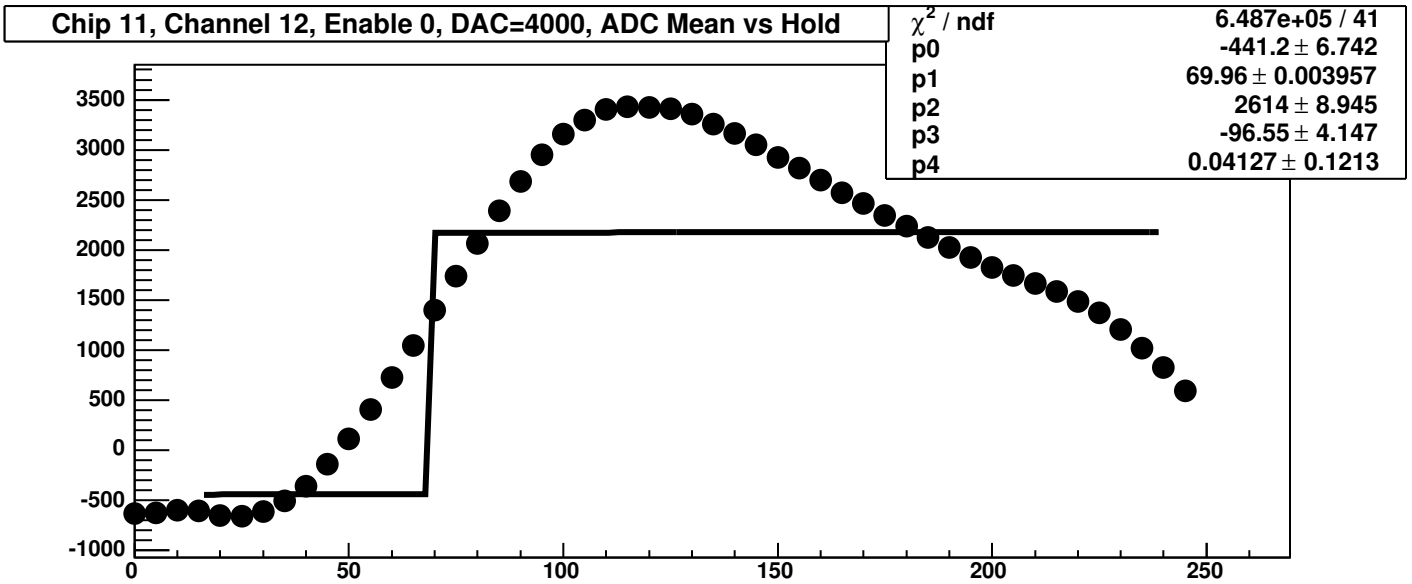
Chip 11, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

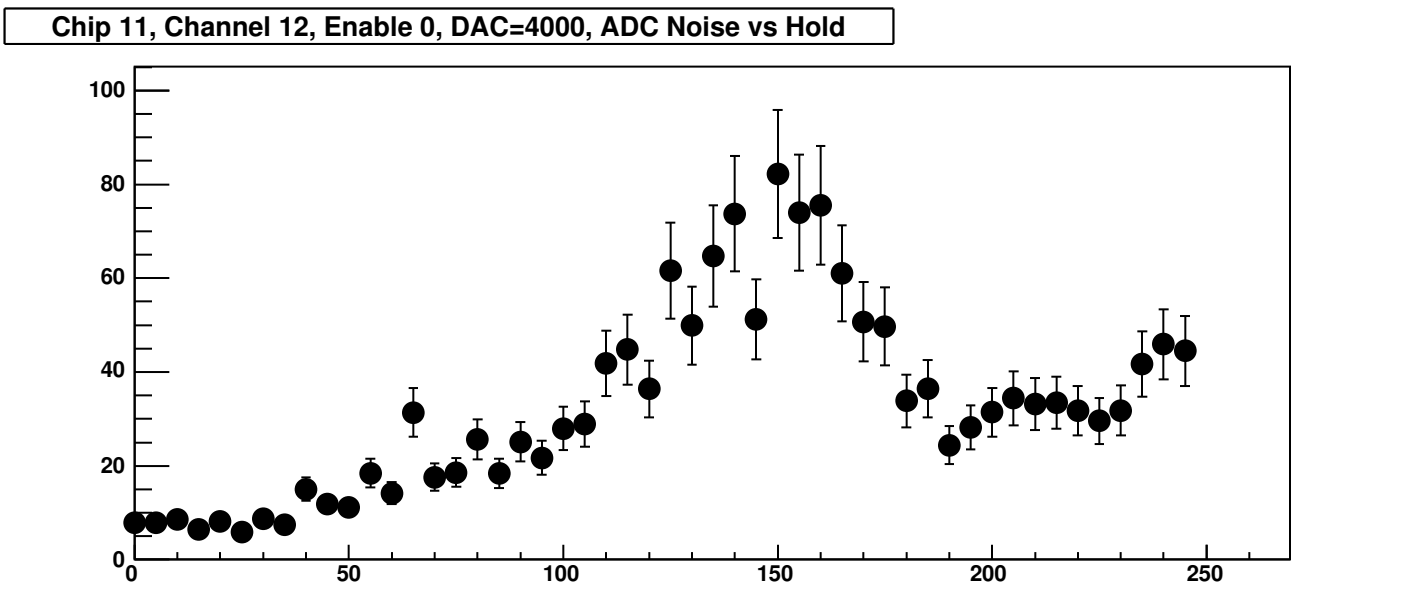


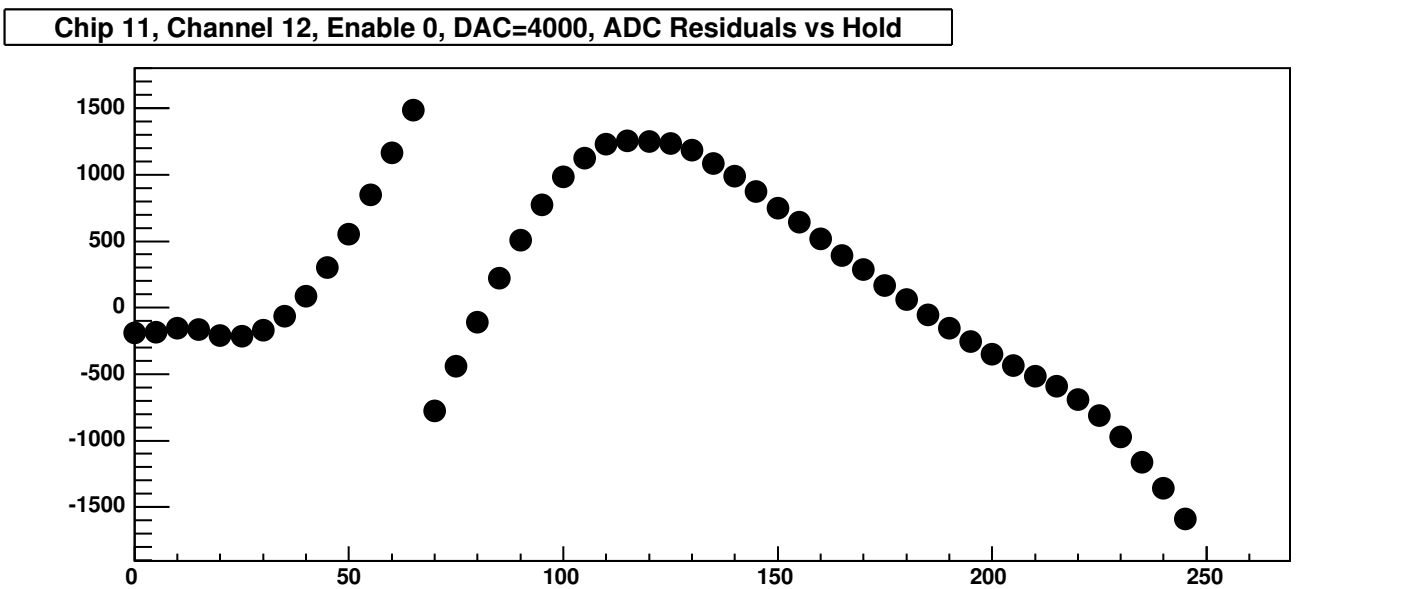

Chip 11, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.214e+06 / 41
p0 998.7 ± 1.333
p1 61.44 ± 0.1095
p2 1769 ± 3.454
p3 -7.583 ± 2.155
p4 29.97 ± 0.01661
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250


Chip 11, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

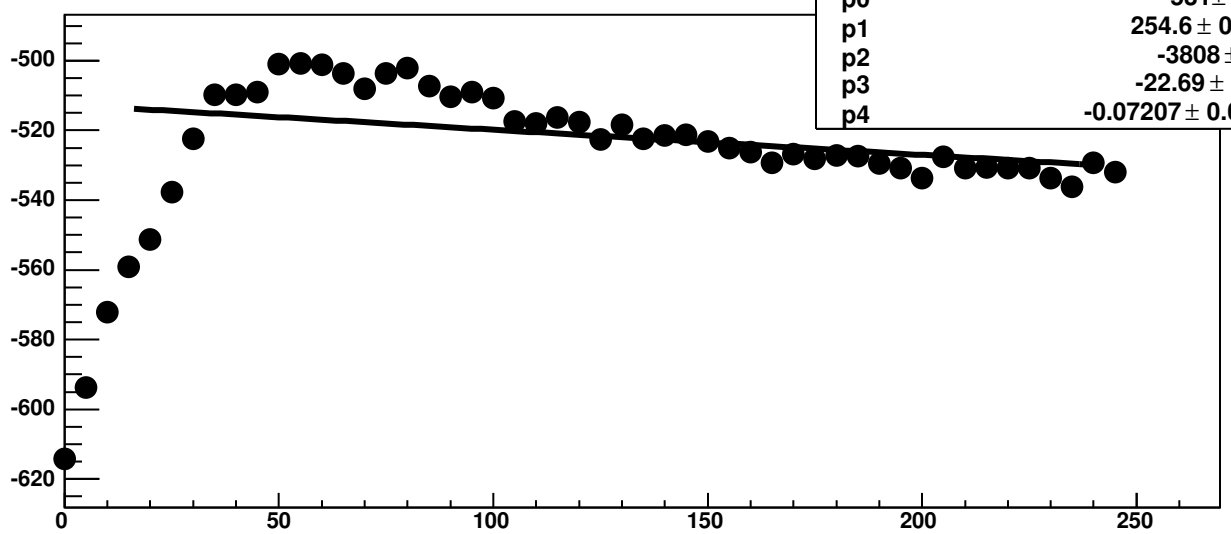

Chip 11, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
-4000
-5000
-6000
0
50
100
150
200
250


Chip 11, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 6.487e+05 / 41
p0 -441.2 ± 6.742
p1 69.96 ± 0.003957
p2 2614 ± 8.945
p3 -96.55 ± 4.147
p4 0.04127 ± 0.1213
3500
3000
2500
2000
1500
1000
500
0
-500
-1000
0
50
100
150
200
250

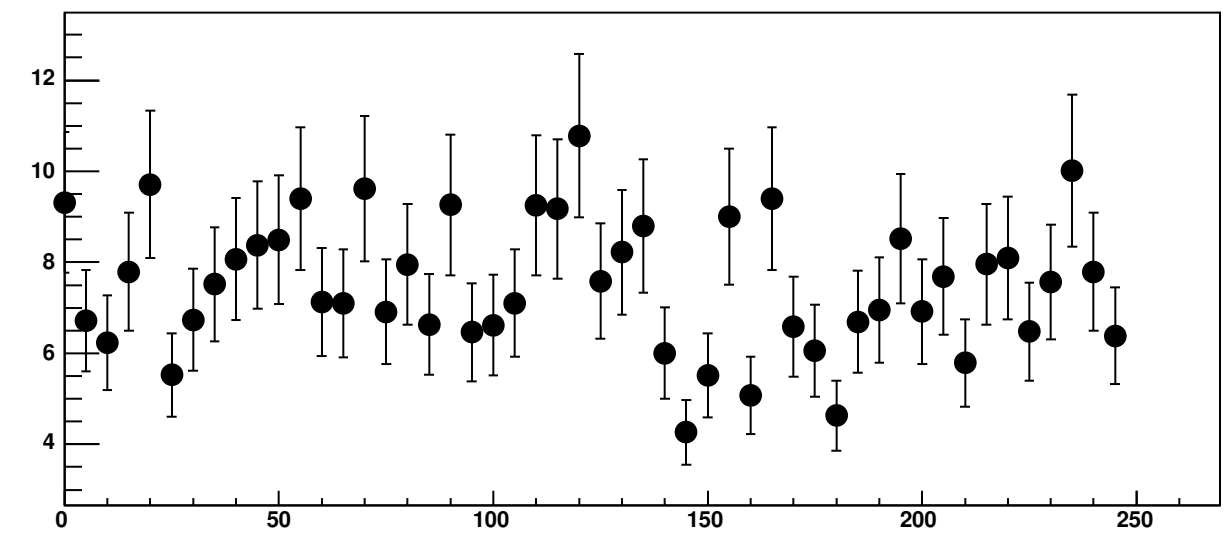

Chip 11, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250


Chip 11, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold
1500
1000
500
0
-500
-1000
-1500
0
50
100
150
200
250

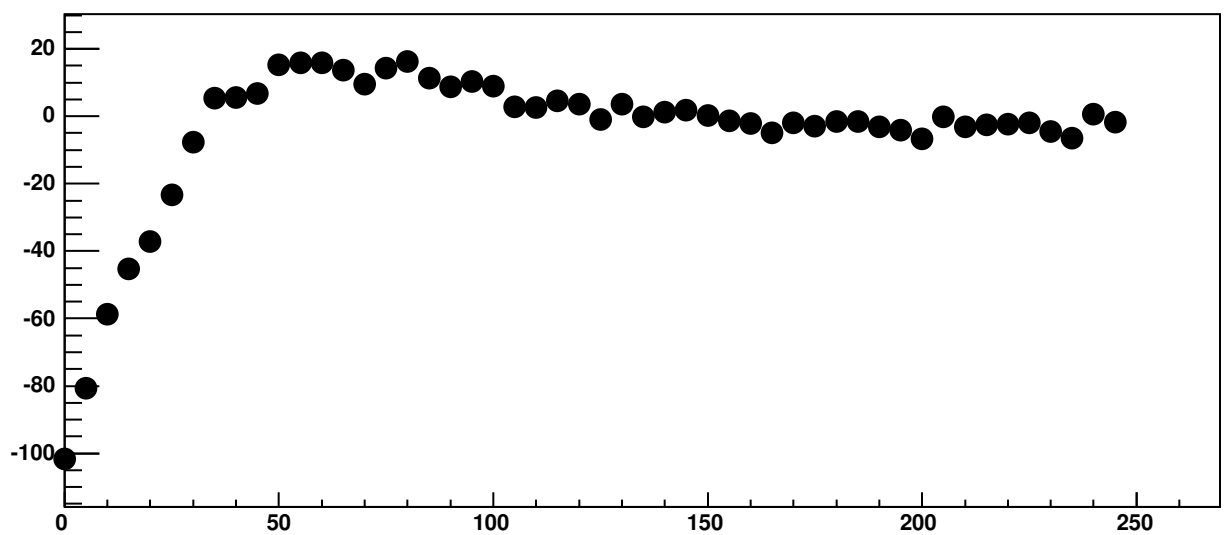
Chip 11, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold

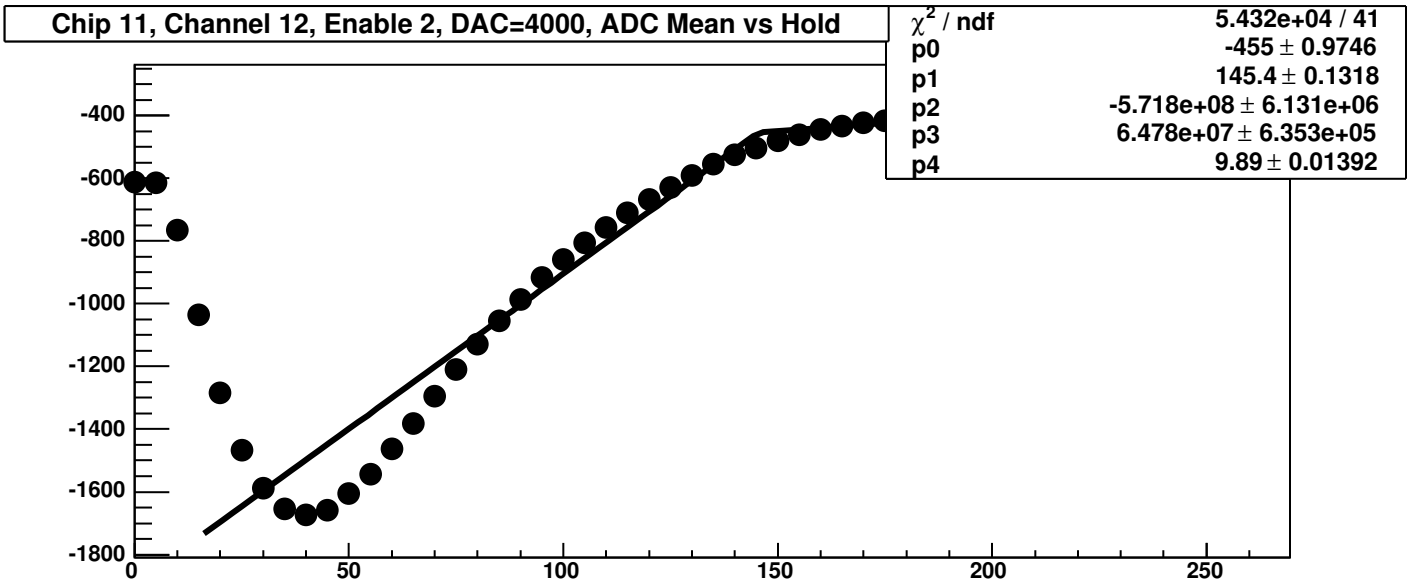


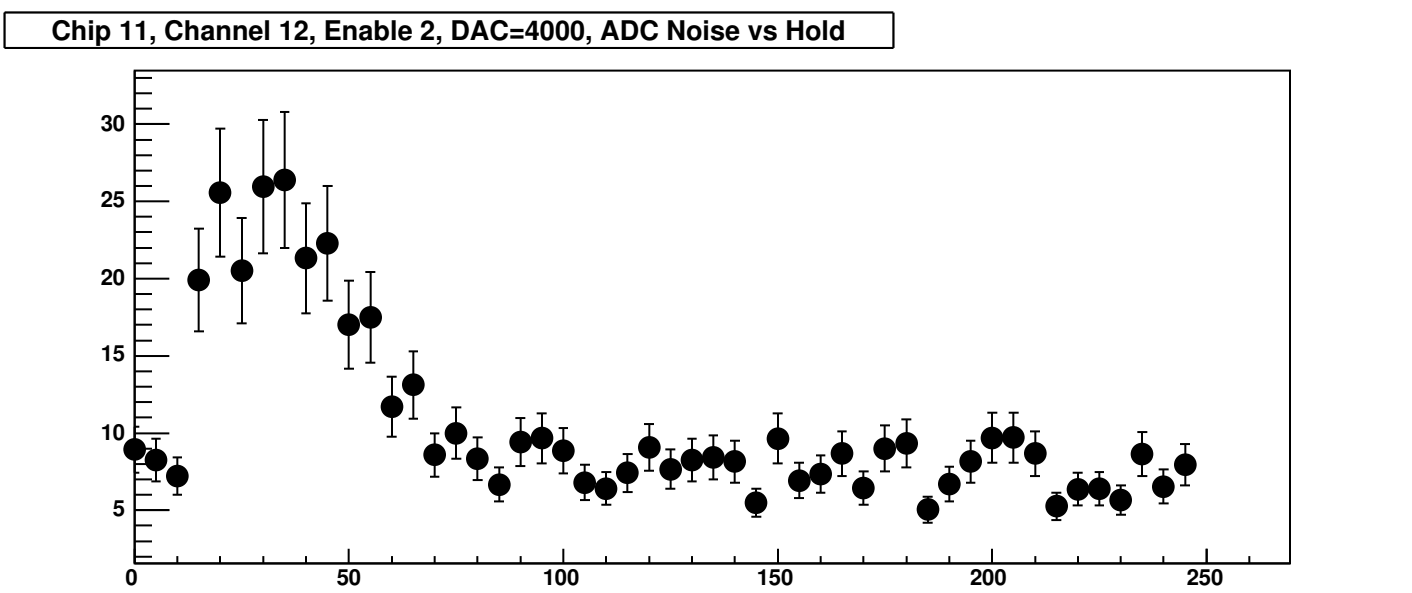
Chip 11, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold

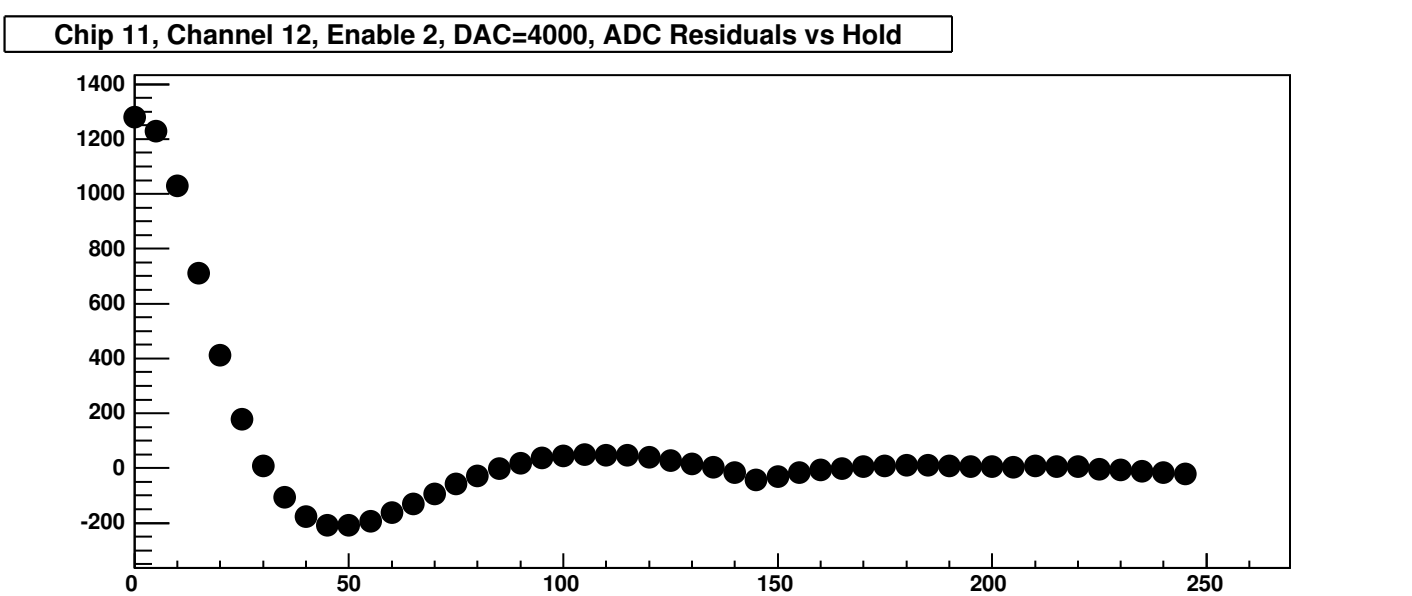


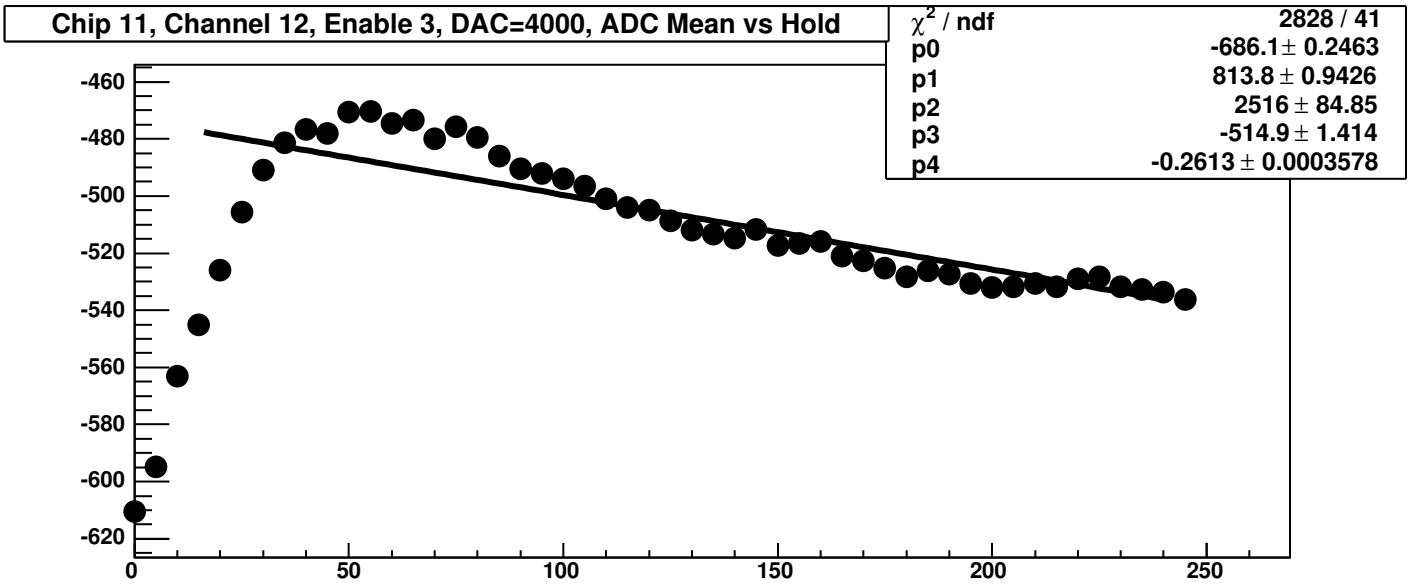
Chip 11, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold

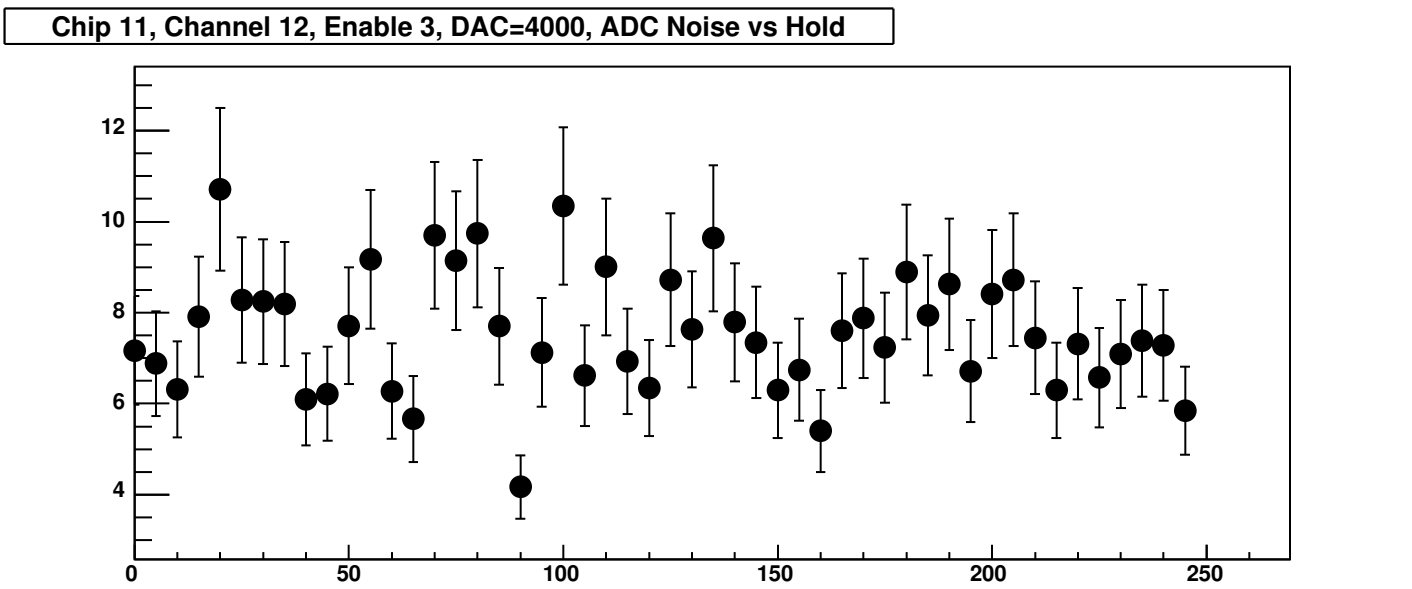


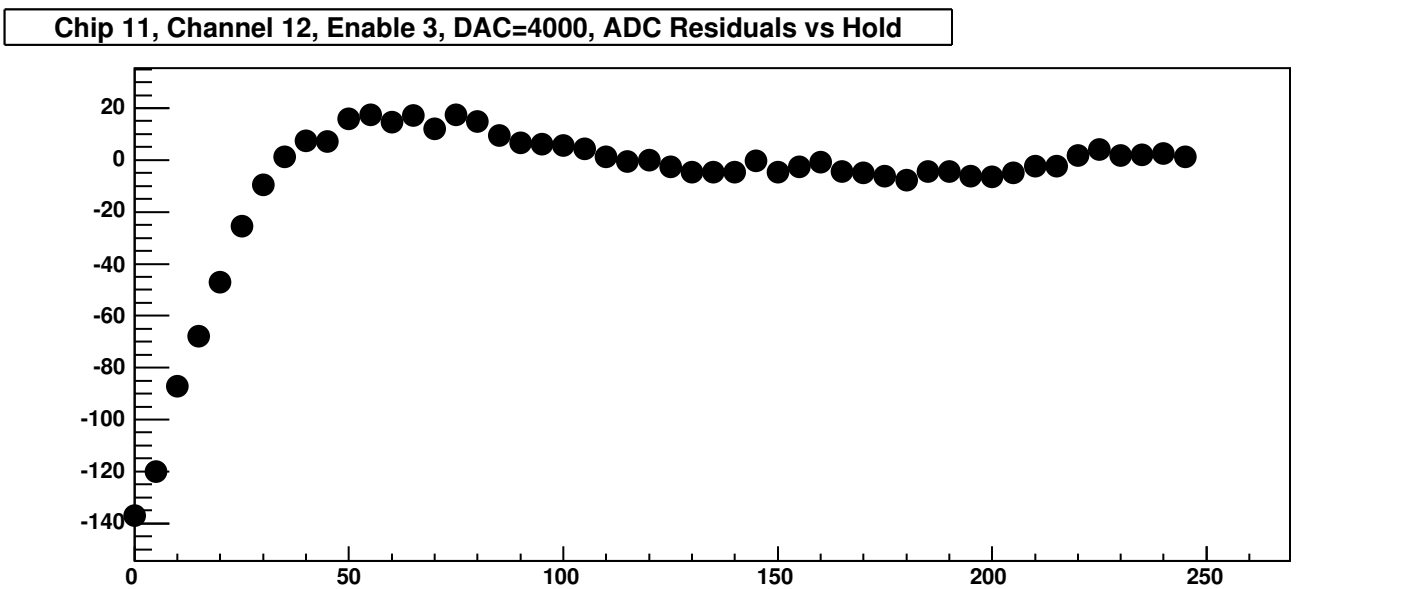

Chip 11, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.432e+04 / 41
p0 -455 ± 0.9746
p1 145.4 ± 0.1318
p2 -5.718e+08 ± 6.131e+06
p3 6.478e+07 ± 6.353e+05
p4 9.89 ± 0.01392
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
50
100
150
200
250

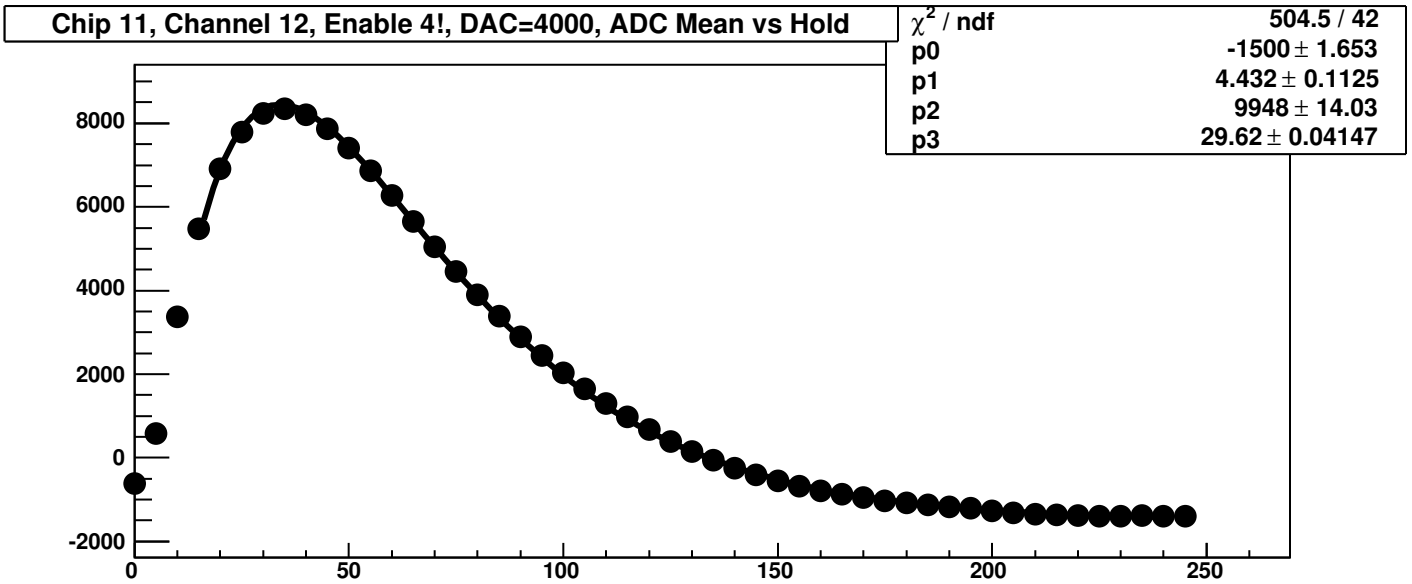

Chip 11, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold
30
25
20
15
10
5
0
50
100
150
200
250

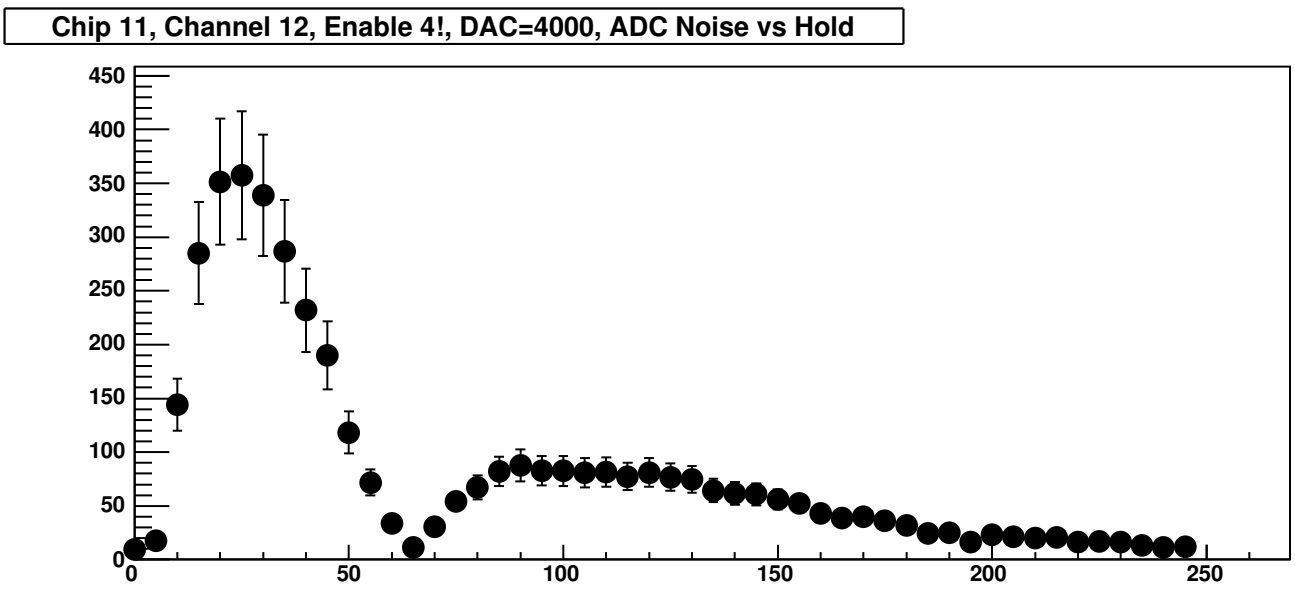

Chip 11, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

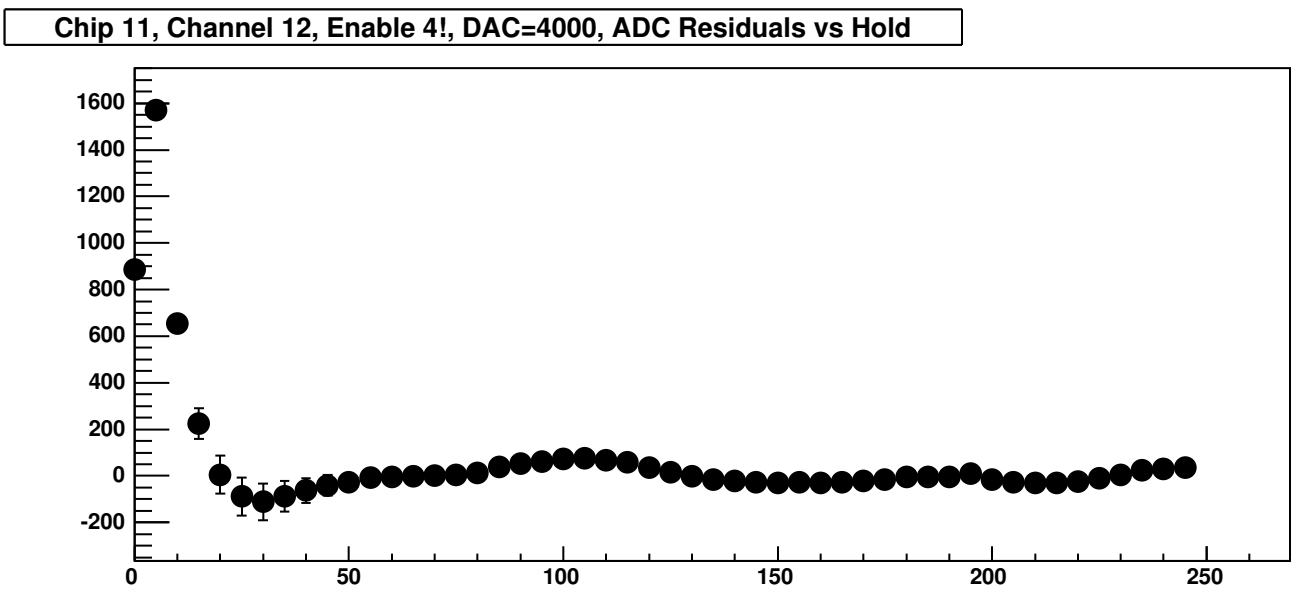

Chip 11, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 2828 / 41
p0 -686.1 ± 0.2463
p1 813.8 ± 0.9426
p2 2516 ± 84.85
p3 -514.9 ± 1.414
p4 -0.2613 ± 0.0003578
-460
-480
-500
-520
-540
-560
-580
-600
-620
0
50
100
150
200
250

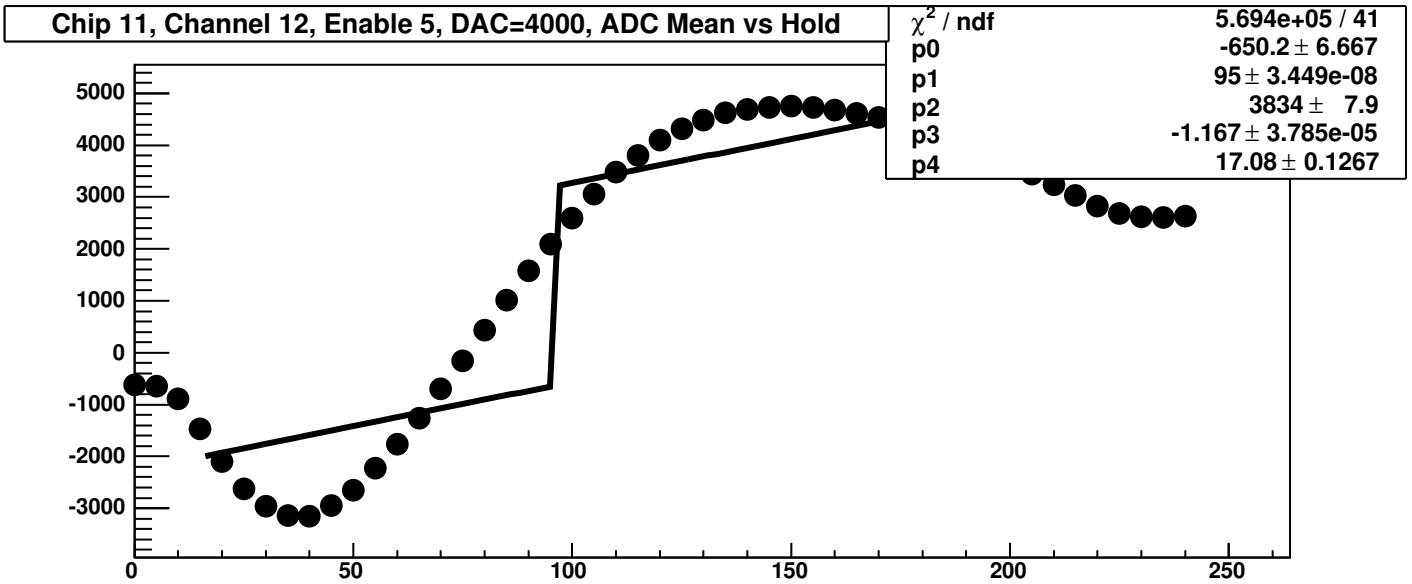

Chip 11, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

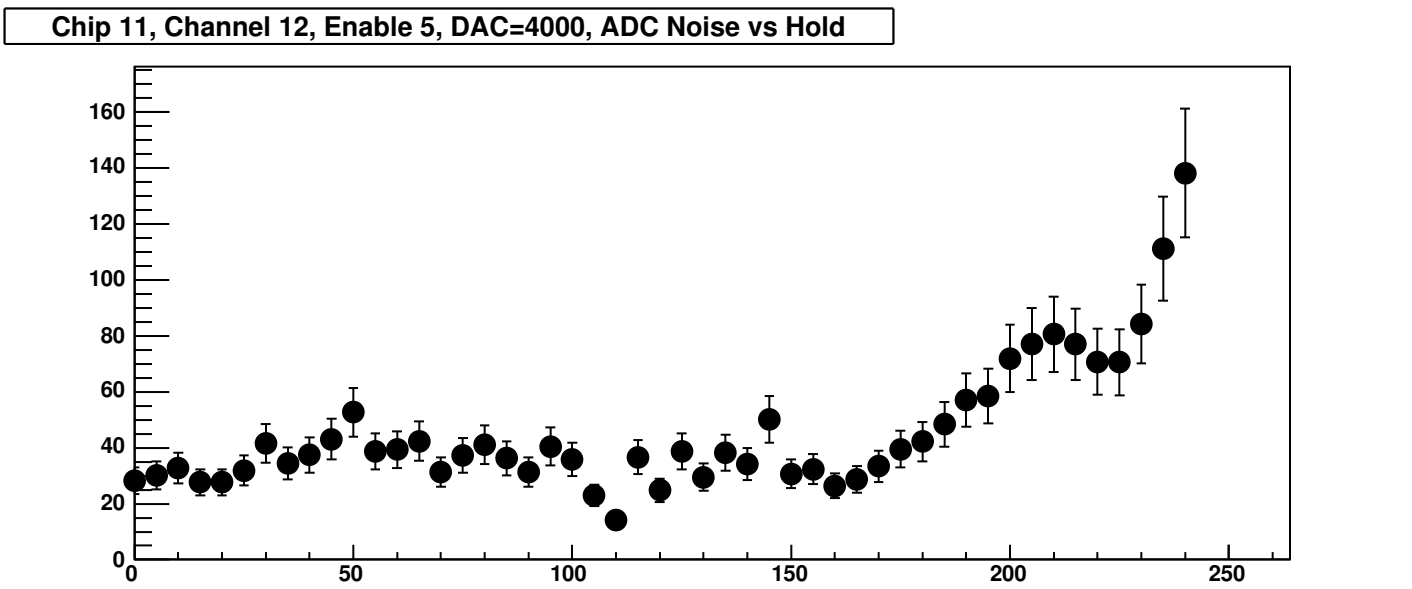

Chip 11, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
-80
-100
-120
-140
0
50
100
150
200
250

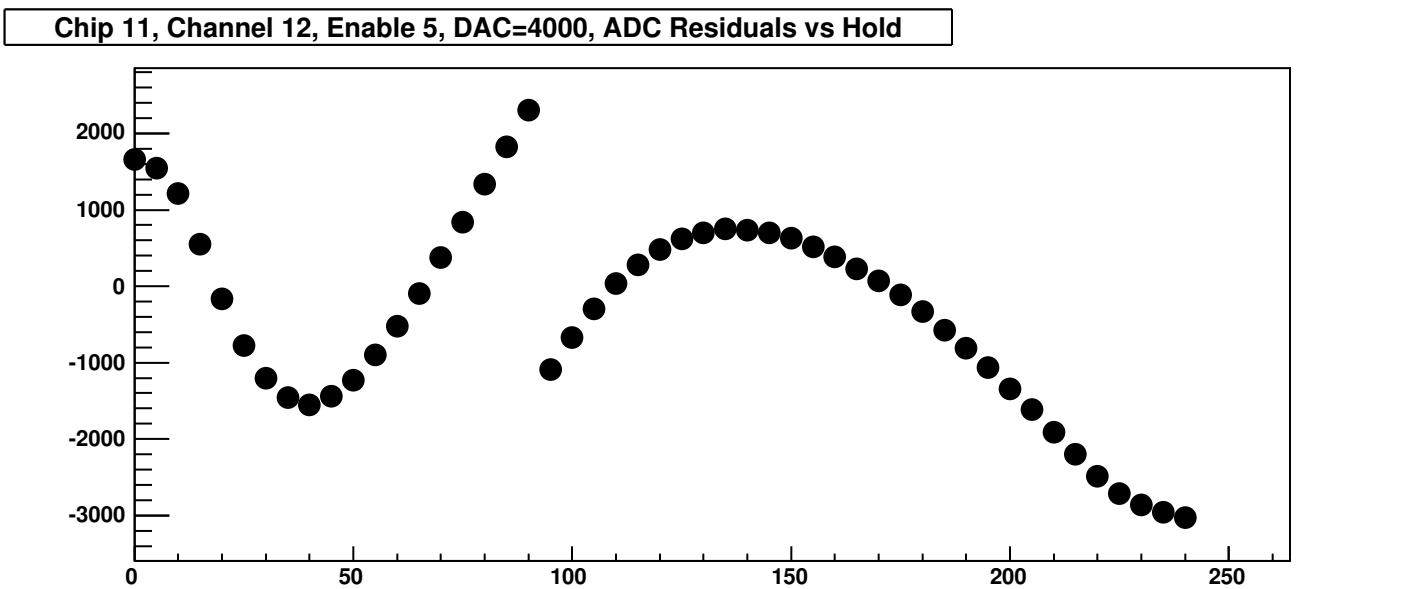

Chip 11, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 504.5 / 42
p0 -1500 ± 1.653
p1 4.432 ± 0.1125
p2 9948 ± 14.03
p3 29.62 ± 0.04147
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

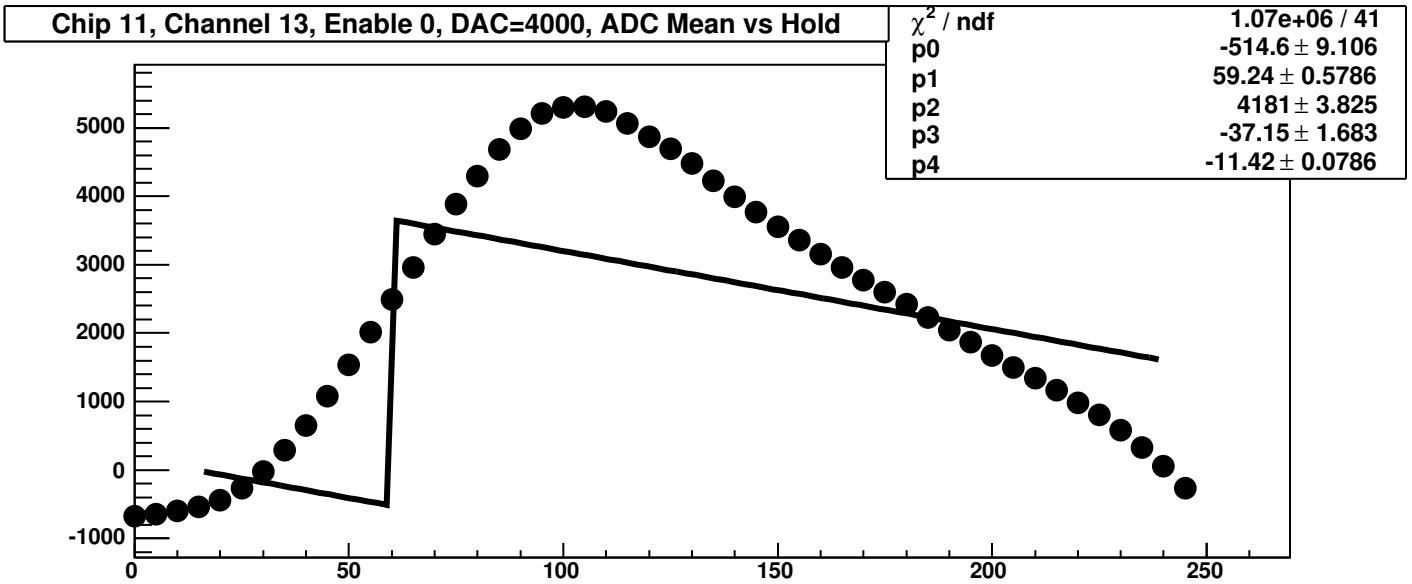

Chip 11, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold
450
400
350
300
250
200
150
100
50
0
0
50
100
150
200
250

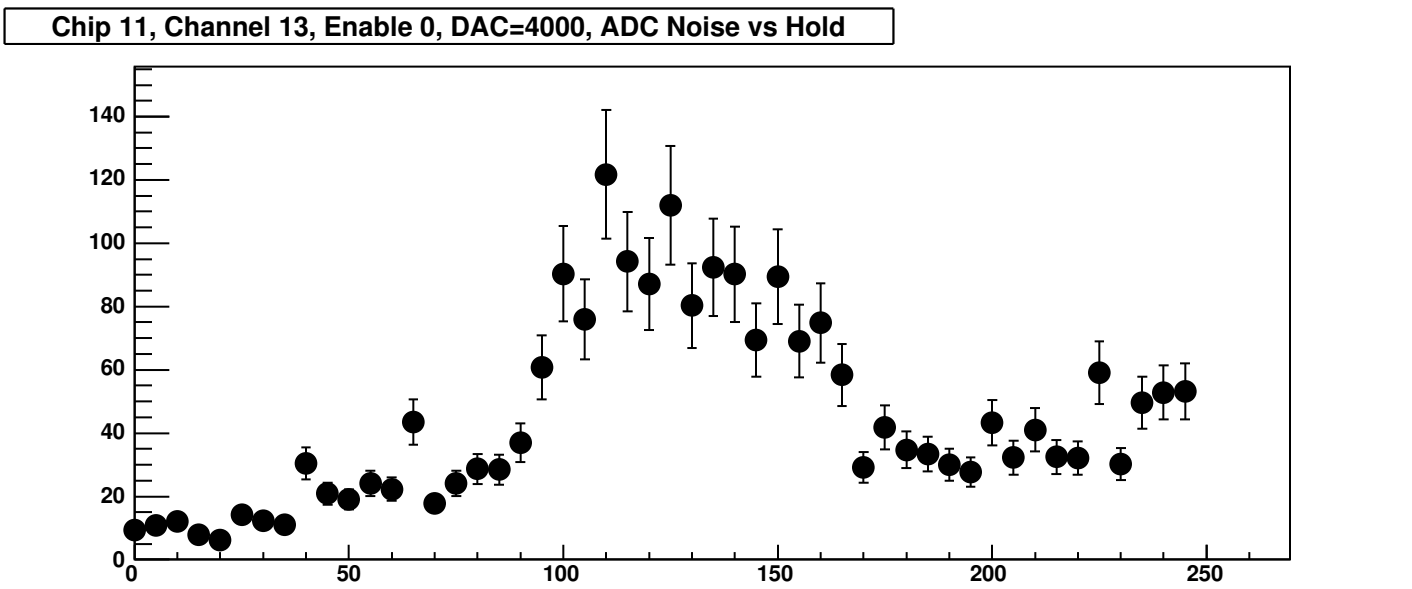

Chip 11, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

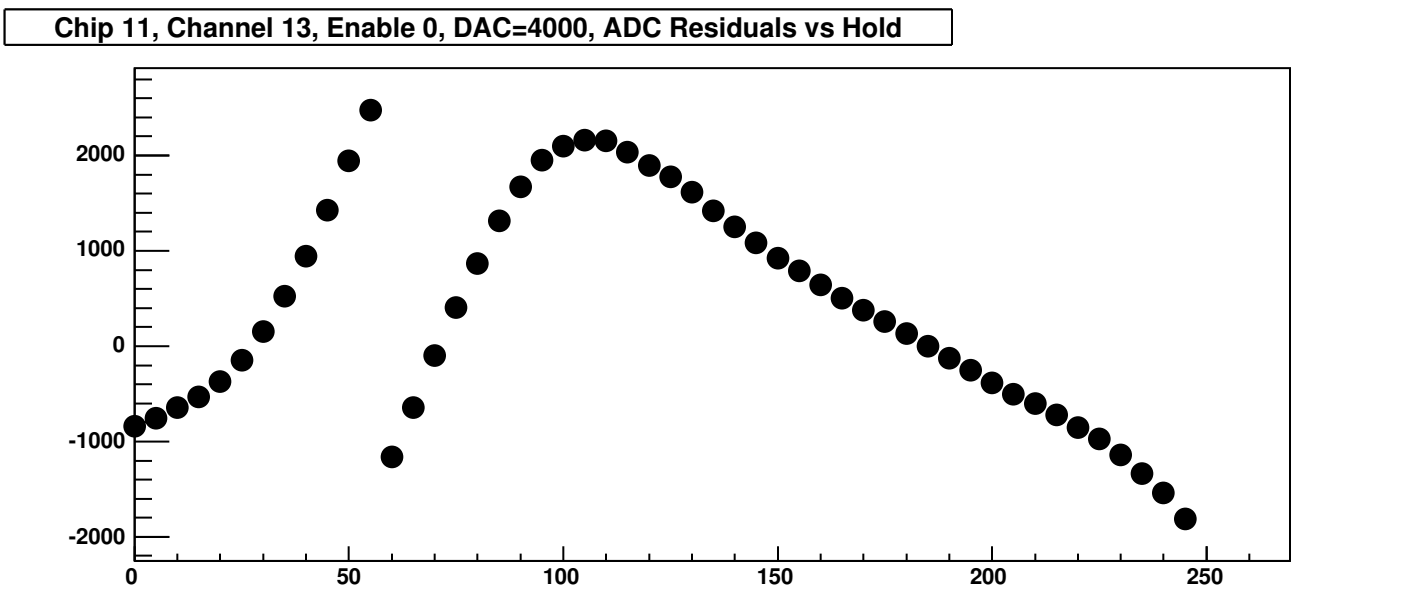

Chip 11, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.694e+05 / 41
p0 -650.2 ± 6.667
p1 95 ± 3.449e-08
p2 3834 ± 7.9
p3 -1.167 ± 3.785e-05
p4 17.08 ± 0.1267
5000
4000
3000
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

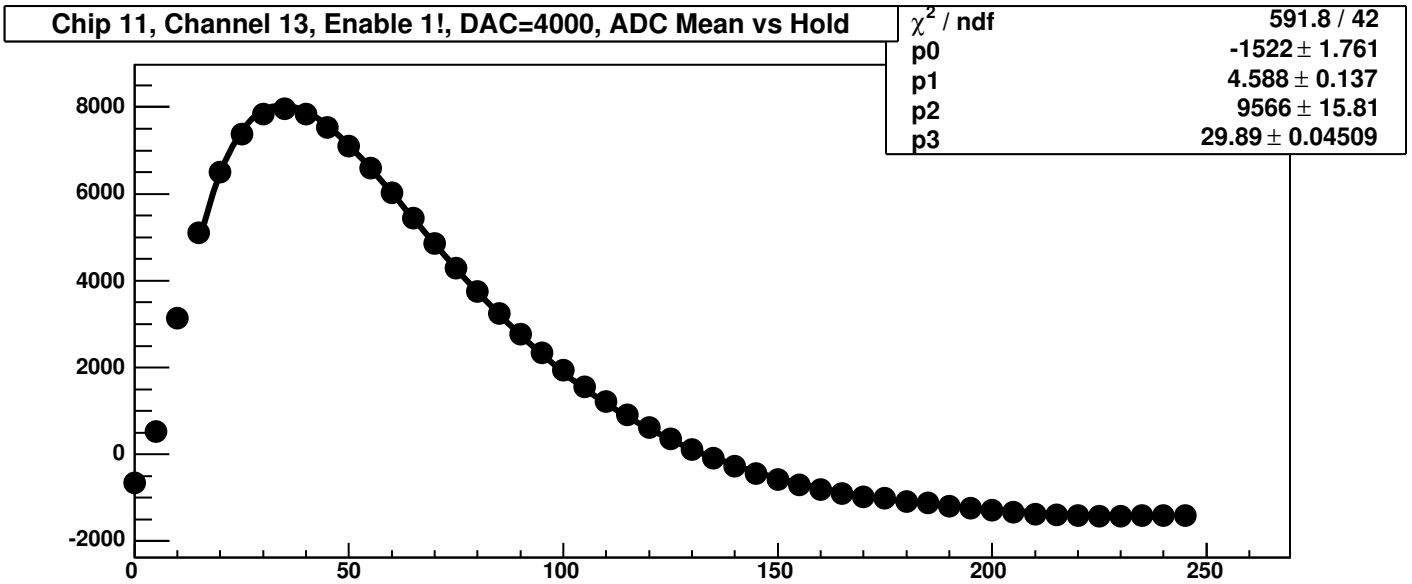

Chip 11, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

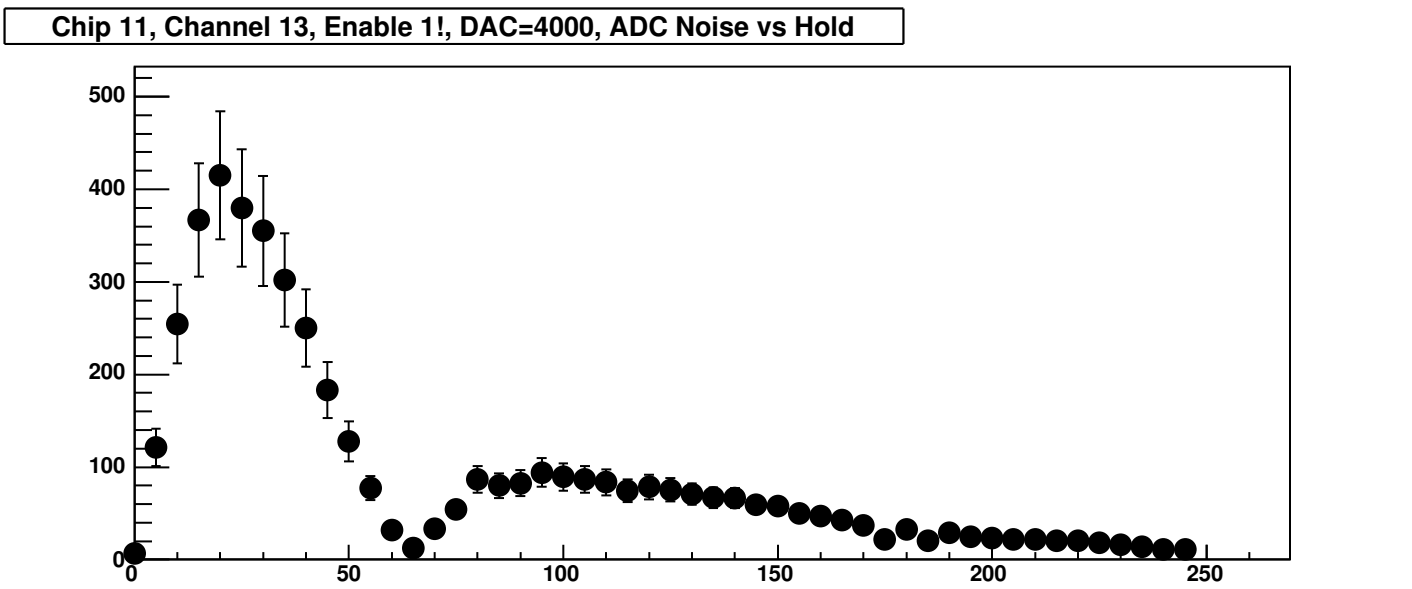

Chip 11, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
-3000
0
50
100
150
200
250

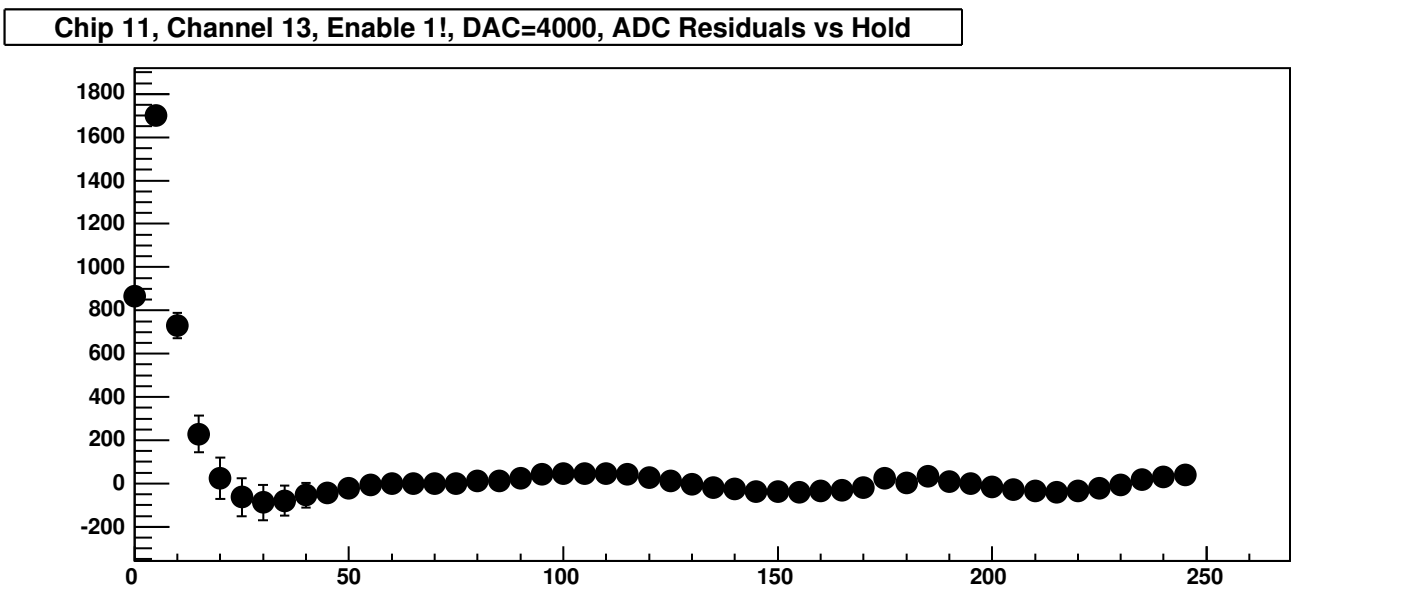

Chip 11, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.07e+06 / 41
p0 -514.6 ± 9.106
p1 59.24 ± 0.5786
p2 4181 ± 3.825
p3 -37.15 ± 1.683
p4 -11.42 ± 0.0786
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

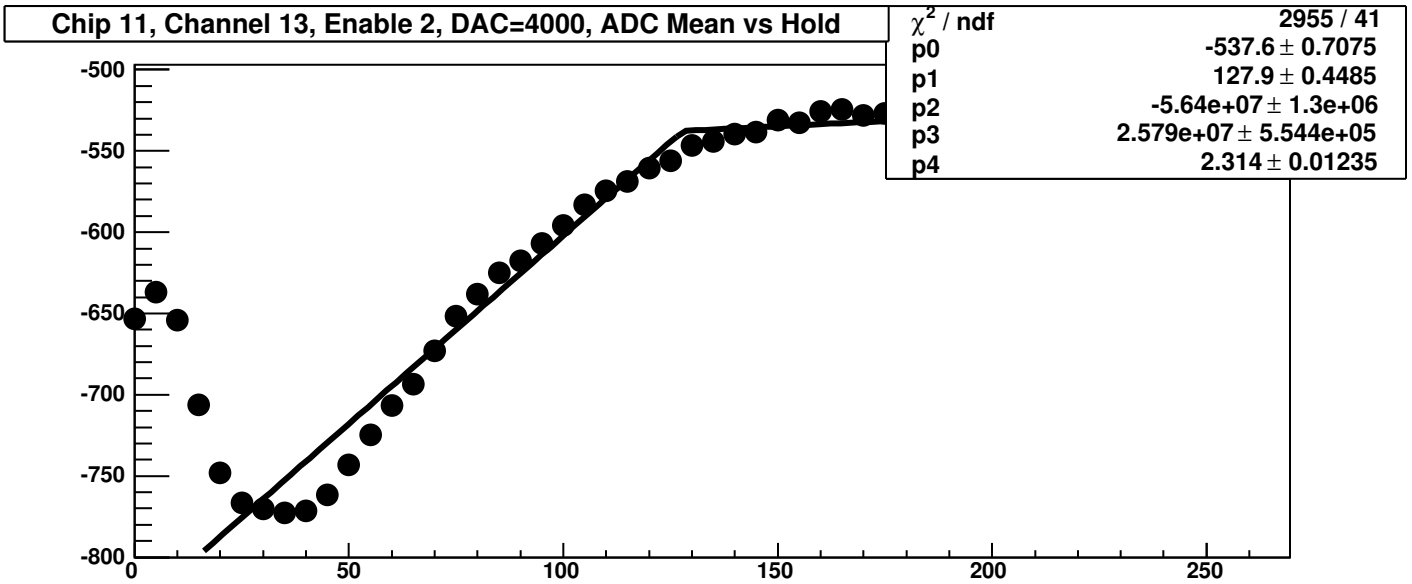

Chip 11, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250

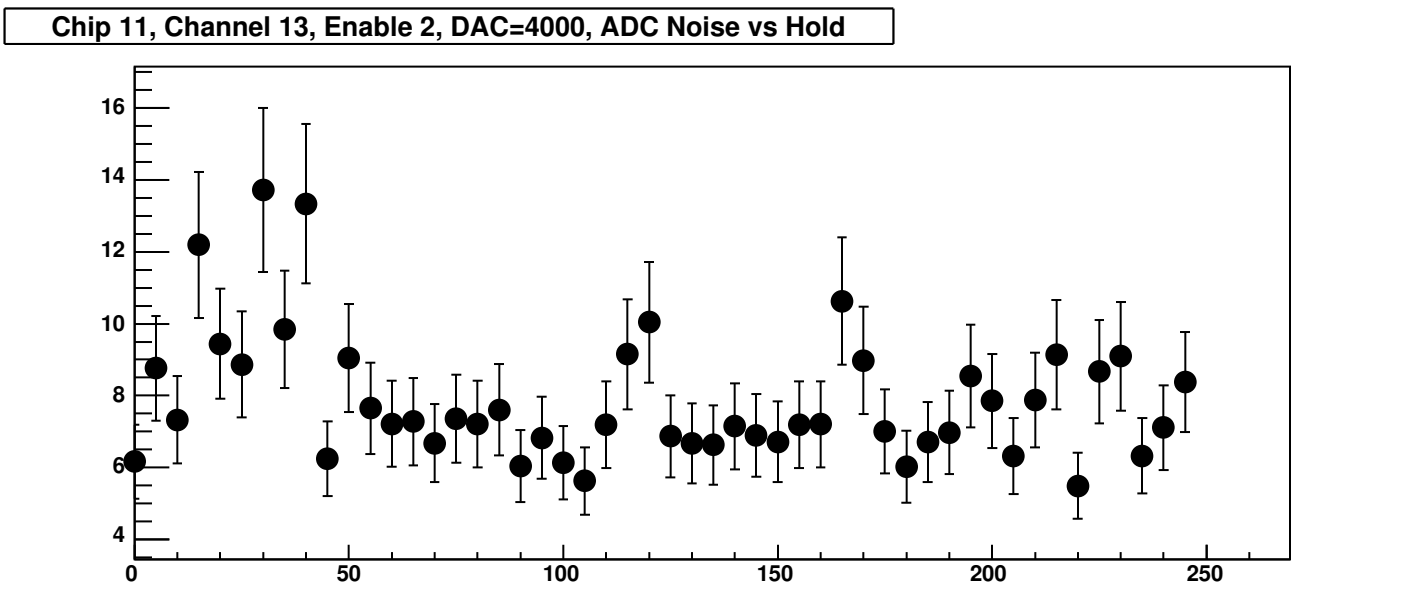

Chip 11, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
0
50
100
150
200
250

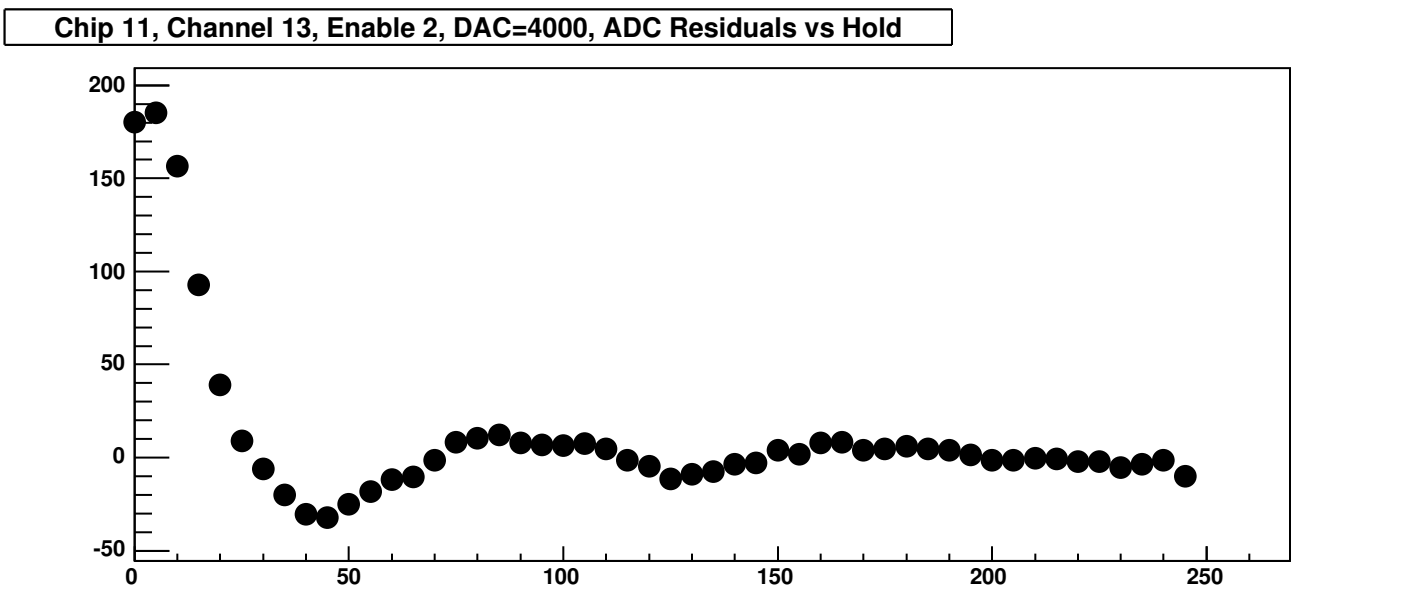

Chip 11, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 591.8 / 42
p0 -1522 ± 1.761
p1 4.588 ± 0.137
p2 9566 ± 15.81
p3 29.89 ± 0.04509
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

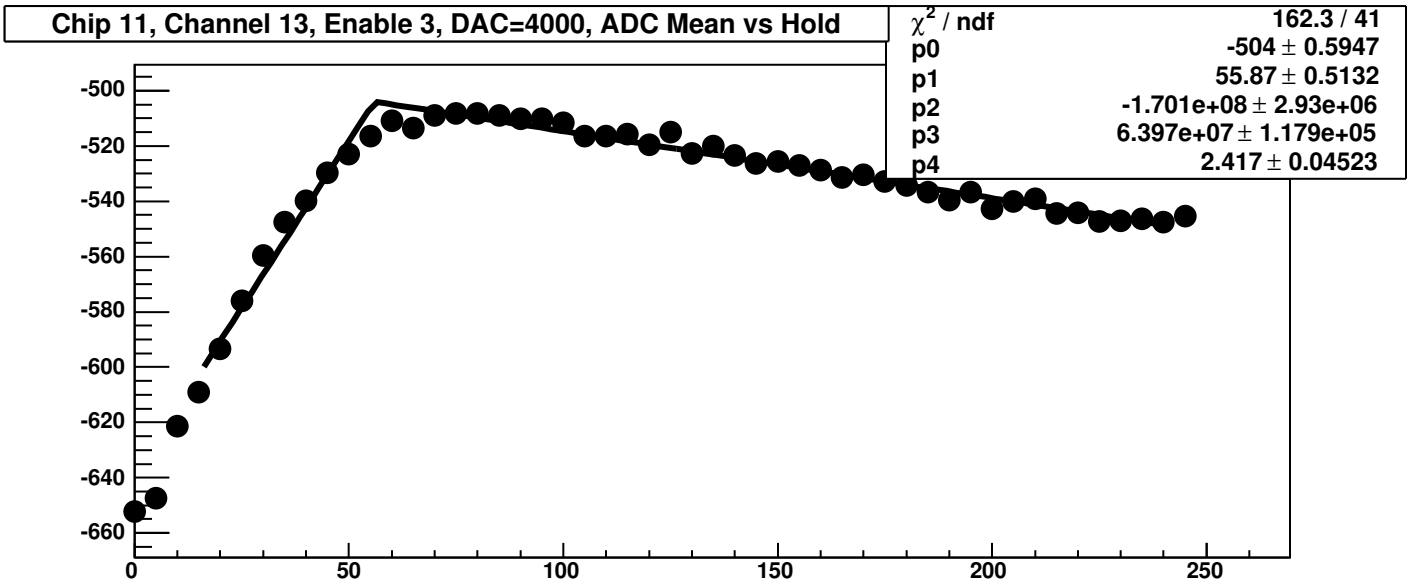

Chip 11, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold
500
400
300
200
100
0
0
50
100
150
200
250

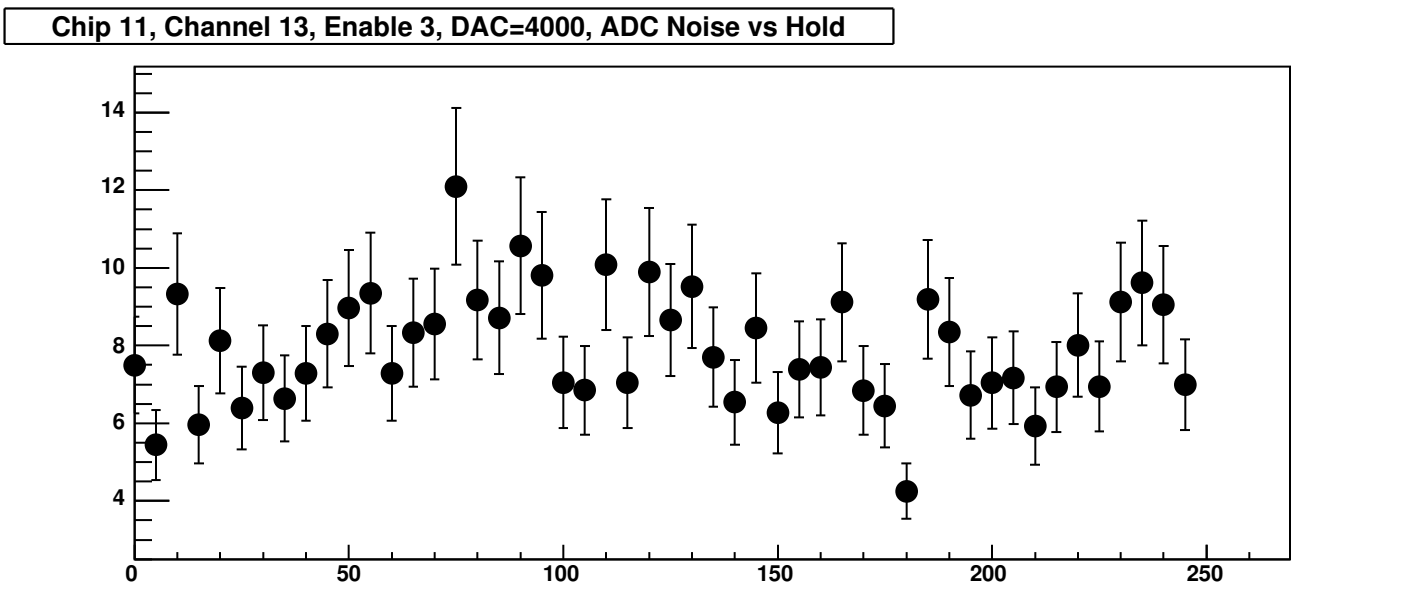

Chip 11, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

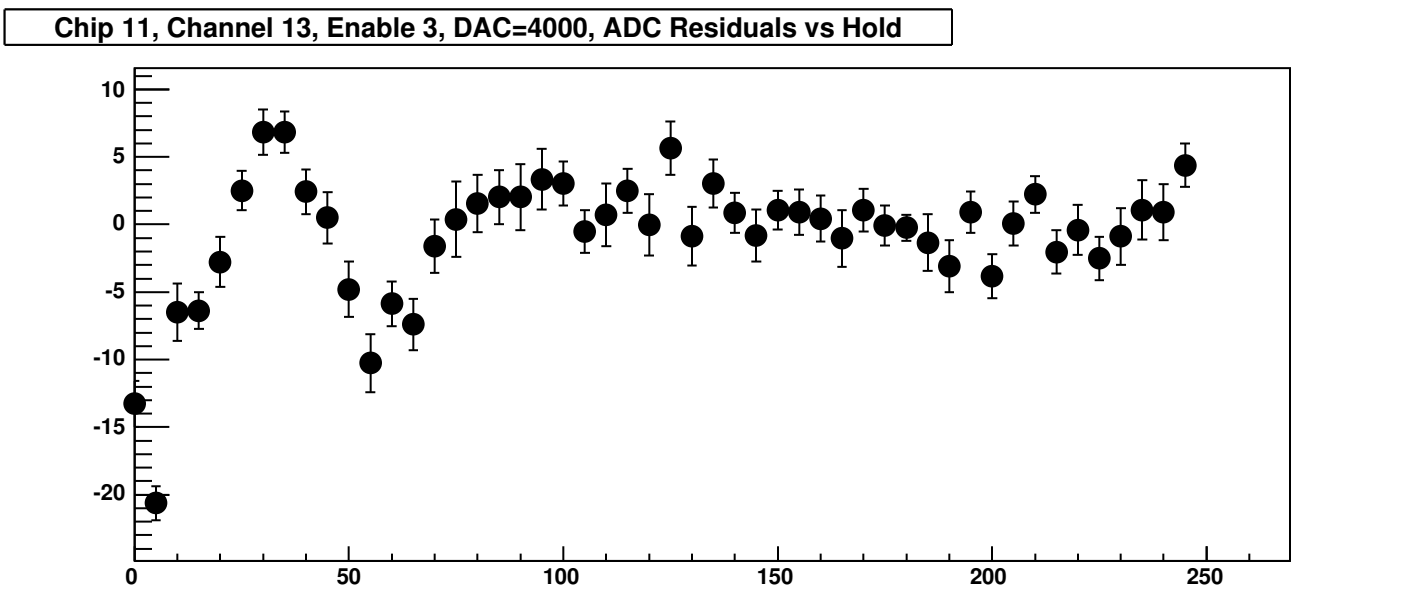

Chip 11, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 2955 / 41
p0 -537.6 ± 0.7075
p1 127.9 ± 0.4485
p2 -5.64e+07 ± 1.3e+06
p3 2.579e+07 ± 5.544e+05
p4 2.314 ± 0.01235
-500
-550
-600
-650
-700
-750
-800
0
50
100
150
200
250

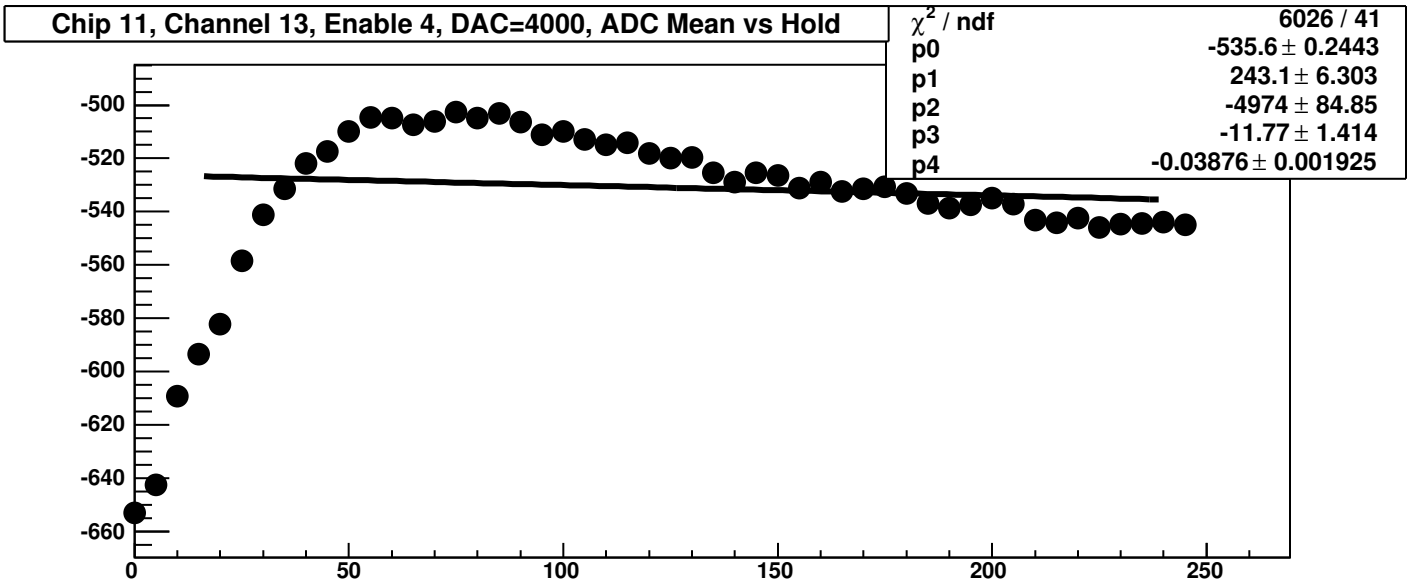

Chip 11, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold
16
14
12
10
8
6
4
0
50
100
150
200
250

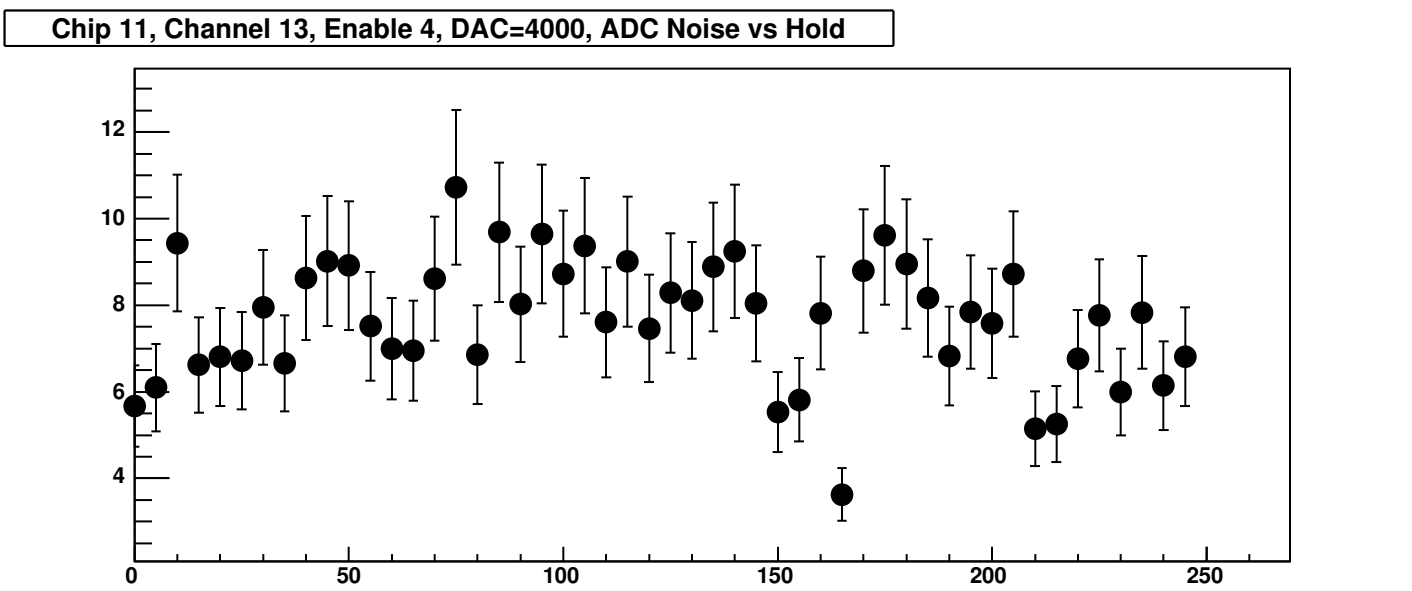

Chip 11, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold
200
150
100
50
0
-50
0
50
100
150
200
250

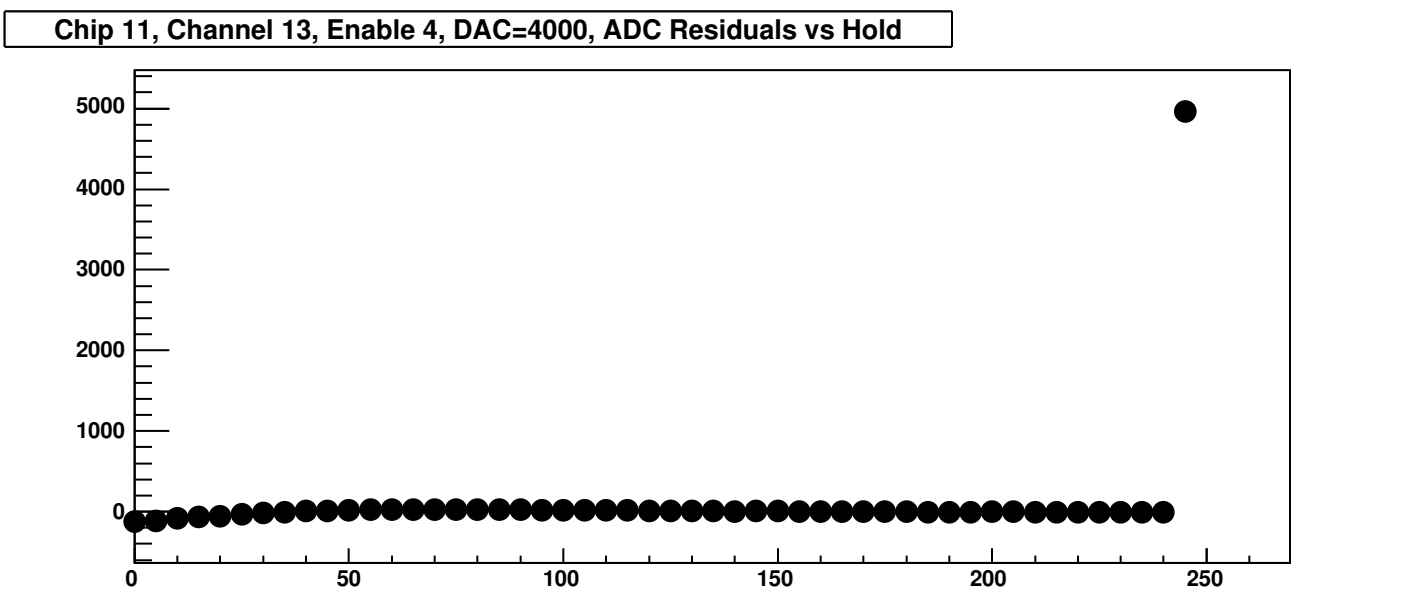

Chip 11, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 162.3 / 41
p0 -504 ± 0.5947
p1 55.87 ± 0.5132
p2 -1.701e+08 ± 2.93e+06
p3 6.397e+07 ± 1.179e+05
p4 2.417 ± 0.04523
-500
-520
-540
-560
-580
-600
-620
-640
-660
0
50
100
150
200
250

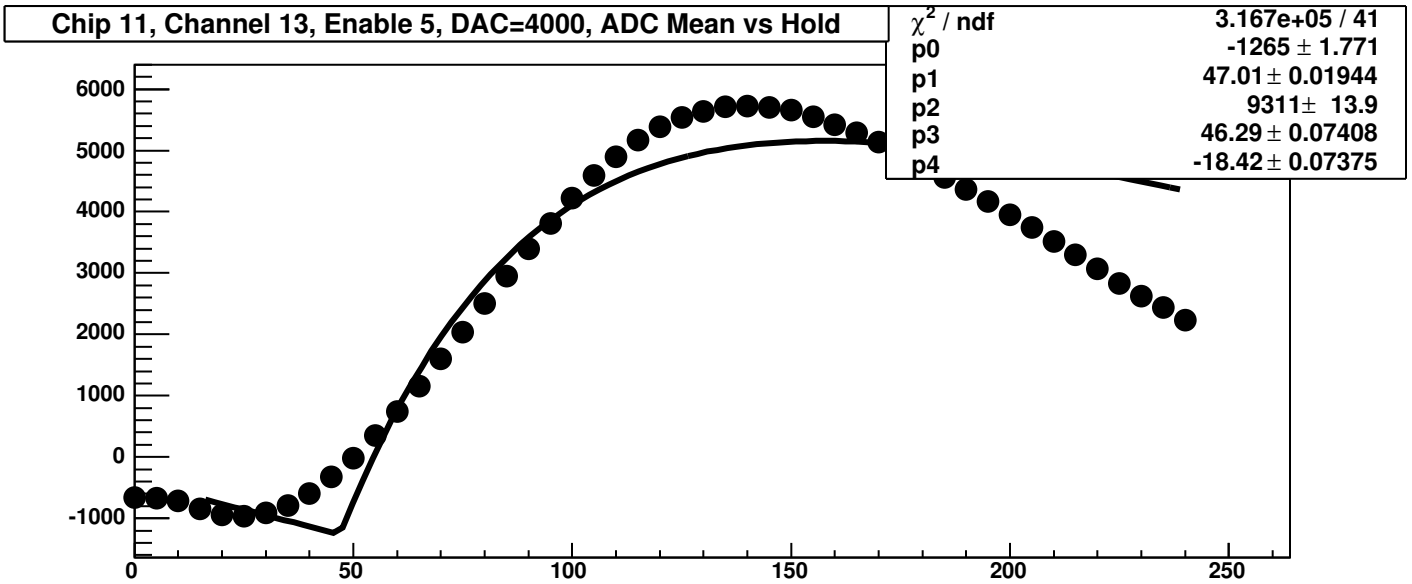

Chip 11, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

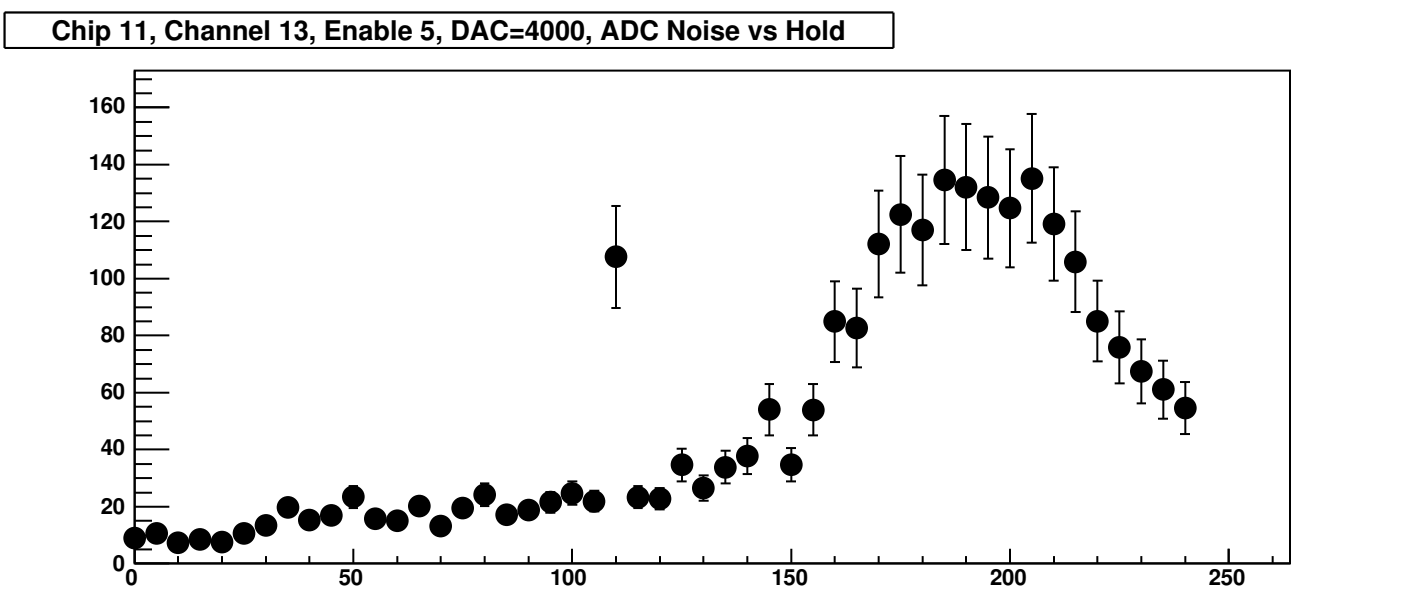

Chip 11, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
-20
0
50
100
150
200
250

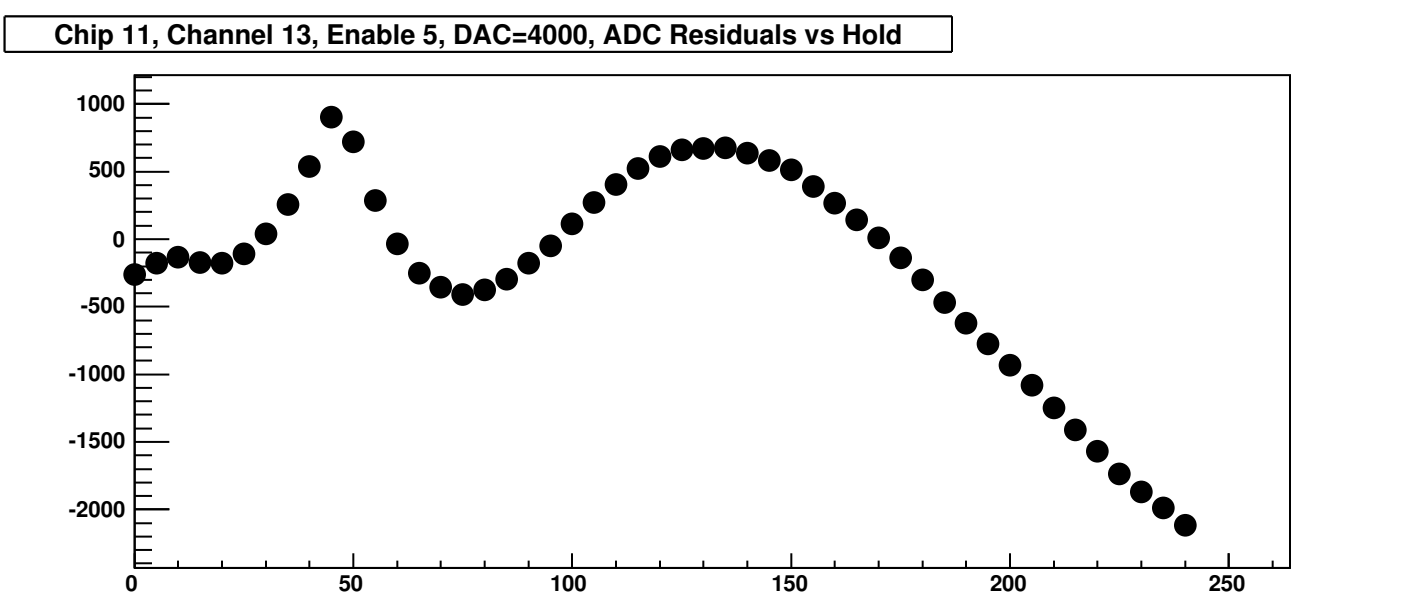

Chip 11, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 6026 / 41
p0 -535.6 ± 0.2443
p1 243.1 ± 6.303
p2 -4974 ± 84.85
p3 -11.77 ± 1.414
p4 -0.03876 ± 0.001925
-500
-520
-540
-560
-580
-600
-620
-640
-660
0
50
100
150
200
250

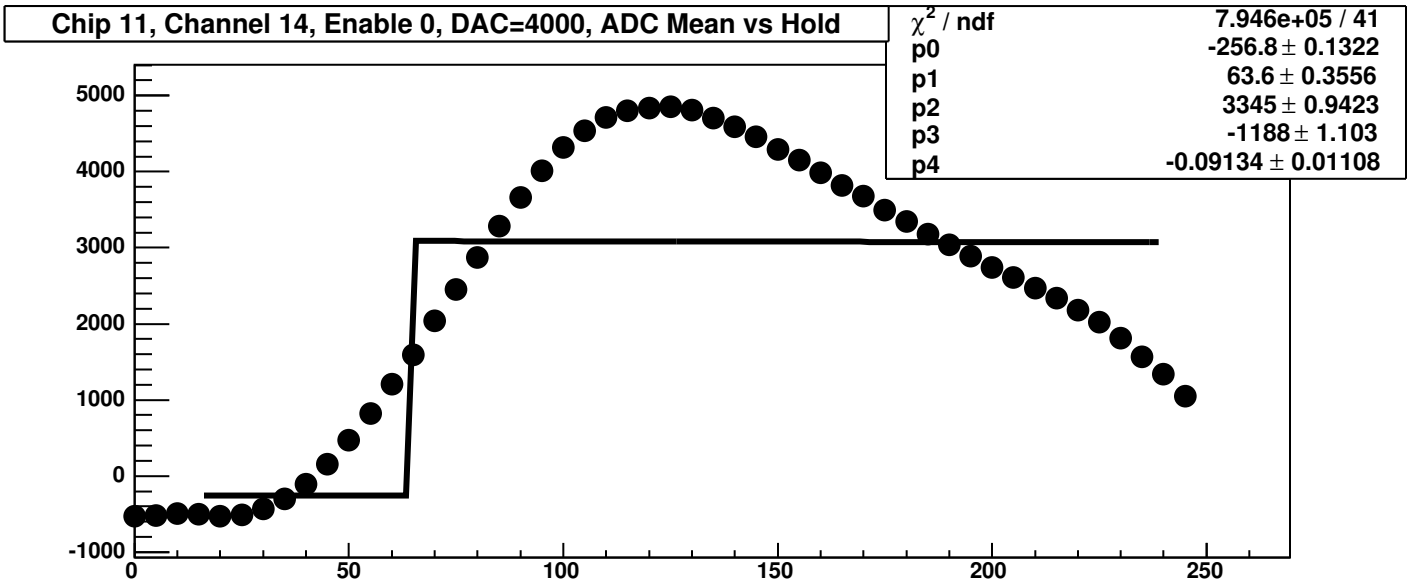

Chip 11, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

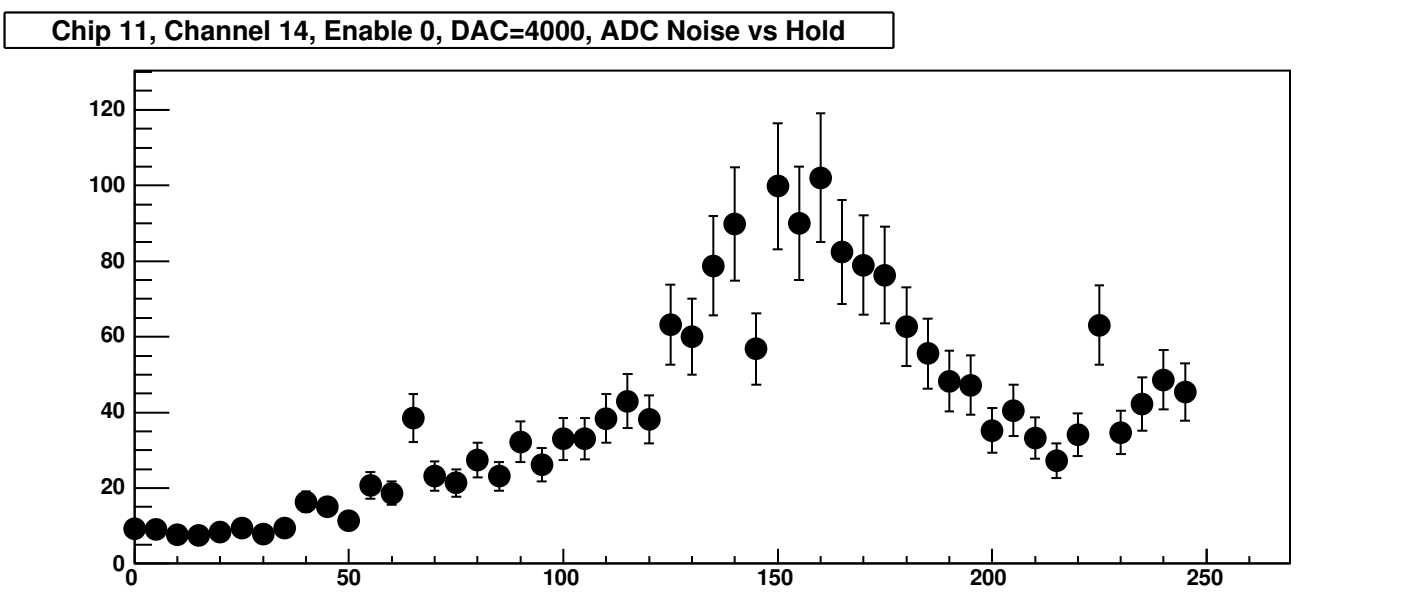

Chip 11, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold
5000
4000
3000
2000
1000
0
0
50
100
150
200
250

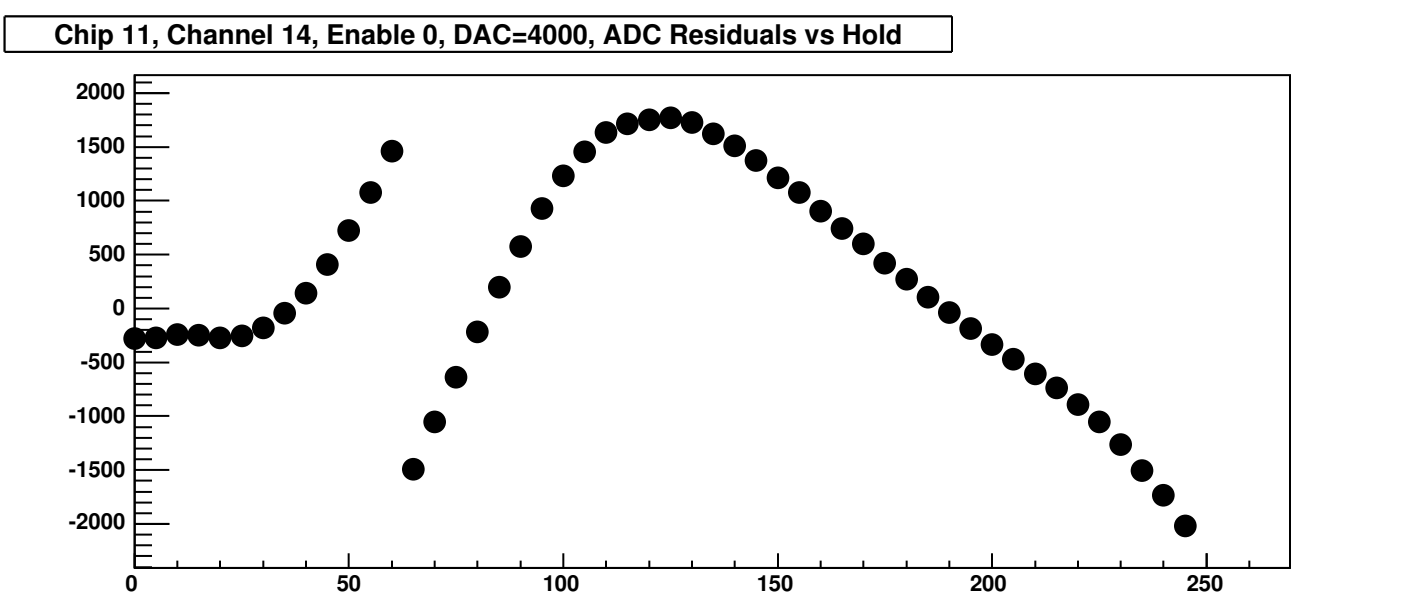

Chip 11, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.167e+05 / 41
p0 -1265 ± 1.771
p1 47.01 ± 0.01944
p2 9311 ± 13.9
p3 46.29 ± 0.07408
p4 -18.42 ± 0.07375
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

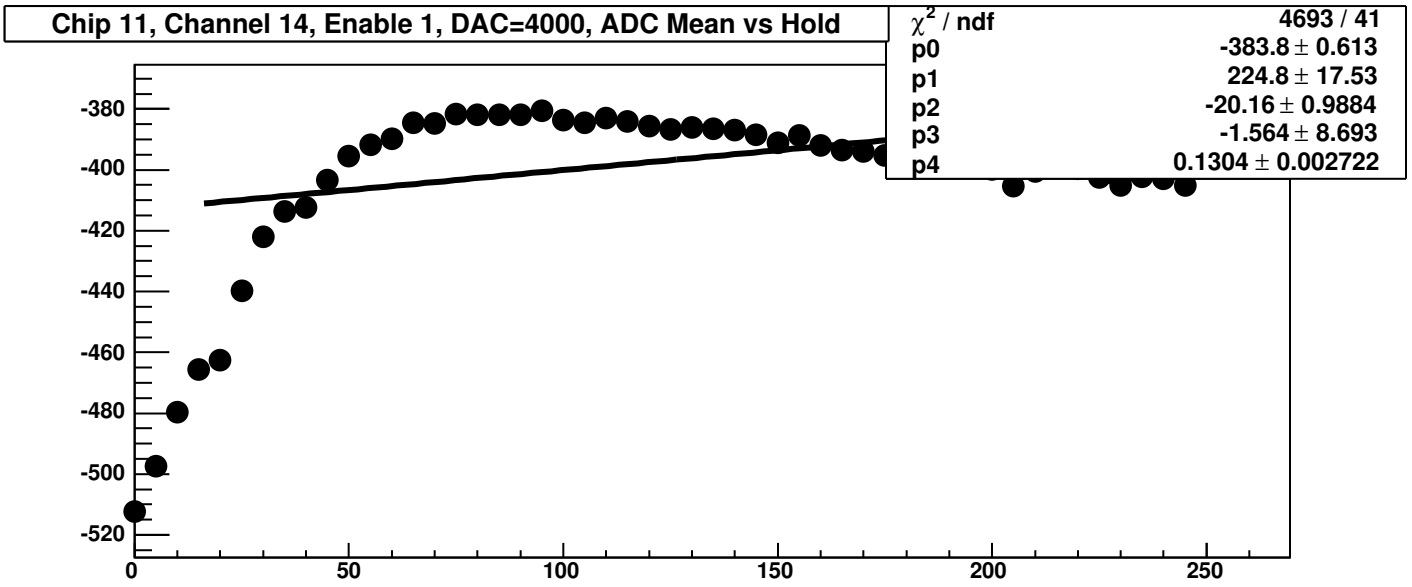

Chip 11, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

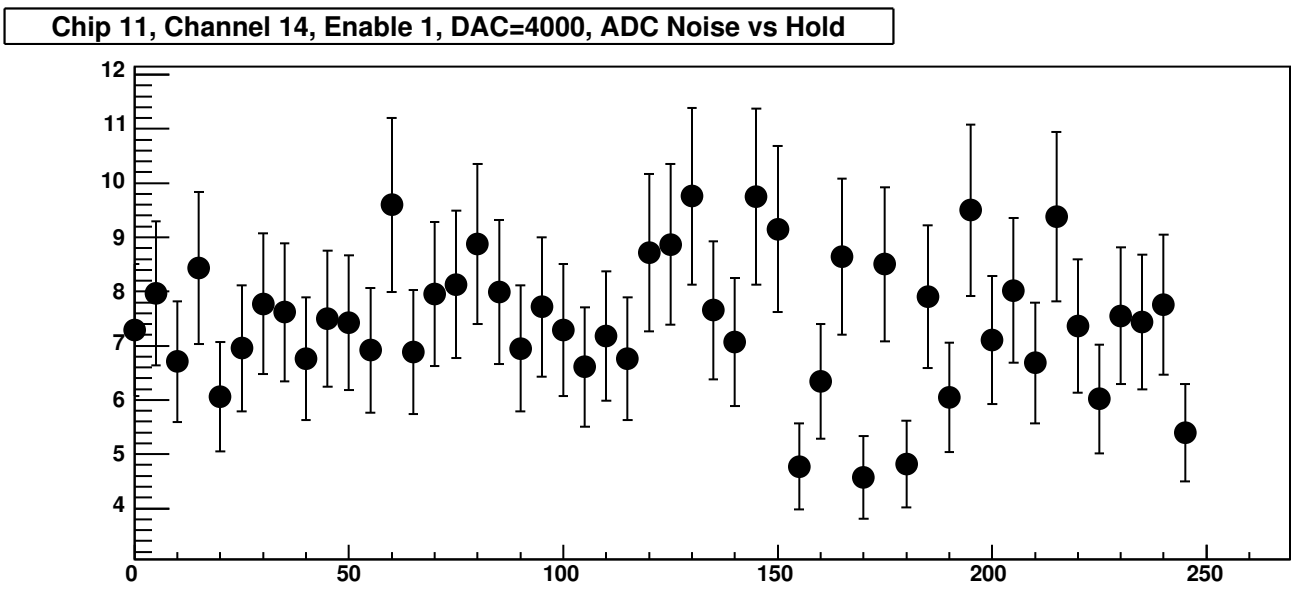

Chip 11, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

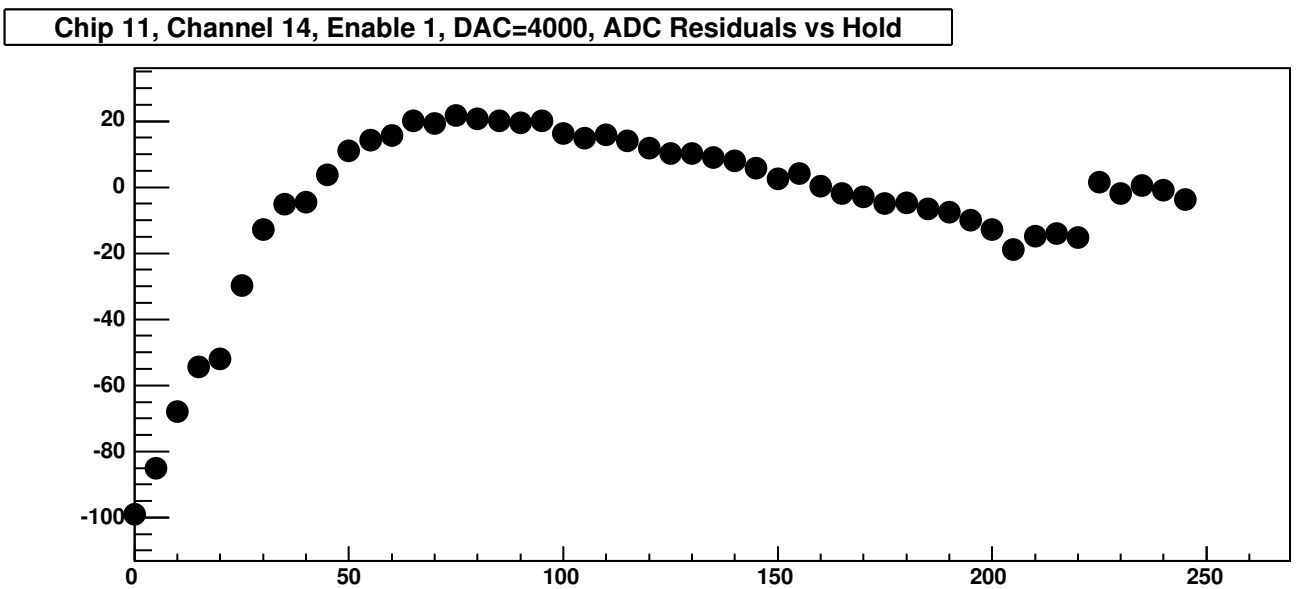

Chip 11, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 7.946e+05 / 41
p0 -256.8 ± 0.1322
p1 63.6 ± 0.3556
p2 3345 ± 0.9423
p3 -1188 ± 1.103
p4 -0.09134 ± 0.01108
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

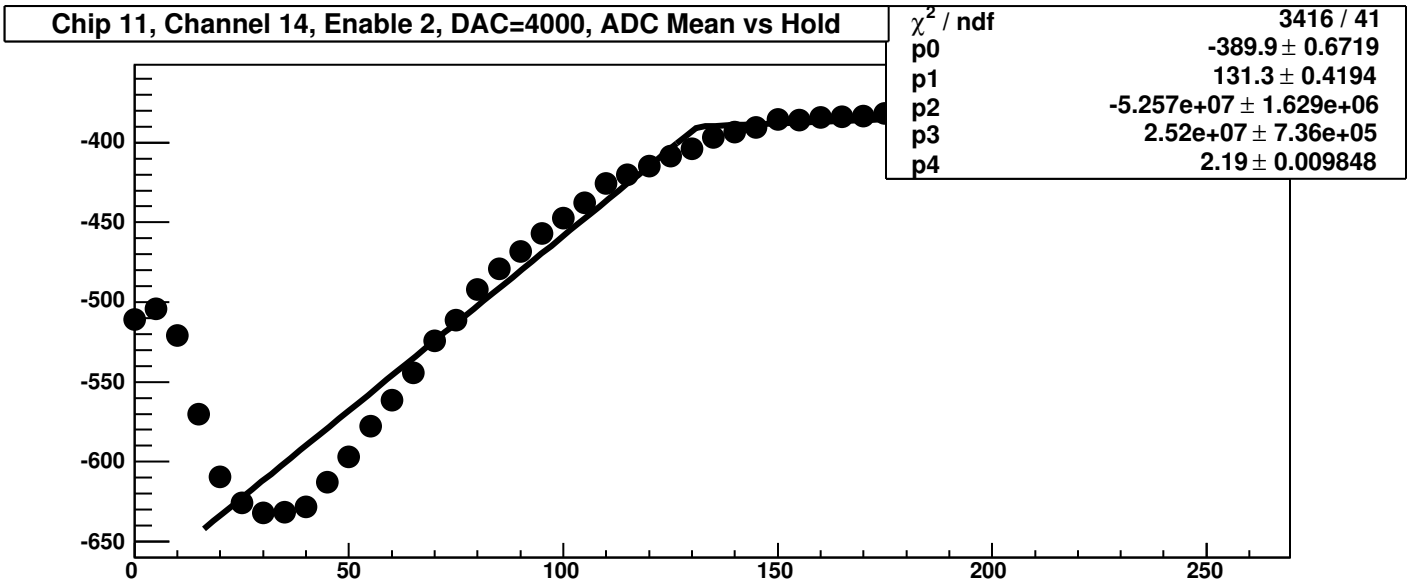

Chip 11, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250

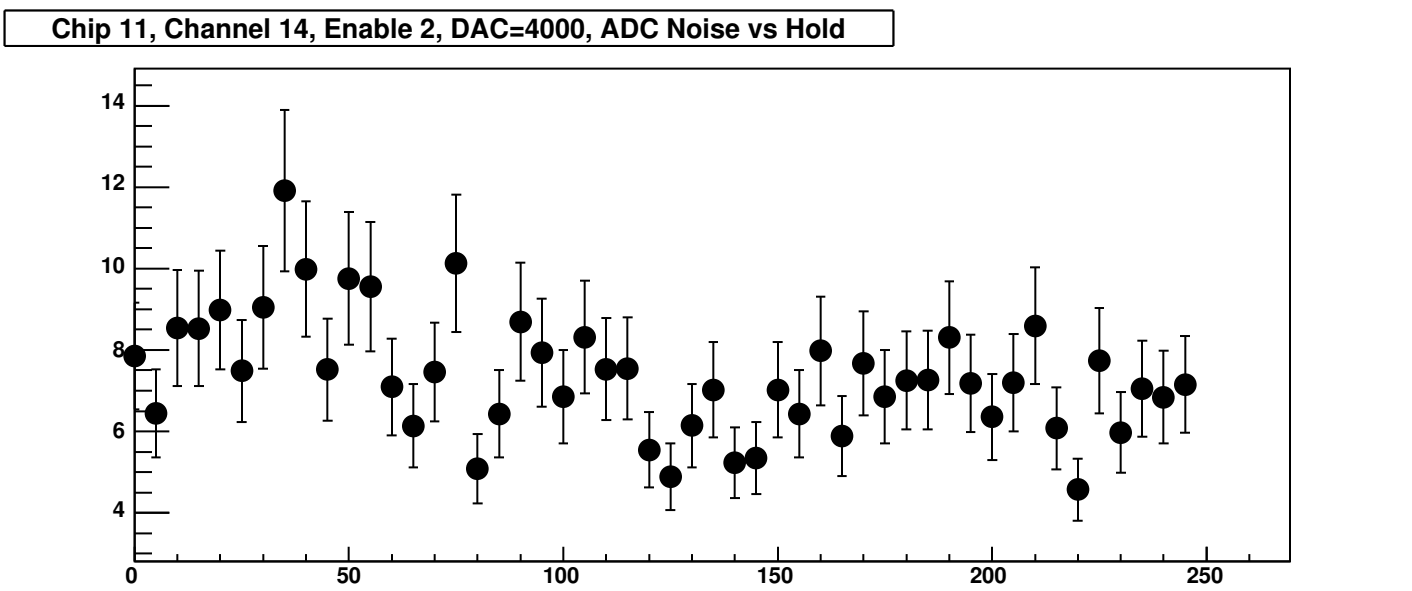

Chip 11, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
-500
-1000
-1500
-2000
0
50
100
150
200
250

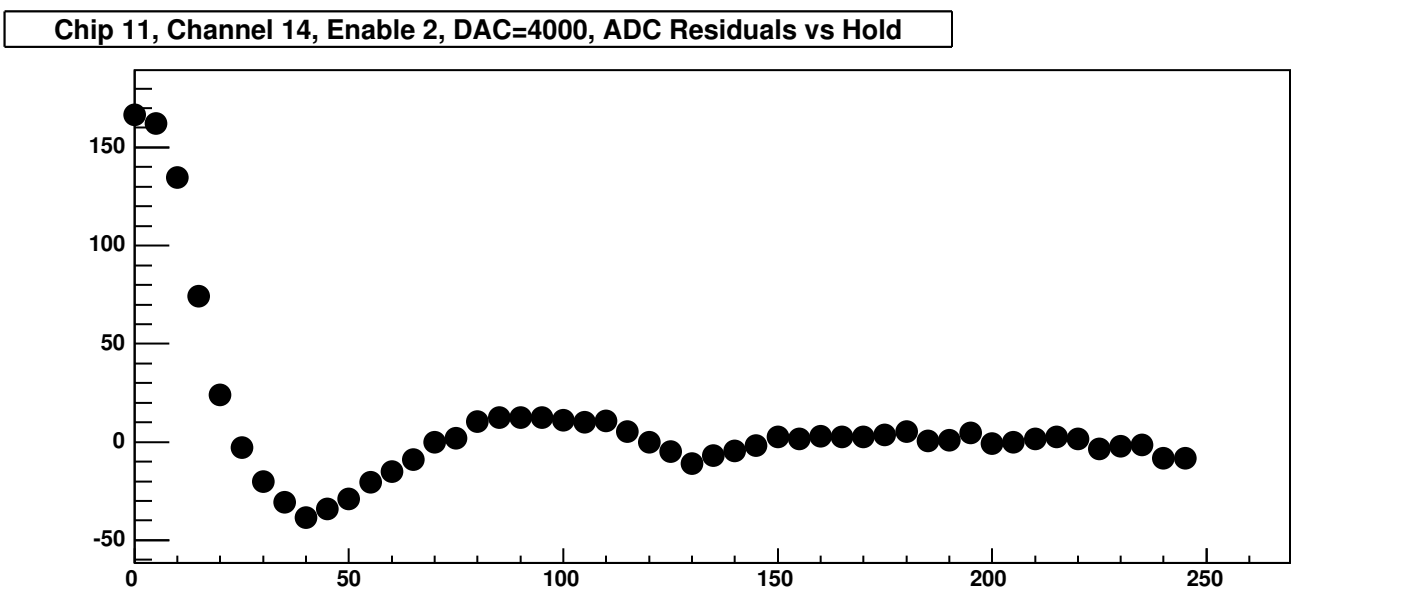

Chip 11, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 4693 / 41
p0 -383.8 ± 0.613
p1 224.8 ± 17.53
p2 -20.16 ± 0.9884
p3 -1.564 ± 8.693
p4 0.1304 ± 0.002722
-380
-400
-420
-440
-460
-480
-500
-520
0
50
100
150
200
250


Chip 11, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

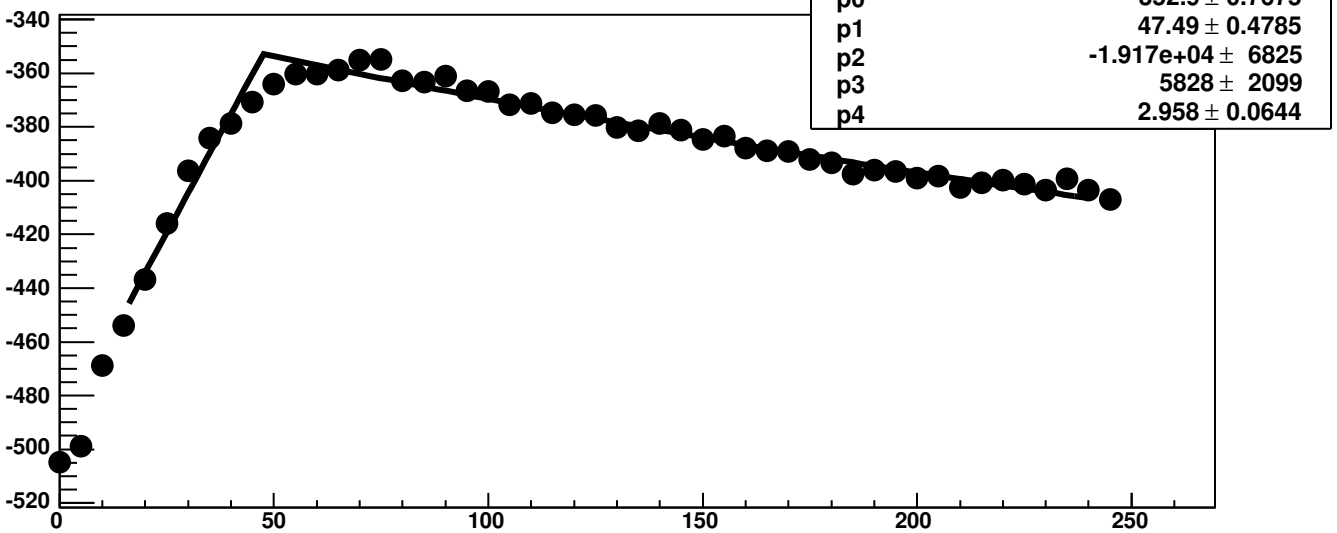

Chip 11, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
-80
-100
0
50
100
150
200
250


Chip 11, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 3416 / 41
p0 -389.9 ± 0.6719
p1 131.3 ± 0.4194
p2 -5.257e+07 ± 1.629e+06
p3 2.52e+07 ± 7.36e+05
p4 2.19 ± 0.009848
-400
-450
-500
-550
-600
-650
0
50
100
150
200
250

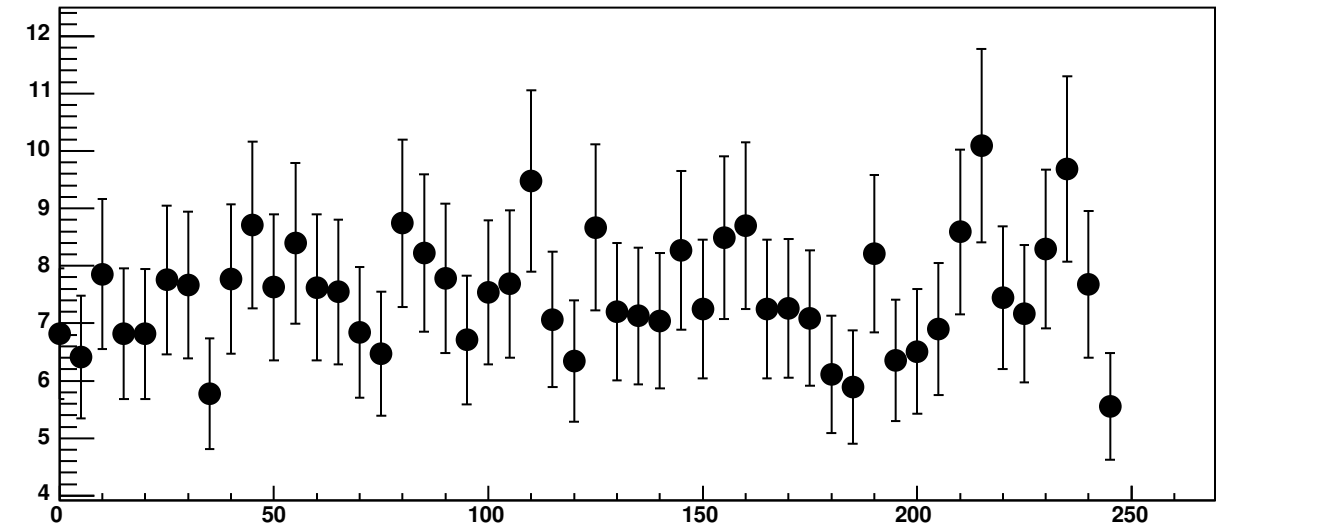

Chip 11, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 11, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold
150
100
50
0
-50
0
50
100
150
200
250

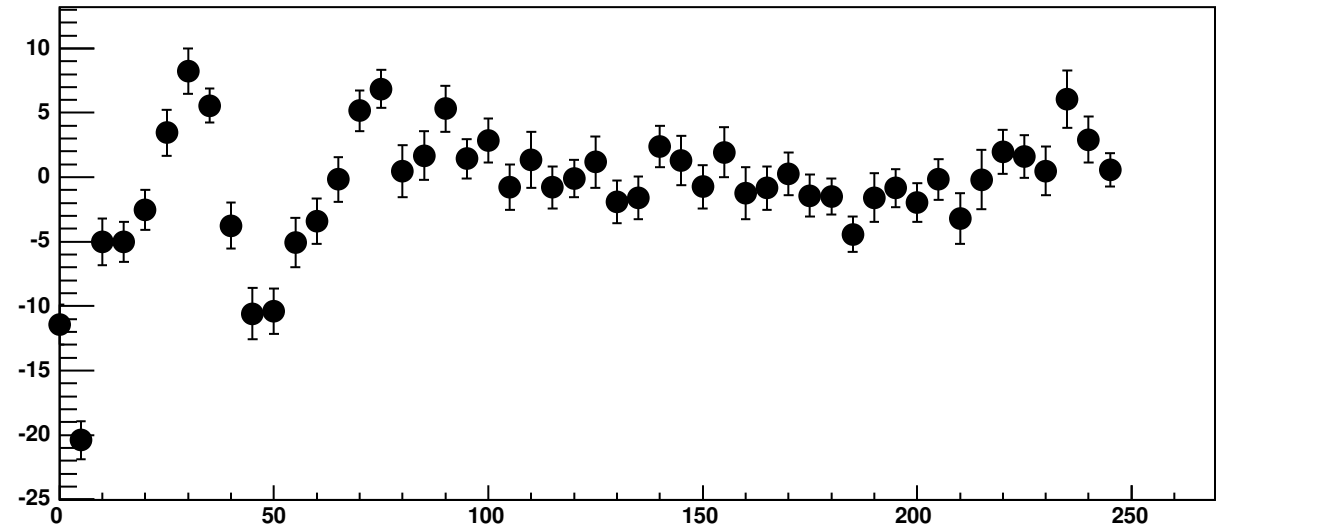
Chip 11, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

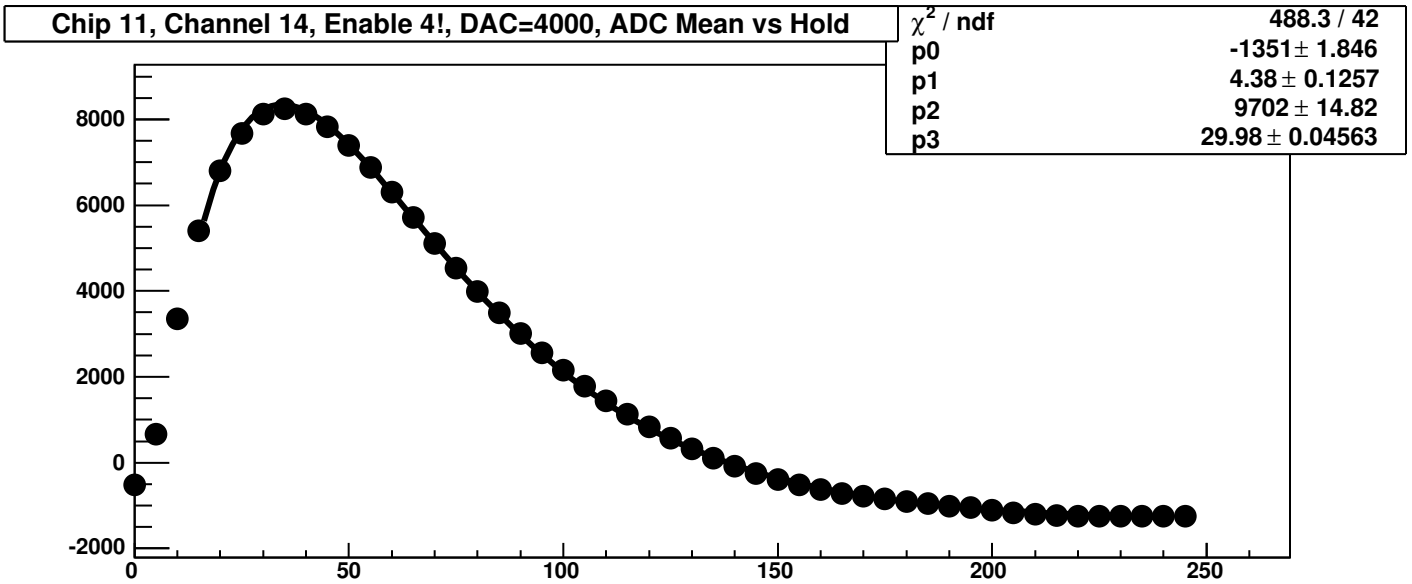


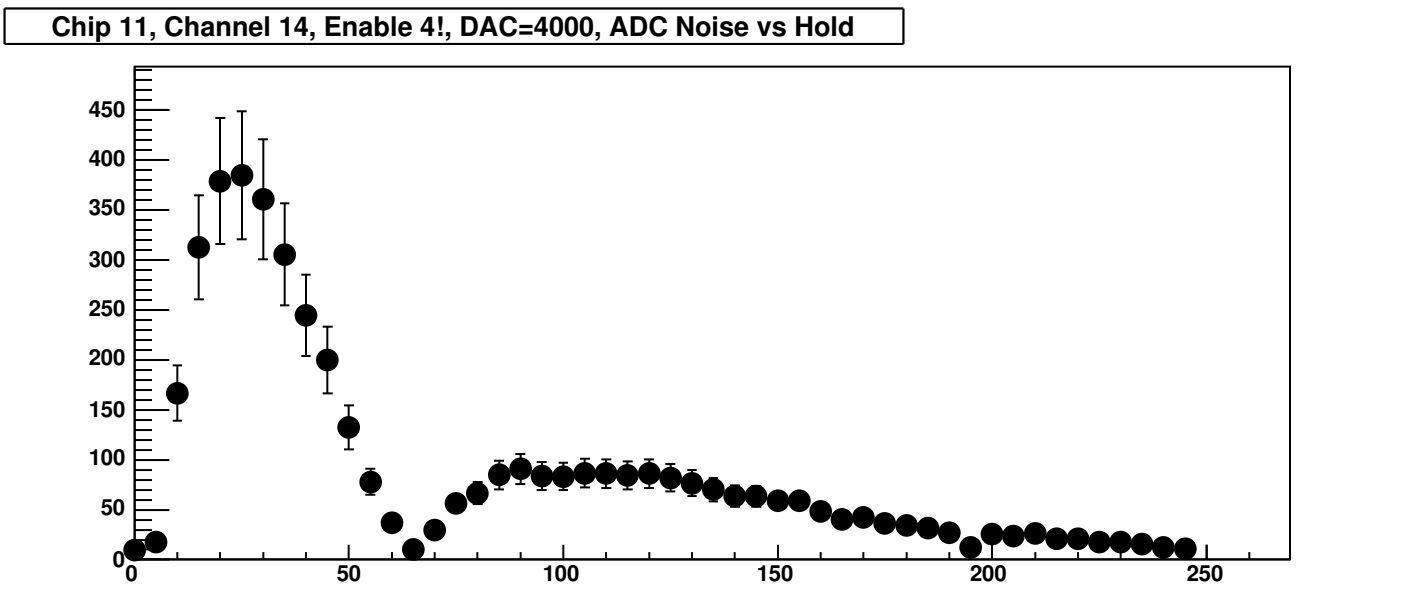
Chip 11, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold

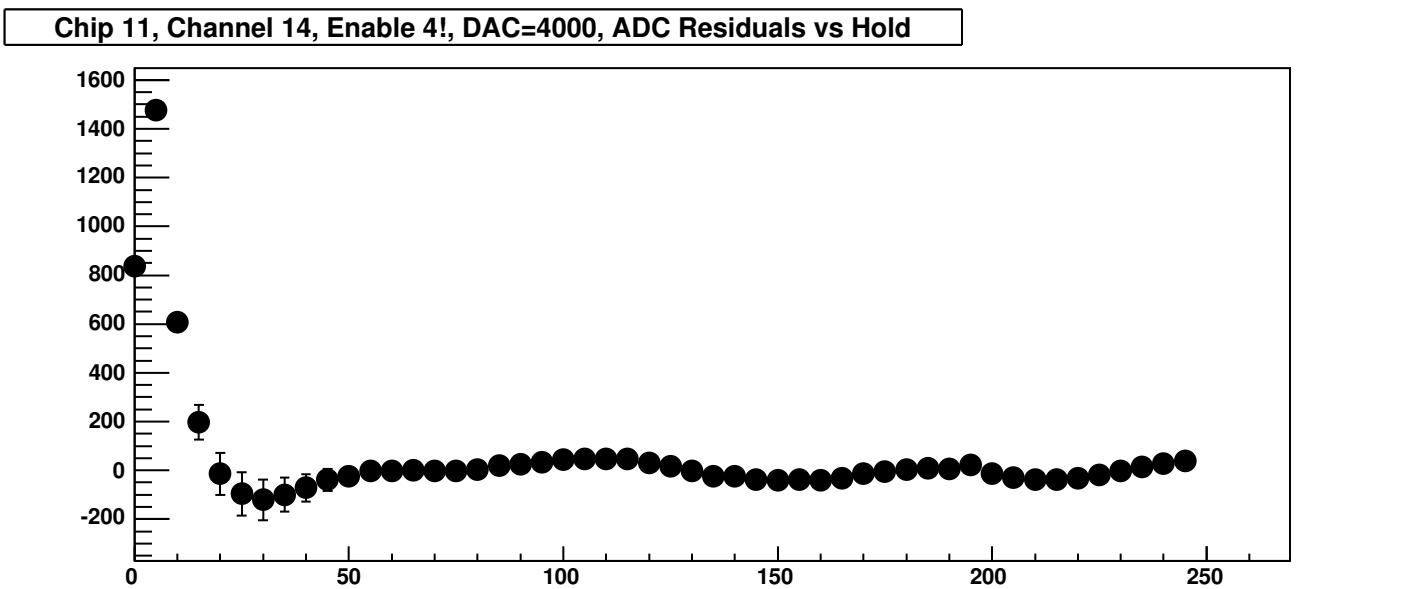


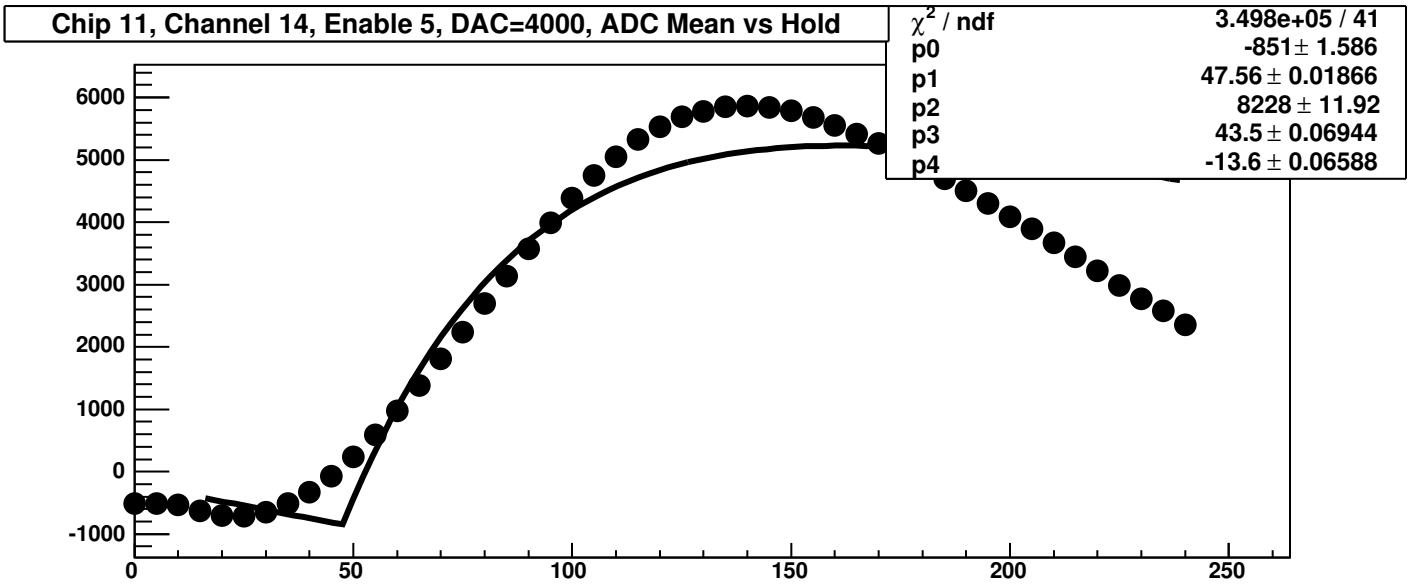
Chip 11, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

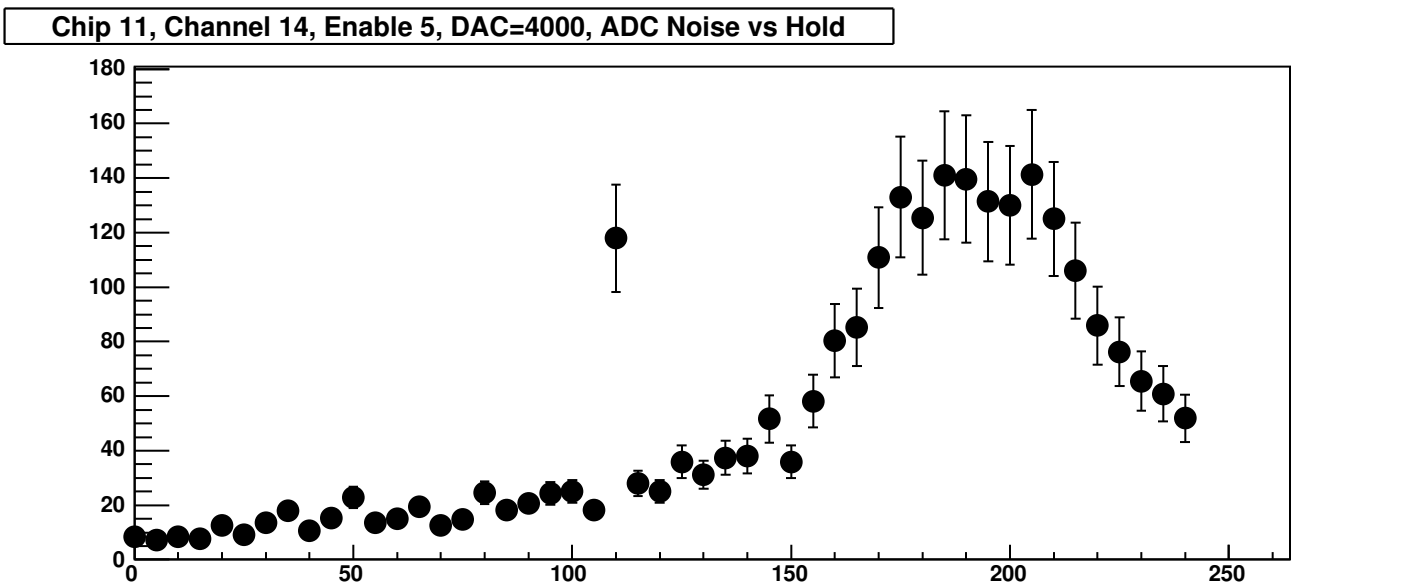


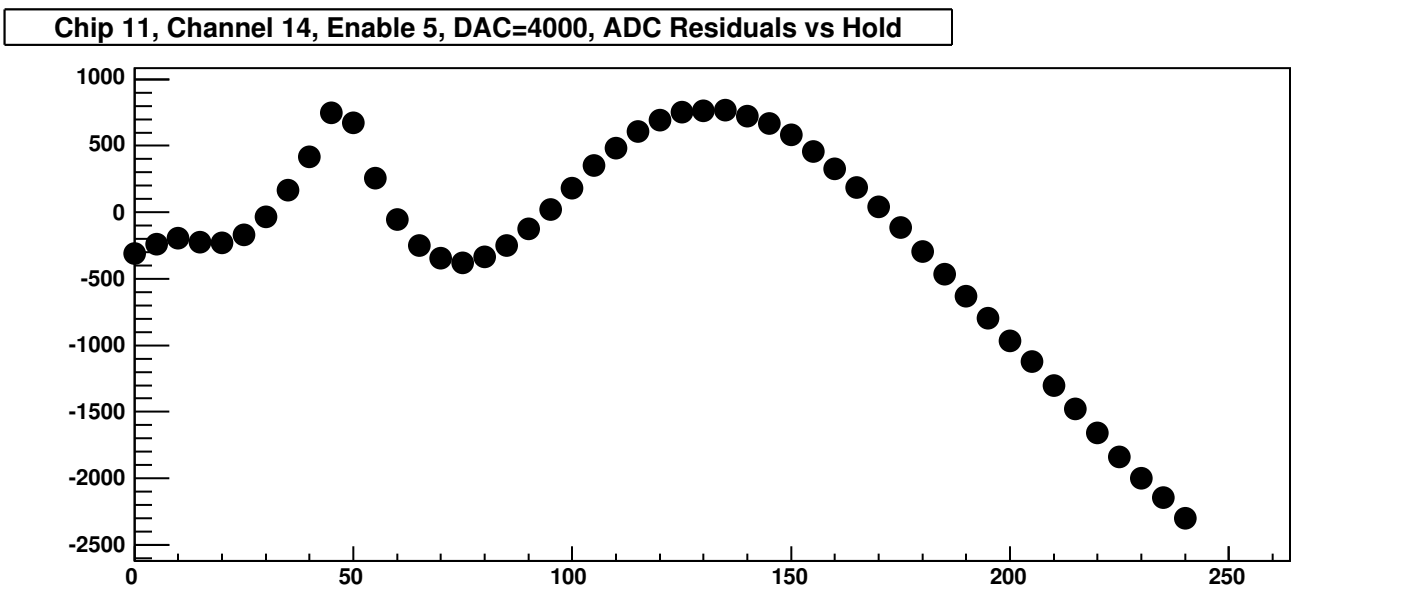

Chip 11, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 488.3 / 42
p0 -1351 ± 1.846
p1 4.38 ± 0.1257
p2 9702 ± 14.82
p3 29.98 ± 0.04563
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

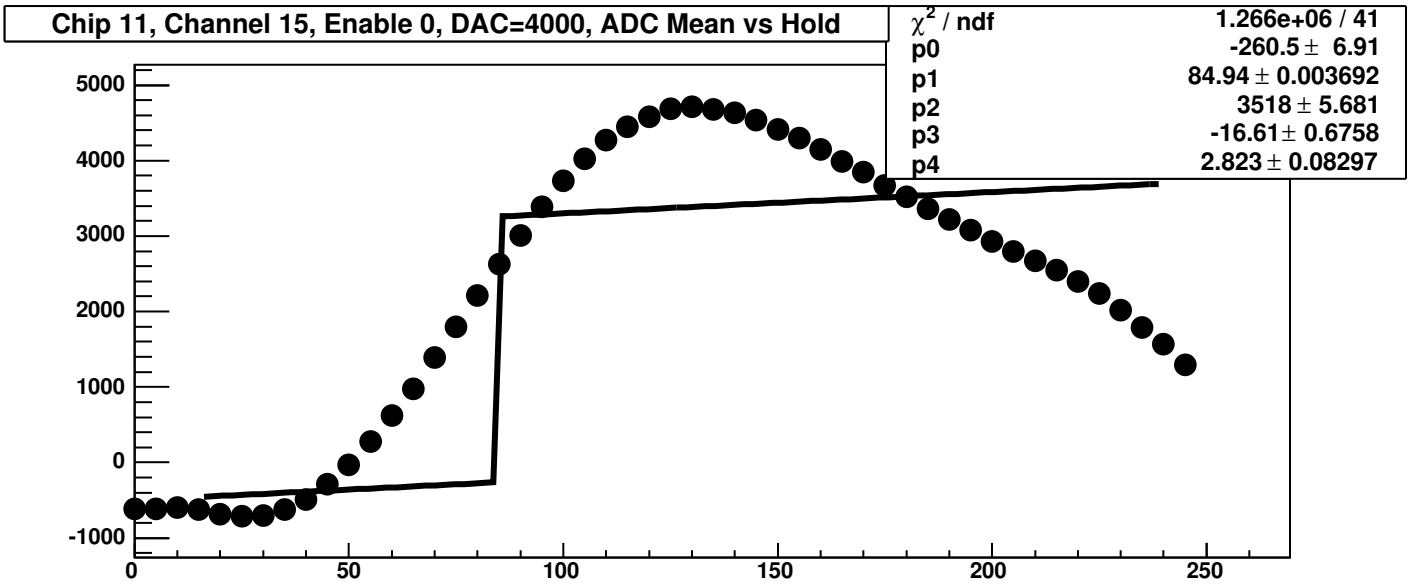

Chip 11, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold
450
400
350
300
250
200
150
100
50
0
0
50
100
150
200
250

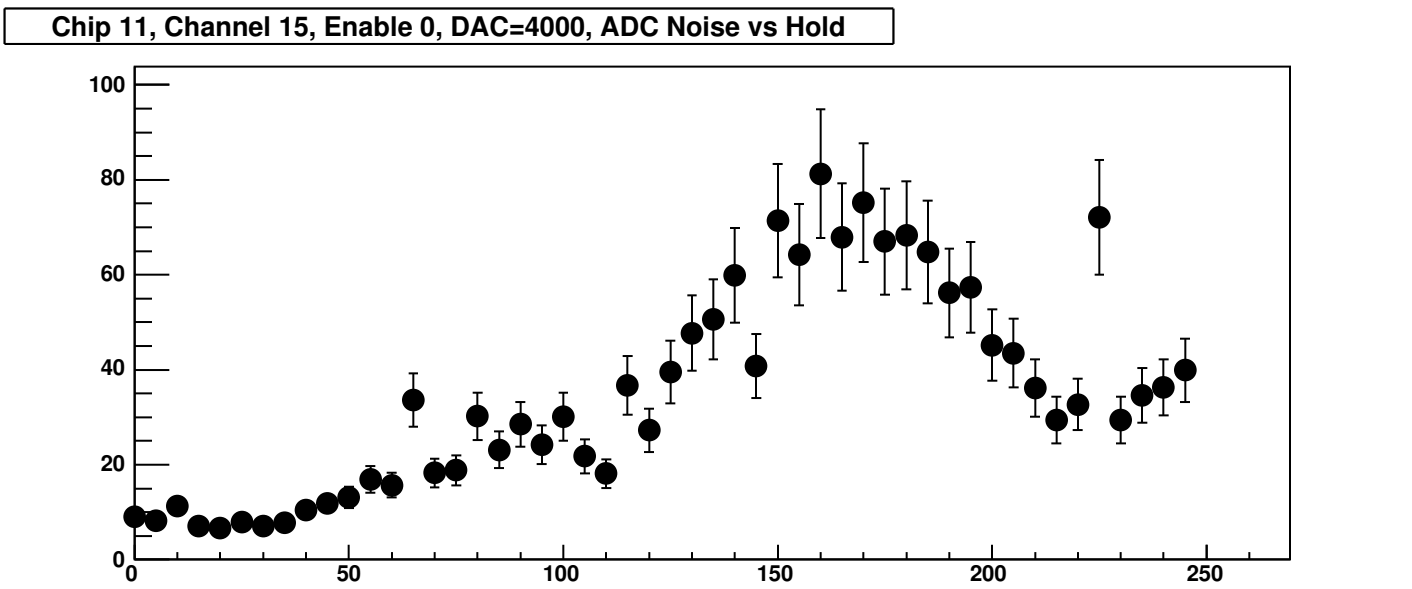

Chip 11, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

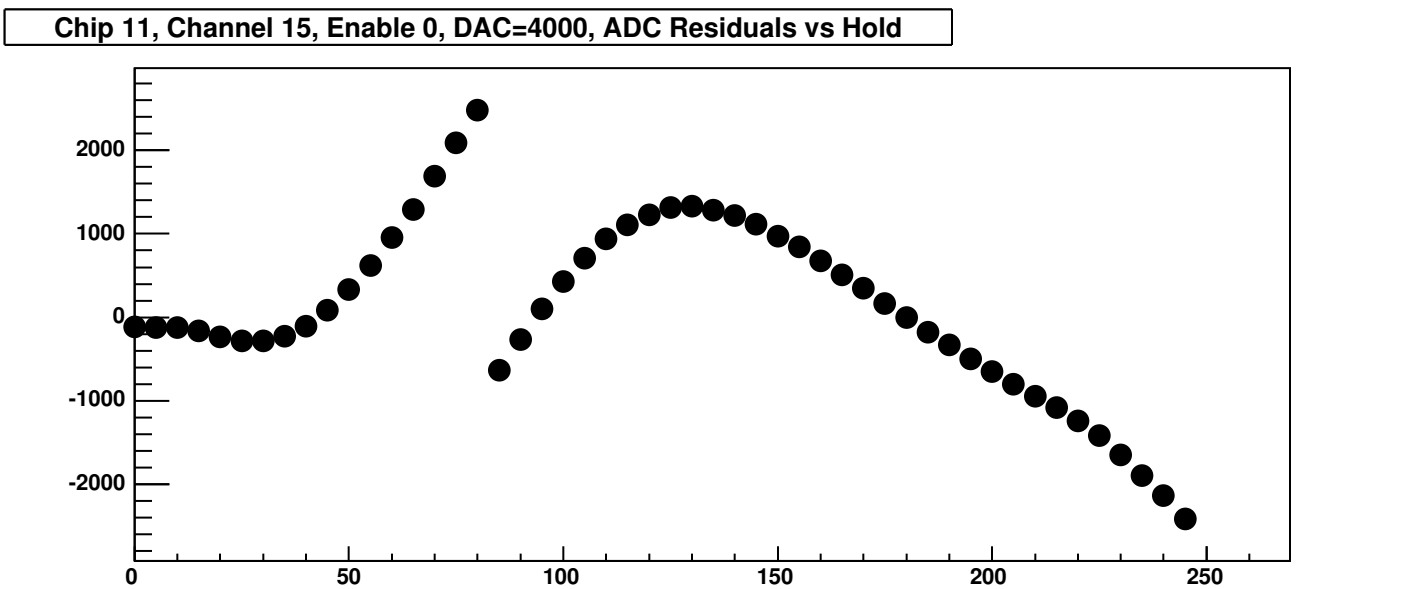

Chip 11, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 3.498e+05 / 41
p0 -851 ± 1.586
p1 47.56 ± 0.01866
p2 8228 ± 11.92
p3 43.5 ± 0.06944
p4 -13.6 ± 0.06588
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

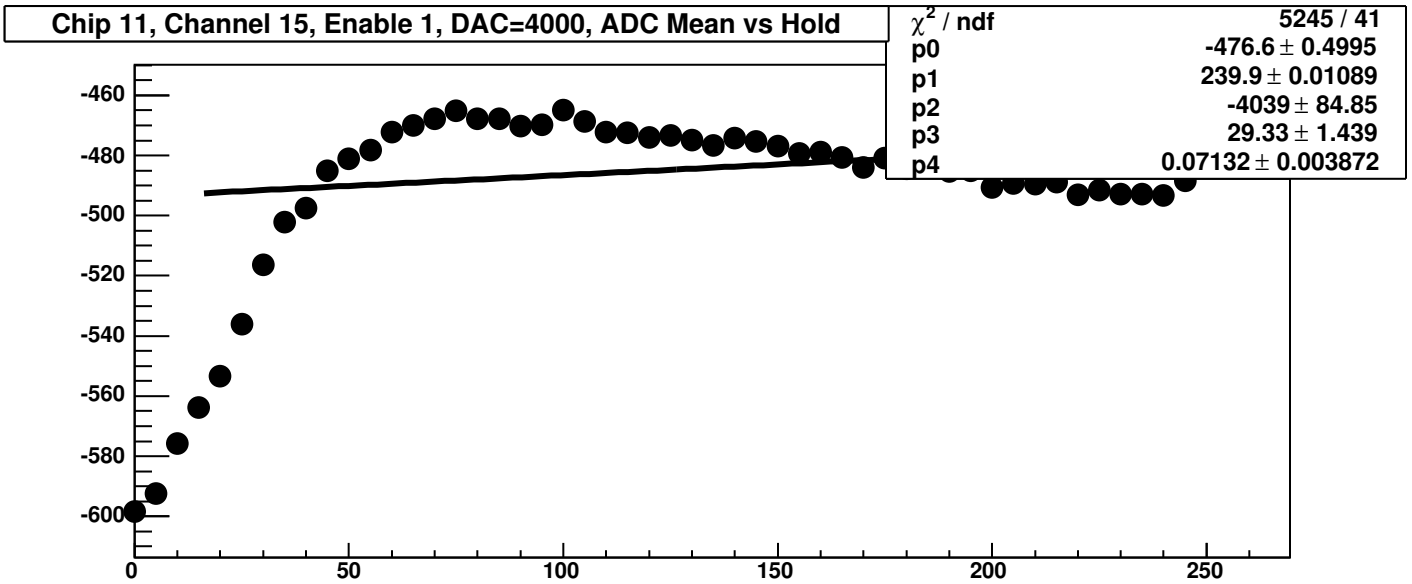

Chip 11, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

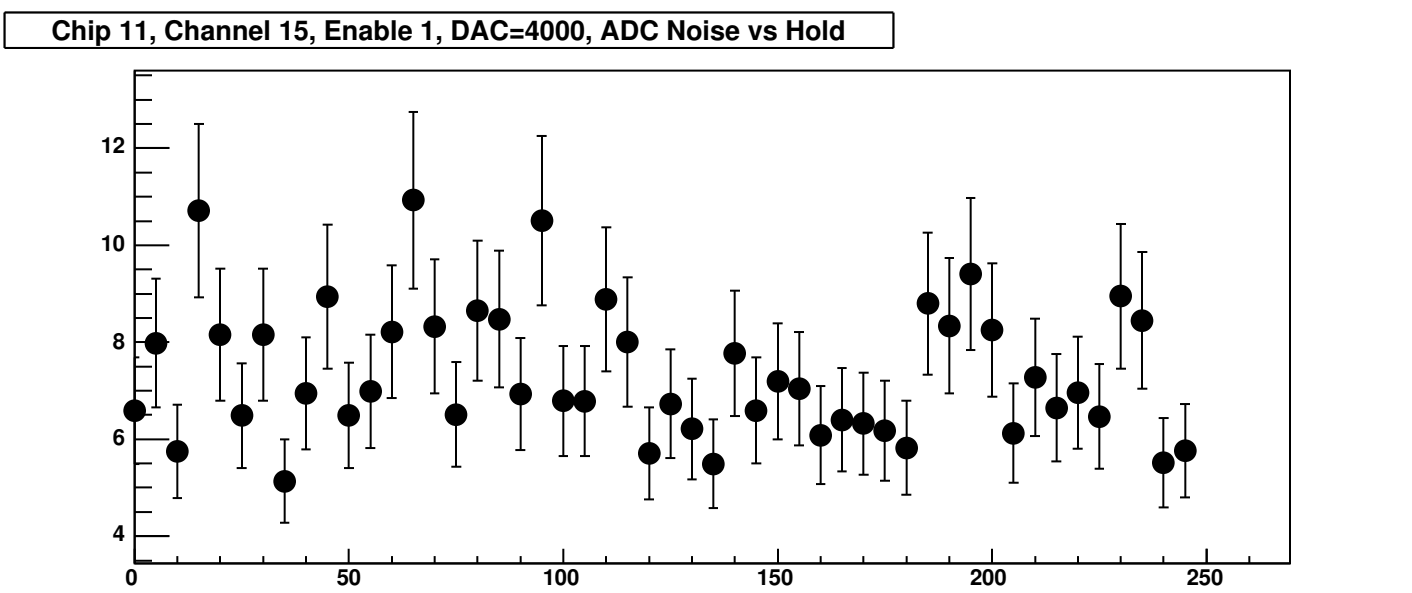

Chip 11, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
0
50
100
150
200
250

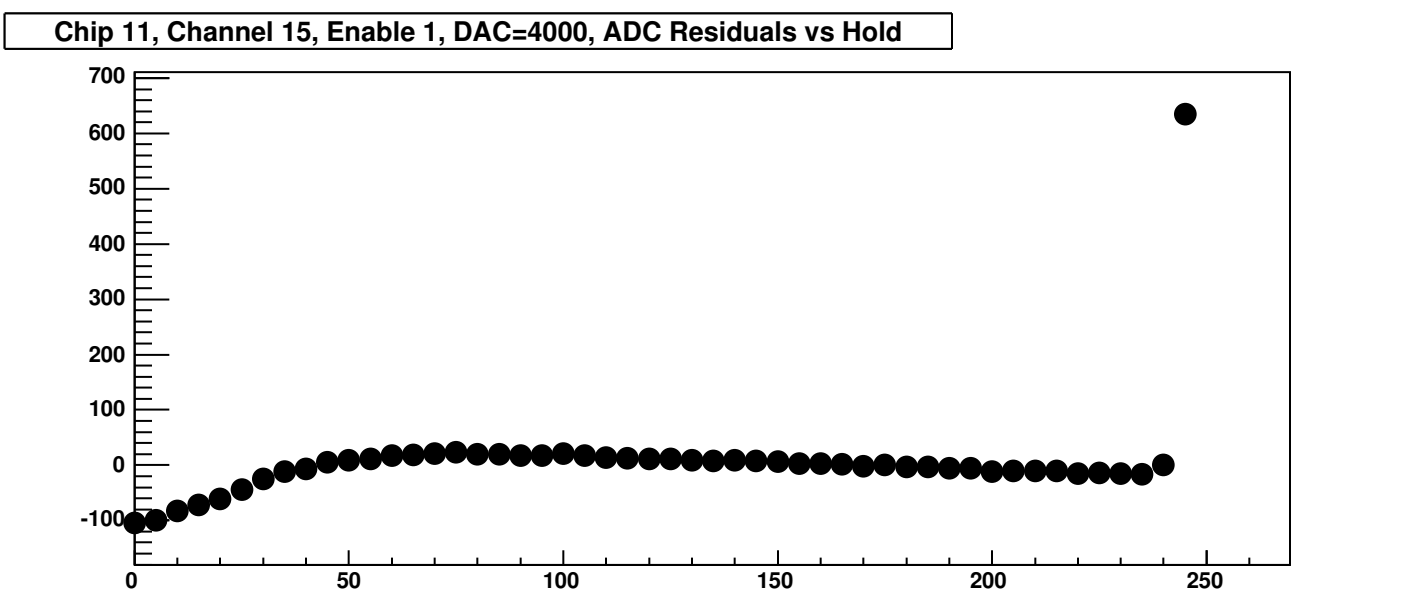

Chip 11, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.266e+06 / 41
p0 -260.5 ± 6.91
p1 84.94 ± 0.003692
p2 3518 ± 5.681
p3 -16.61 ± 0.6758
p4 2.823 ± 0.08297
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

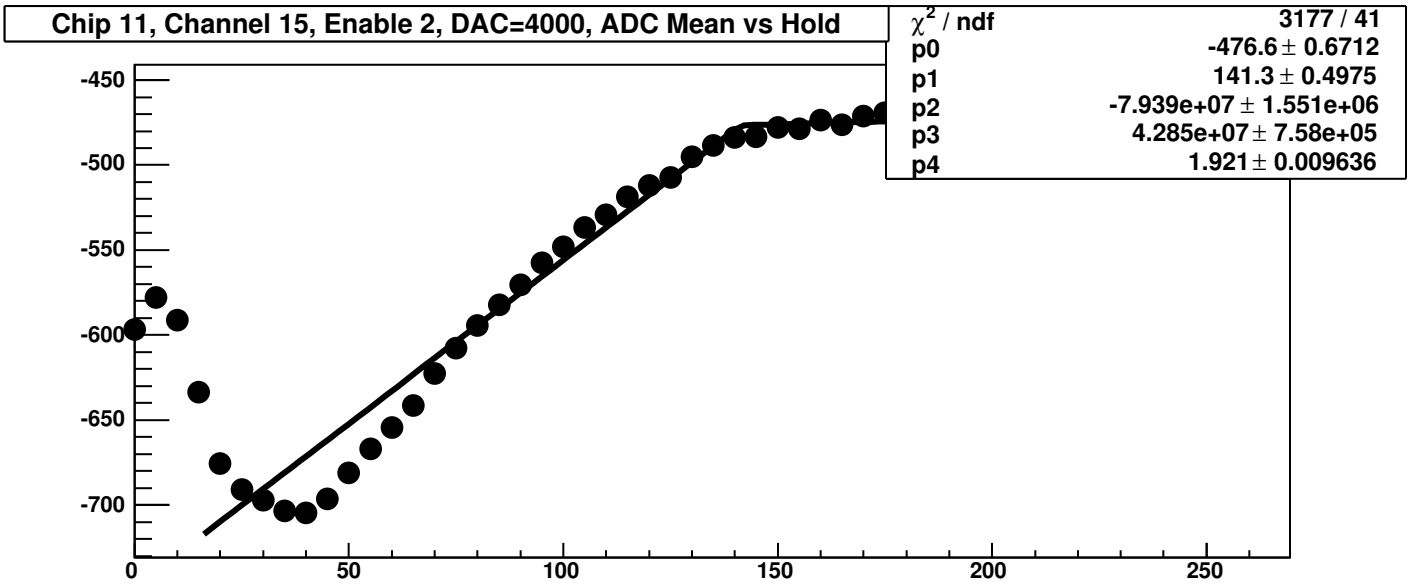

Chip 11, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250

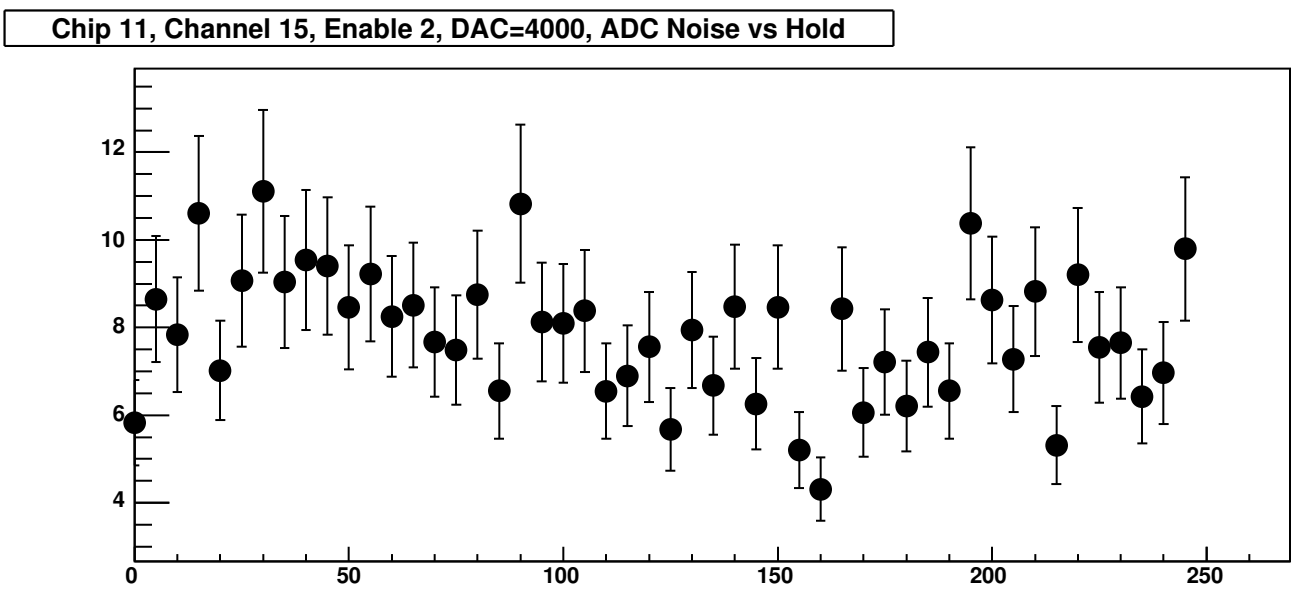

Chip 11, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
0
50
100
150
200
250

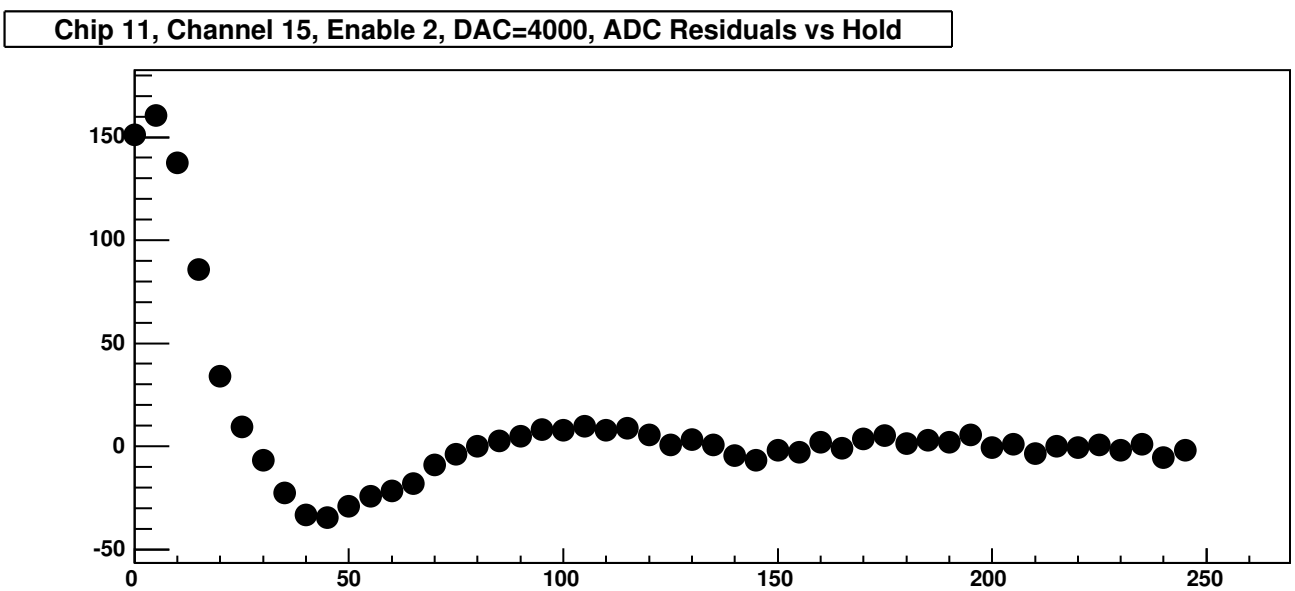

Chip 11, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 5245 / 41
p0 -476.6 ± 0.4995
p1 239.9 ± 0.01089
p2 -4039 ± 84.85
p3 29.33 ± 1.439
p4 0.07132 ± 0.003872
-460
-480
-500
-520
-540
-560
-580
-600
0
50
100
150
200
250


Chip 11, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

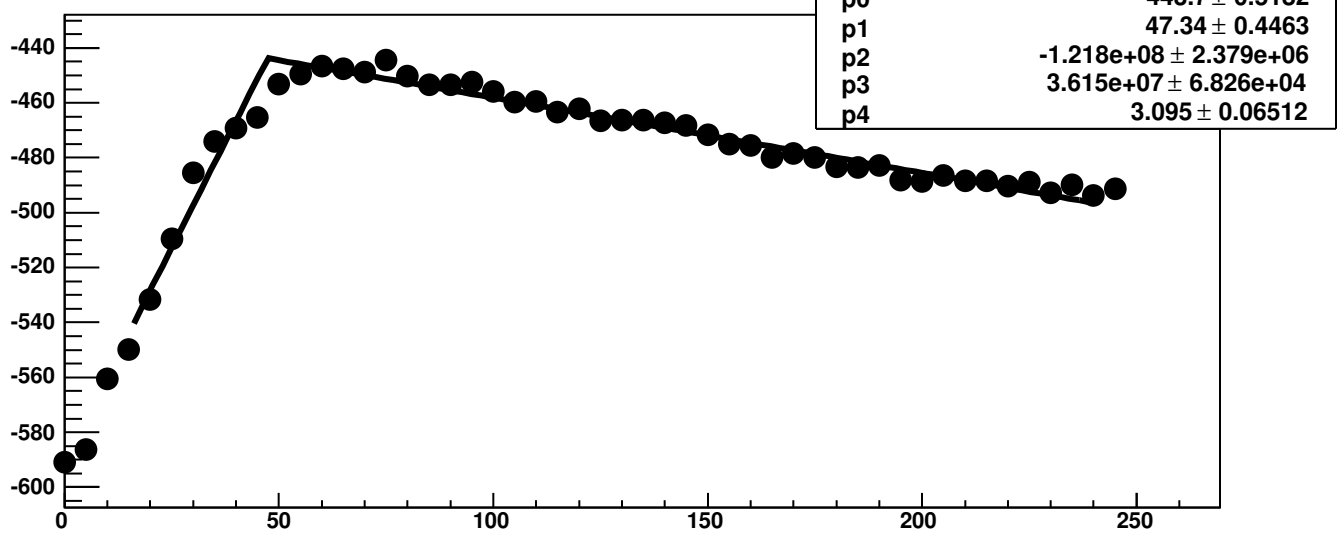

Chip 11, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold
700
600
500
400
300
200
100
0
-100
0
50
100
150
200
250


Chip 11, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 3177 / 41
p0 -476.6 ± 0.6712
p1 141.3 ± 0.4975
p2 -7.939e+07 ± 1.551e+06
p3 4.285e+07 ± 7.58e+05
p4 1.921 ± 0.009636
-450
-500
-550
-600
-650
-700
0
50
100
150
200
250

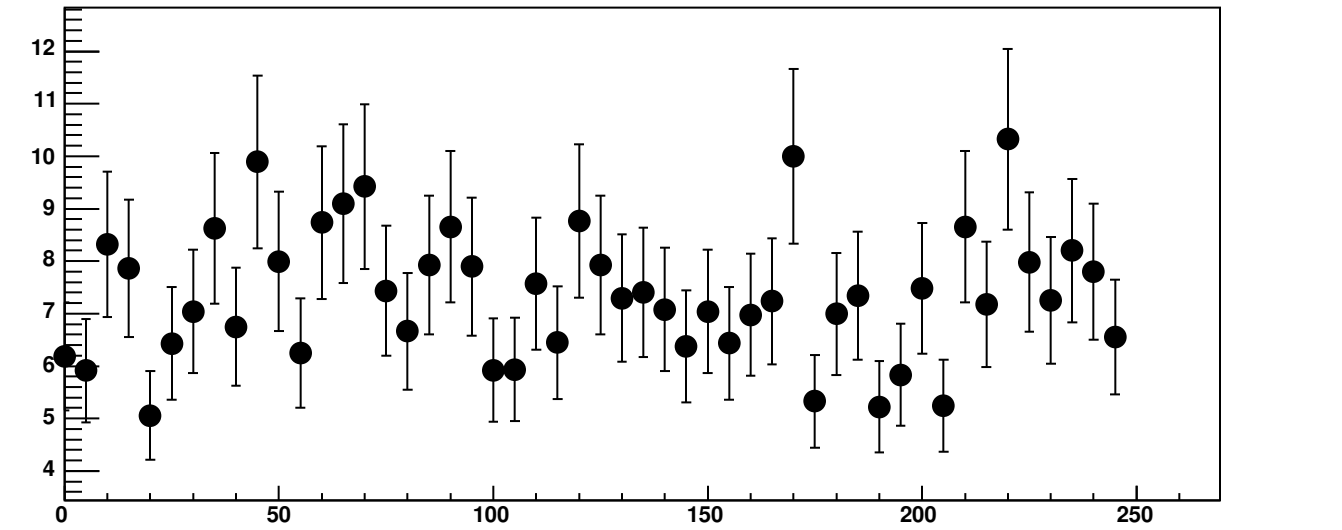

Chip 11, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 11, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold
150
100
50
0
-50
0
50
100
150
200
250

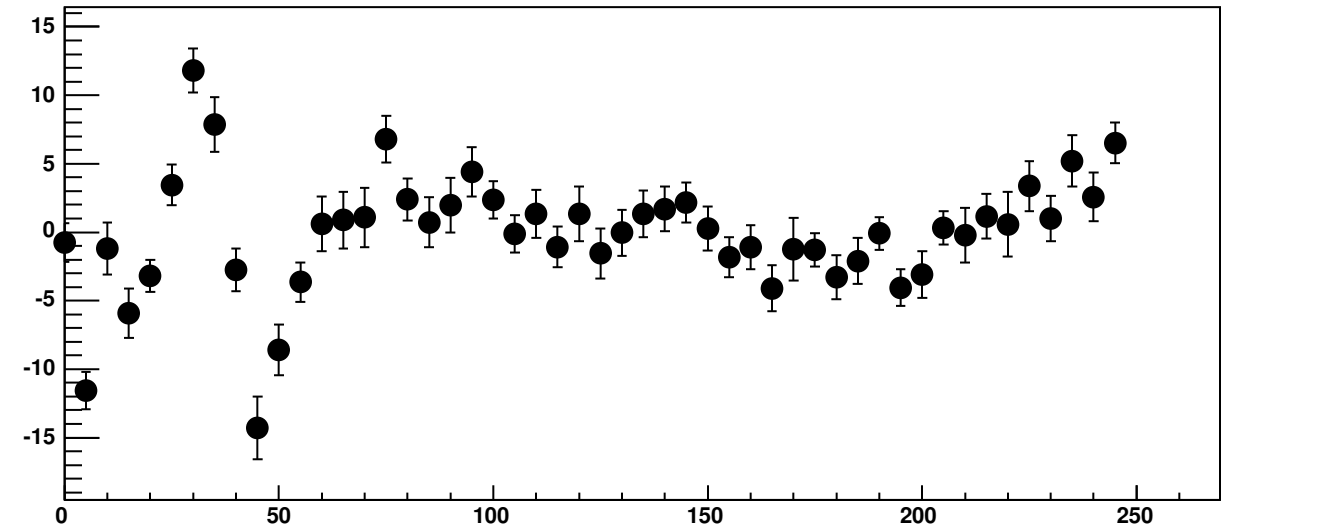
Chip 11, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

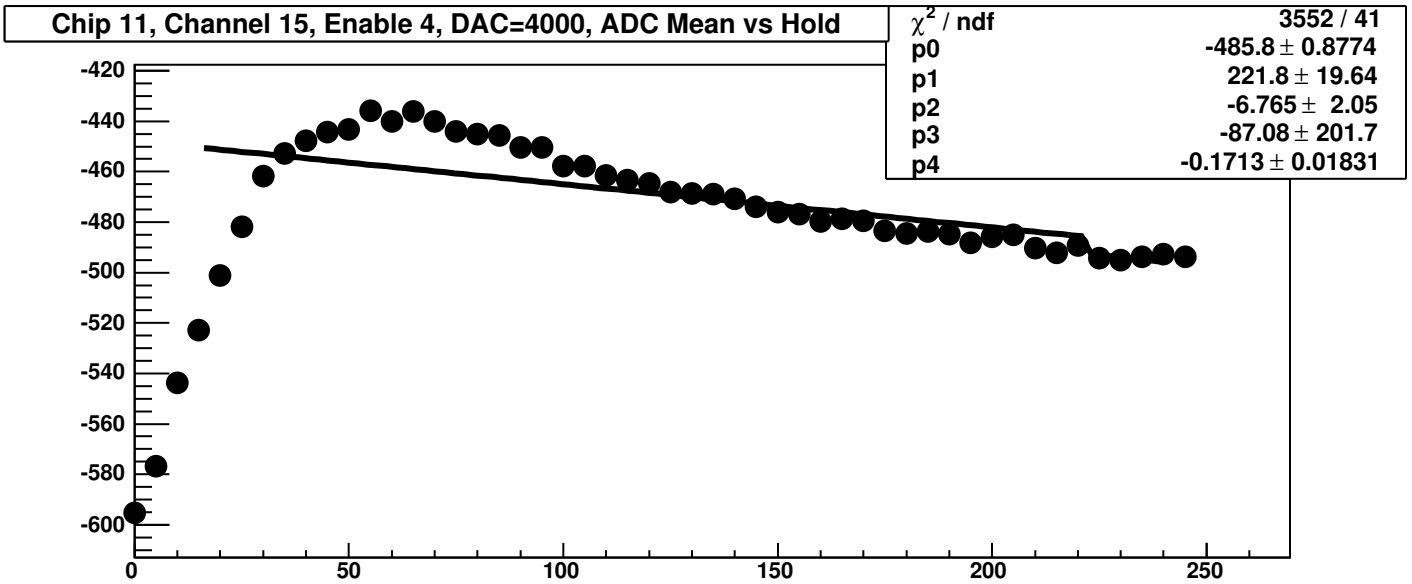


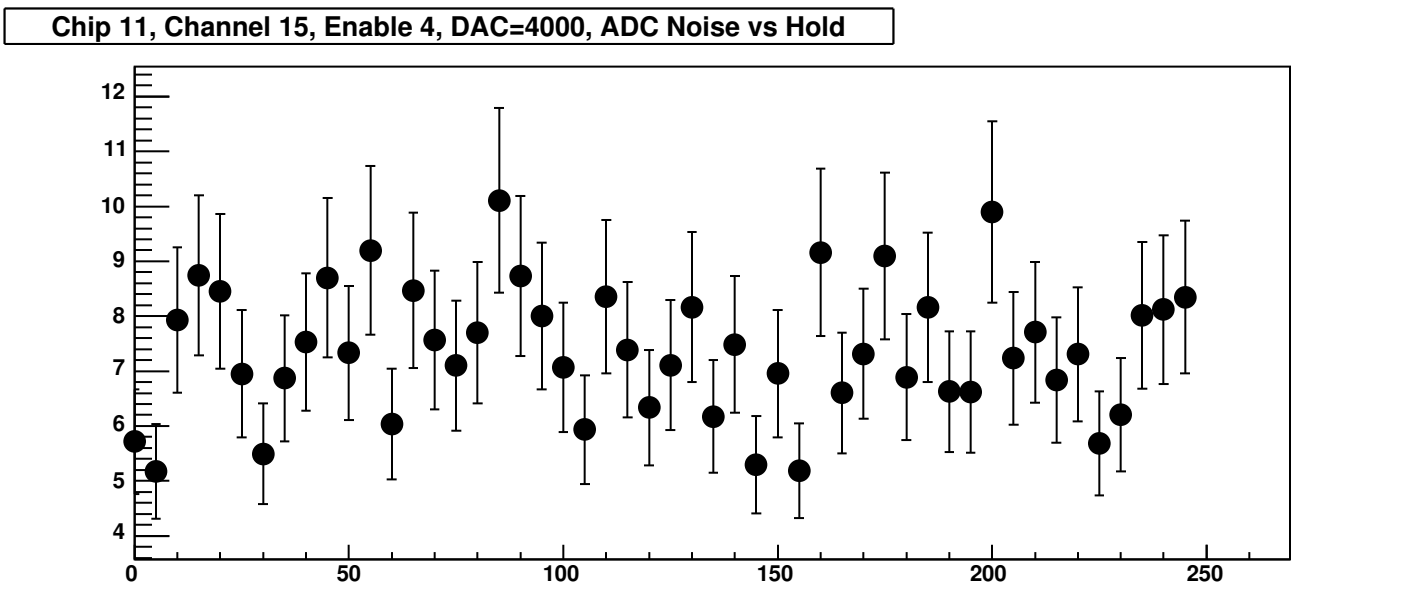
Chip 11, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold

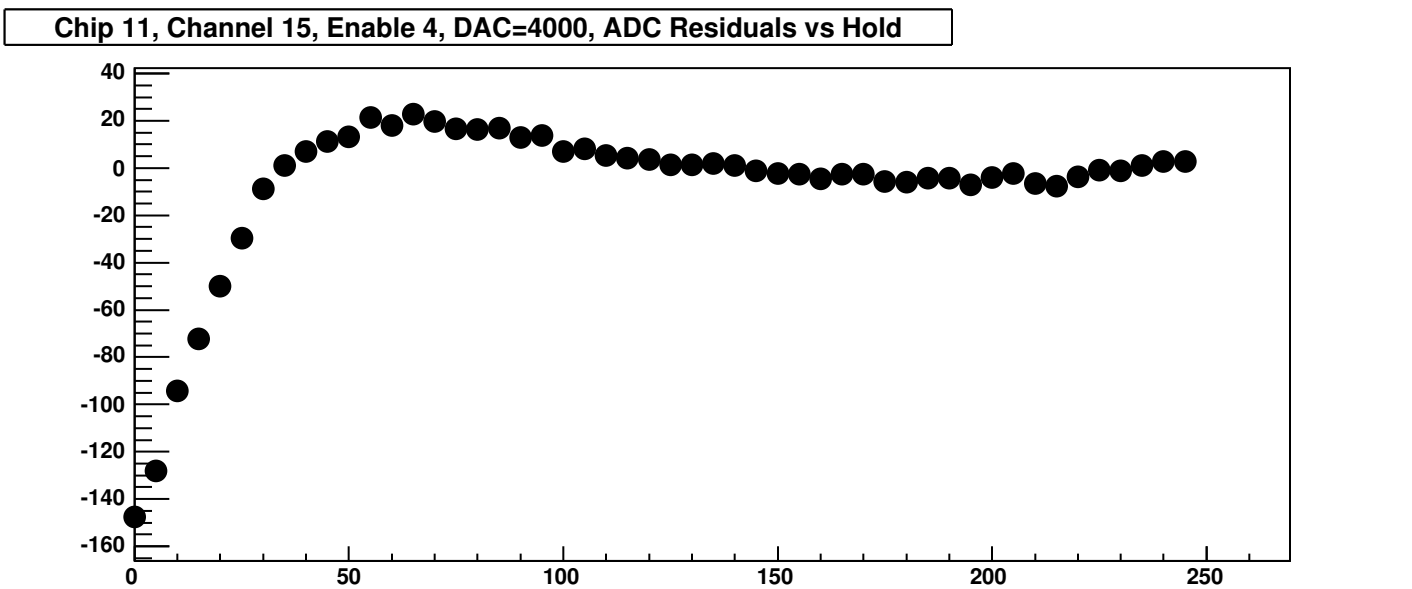


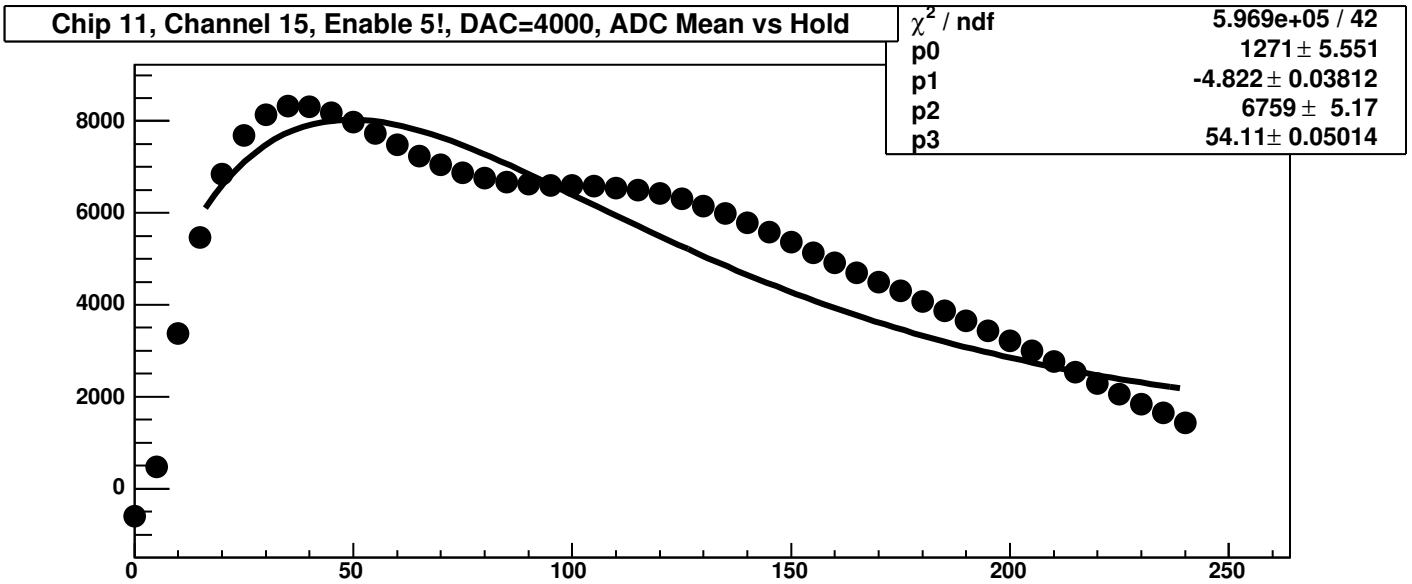
Chip 11, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold

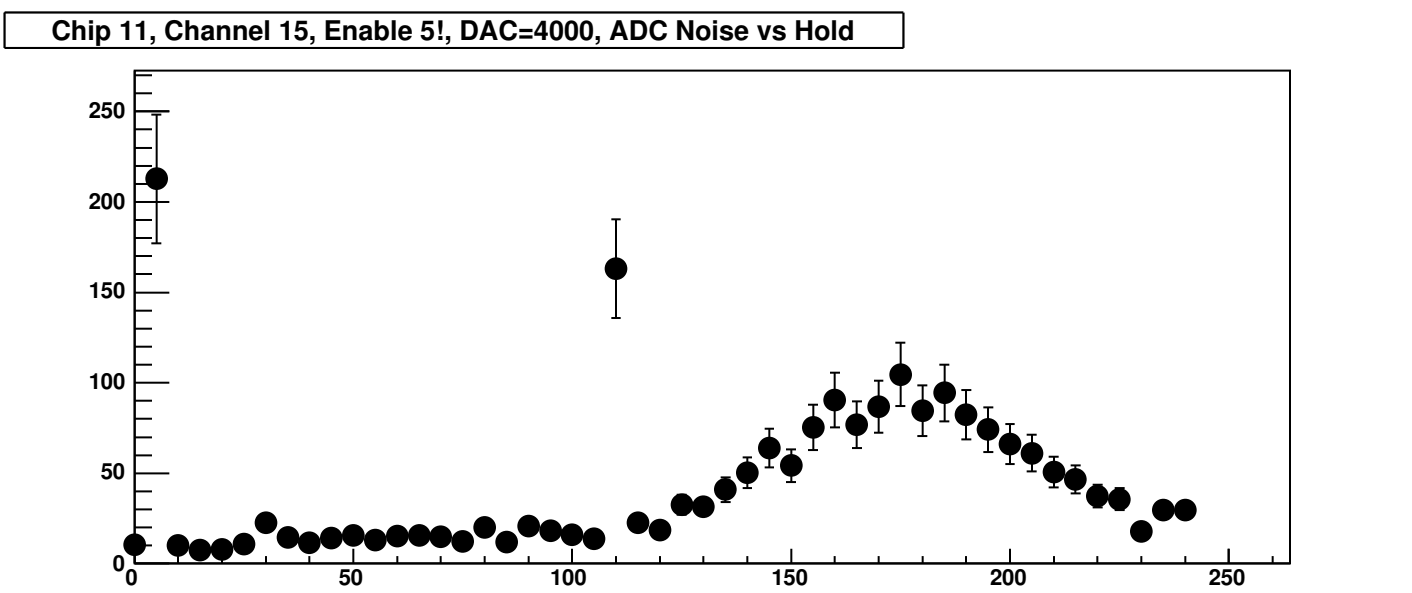


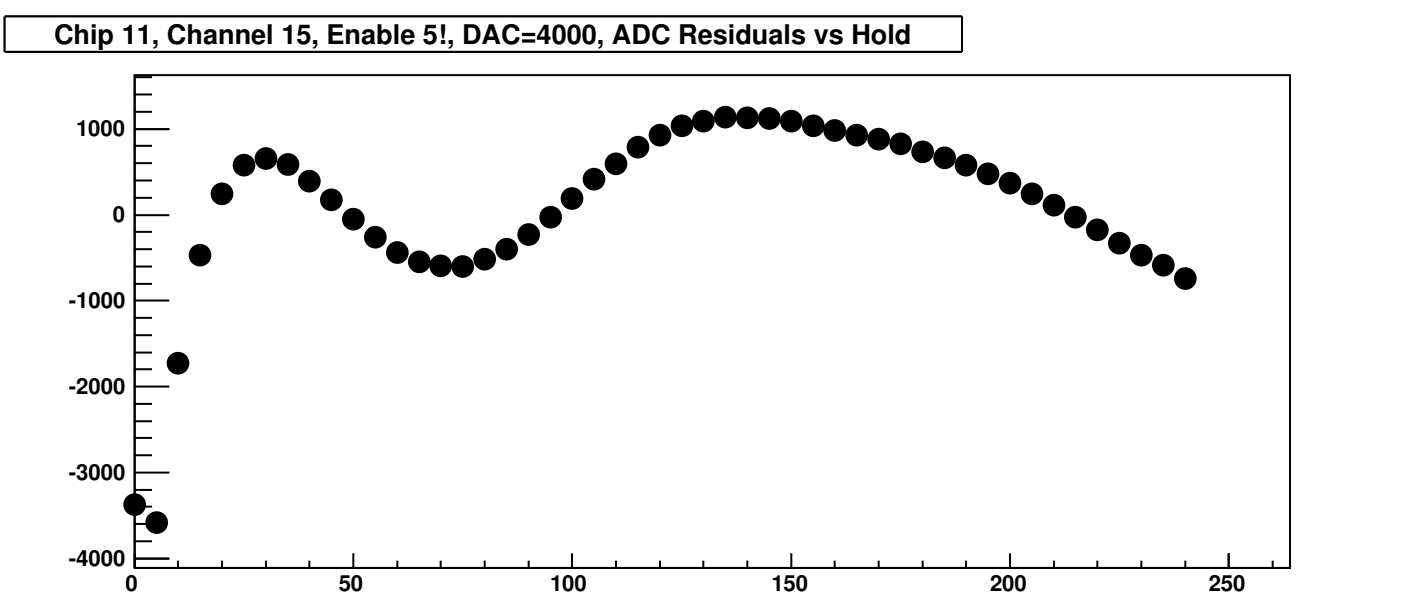

Chip 11, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 3552 / 41
p0 -485.8 ± 0.8774
p1 221.8 ± 19.64
p2 -6.765 ± 2.05
p3 -87.08 ± 201.7
p4 -0.1713 ± 0.01831
-420
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
50
100
150
200
250

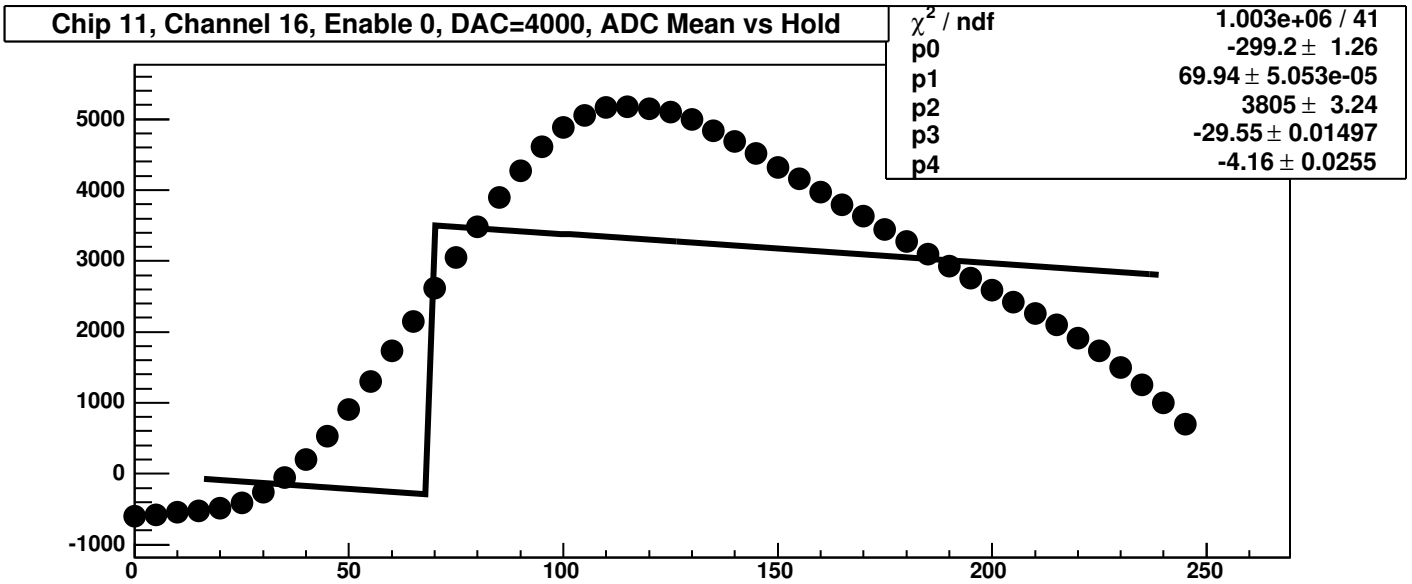

Chip 11, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

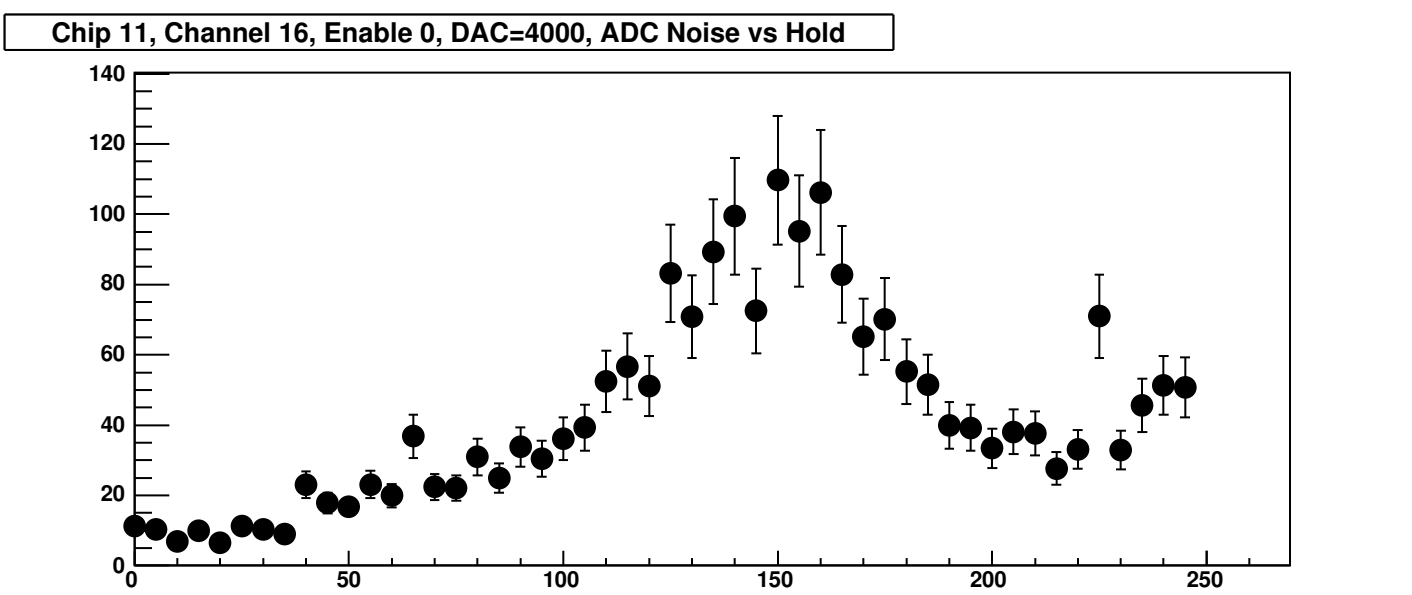

Chip 11, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
-100
-120
-140
-160
0
50
100
150
200
250

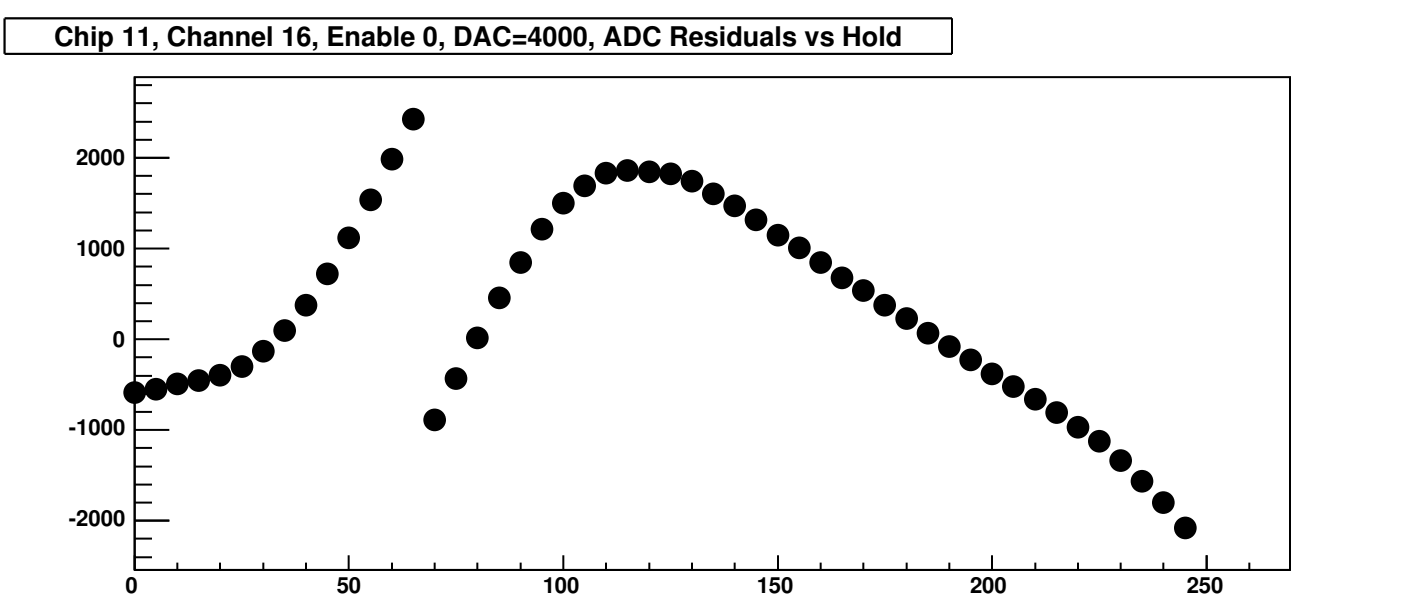

Chip 11, Channel 15, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.969e+05 / 42
p0 1271 ± 5.551
p1 -4.822 ± 0.03812
p2 6759 ± 5.17
p3 54.11 ± 0.05014
8000
6000
4000
2000
0
0
50
100
150
200
250

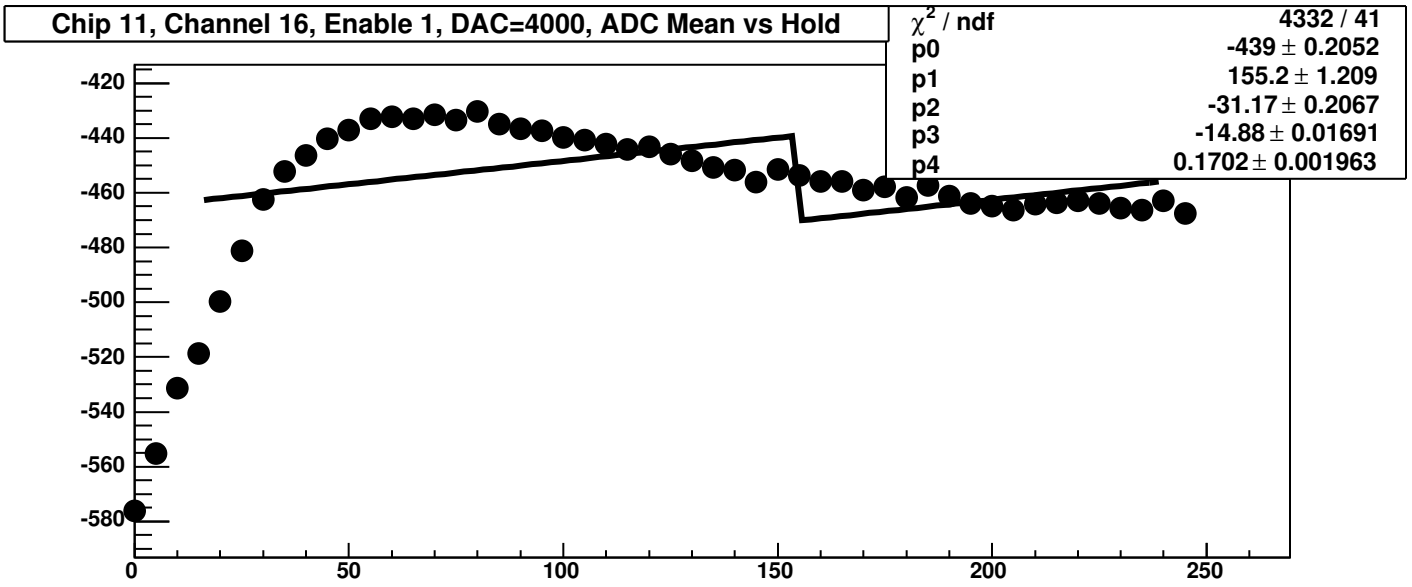

Chip 11, Channel 15, Enable 5!, DAC=4000, ADC Noise vs Hold
250
200
150
100
50
0
0
50
100
150
200
250

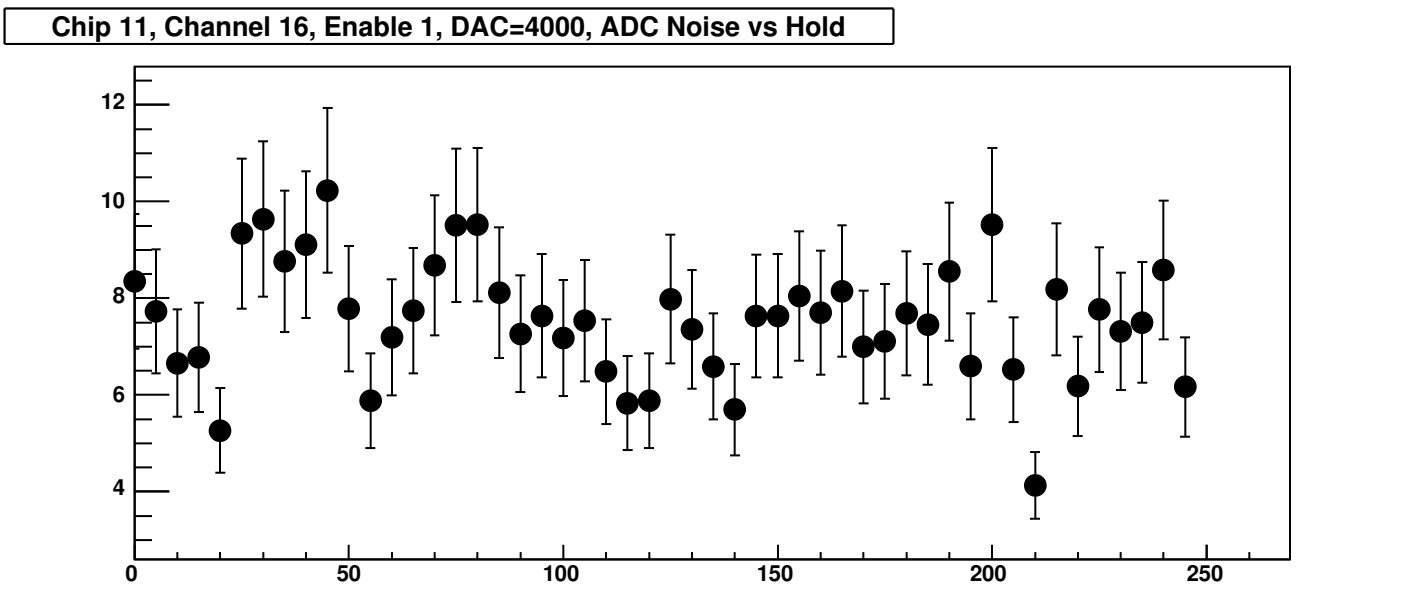

Chip 11, Channel 15, Enable 5!, DAC=4000, ADC Residuals vs Hold
1000
0
-1000
-2000
-3000
-4000
0
50
100
150
200
250

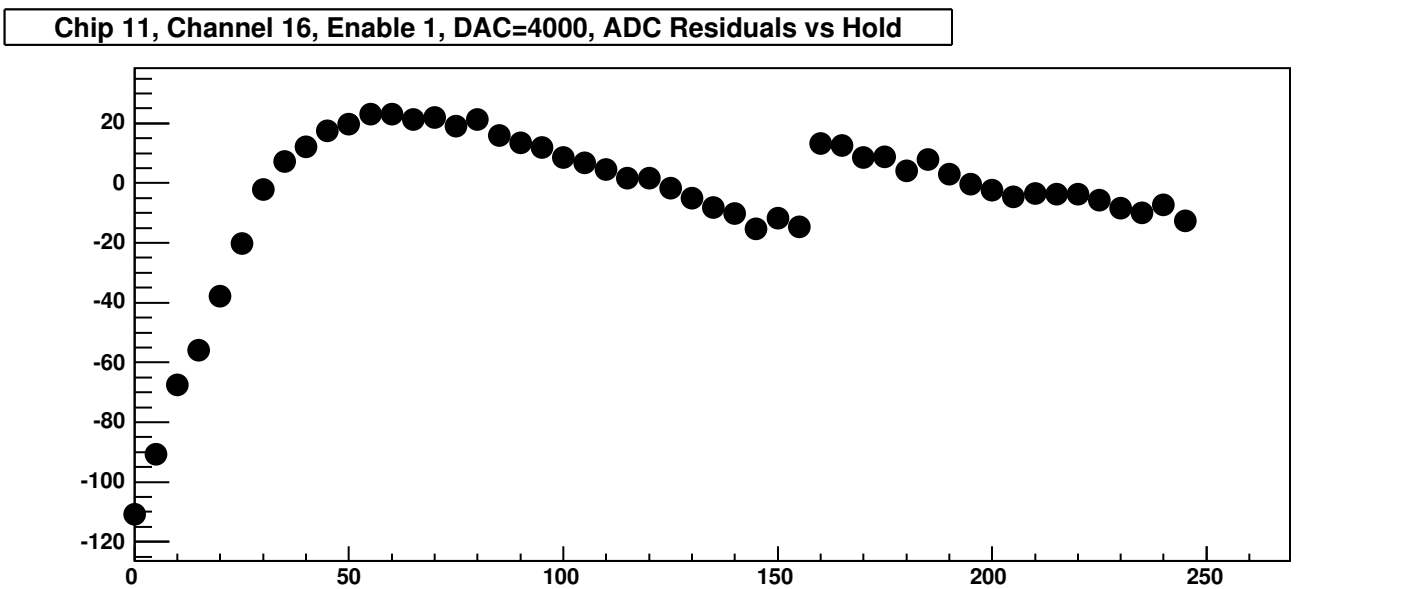

Chip 11, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.003e+06 / 41
p0 -299.2 ± 1.26
p1 69.94 ± 5.053e-05
p2 3805 ± 3.24
p3 -29.55 ± 0.01497
p4 -4.16 ± 0.0255
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

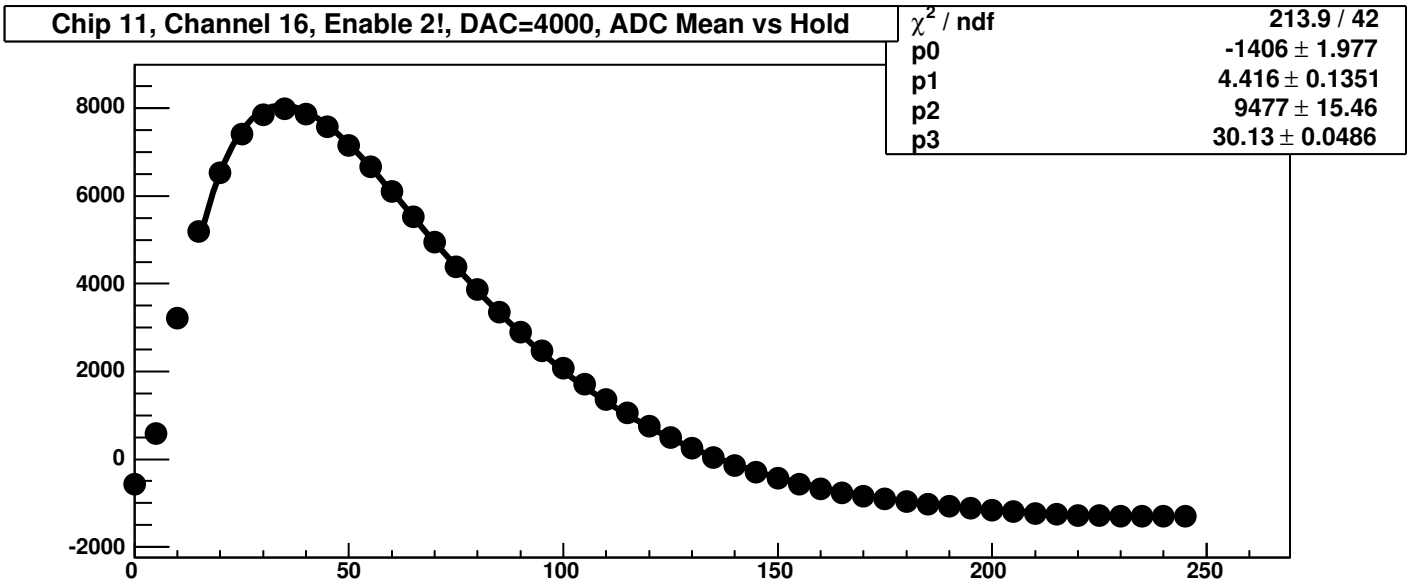

Chip 11, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250

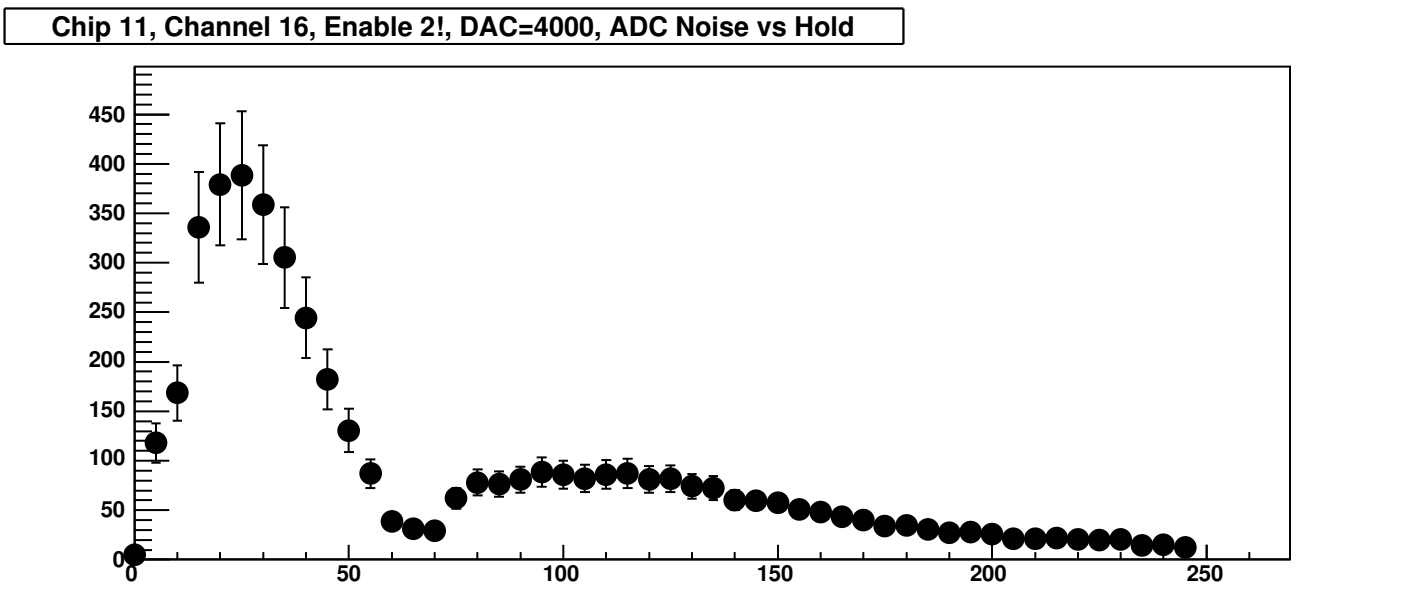

Chip 11, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1000
0
-1000
-2000
0
50
100
150
200
250

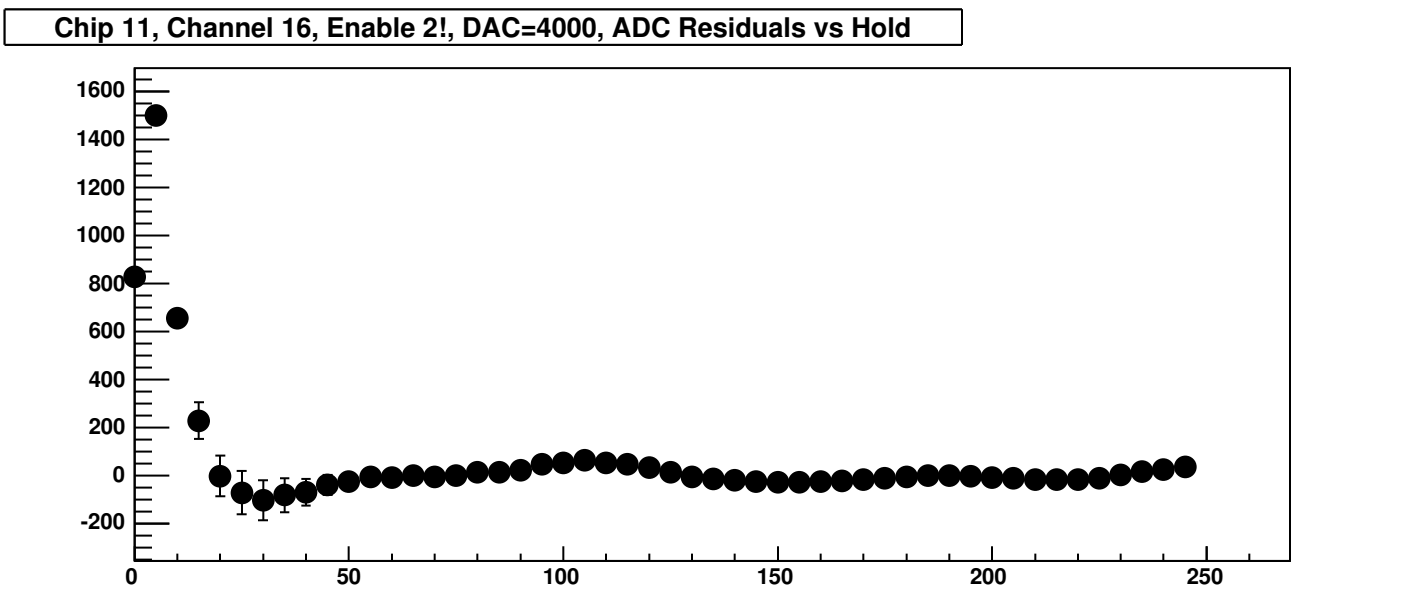

Chip 11, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 4332 / 41
p0 -439 ± 0.2052
p1 155.2 ± 1.209
p2 -31.17 ± 0.2067
p3 -14.88 ± 0.01691
p4 0.1702 ± 0.001963
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
50
100
150
200
250

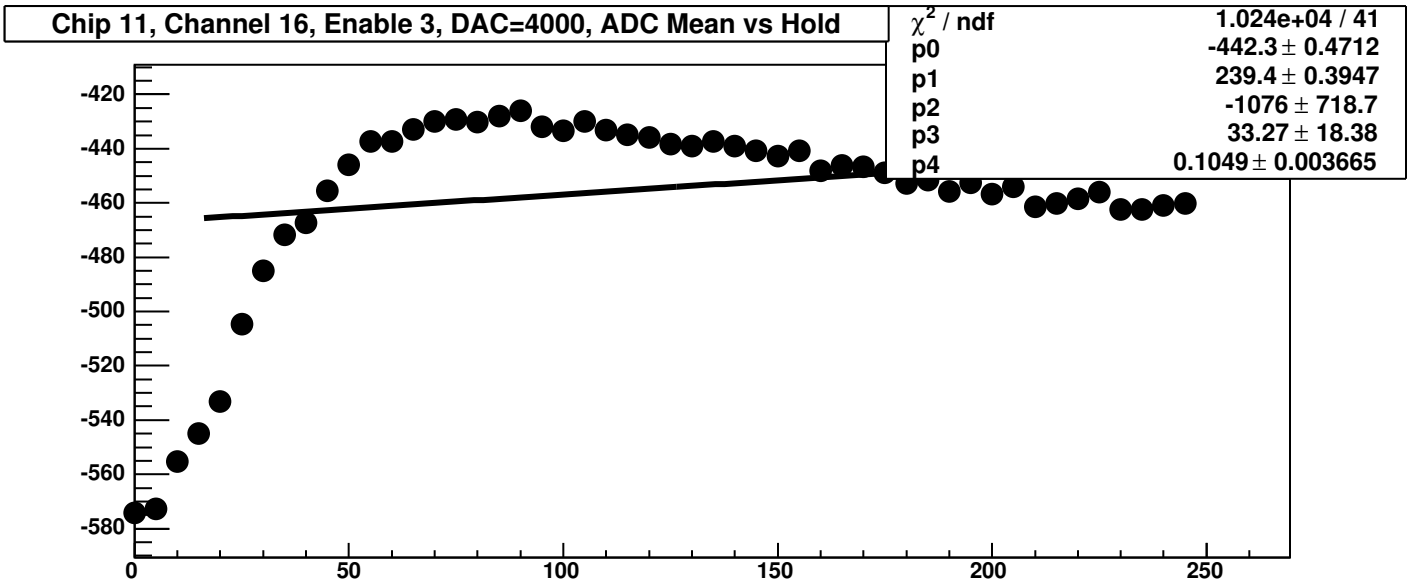

Chip 11, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

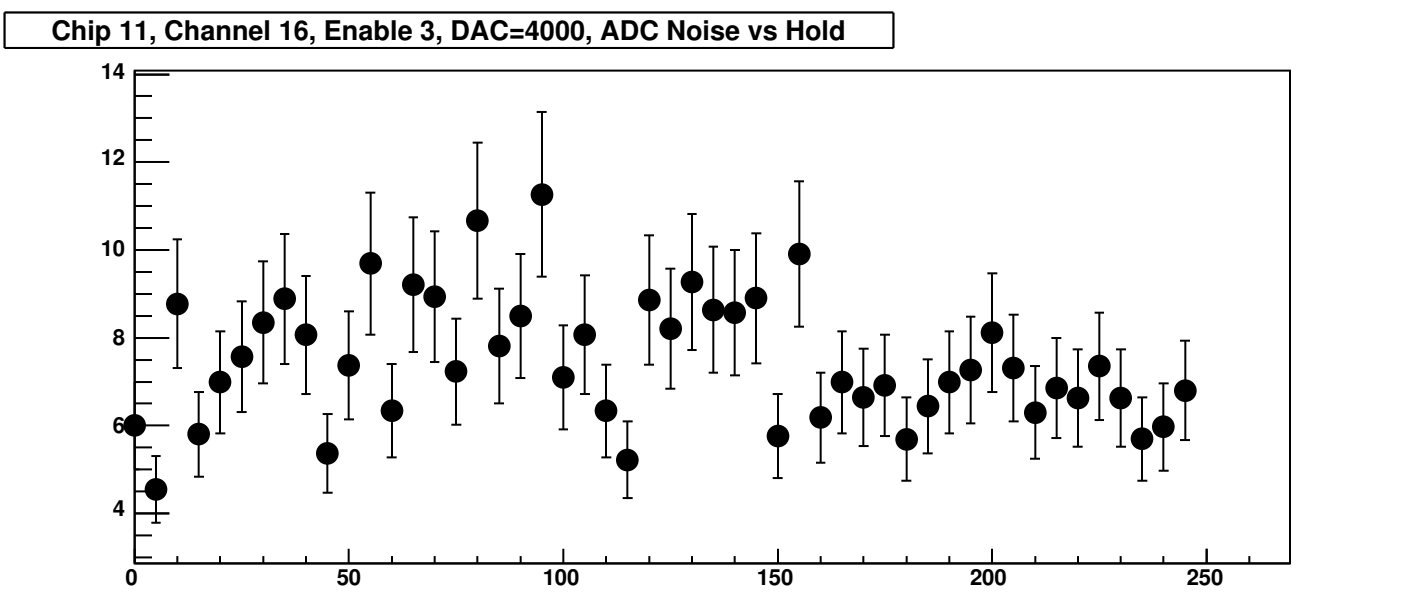

Chip 11, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
-80
-100
-120
0
50
100
150
200
250

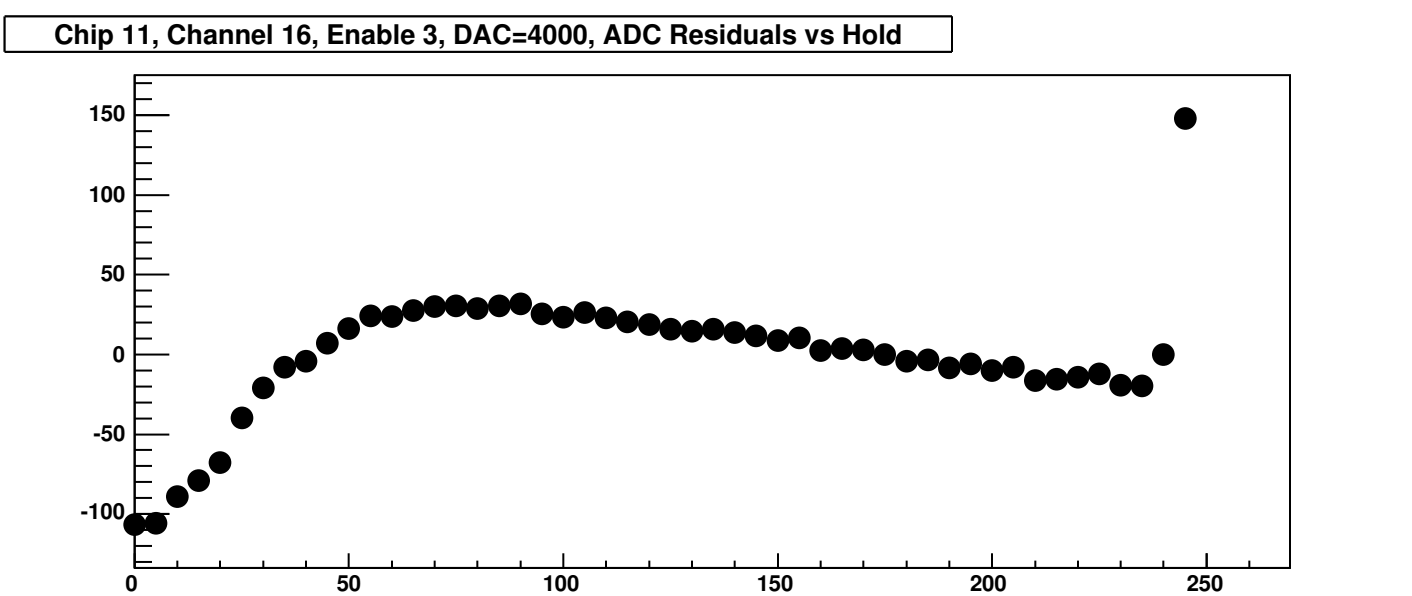

Chip 11, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 213.9 / 42
p0 -1406 ± 1.977
p1 4.416 ± 0.1351
p2 9477 ± 15.46
p3 30.13 ± 0.0486
8000
6000
4000
2000
0
-2000
0
50
100
150
200
250

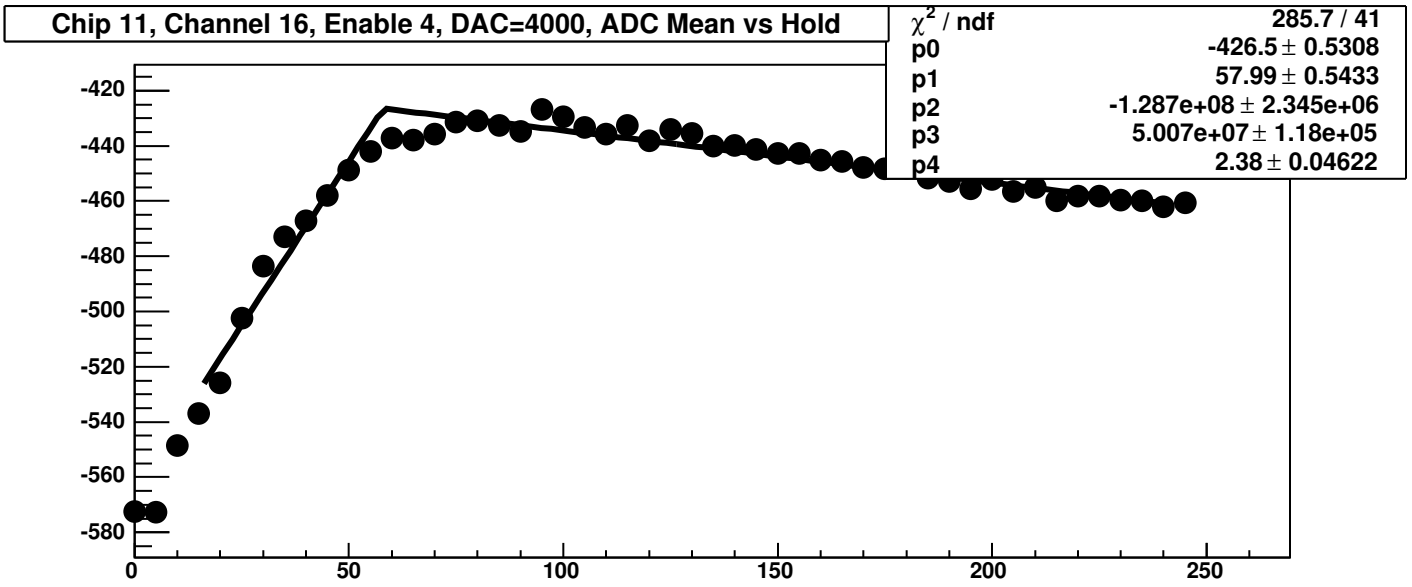

Chip 11, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold
450
400
350
300
250
200
150
100
50
0
0
50
100
150
200
250

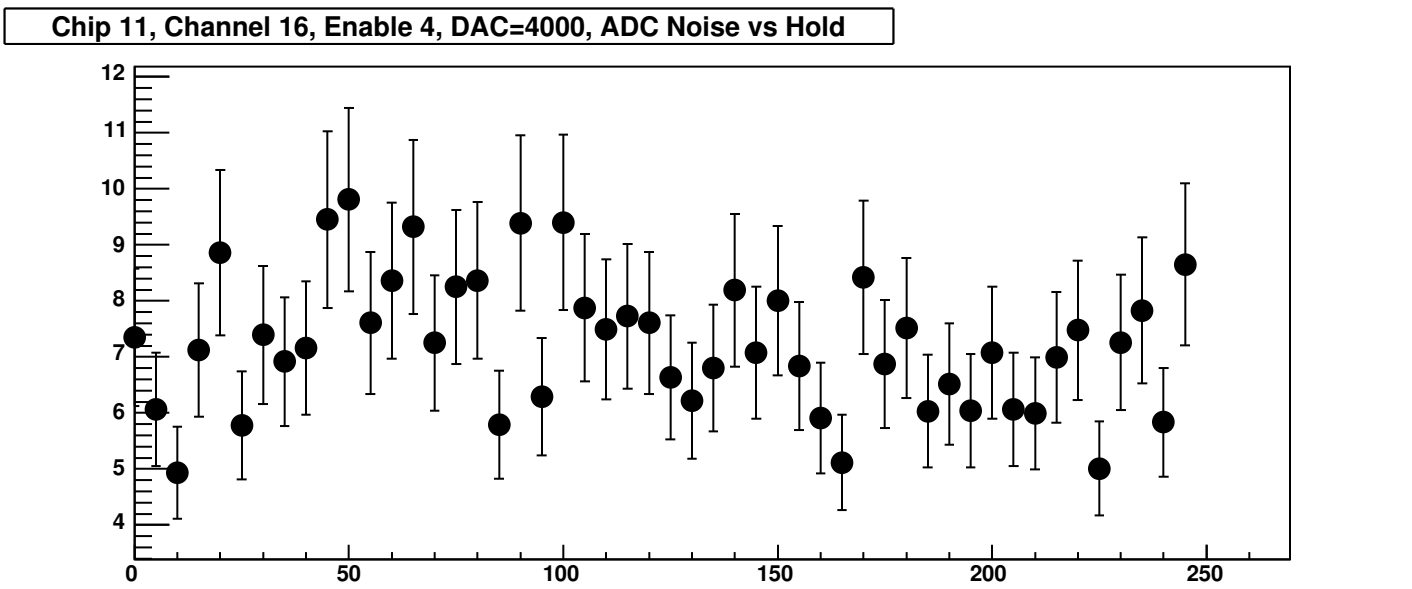

Chip 11, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

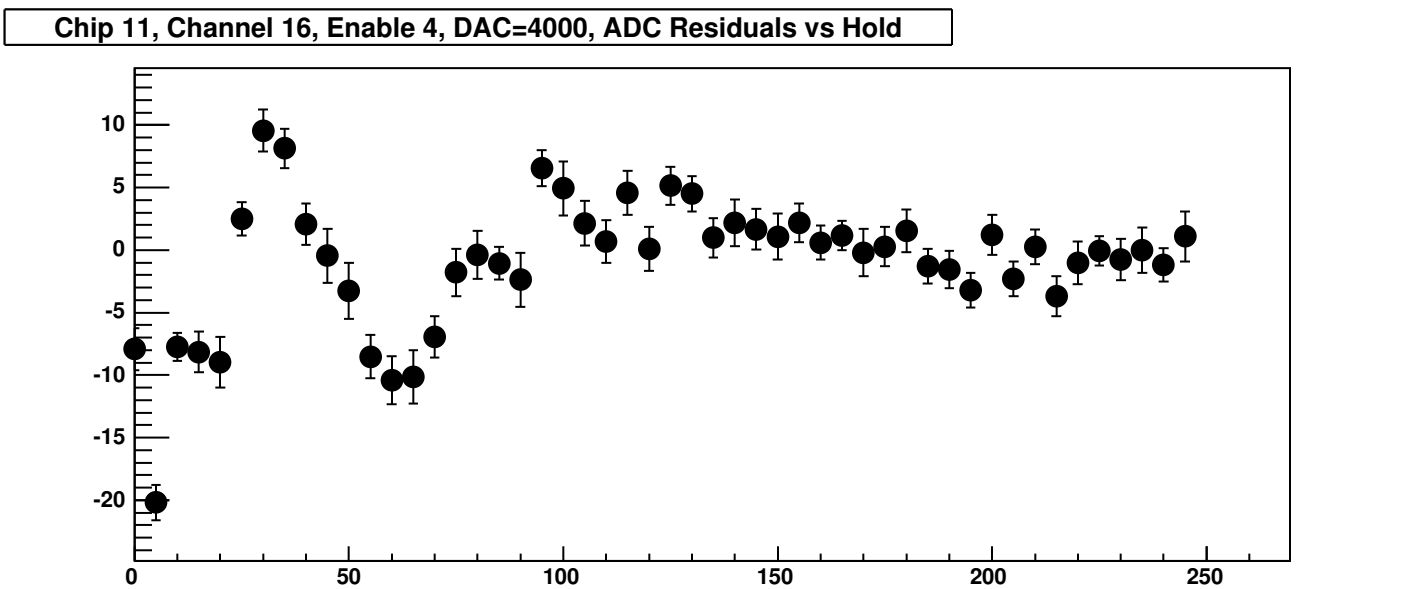

Chip 11, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.024e+04 / 41
p0 -442.3 ± 0.4712
p1 239.4 ± 0.3947
p2 -1076 ± 718.7
p3 33.27 ± 18.38
p4 0.1049 ± 0.003665
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
50
100
150
200
250

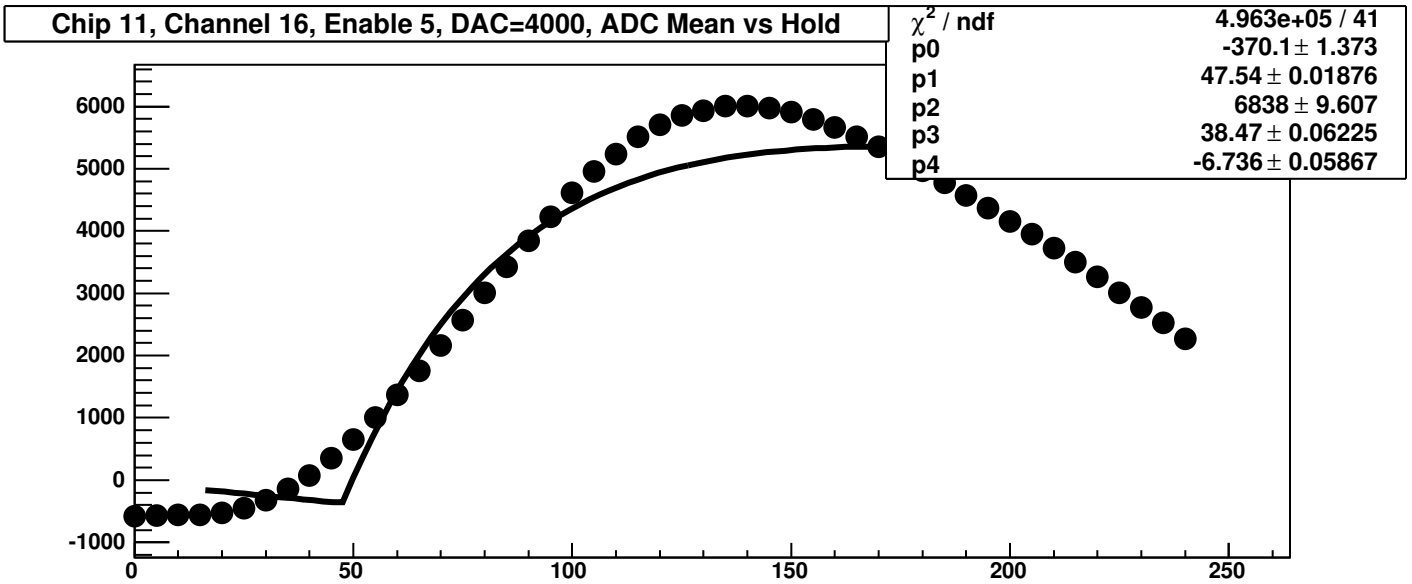

Chip 11, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

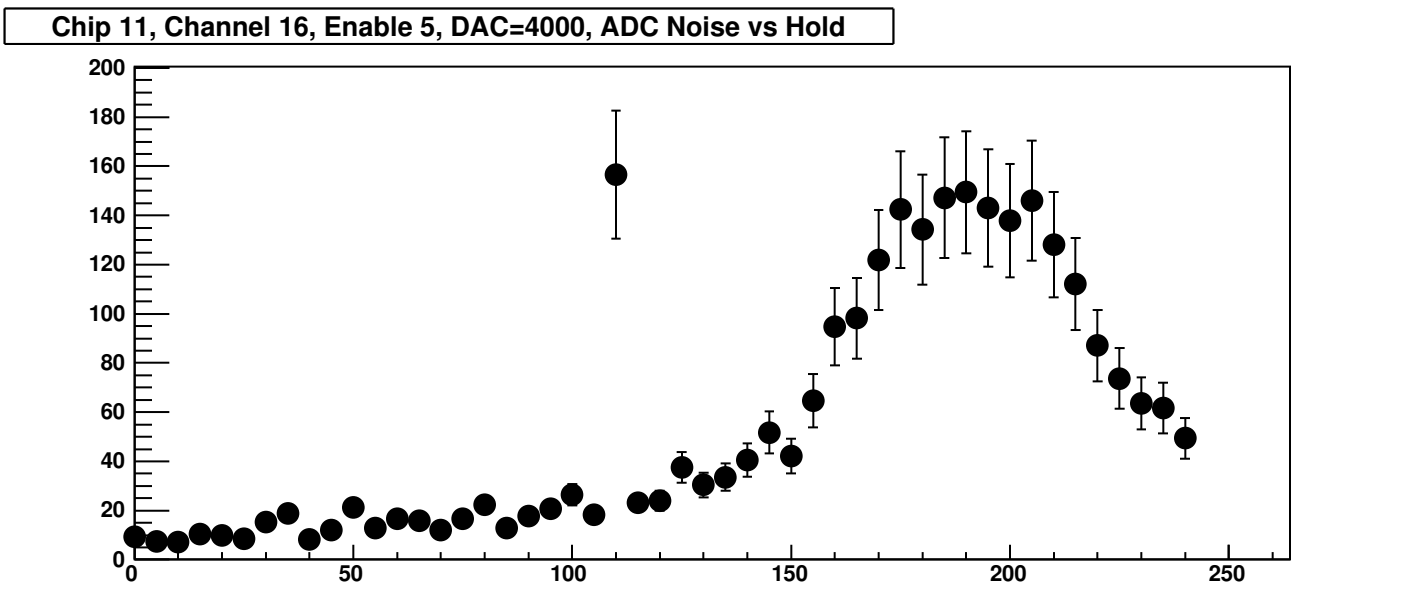

Chip 11, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold
150
100
50
0
-50
-100
0
50
100
150
200
250

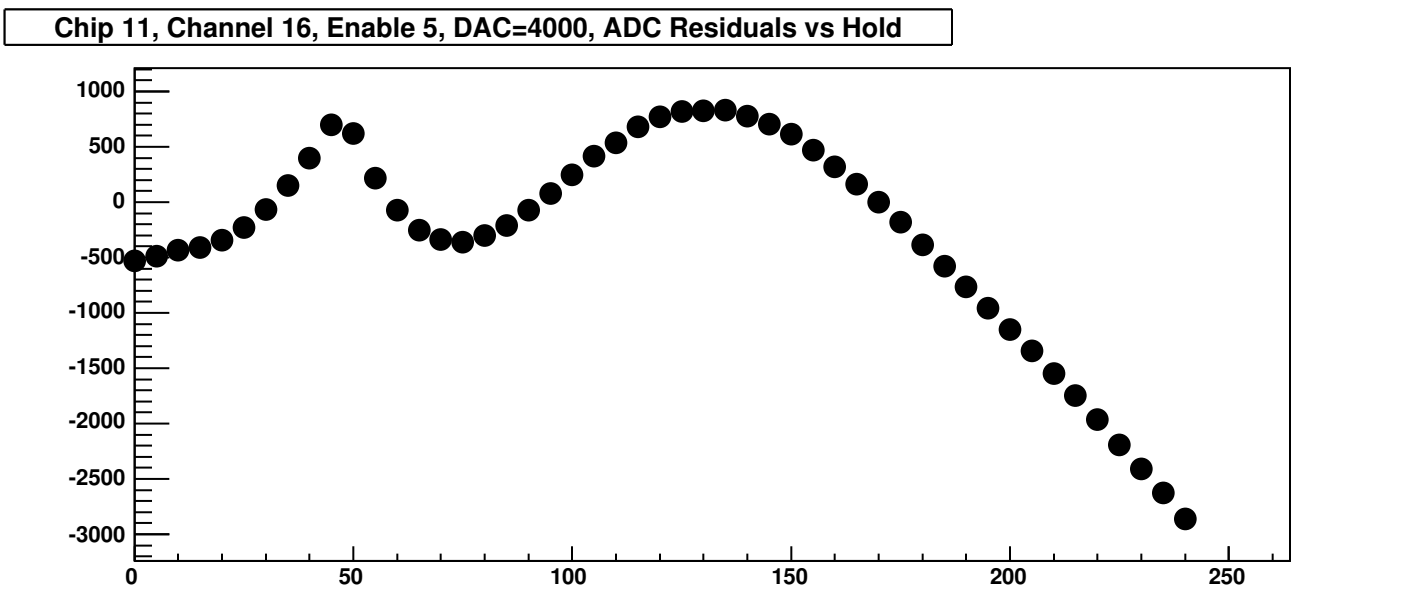

Chip 11, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 285.7 / 41
p0 -426.5 ± 0.5308
p1 57.99 ± 0.5433
p2 -1.287e+08 ± 2.345e+06
p3 5.007e+07 ± 1.18e+05
p4 2.38 ± 0.04622
-420
-440
-460
-480
-500
-520
-540
-560
-580
0
50
100
150
200
250

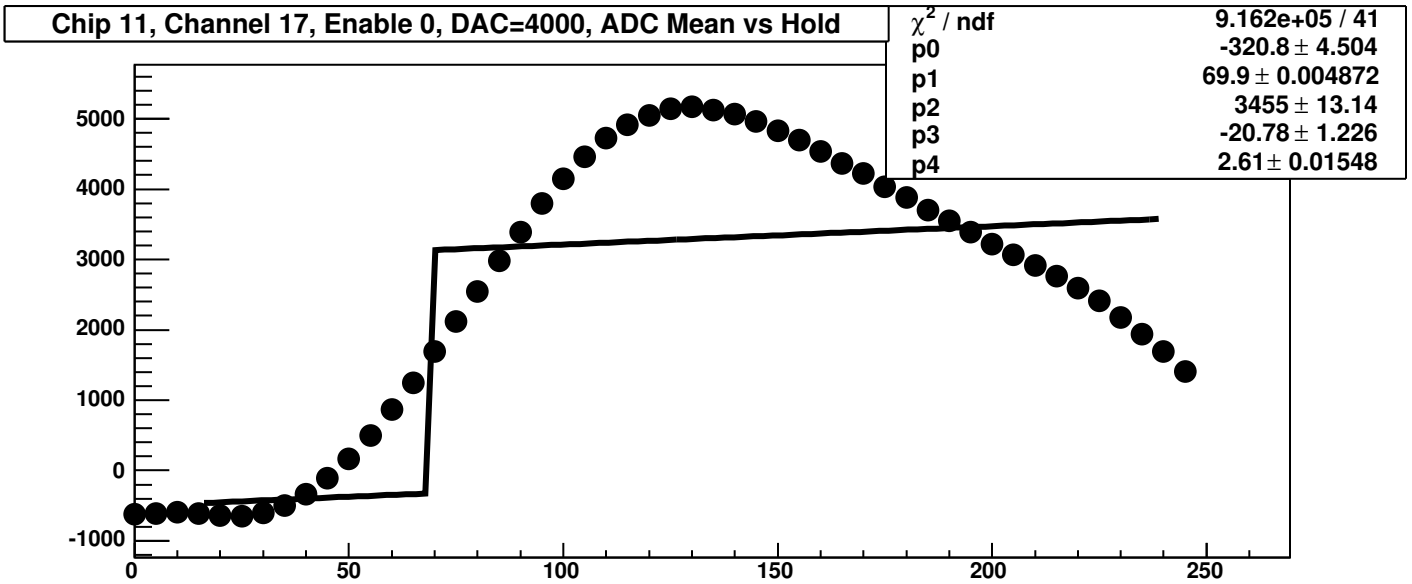

Chip 11, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

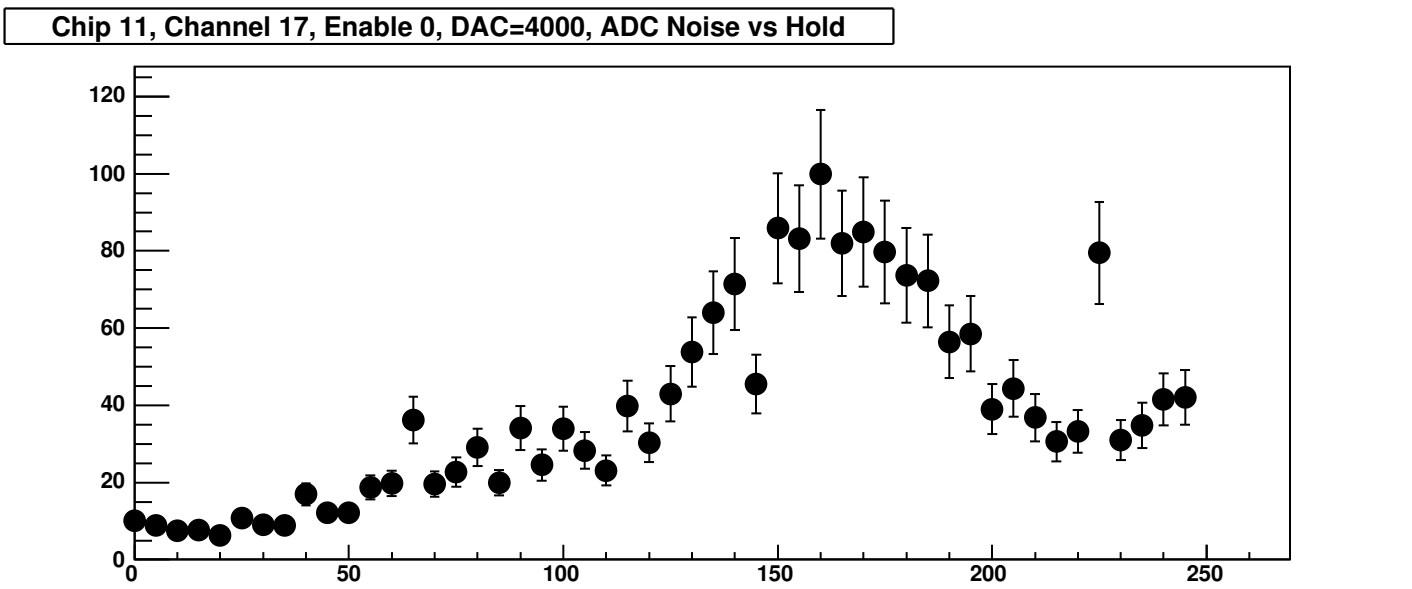

Chip 11, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
-20
0
50
100
150
200
250

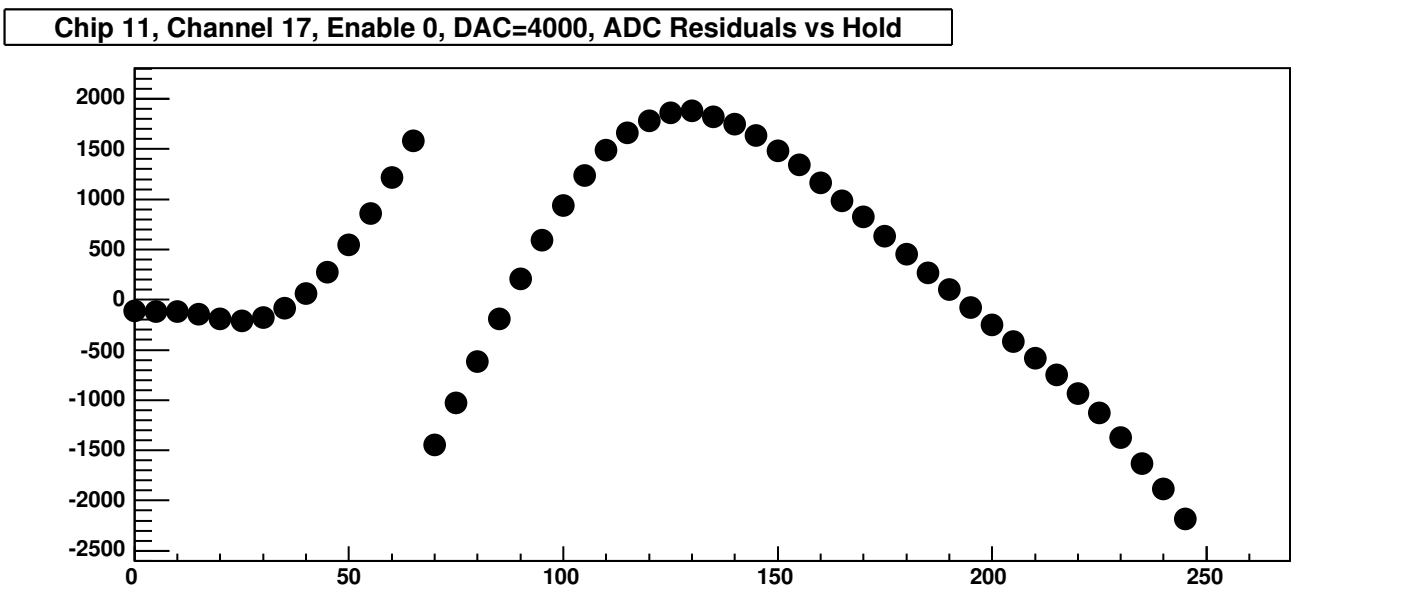

Chip 11, Channel 16, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 4.963e+05 / 41
p0 -370.1 ± 1.373
p1 47.54 ± 0.01876
p2 6838 ± 9.607
p3 38.47 ± 0.06225
p4 -6.736 ± 0.05867
6000
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

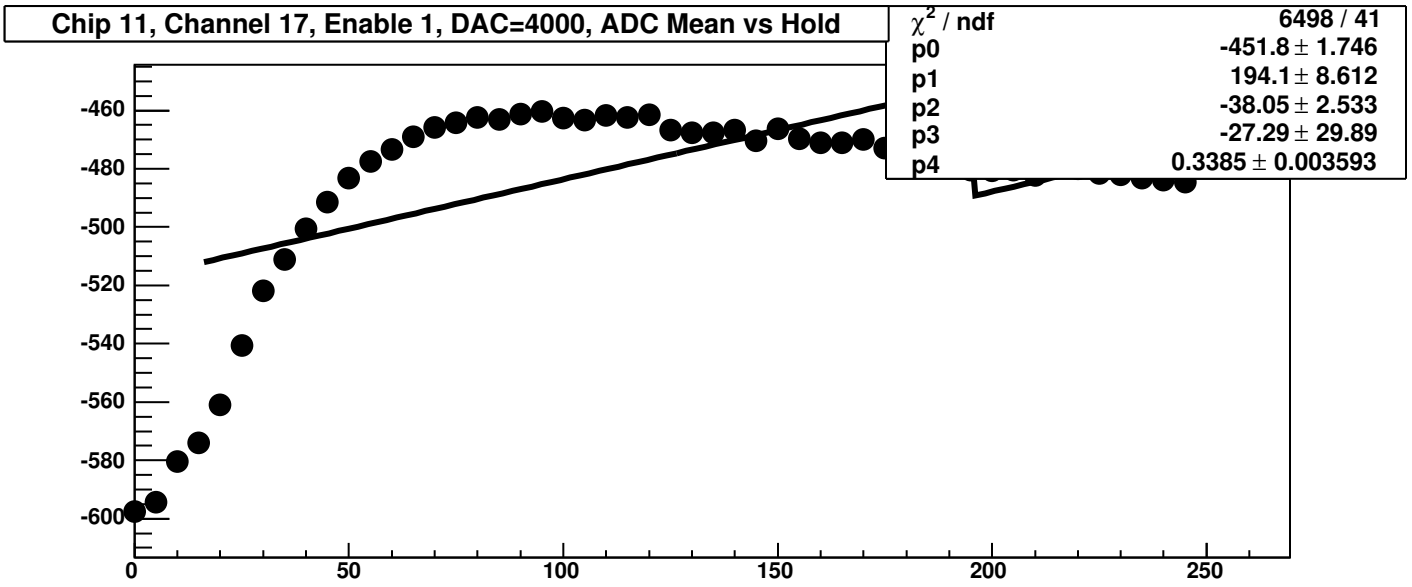

Chip 11, Channel 16, Enable 5, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

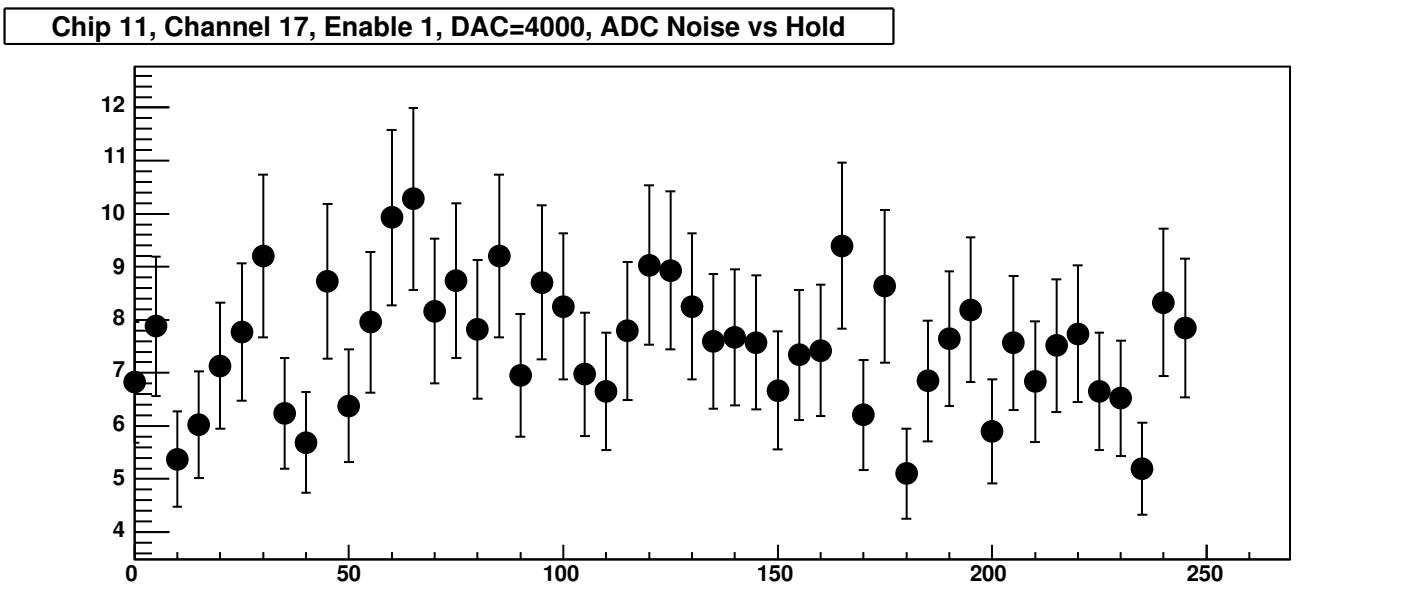

Chip 11, Channel 16, Enable 5, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
50
100
150
200
250

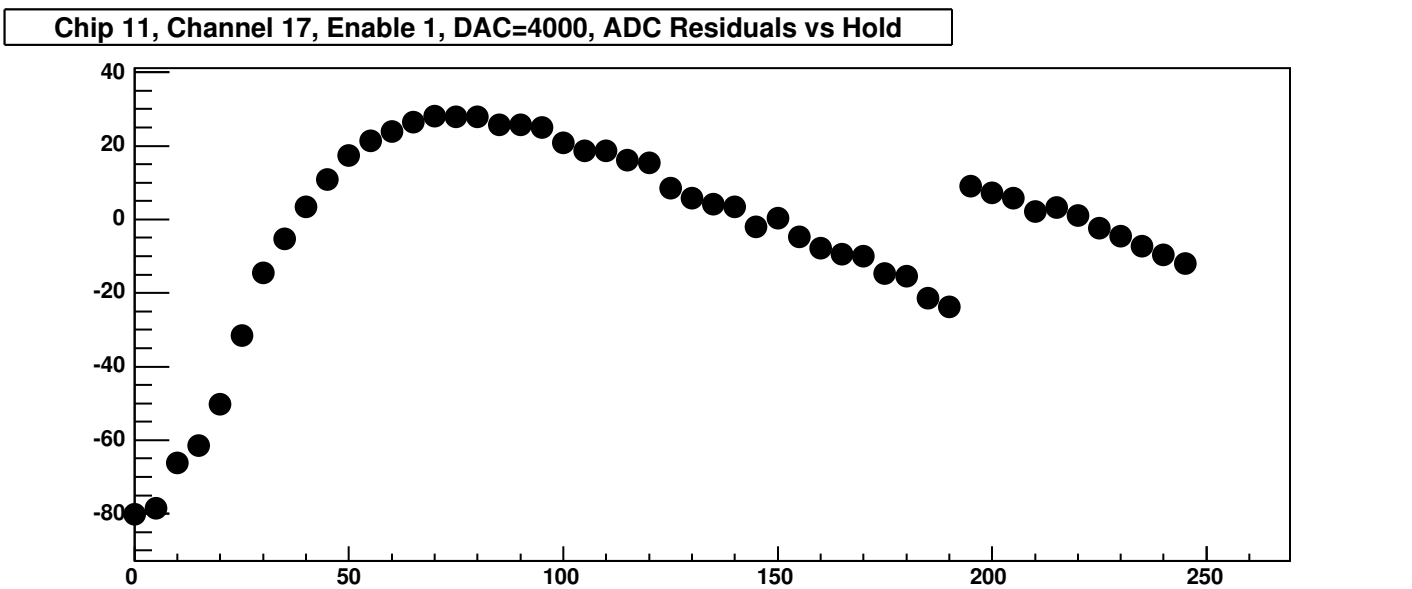

Chip 11, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 9.162e+05 / 41
p0 -320.8 ± 4.504
p1 69.9 ± 0.004872
p2 3455 ± 13.14
p3 -20.78 ± 1.226
p4 2.61 ± 0.01548
5000
4000
3000
2000
1000
0
-1000
0
50
100
150
200
250

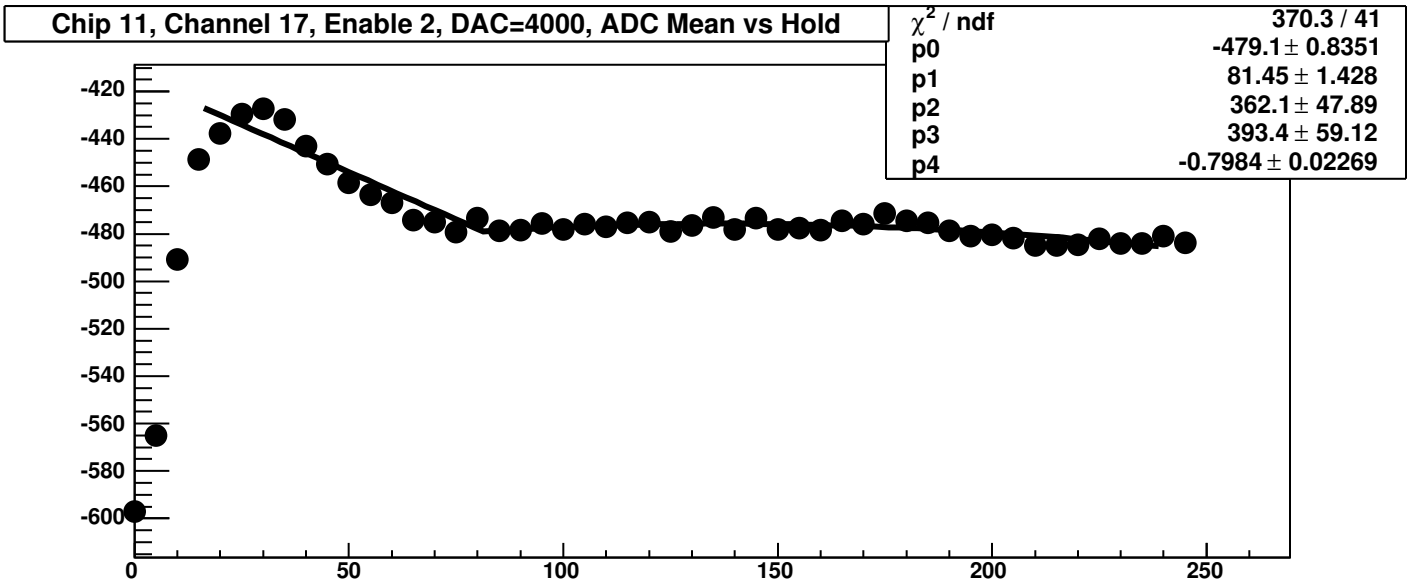

Chip 11, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250

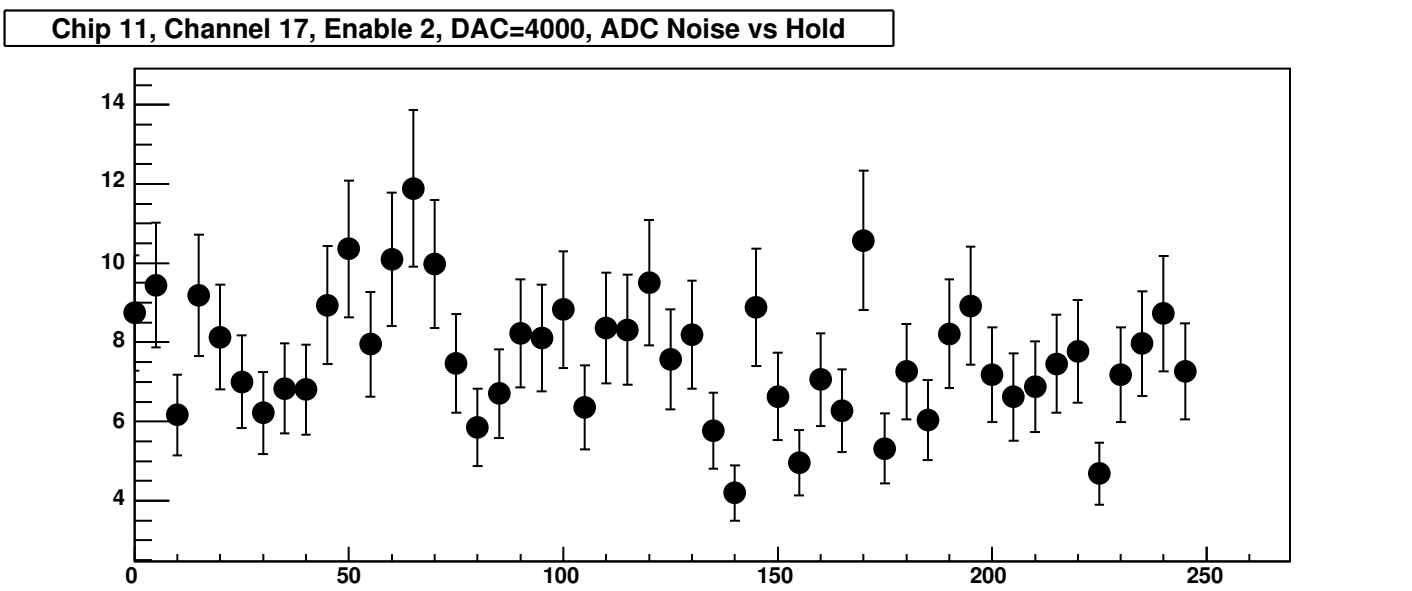

Chip 11, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
-500
-1000
-1500
-2000
-2500
0
50
100
150
200
250

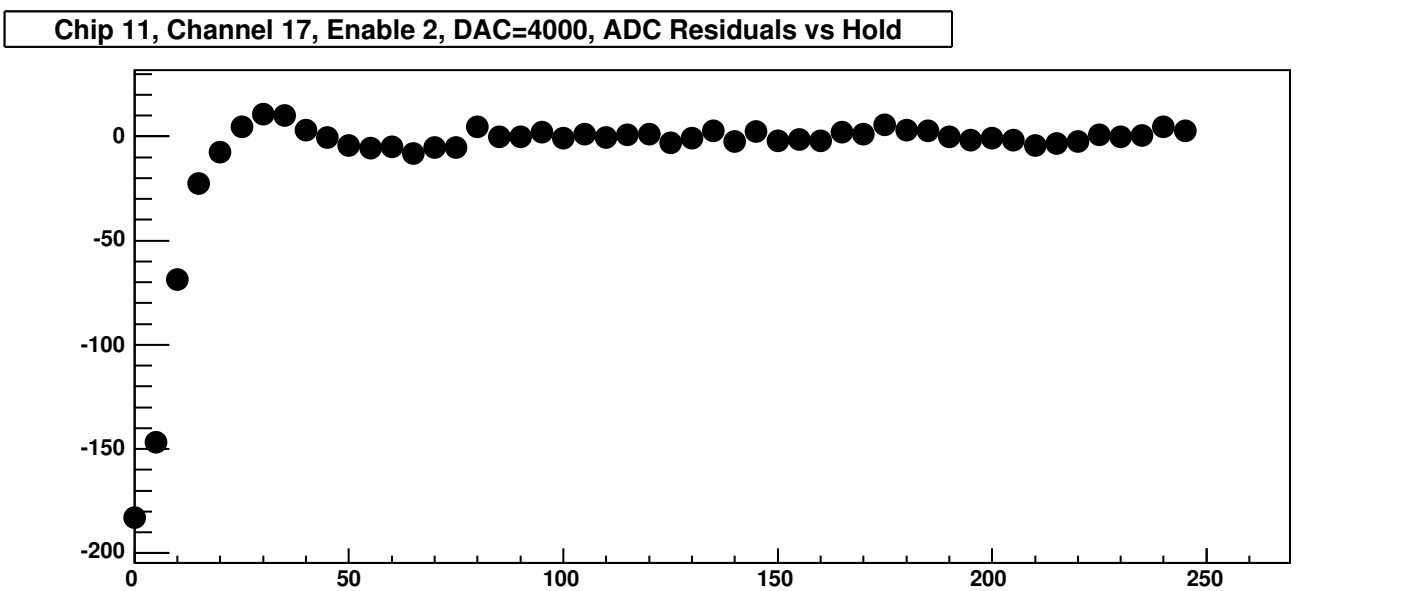

Chip 11, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 6498 / 41
p0 -451.8 ± 1.746
p1 194.1 ± 8.612
p2 -38.05 ± 2.533
p3 -27.29 ± 29.89
p4 0.3385 ± 0.003593
-460
-480
-500
-520
-540
-560
-580
-600
0
50
100
150
200
250

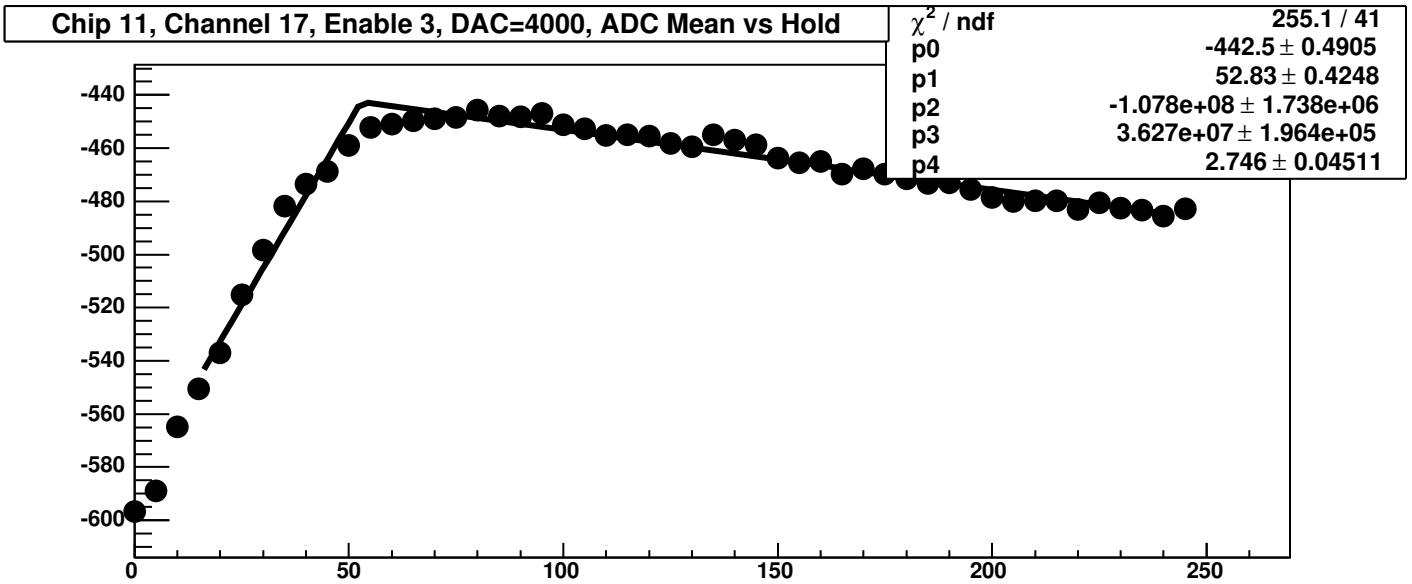

Chip 11, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

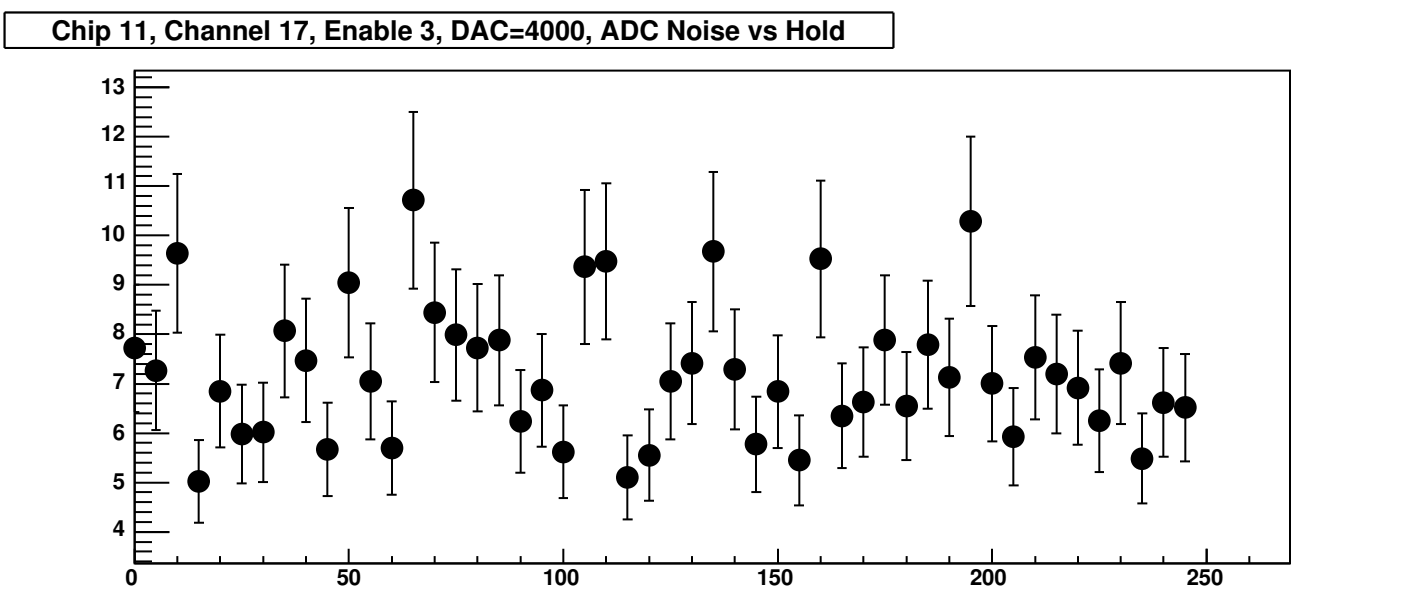

Chip 11, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
0
50
100
150
200
250

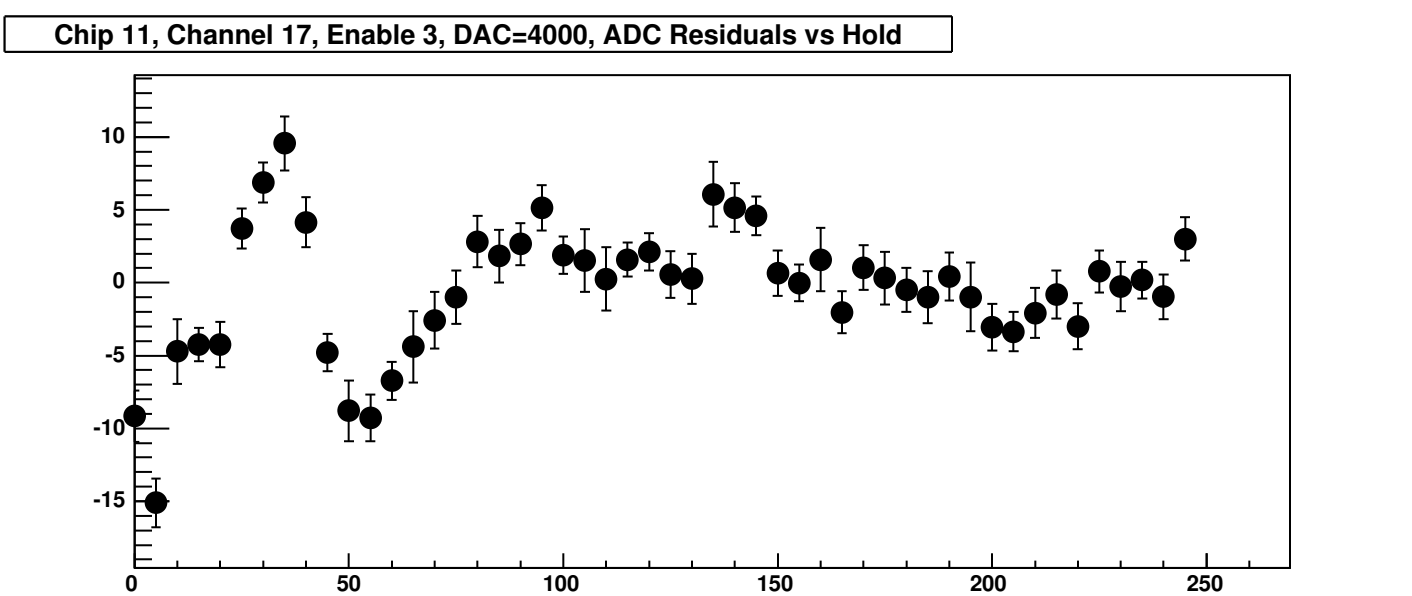

Chip 11, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 370.3 / 41
p0 -479.1 ± 0.8351
p1 81.45 ± 1.428
p2 362.1 ± 47.89
p3 393.4 ± 59.12
p4 -0.7984 ± 0.02269
-420
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
50
100
150
200
250


Chip 11, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

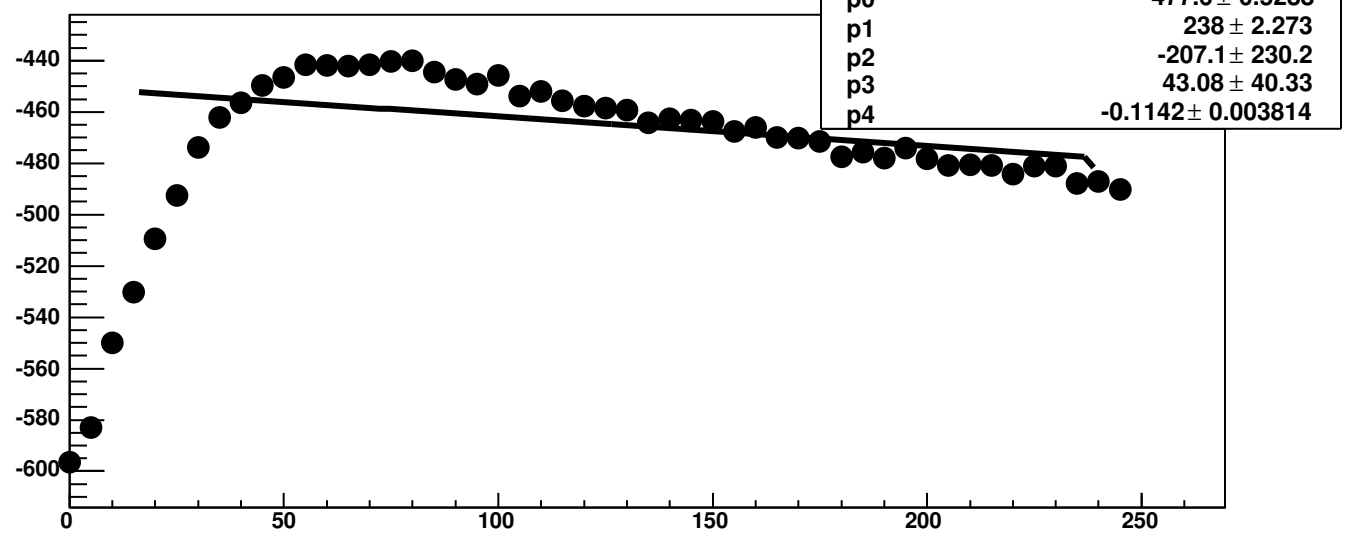

Chip 11, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold
0
-50
-100
-150
-200
0
50
100
150
200
250


Chip 11, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 255.1 / 41
p0 -442.5 ± 0.4905
p1 52.83 ± 0.4248
p2 -1.078e+08 ± 1.738e+06
p3 3.627e+07 ± 1.964e+05
p4 2.746 ± 0.04511
-440
-460
-480
-500
-520
-540
-560
-580
-600
0
50
100
150
200
250

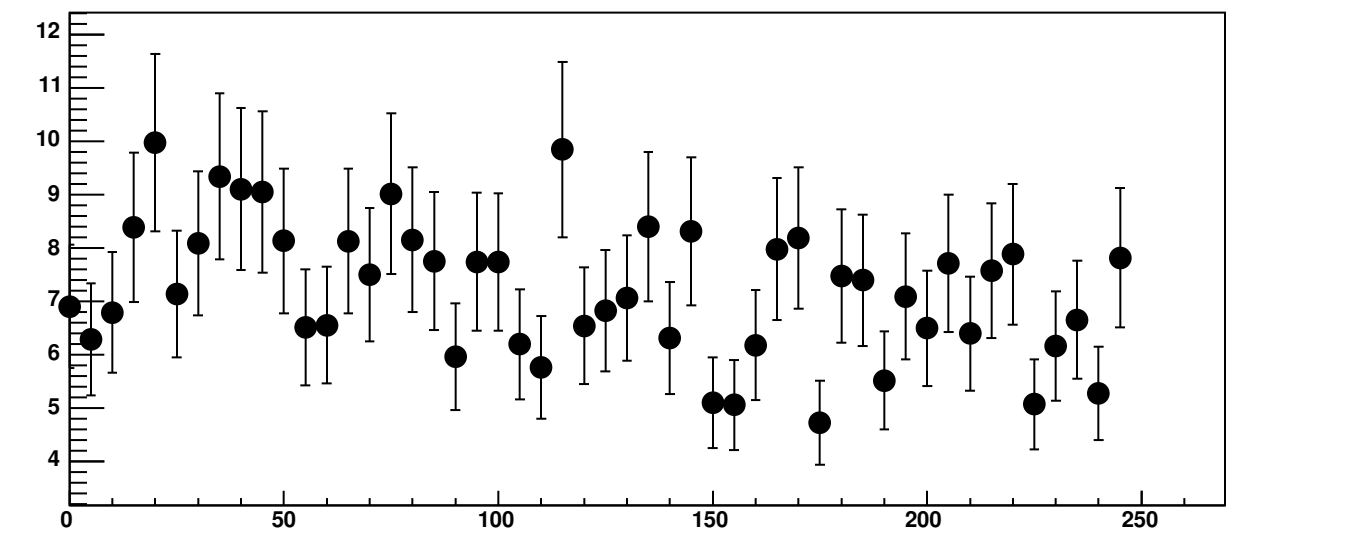
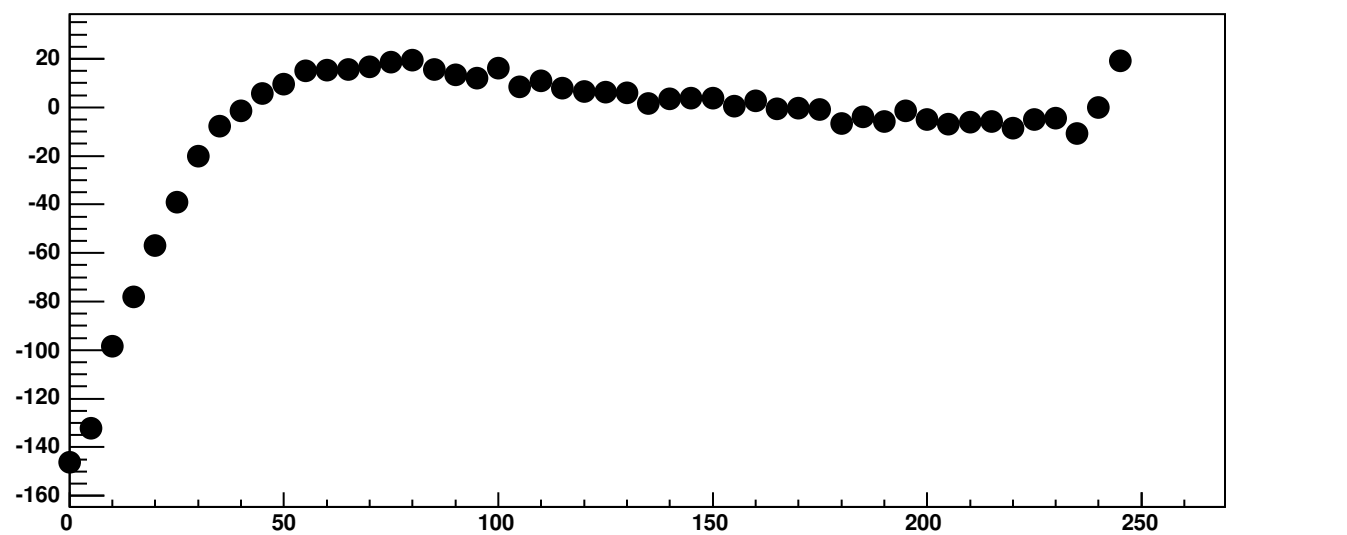

Chip 11, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 11, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
0
50
100
150
200
250

Chip 11, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



Chip 11, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 11, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold