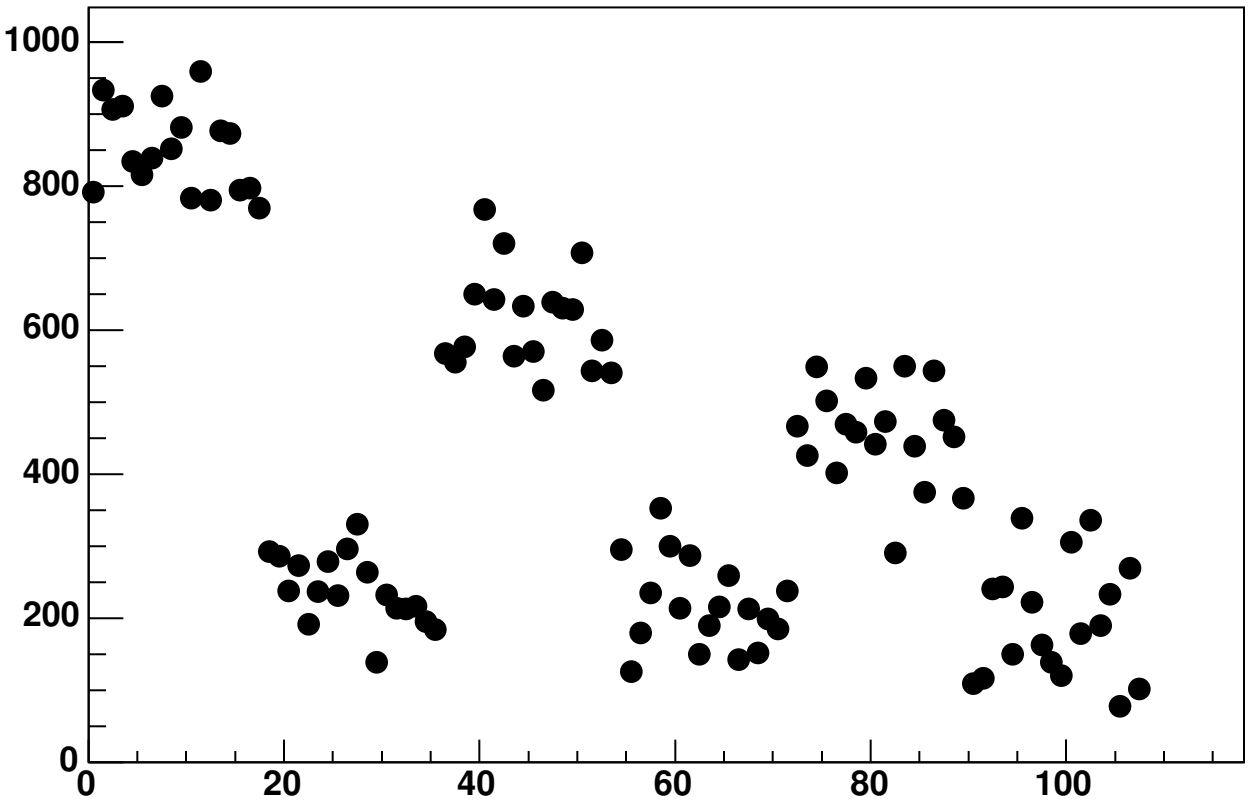
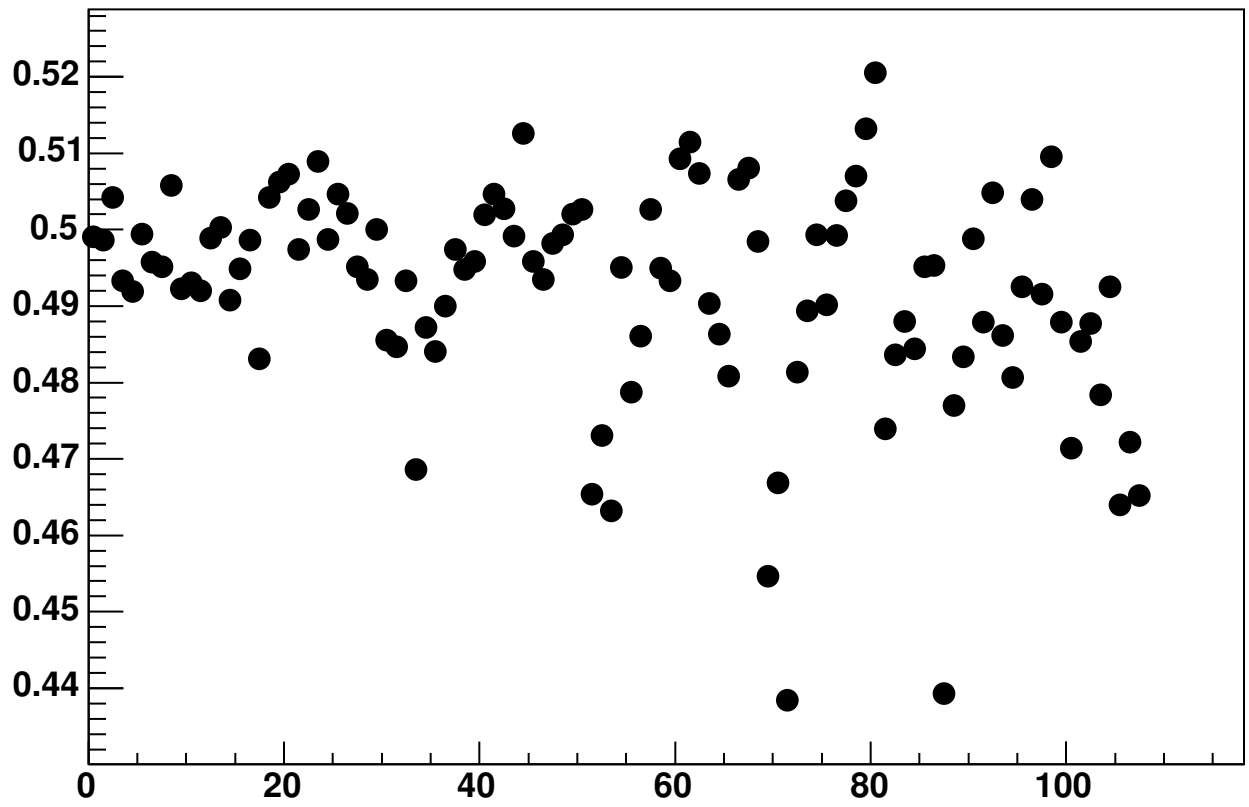
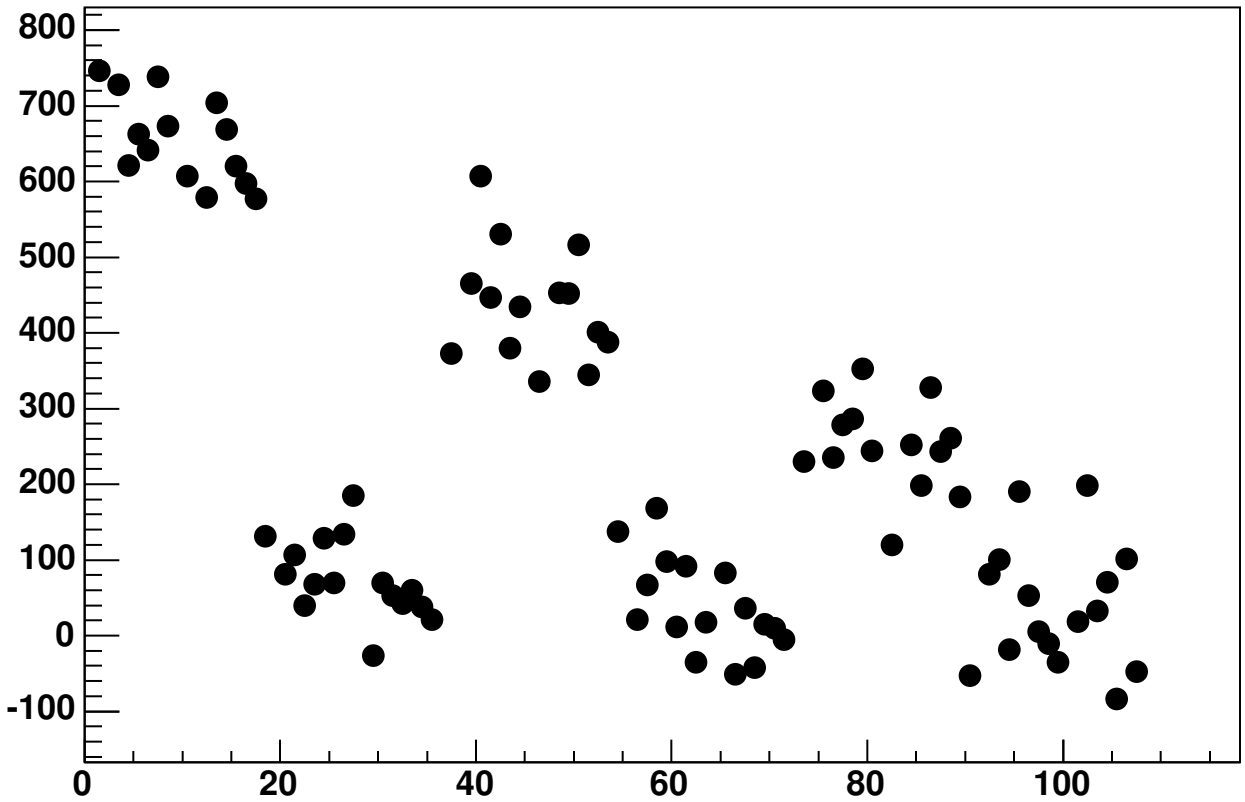
Channel Enabled, Hold=35, Fit Intercept vs 18*Chip+Chan



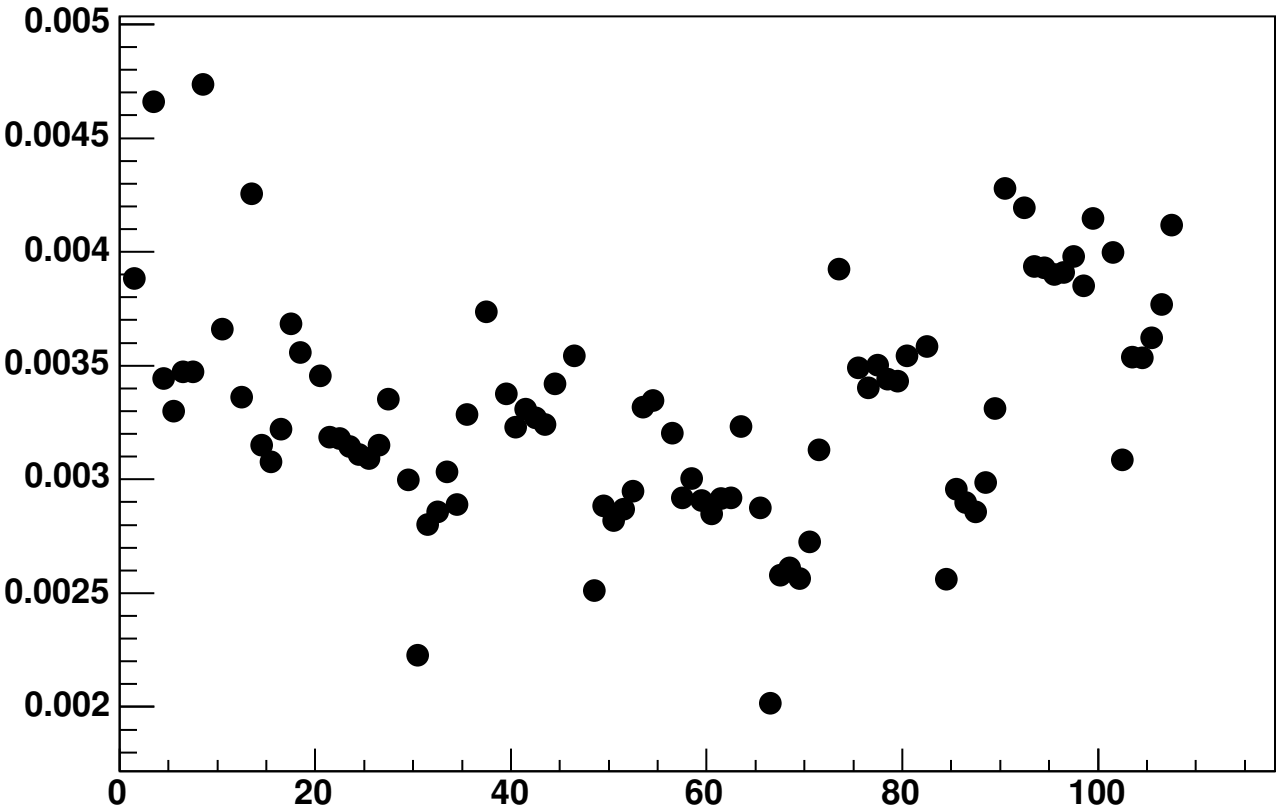
Channel Enabled, Hold=35, Fit Slope vs 18*Chip+Chan



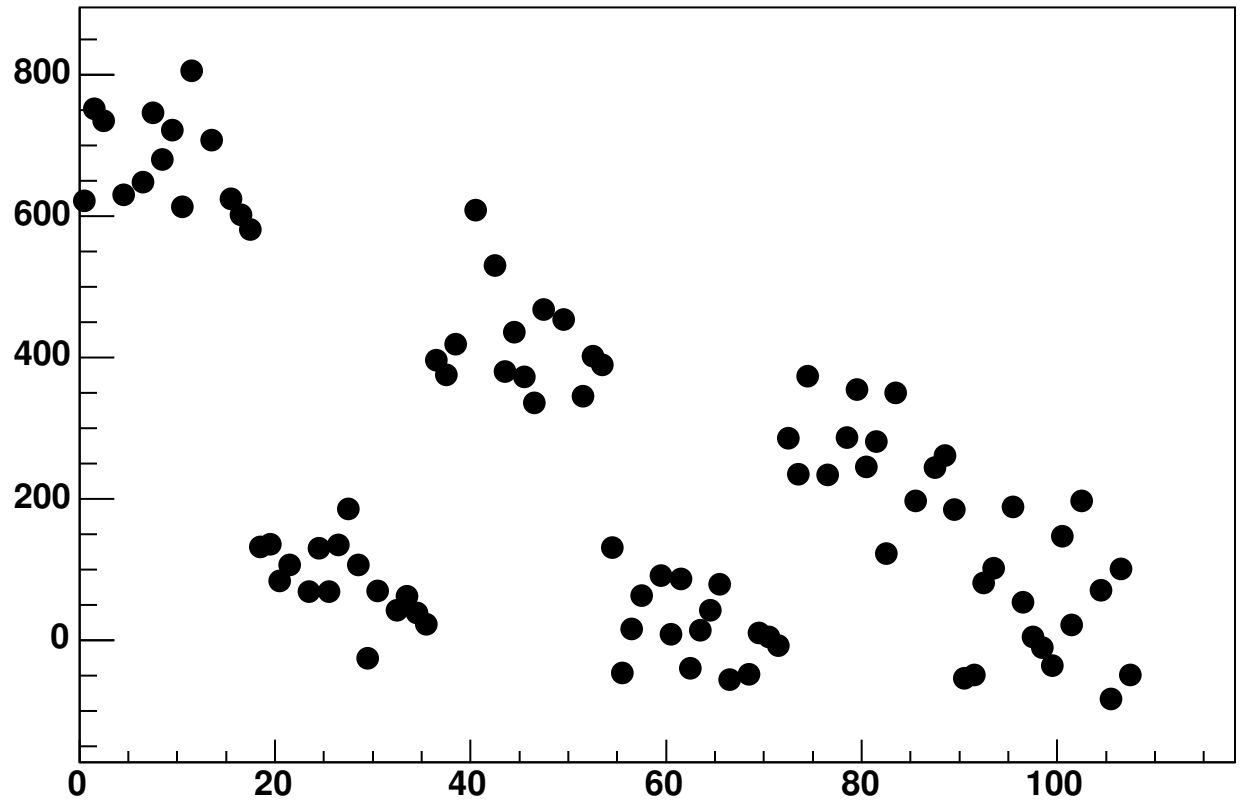
Disabled 0, Hold=35, Fit Intercept vs 18*Chip+Chan



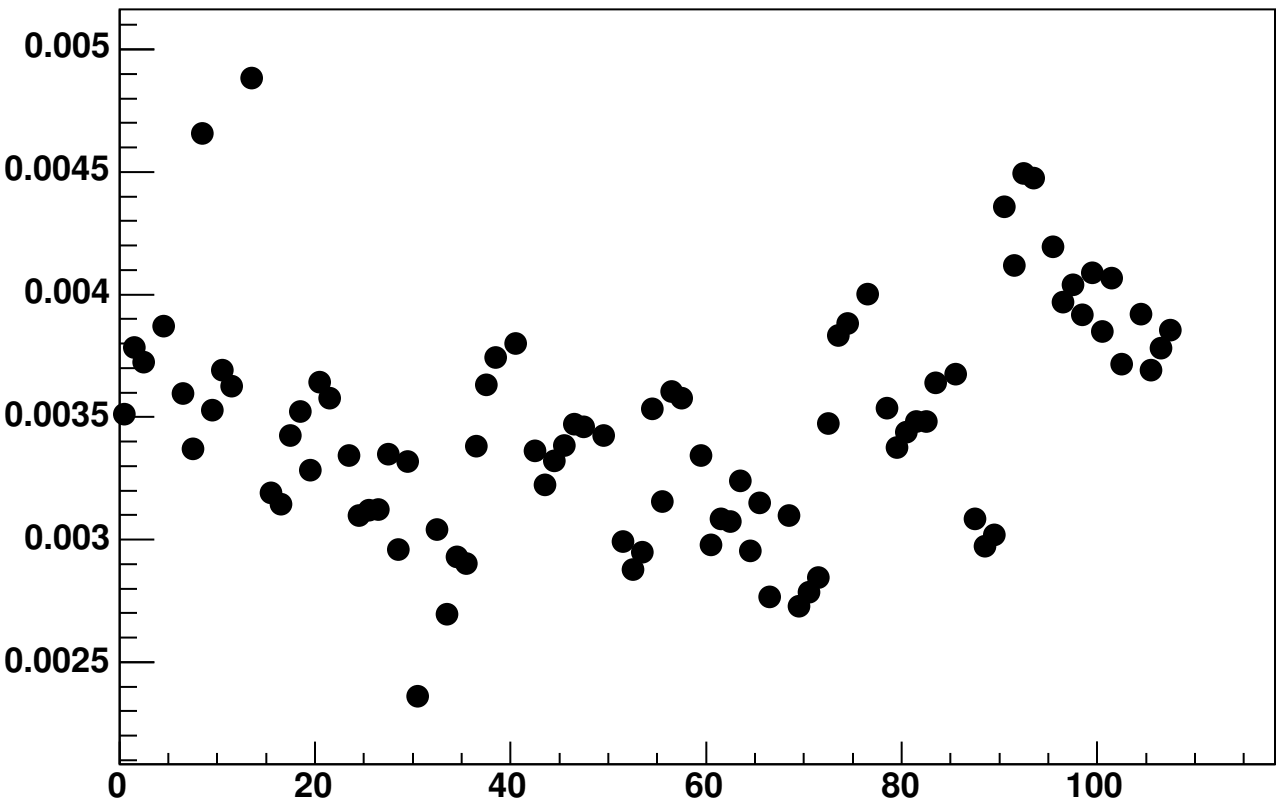
Disabled 0, Hold=35, Fit Slope vs 18*Chip+Chan



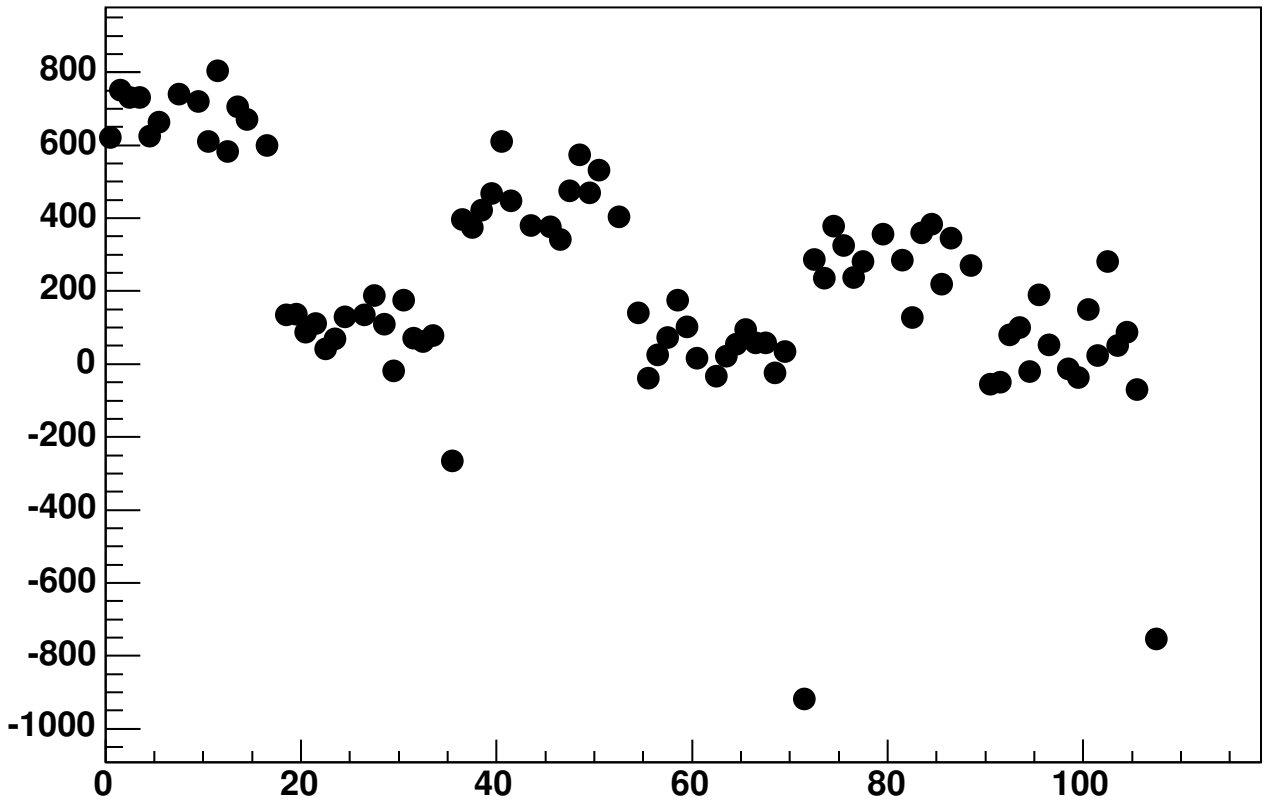
Disabled 1, Hold=35, Fit Intercept vs 18*Chip+Chan



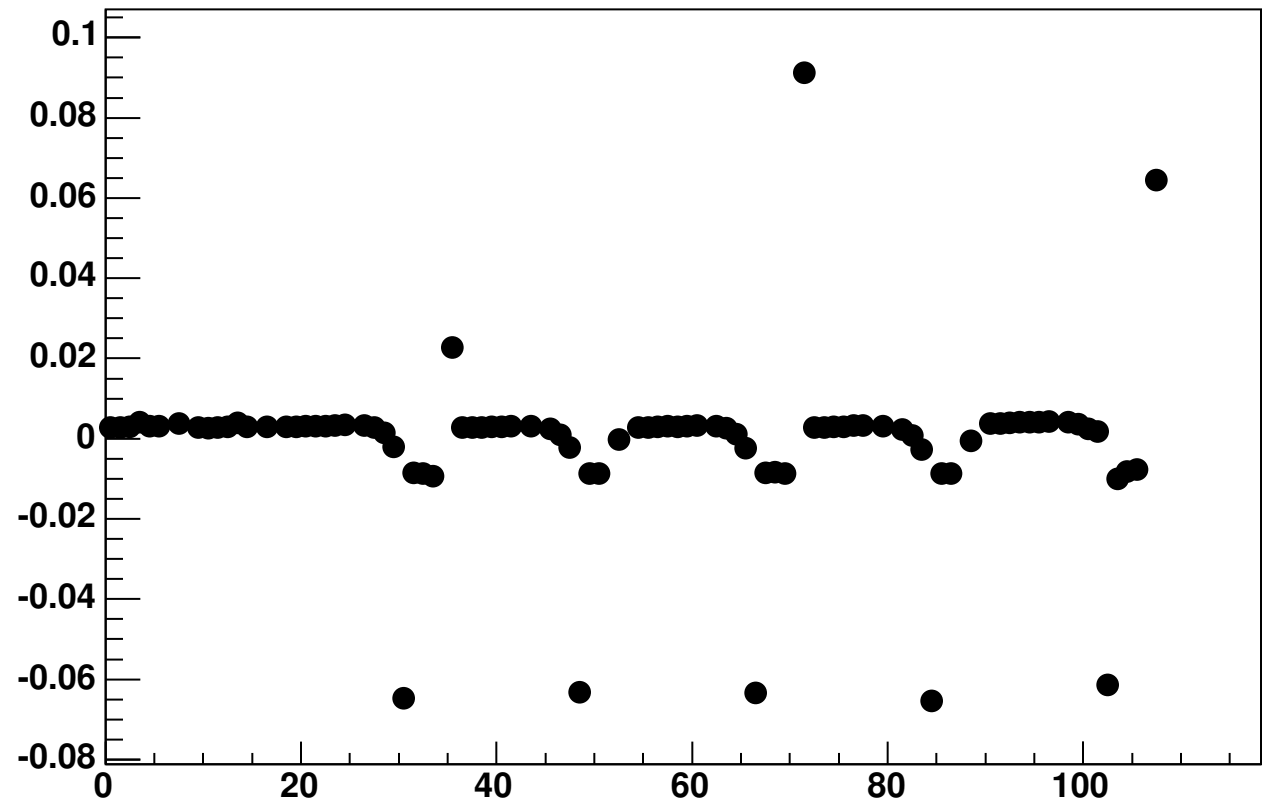
Disabled 1, Hold=35, Fit Slope vs 18*Chip+Chan



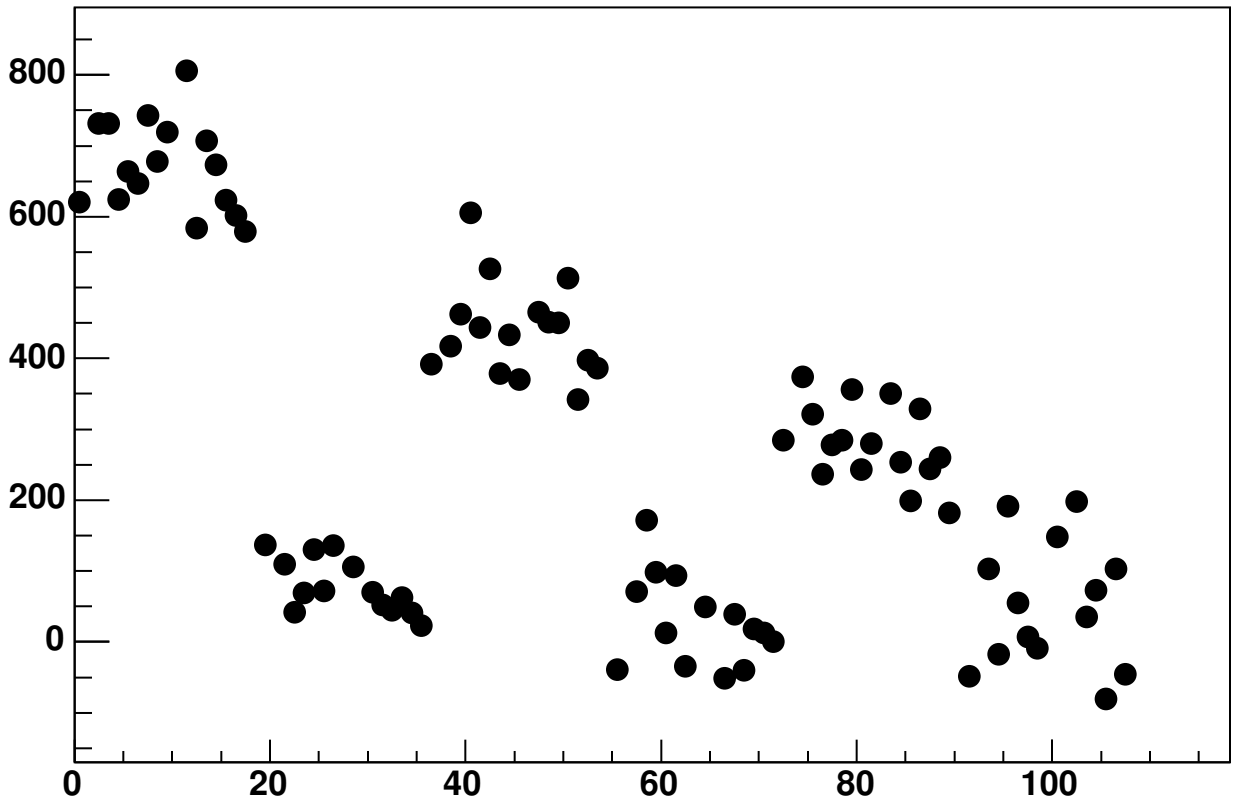
Disabled 2, Hold=35, Fit Intercept vs 18*Chip+Chan



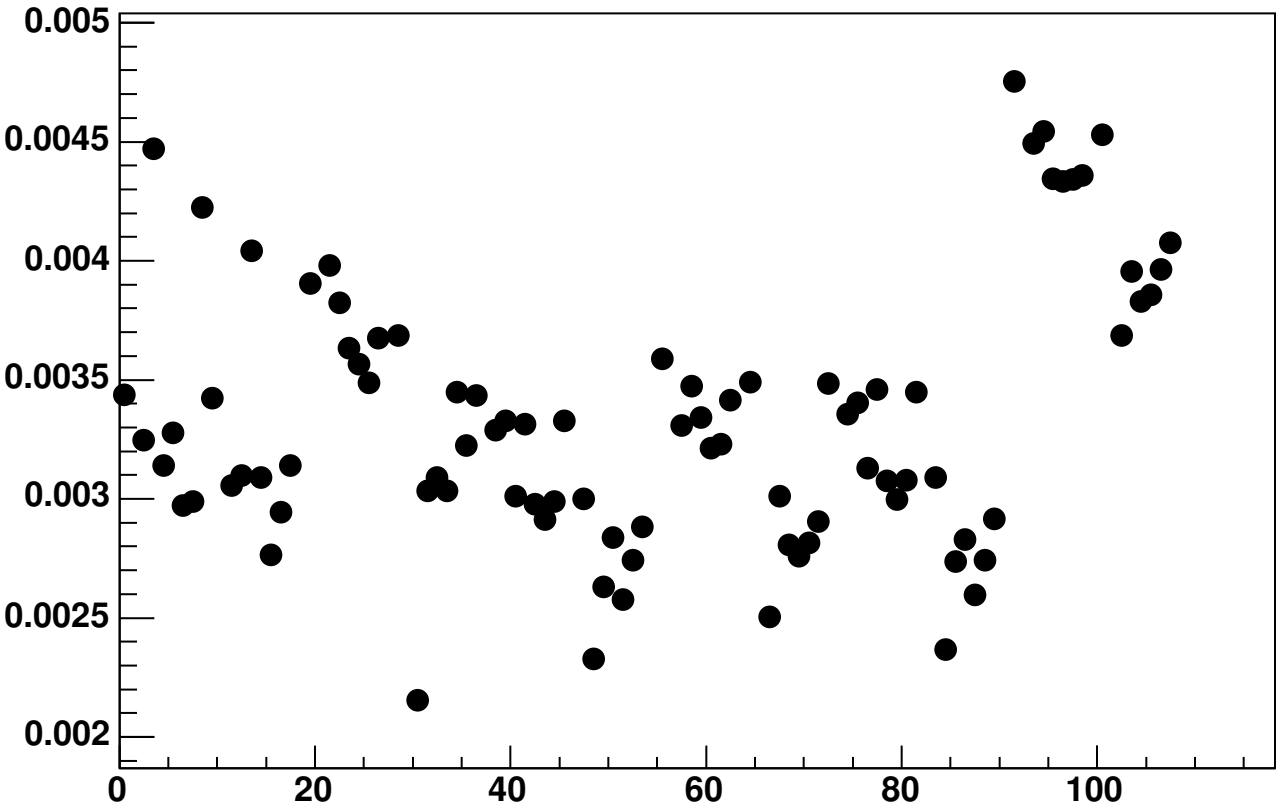
Disabled 2, Hold=35, Fit Slope vs 18*Chip+Chan



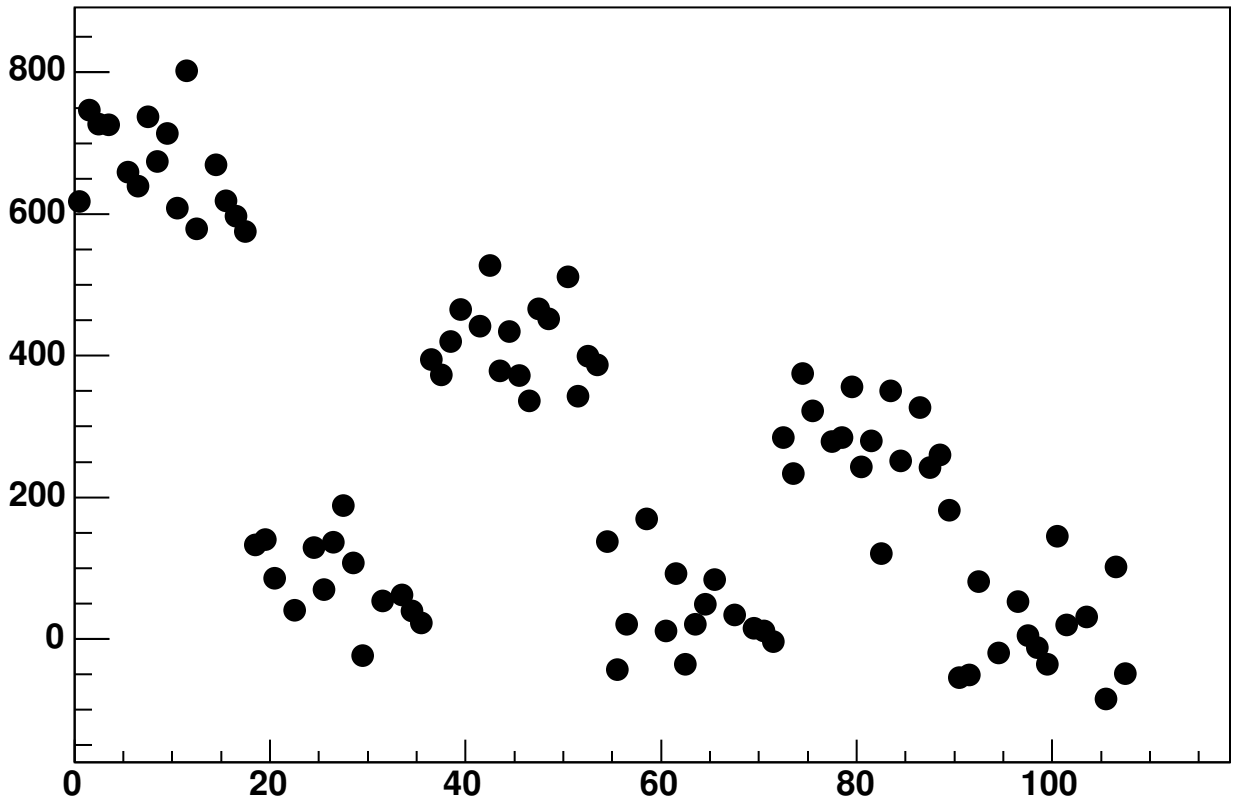
Disabled 3, Hold=35, Fit Intercept vs 18*Chip+Chan



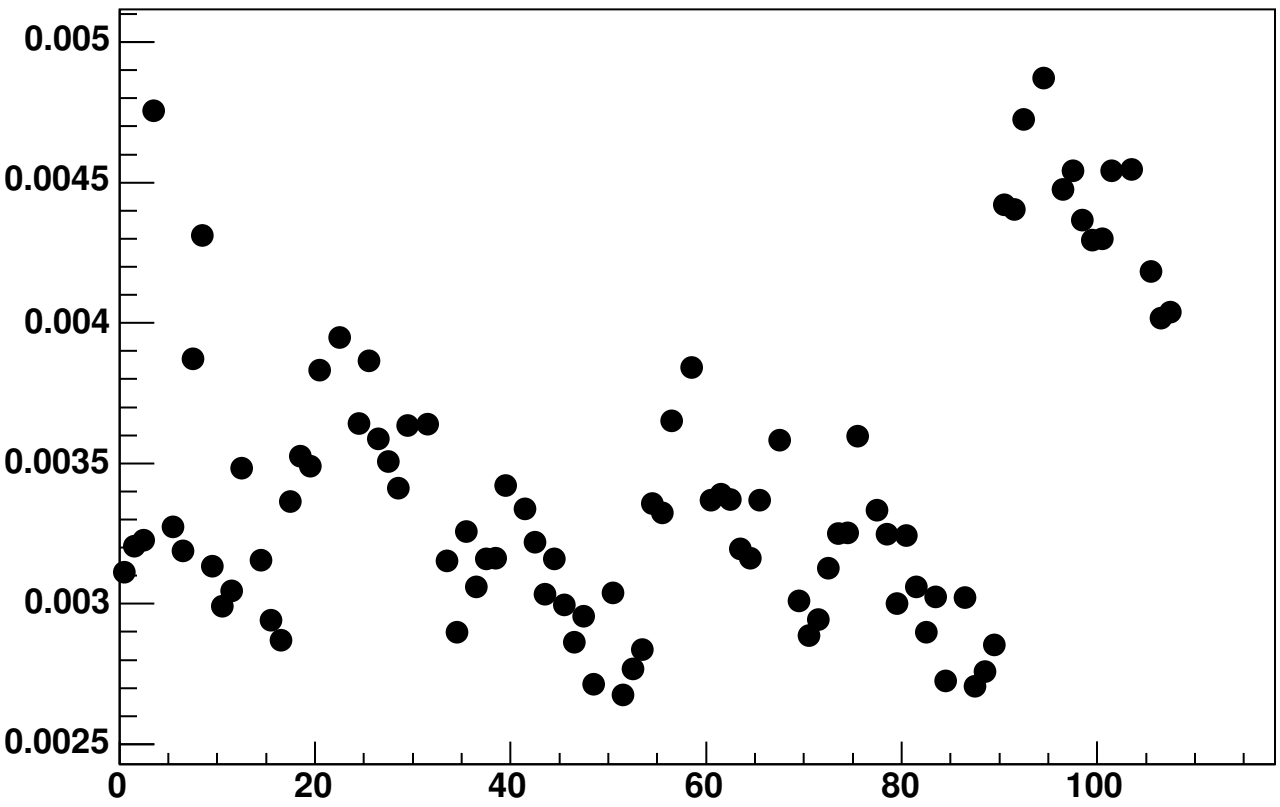
Disabled 3, Hold=35, Fit Slope vs 18*Chip+Chan



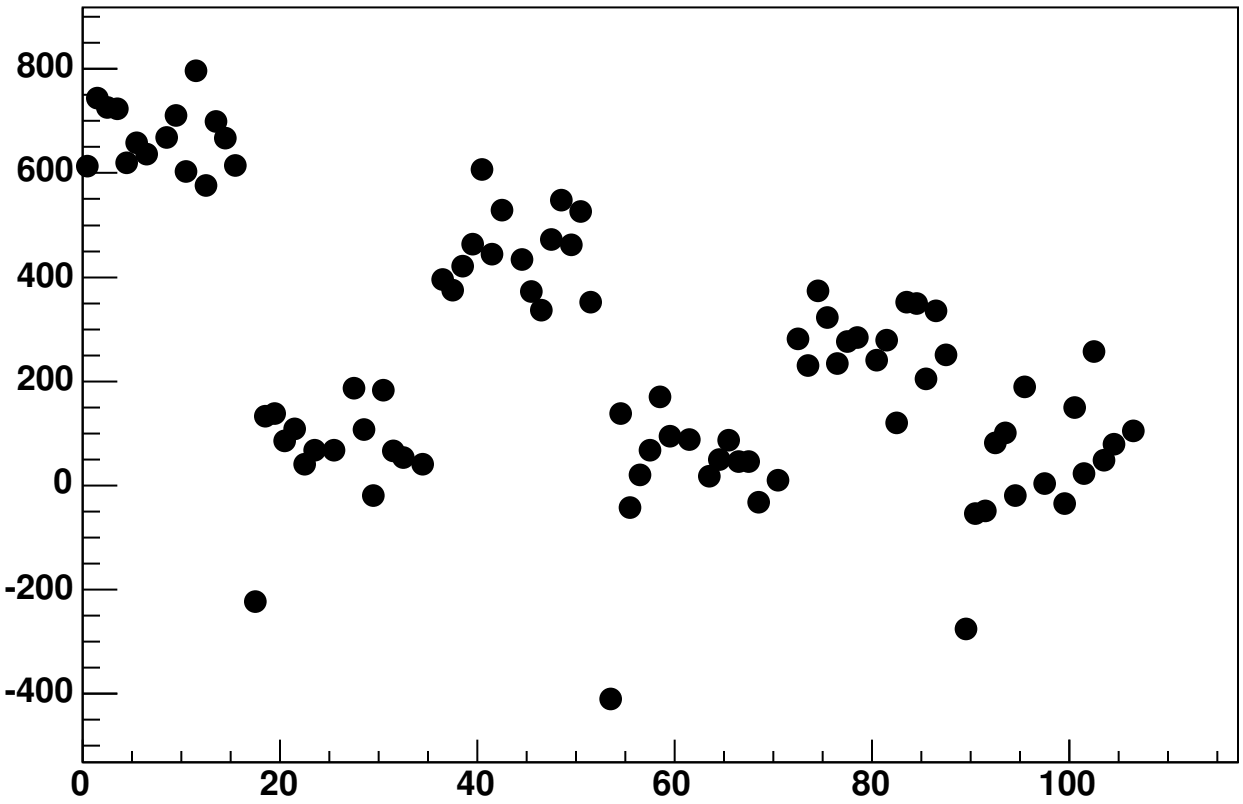
Disabled 4, Hold=35, Fit Intercept vs 18*Chip+Chan



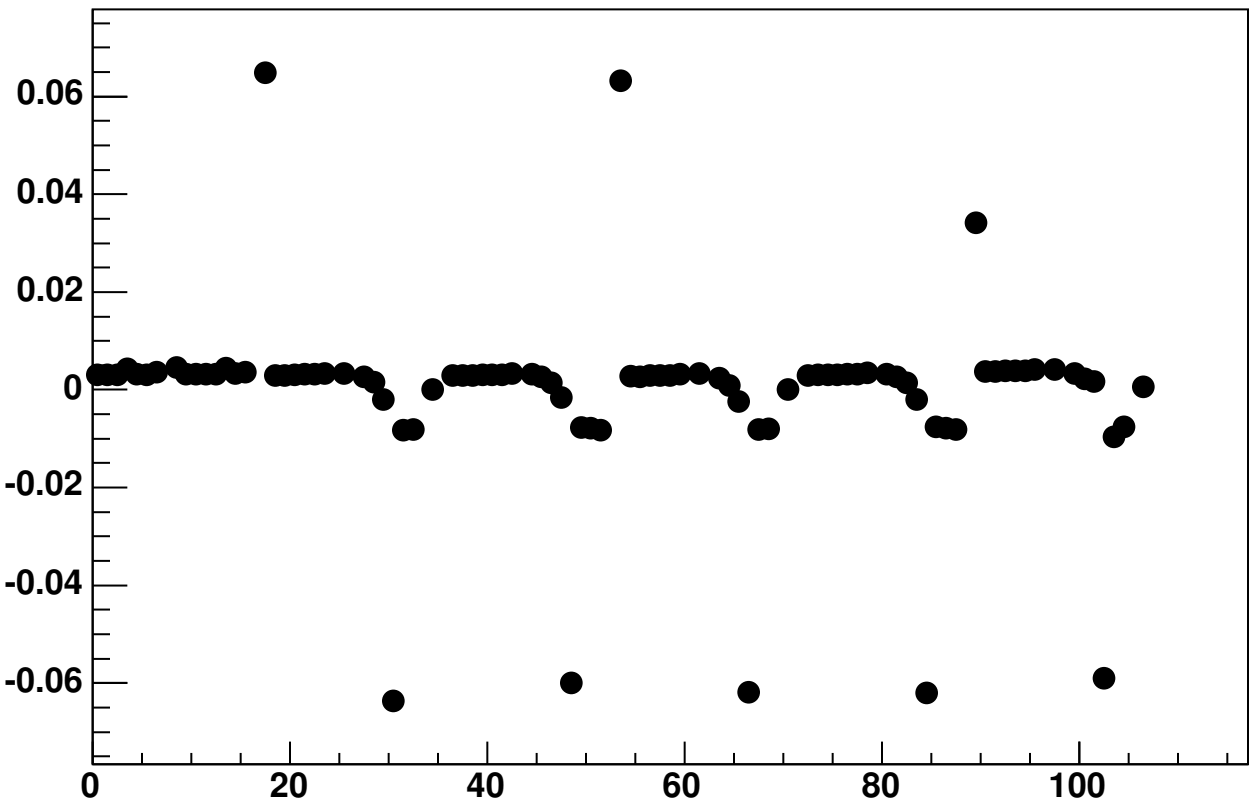
Disabled 4, Hold=35, Fit Slope vs 18*Chip+Chan



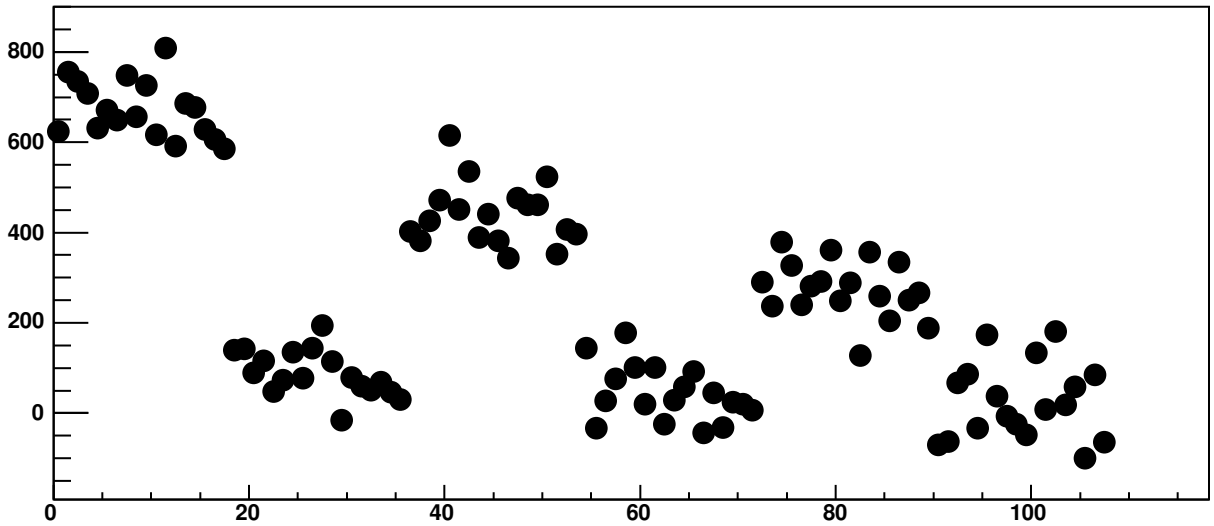
Disabled 5, Hold=35, Fit Intercept vs 18*Chip+Chan



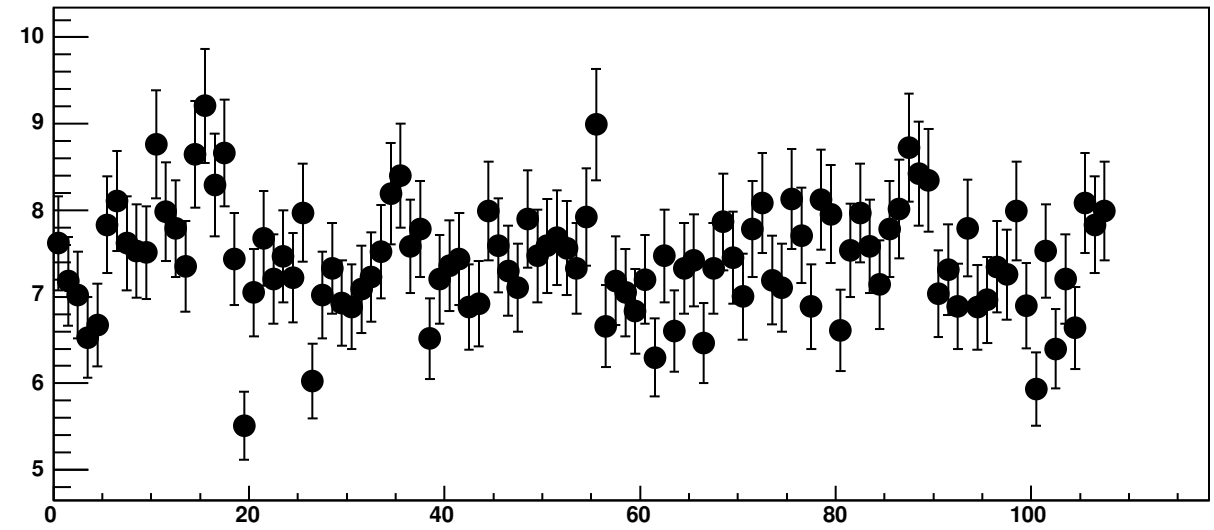
Disabled 5, Hold=35, Fit Slope vs 18*Chip+Chan



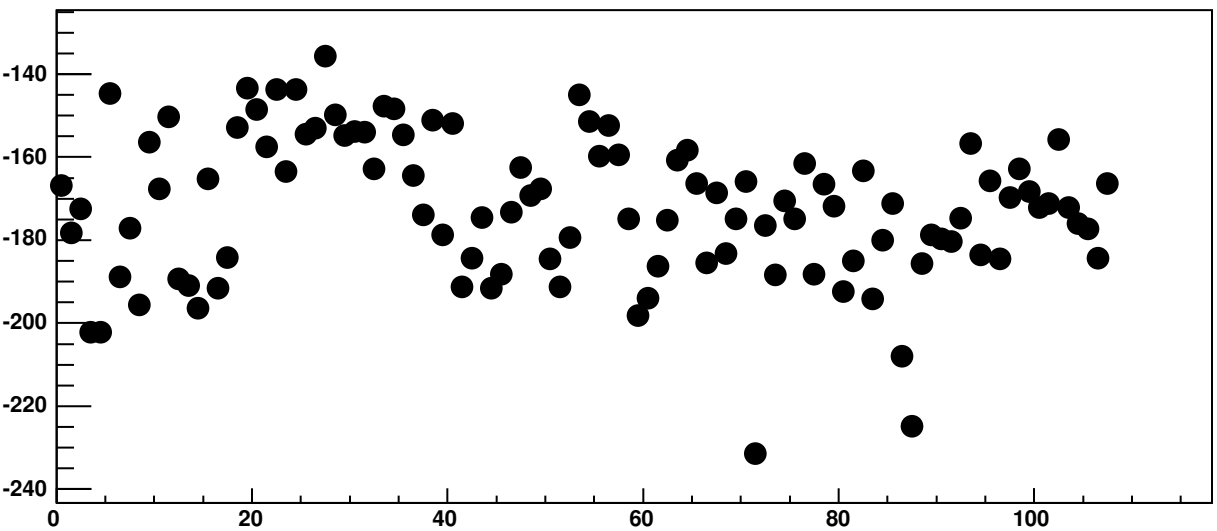
Enables off, ADC Mean vs 18*Chip+Chan



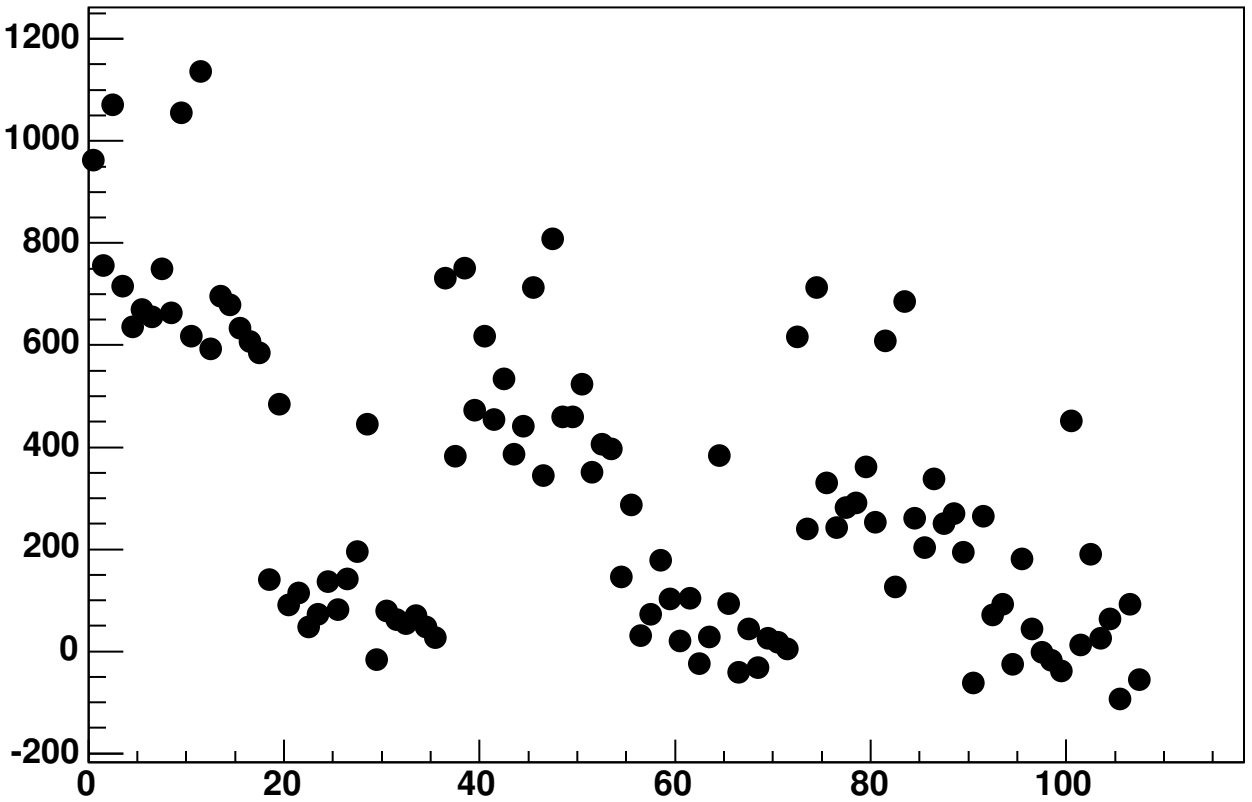
Enables off, ADC Noise vs 18*Chip+Chan



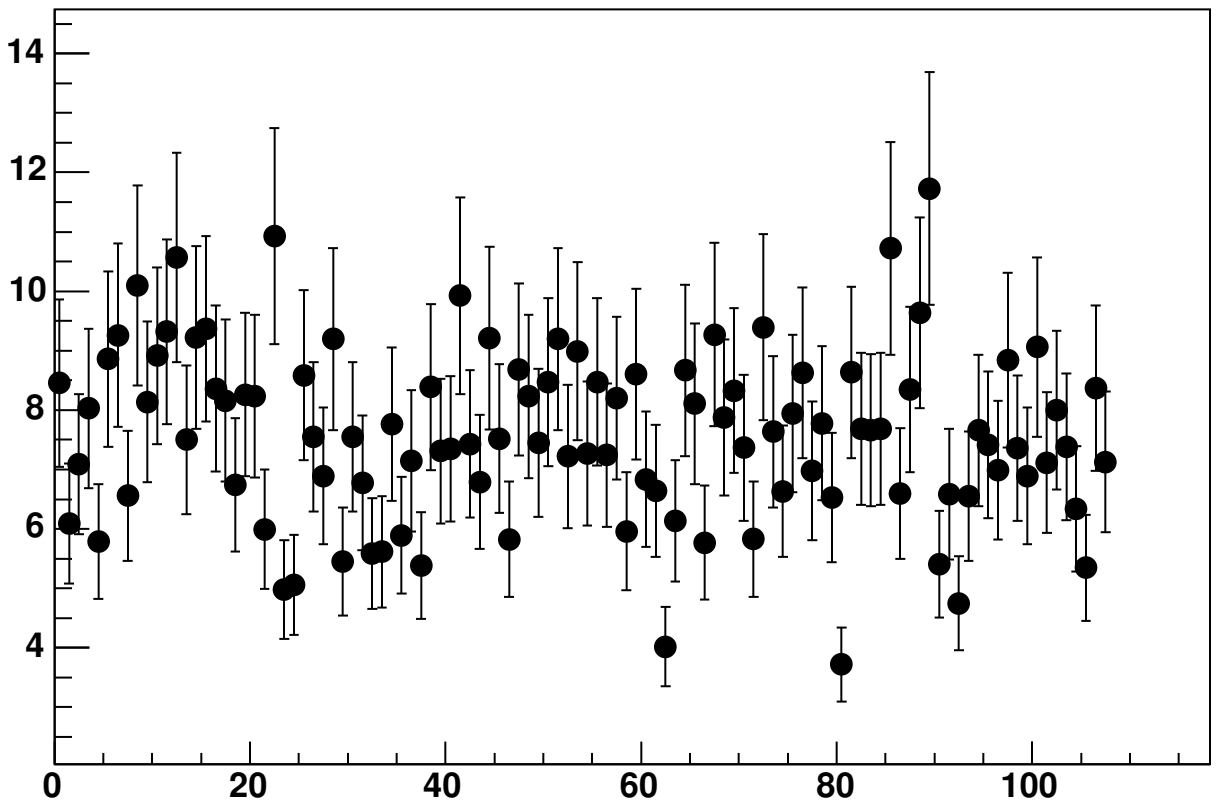
Enables off, ADC Residuals vs 18*Chip+Chan



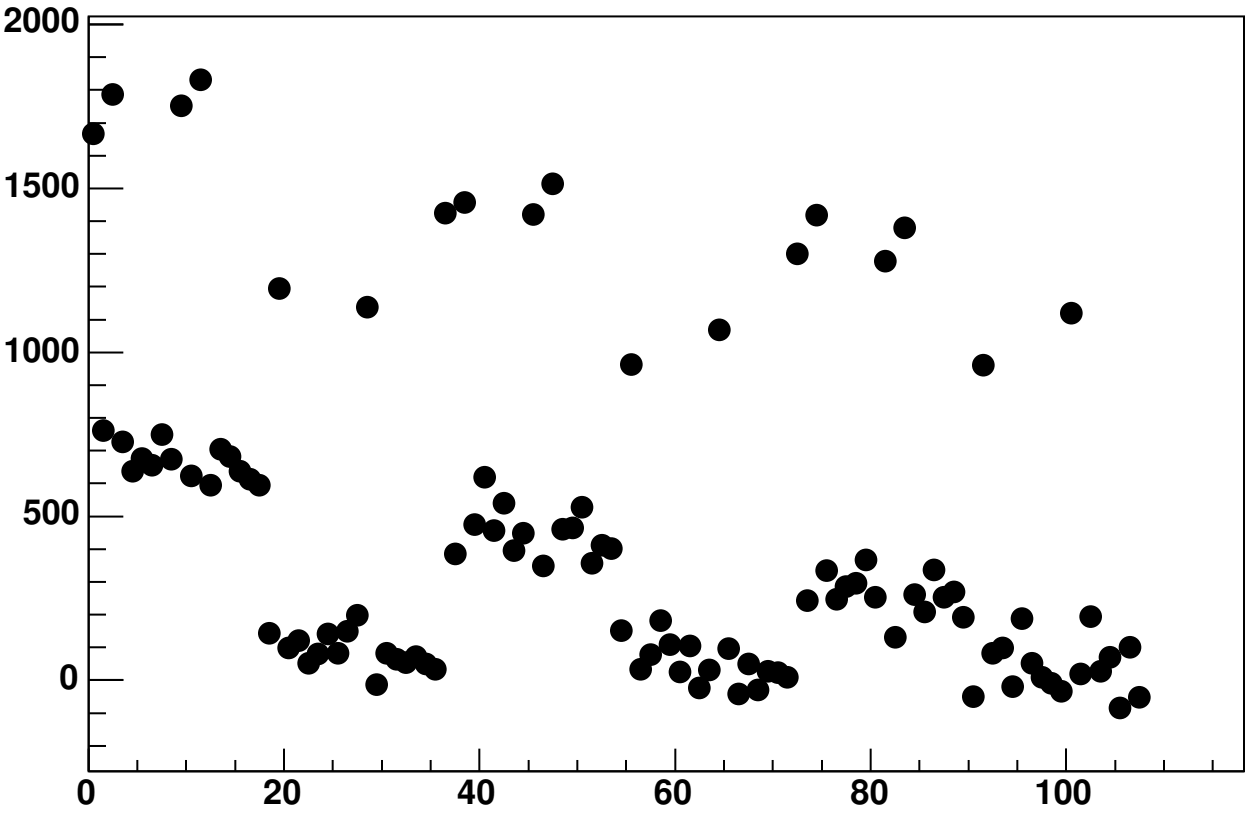
Enable 0, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



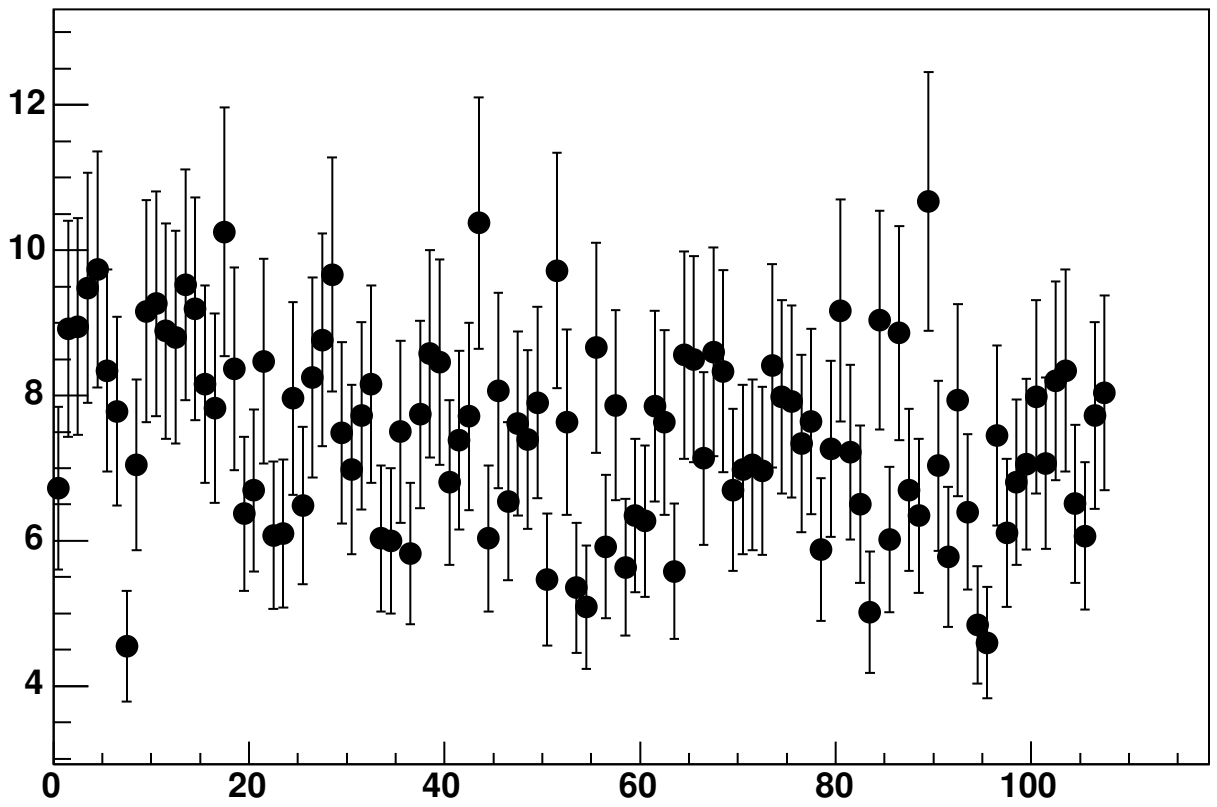
Enable 0, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



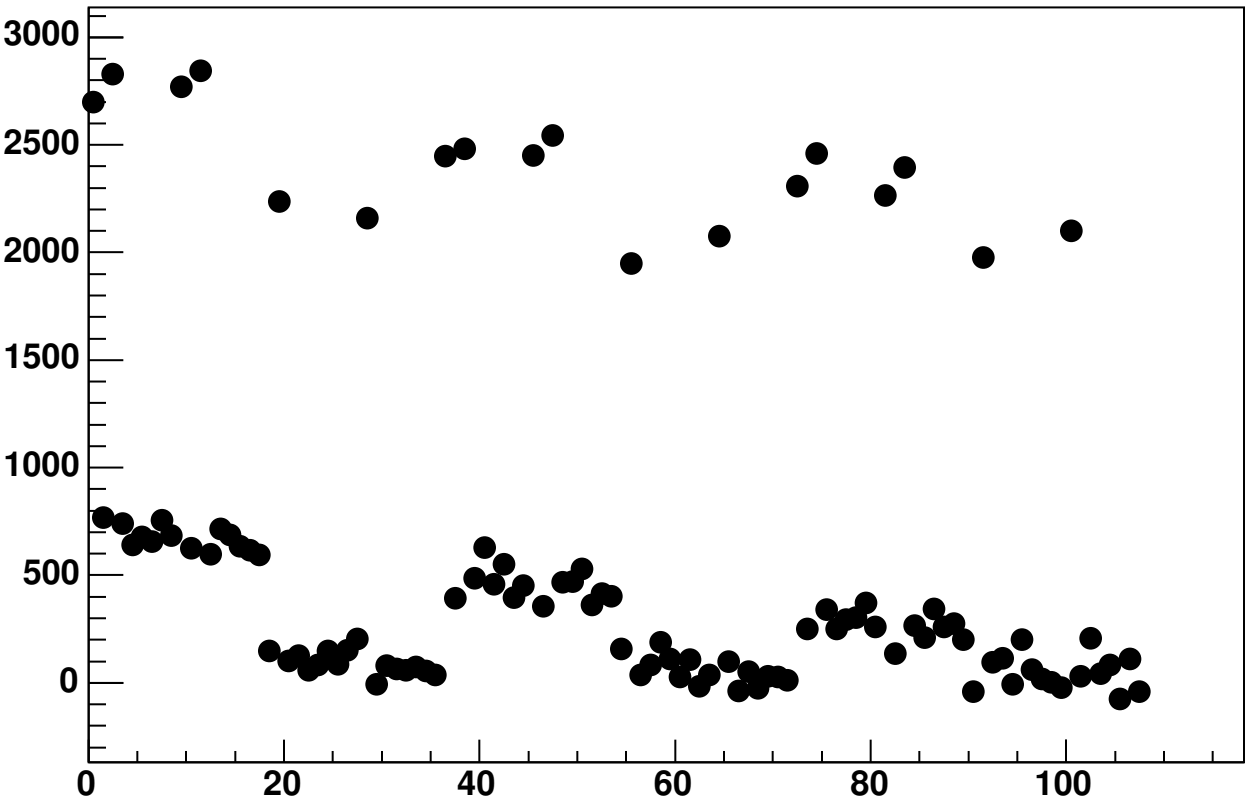
Enable 0, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



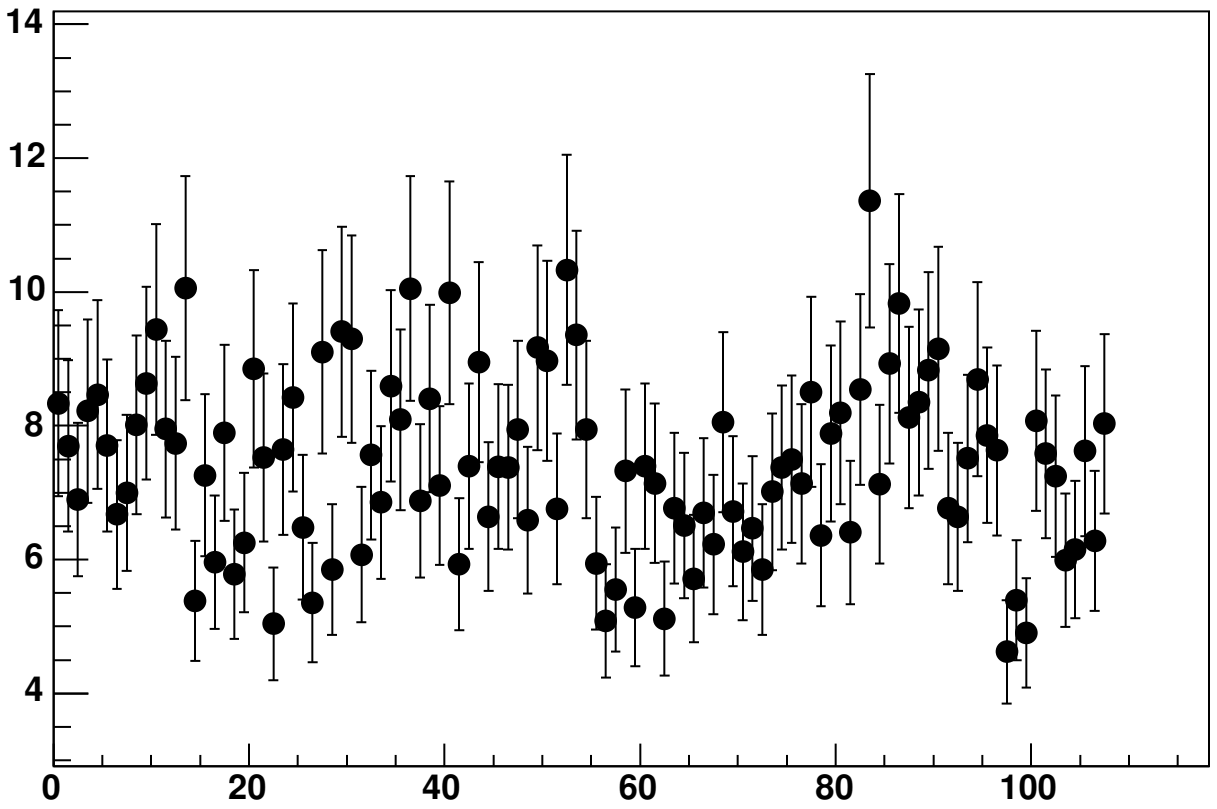
Enable 0, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



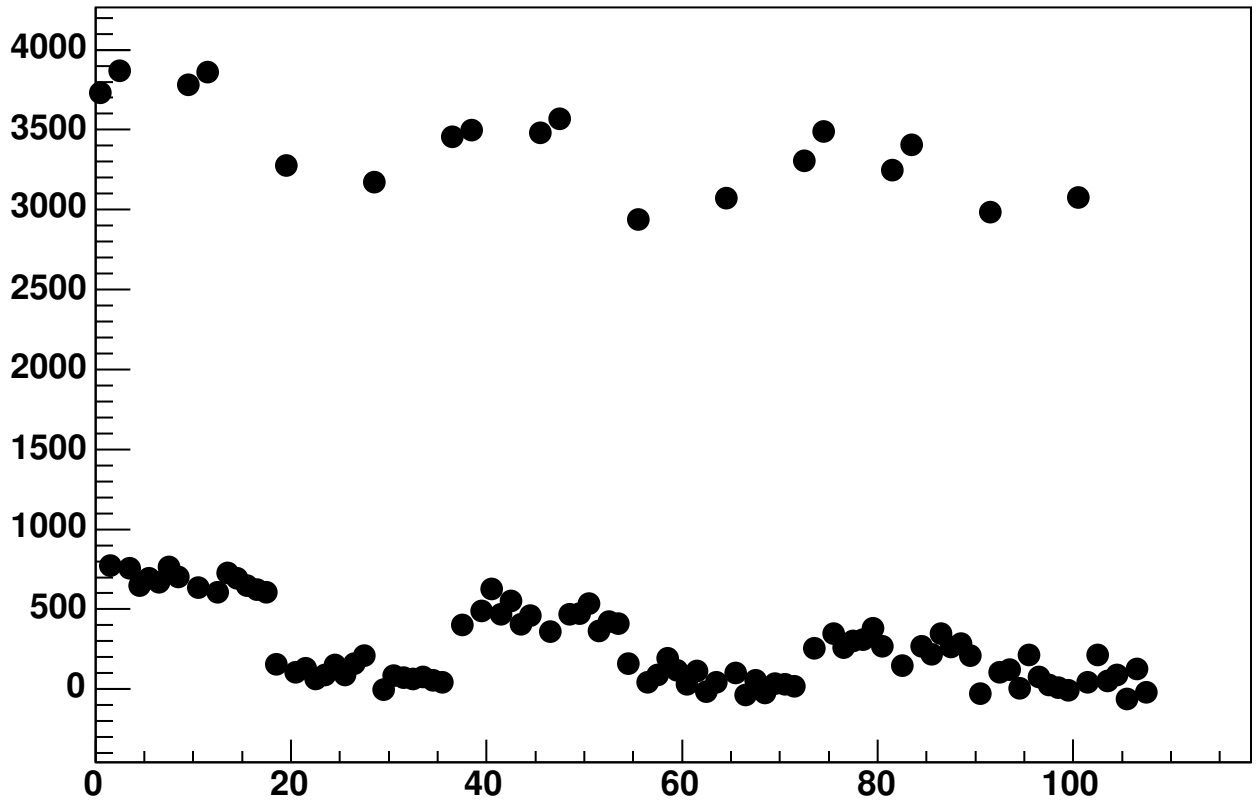
Enable 0, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



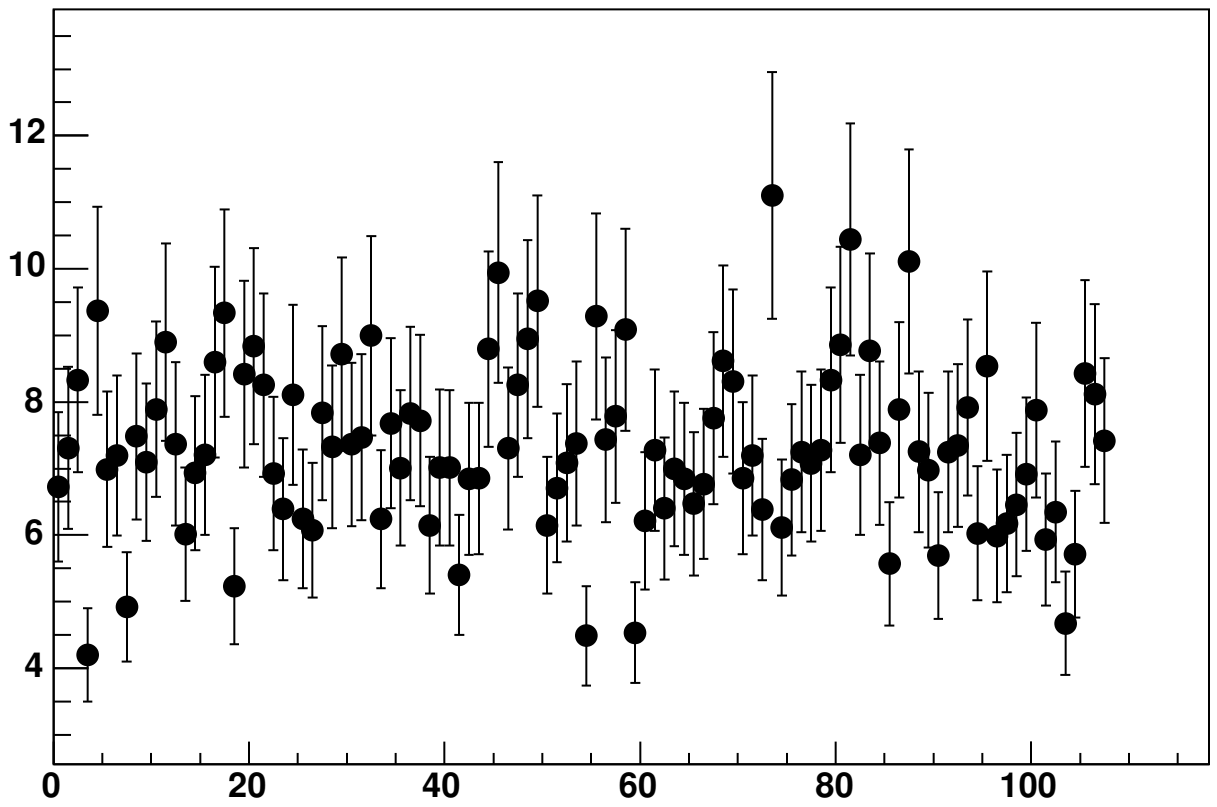
Enable 0, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



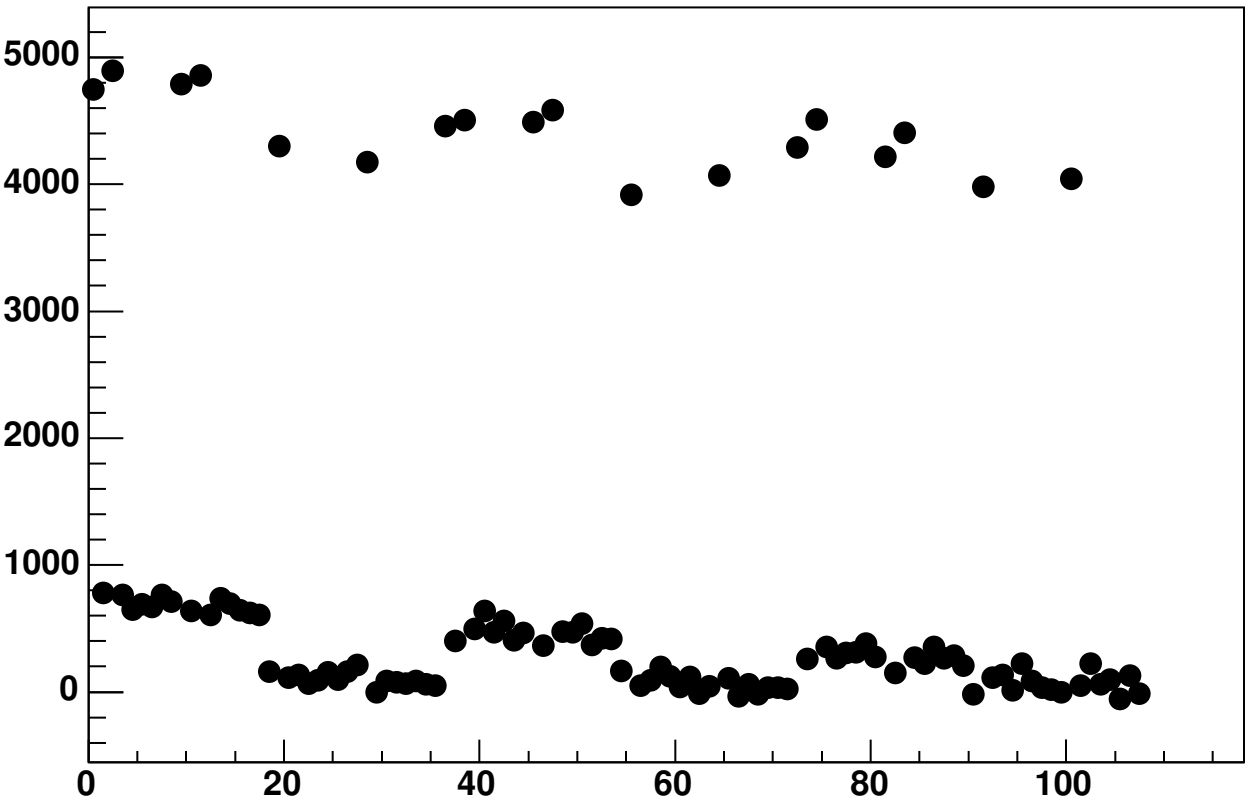
Enable 0, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



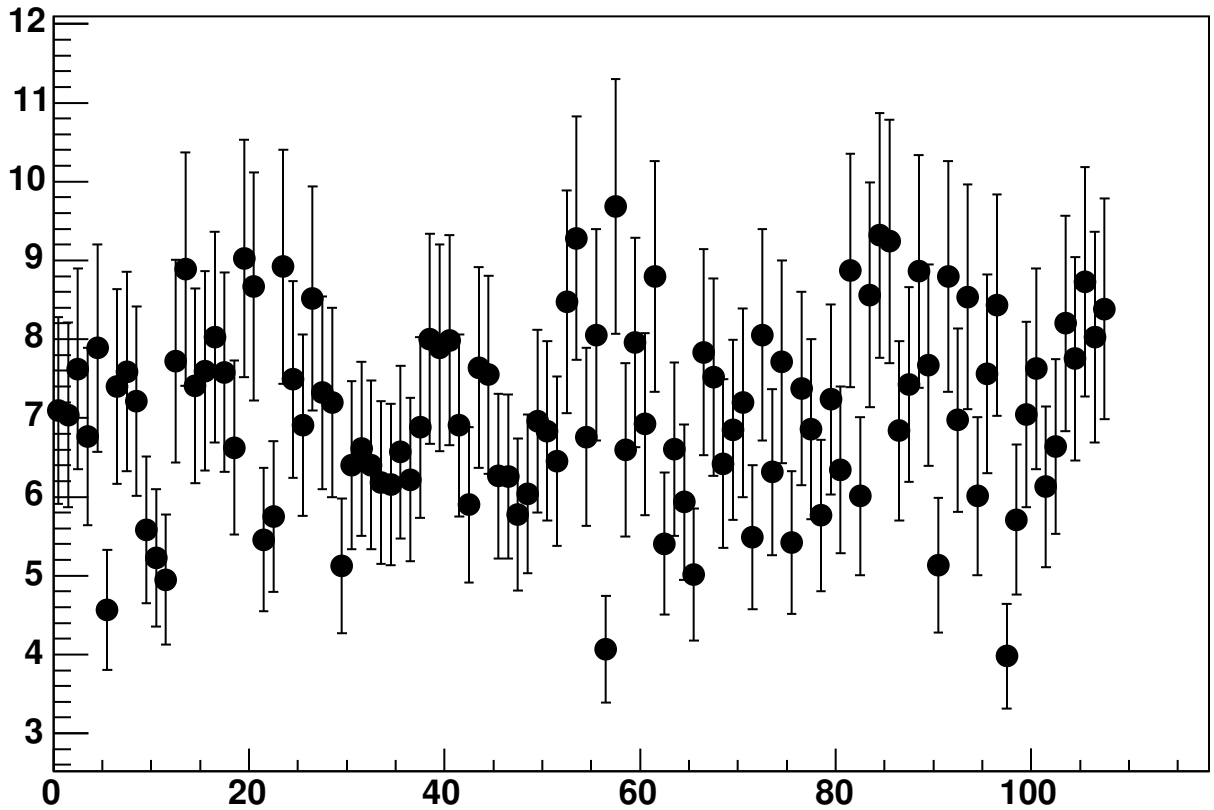
Enable 0, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



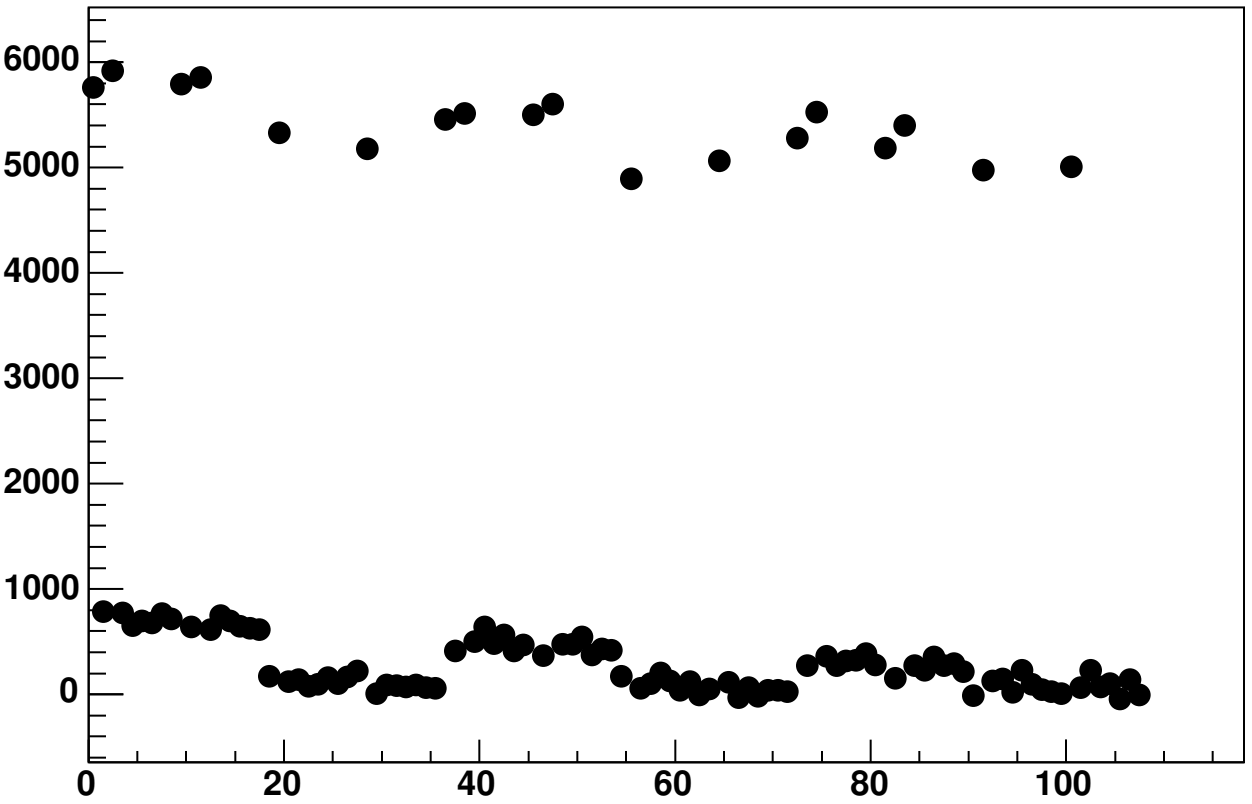
Enable 0, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



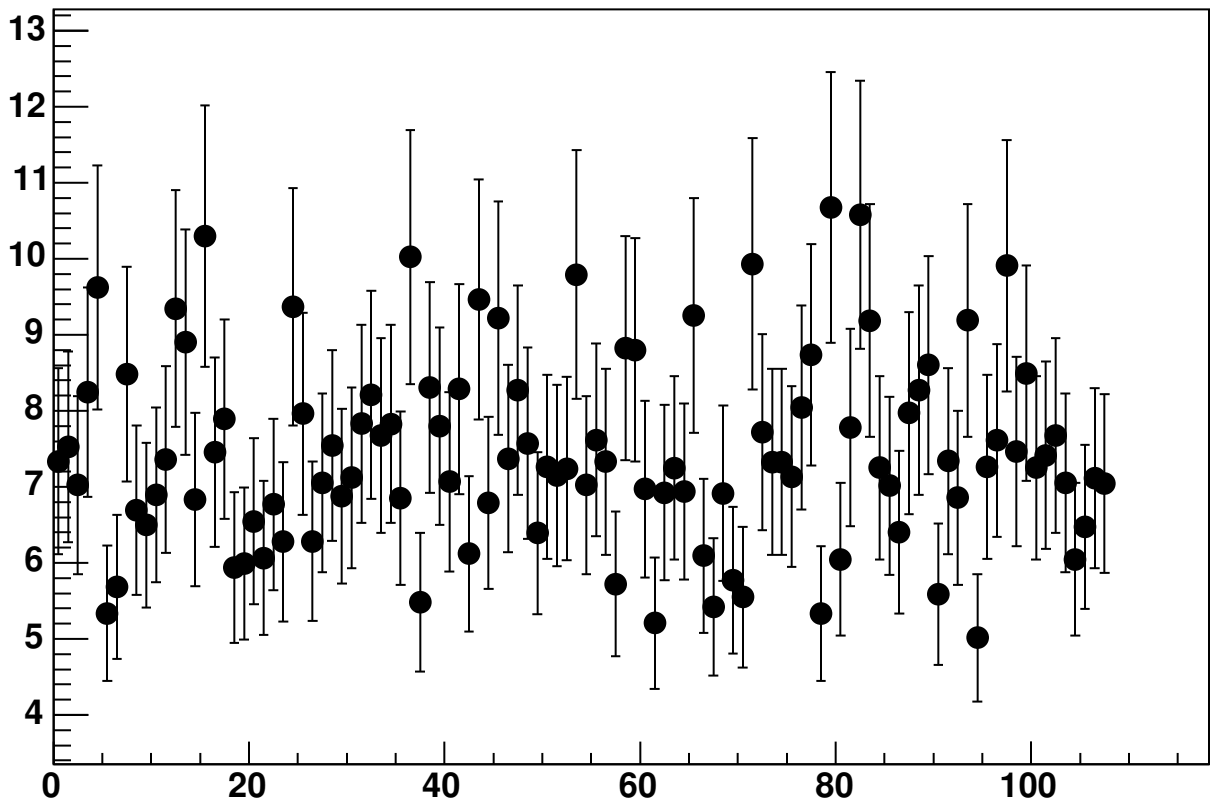
Enable 0, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



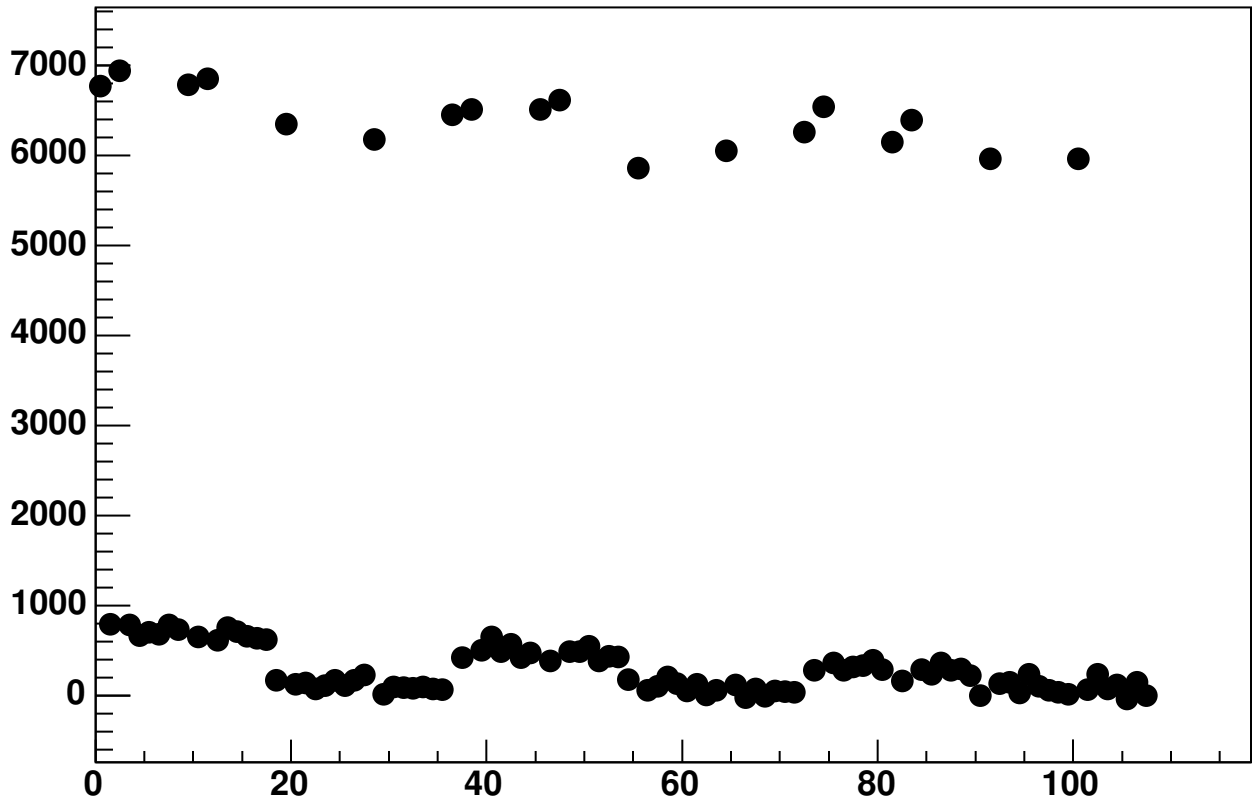
Enable 0, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



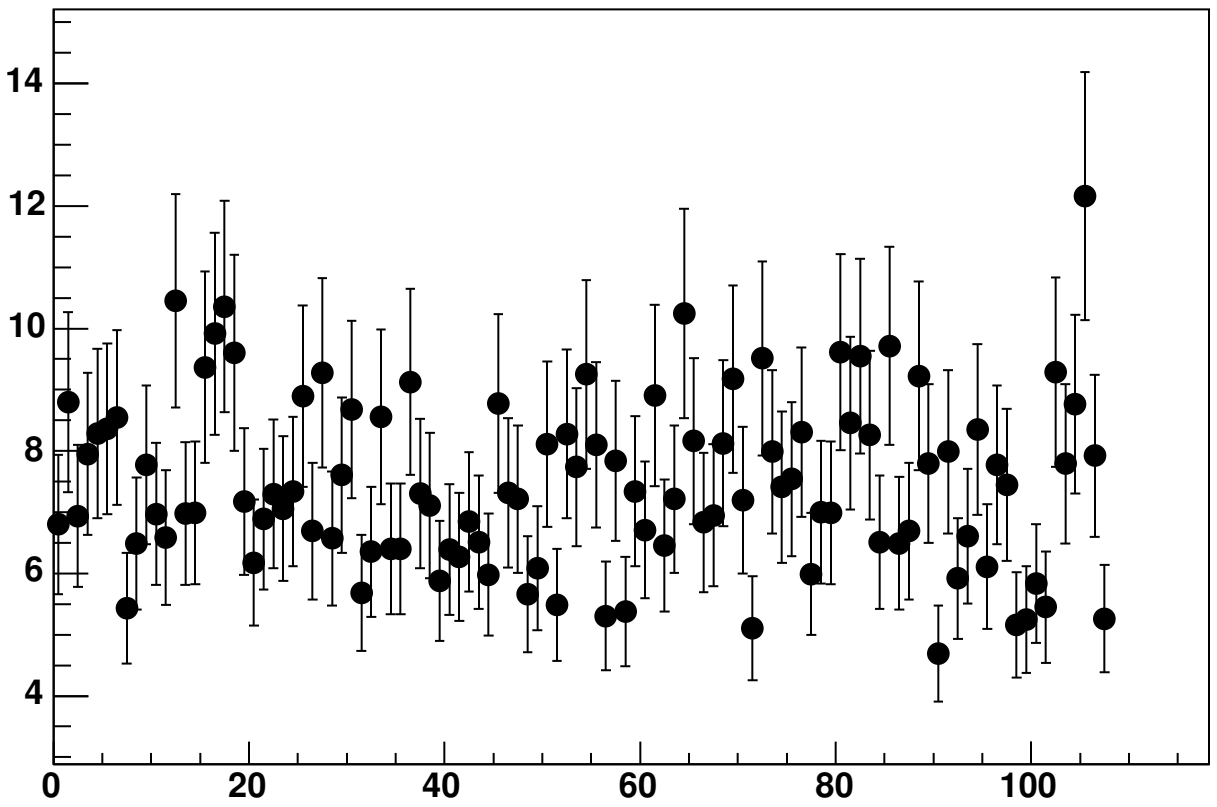
Enable 0, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



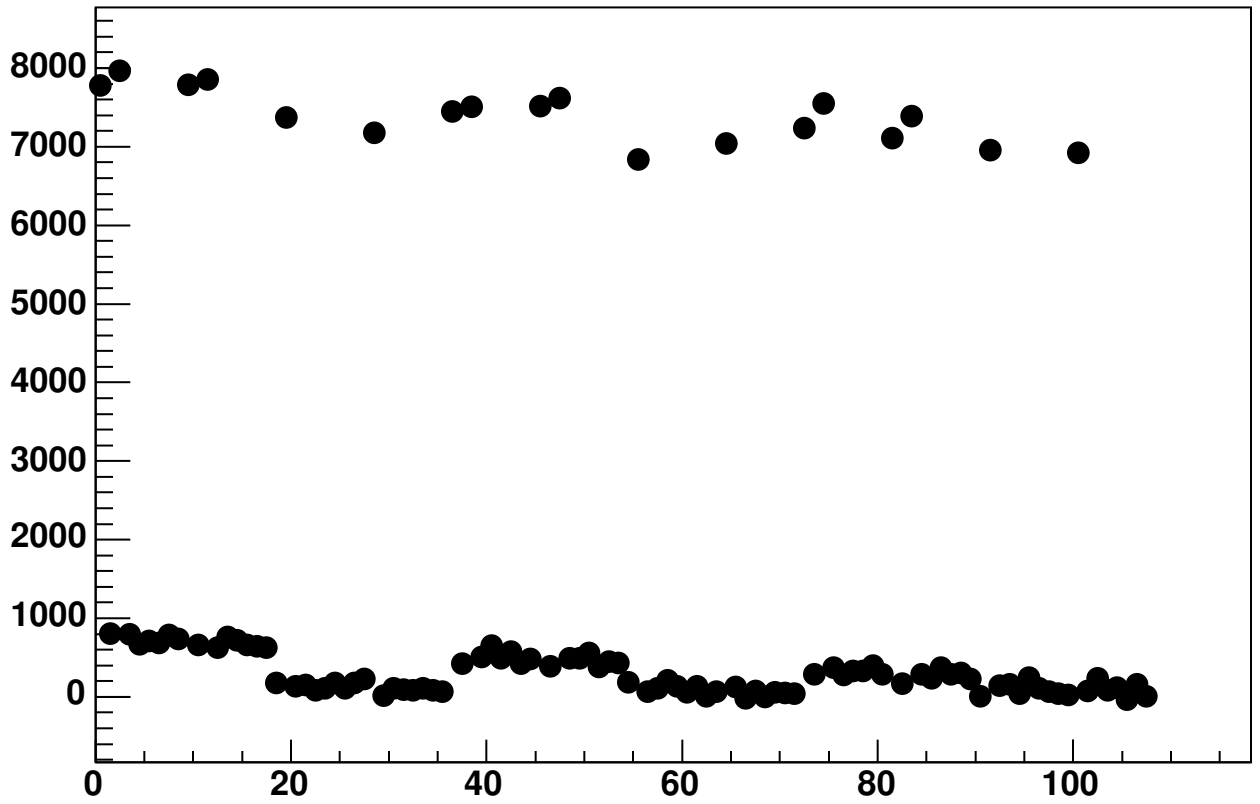
Enable 0, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



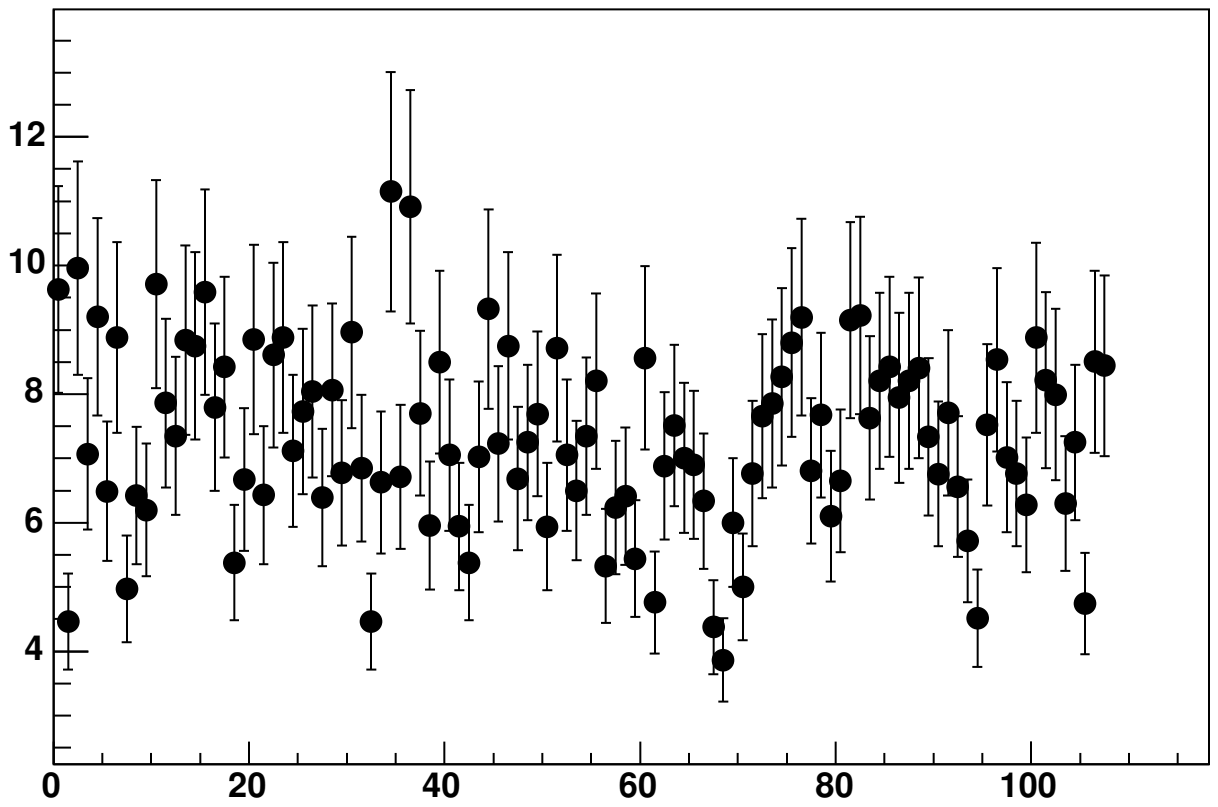
Enable 0, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



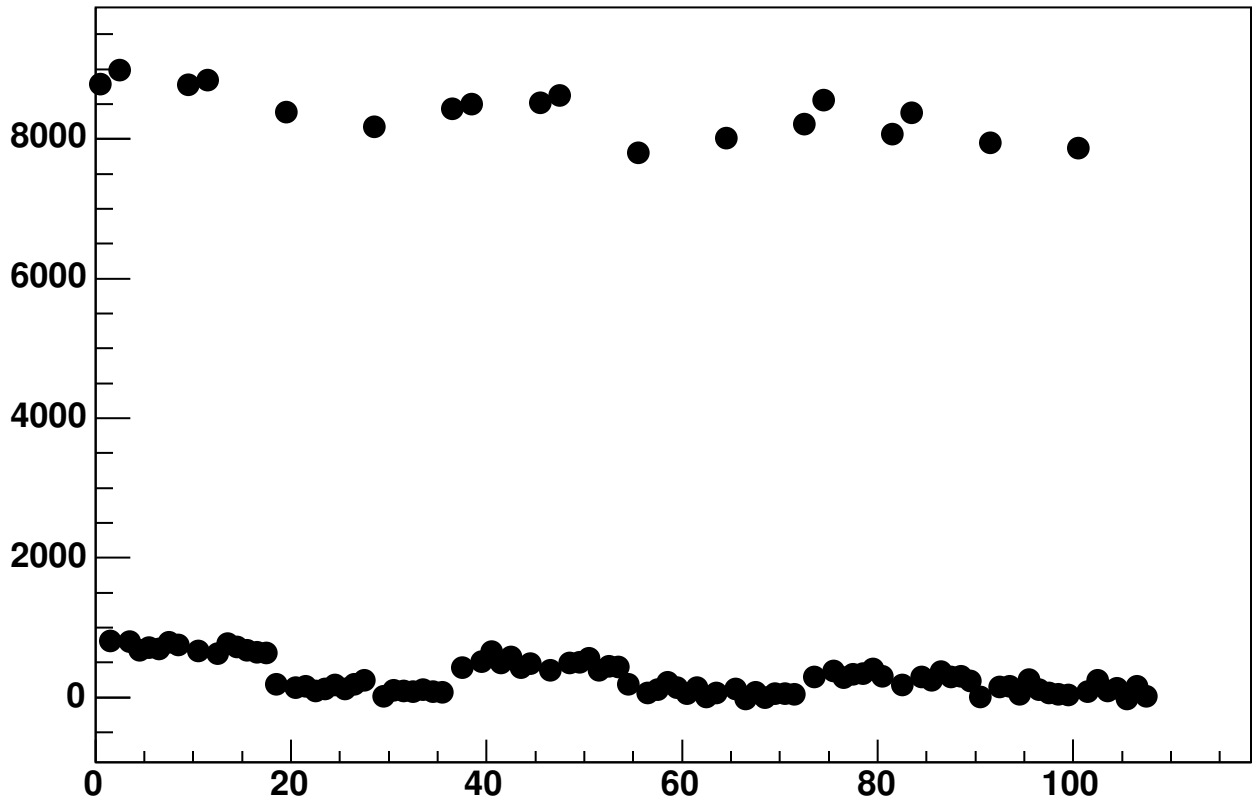
Enable 0, Hold=35, DAC=14000, ADC Mean vs 18*Chip+Chan



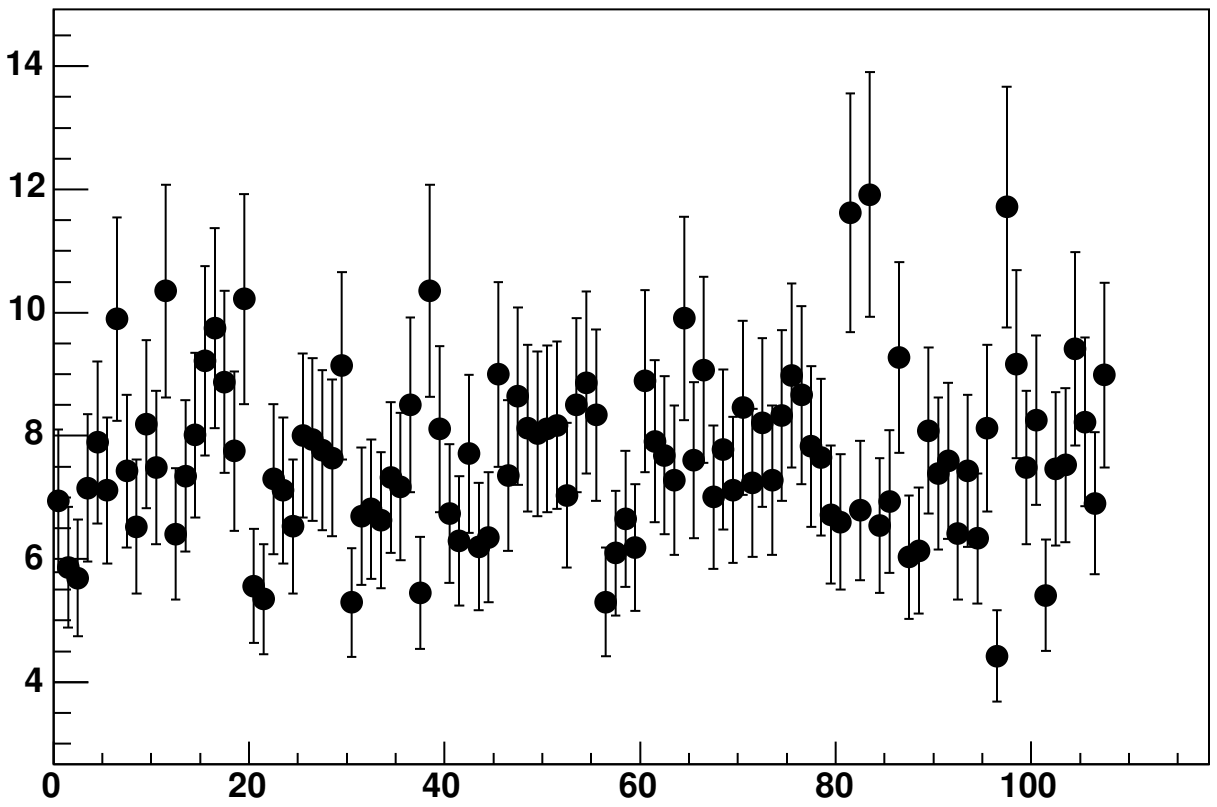
Enable 0, Hold=35, DAC=14000, ADC Noise vs 18*Chip+Chan



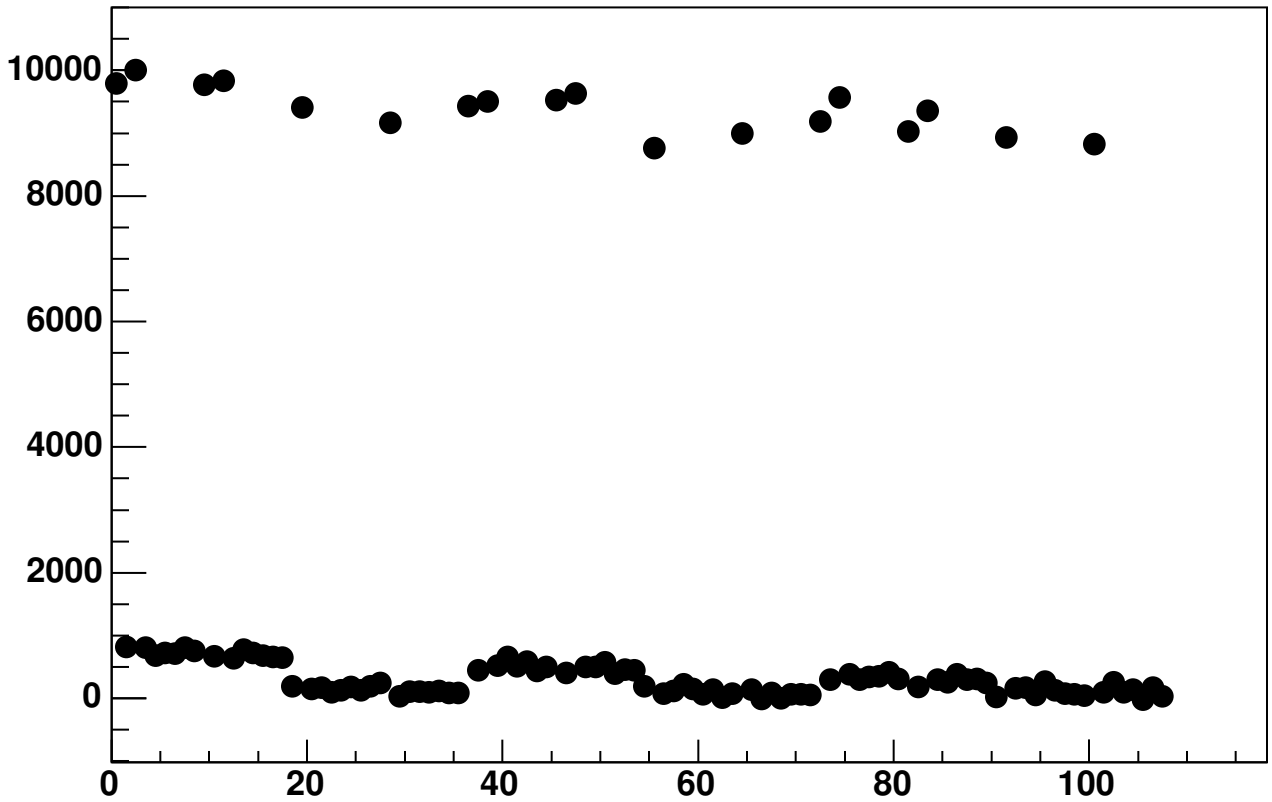
Enable 0, Hold=35, DAC=16000, ADC Mean vs 18*Chip+Chan



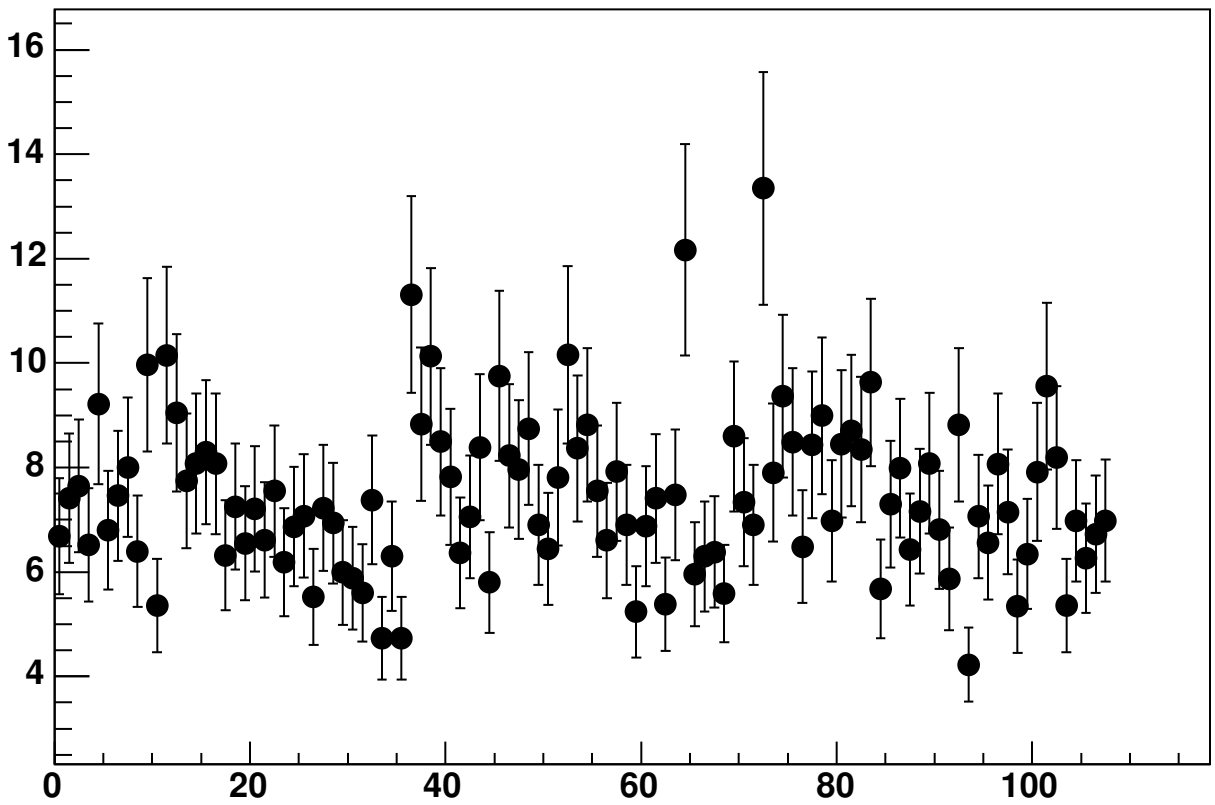
Enable 0, Hold=35, DAC=16000, ADC Noise vs 18*Chip+Chan



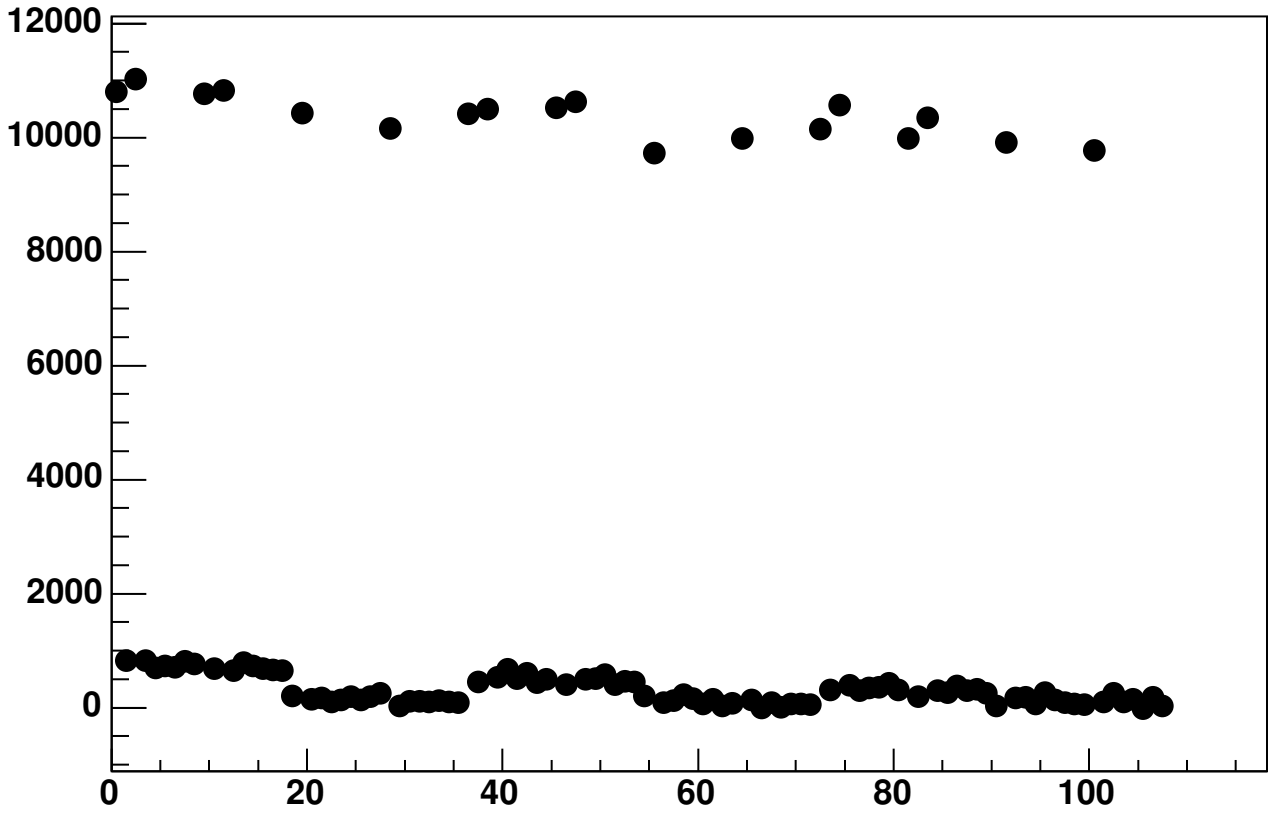
Enable 0, Hold=35, DAC=18000, ADC Mean vs 18*Chip+Chan



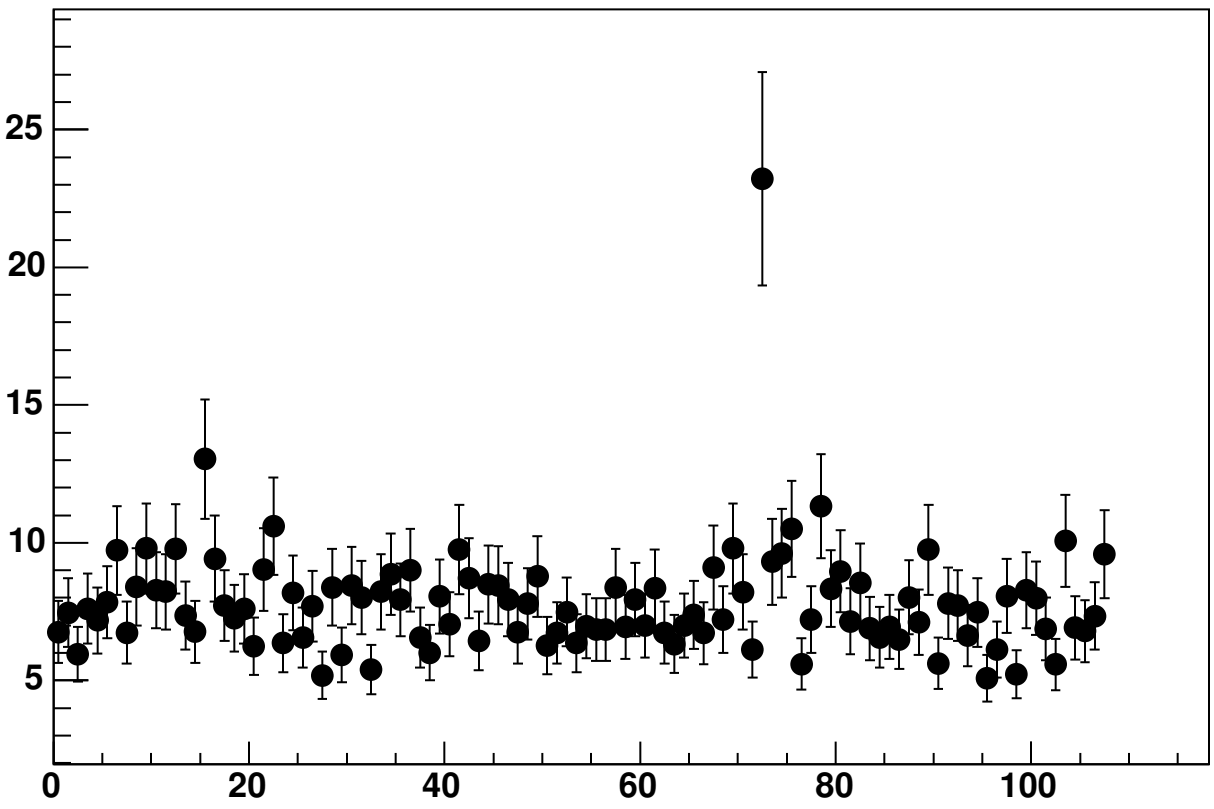
Enable 0, Hold=35, DAC=18000, ADC Noise vs 18*Chip+Chan



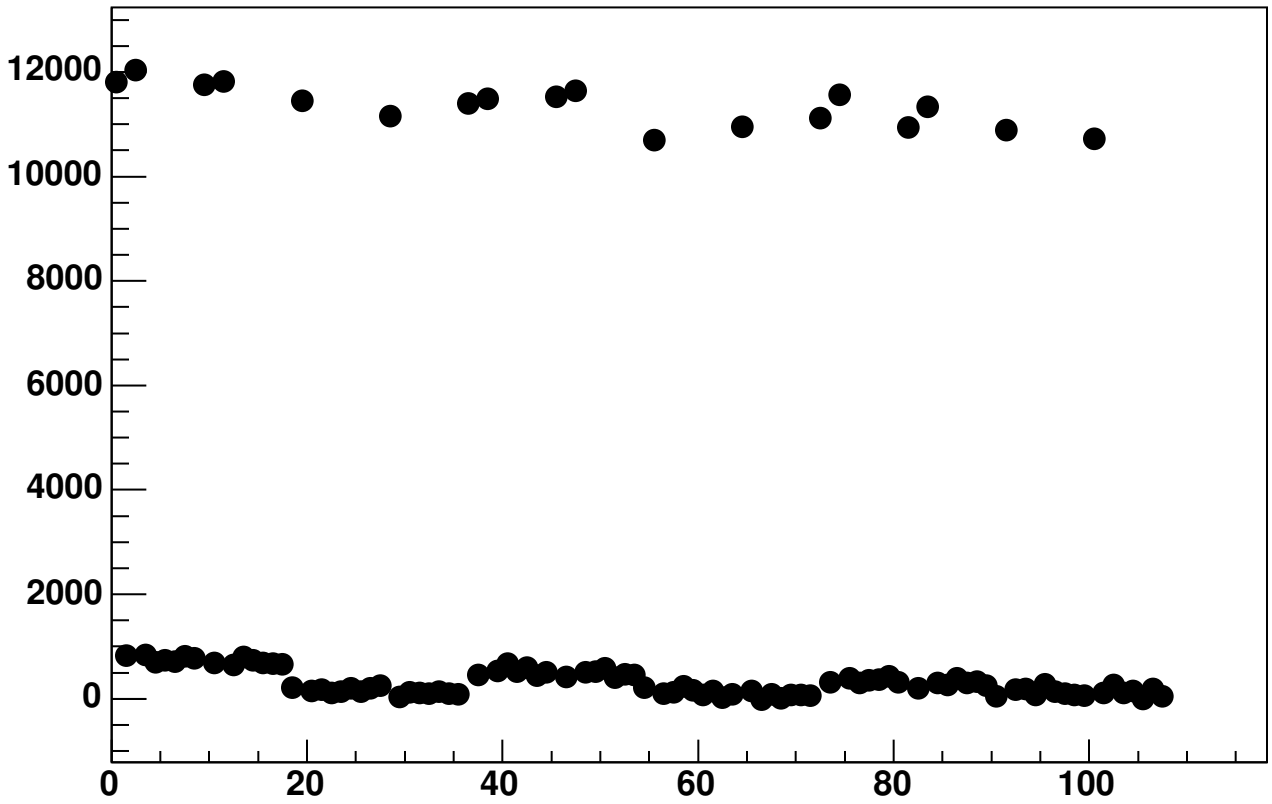
Enable 0, Hold=35, DAC=20000, ADC Mean vs 18*Chip+Chan



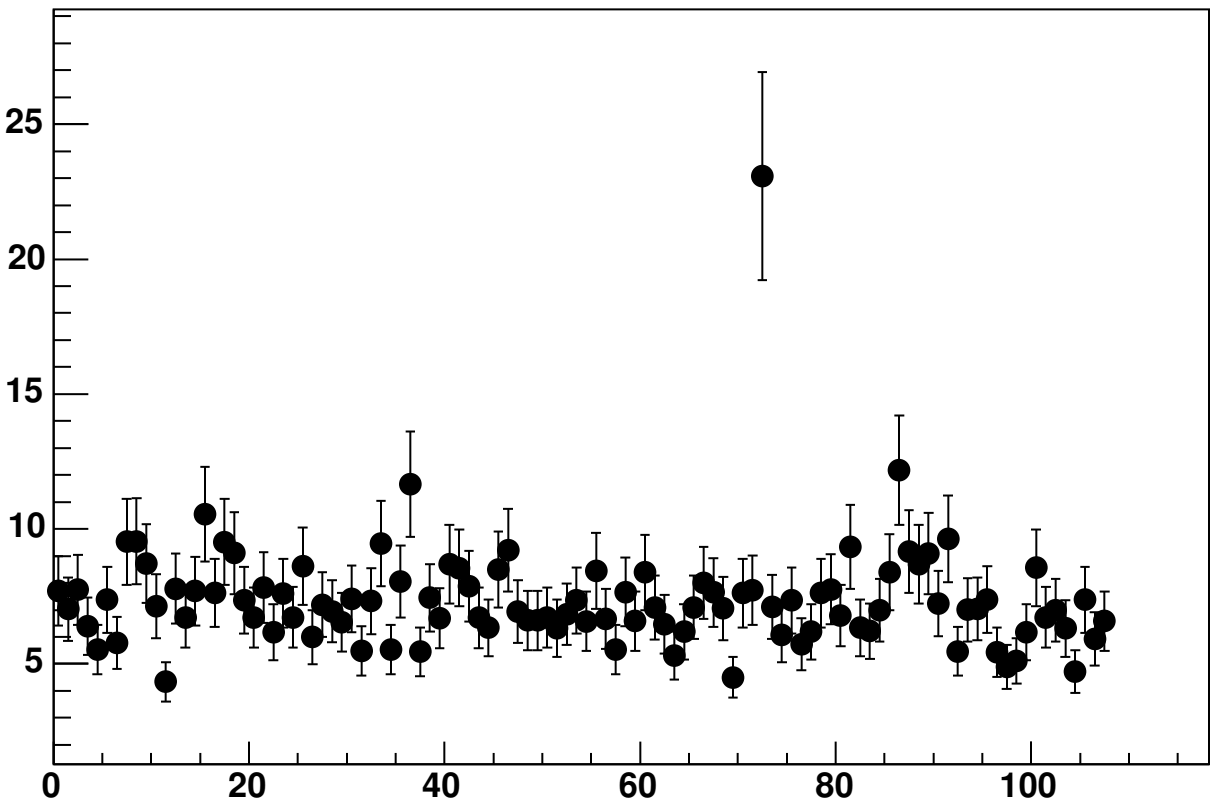
Enable 0, Hold=35, DAC=20000, ADC Noise vs 18*Chip+Chan



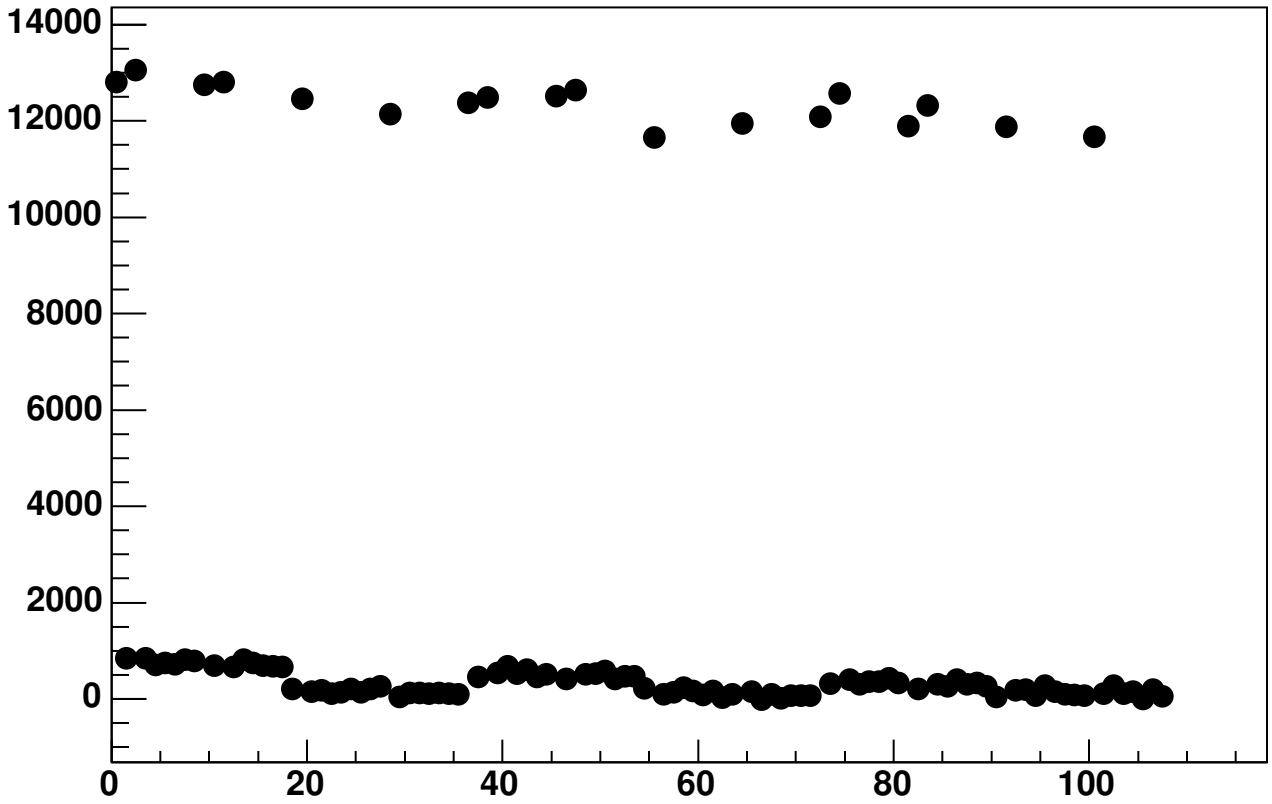
Enable 0, Hold=35, DAC=22000, ADC Mean vs 18*Chip+Chan



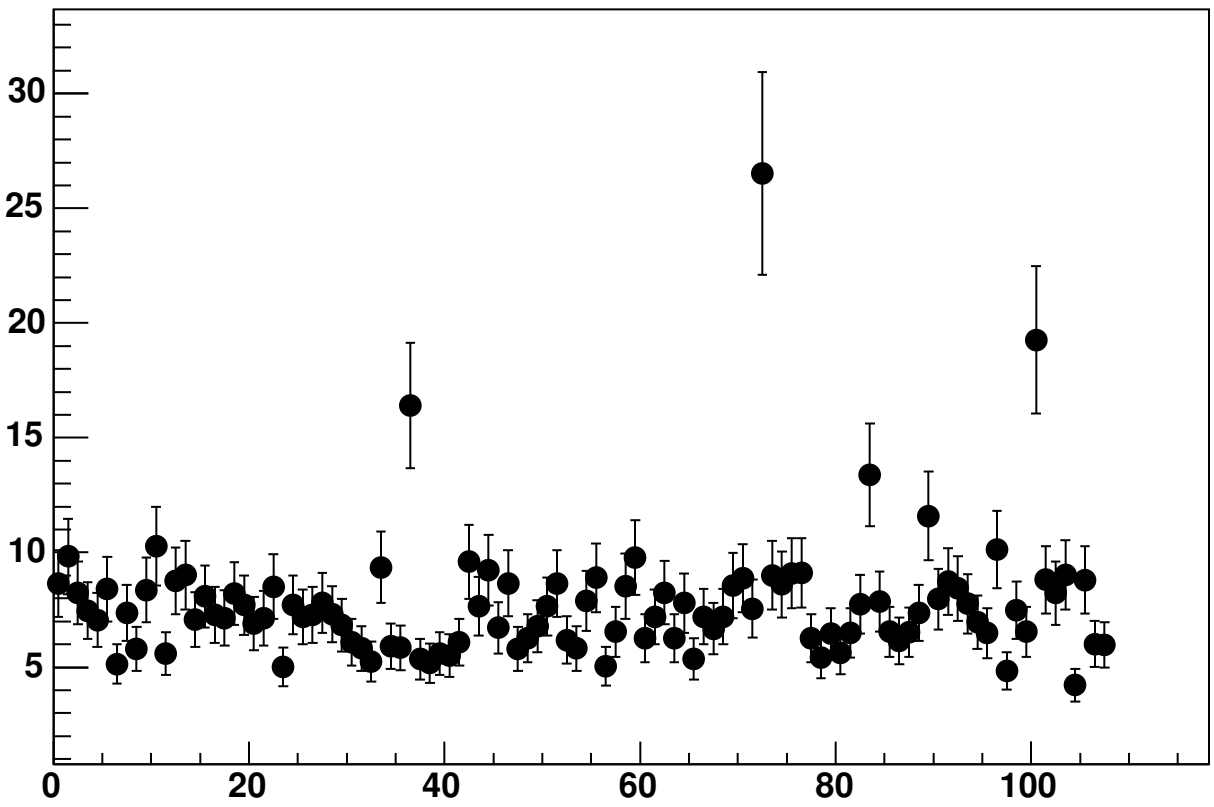
Enable 0, Hold=35, DAC=22000, ADC Noise vs 18*Chip+Chan



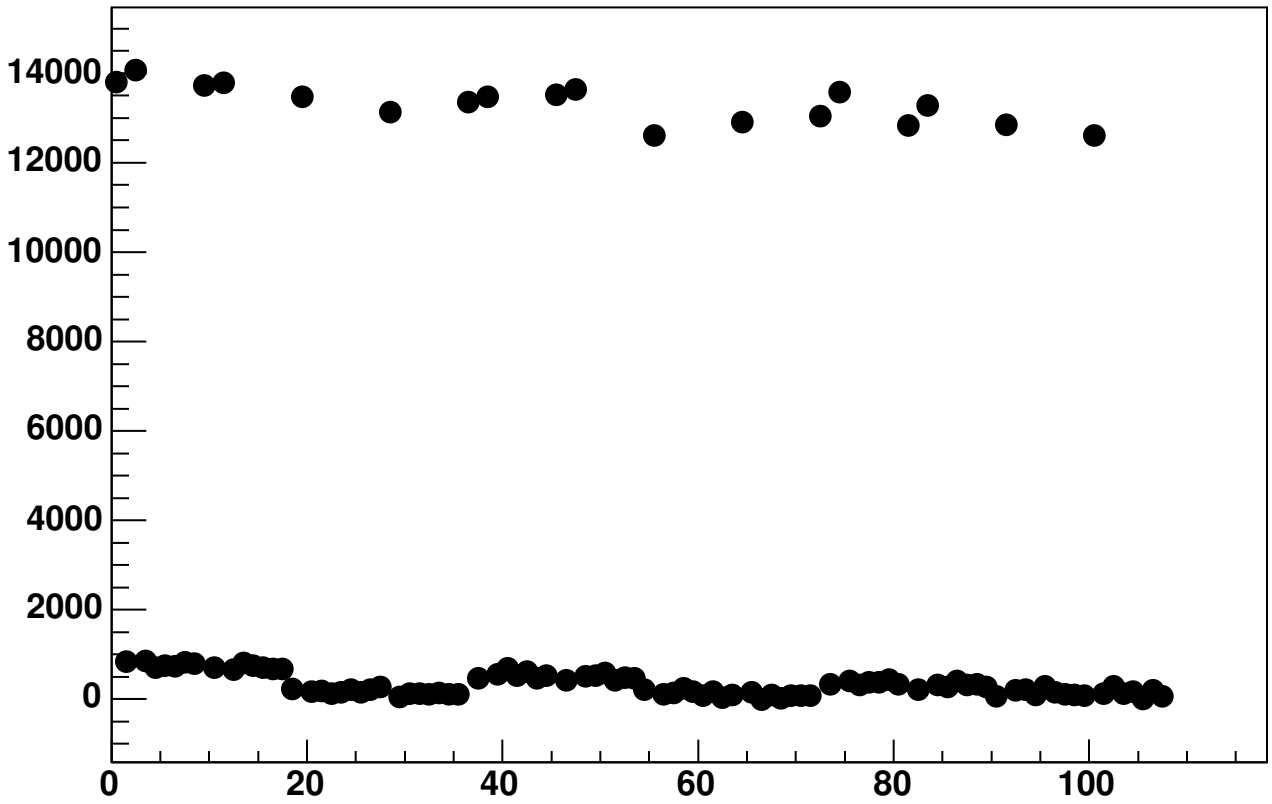
Enable 0, Hold=35, DAC=24000, ADC Mean vs 18*Chip+Chan



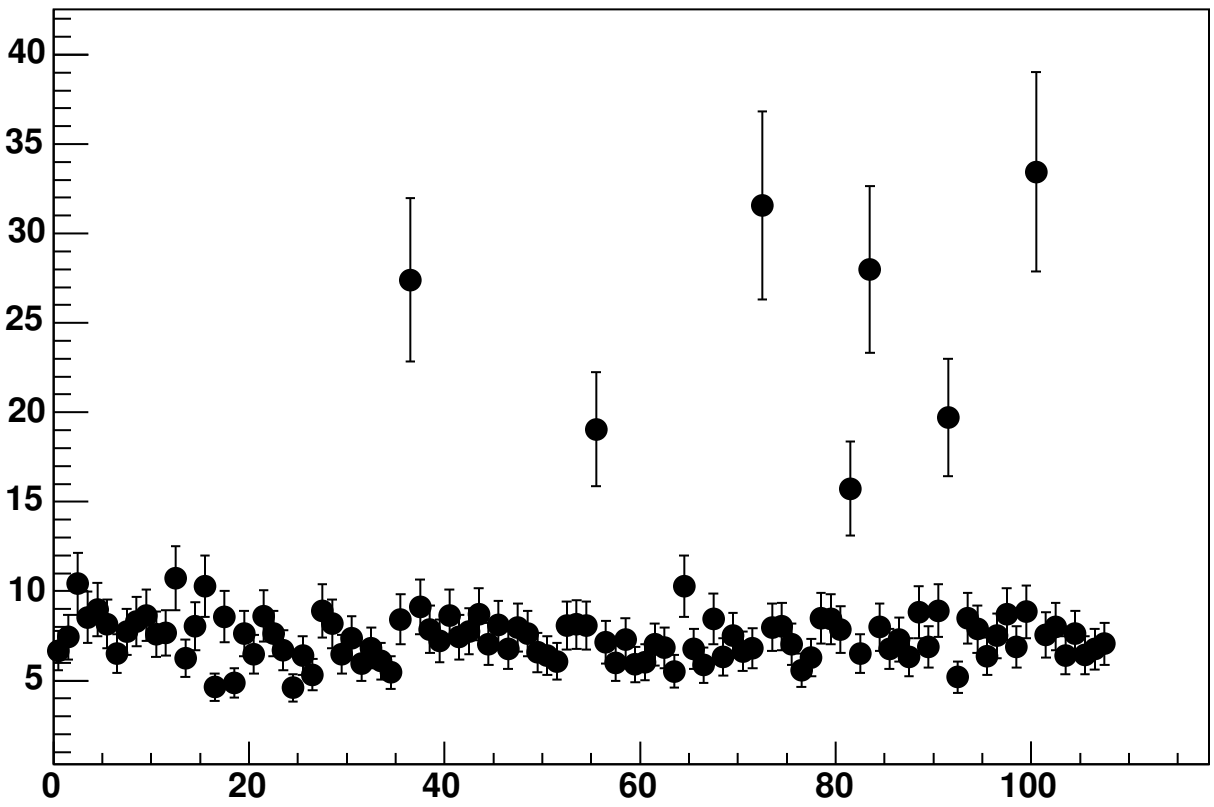
Enable 0, Hold=35, DAC=24000, ADC Noise vs 18*Chip+Chan



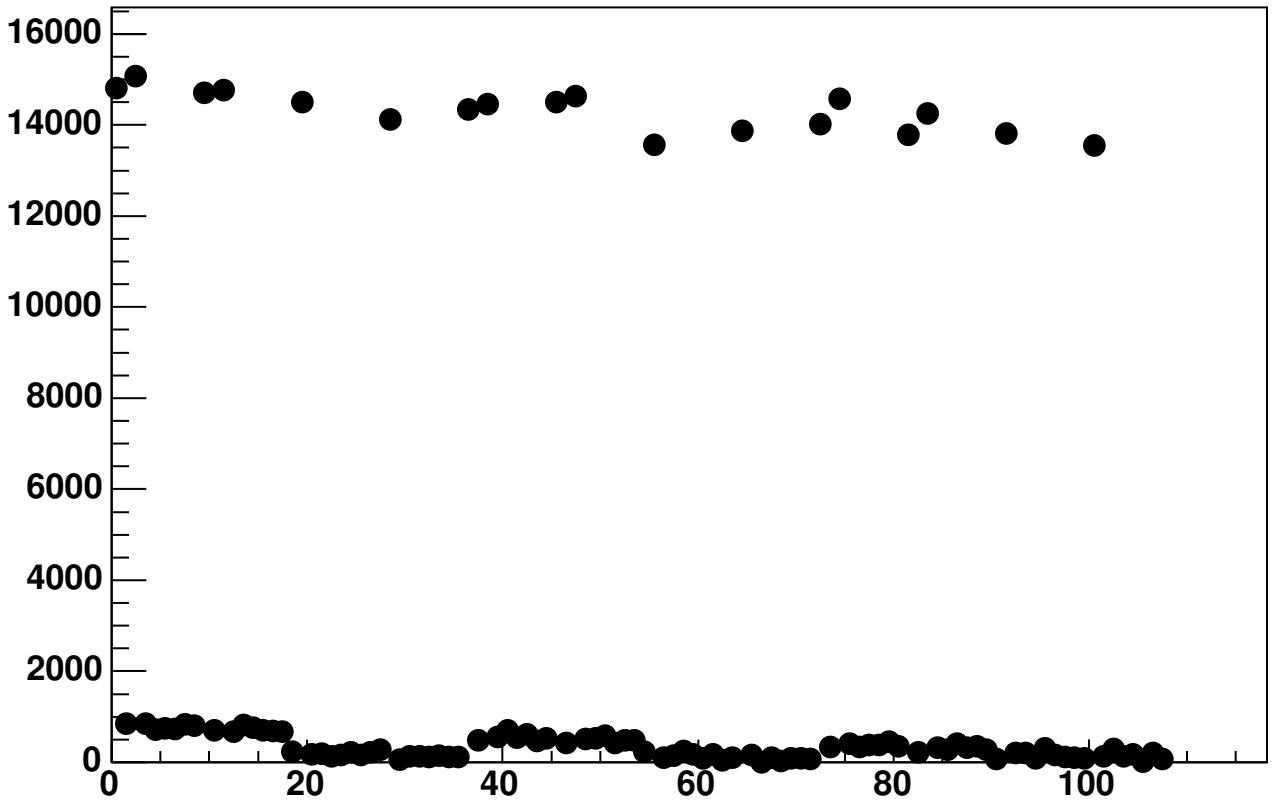
Enable 0, Hold=35, DAC=26000, ADC Mean vs 18*Chip+Chan



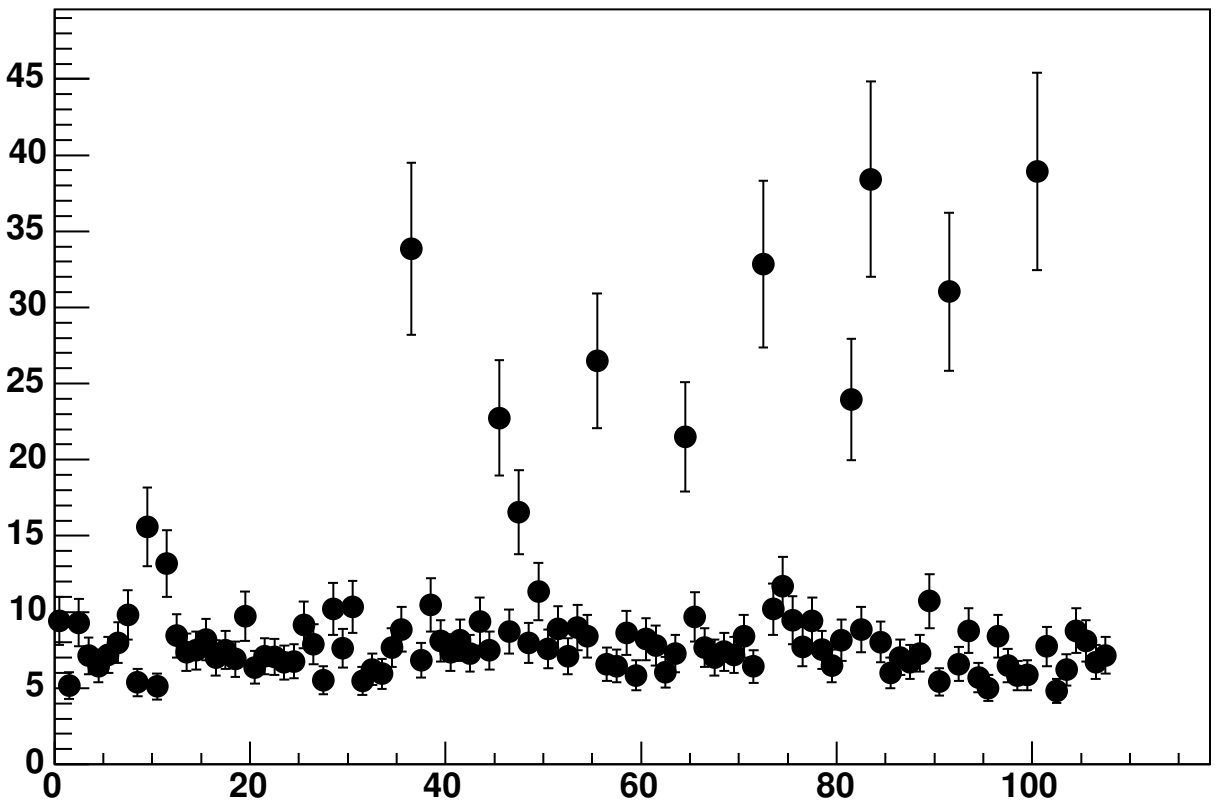
Enable 0, Hold=35, DAC=26000, ADC Noise vs 18*Chip+Chan



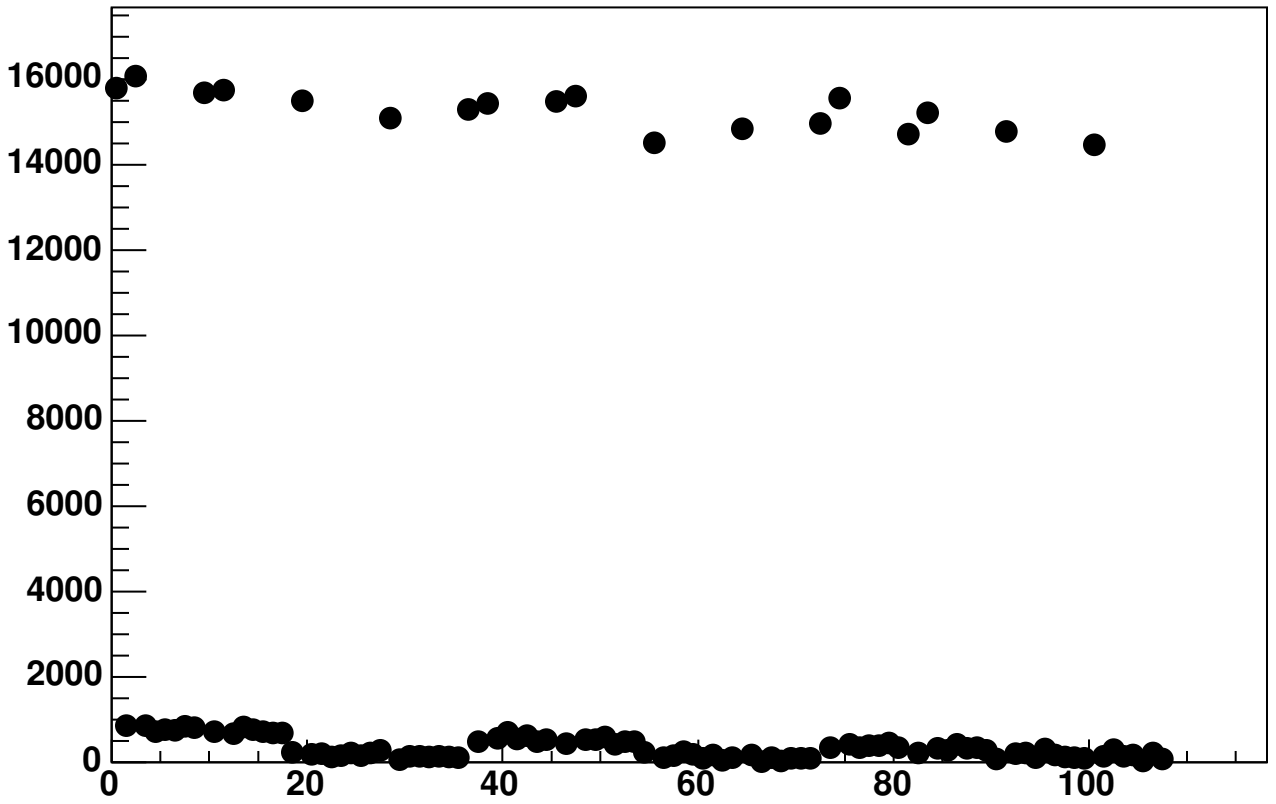
Enable 0, Hold=35, DAC=28000, ADC Mean vs 18*Chip+Chan



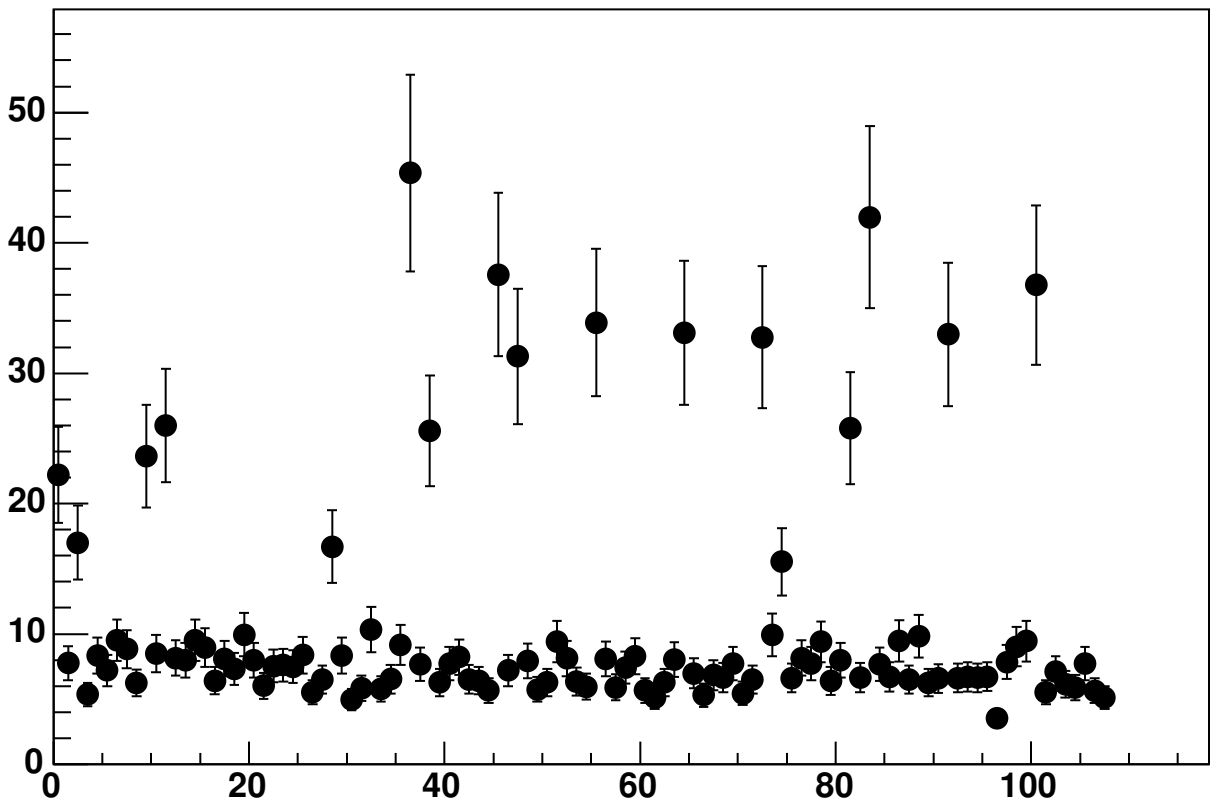
Enable 0, Hold=35, DAC=28000, ADC Noise vs 18*Chip+Chan



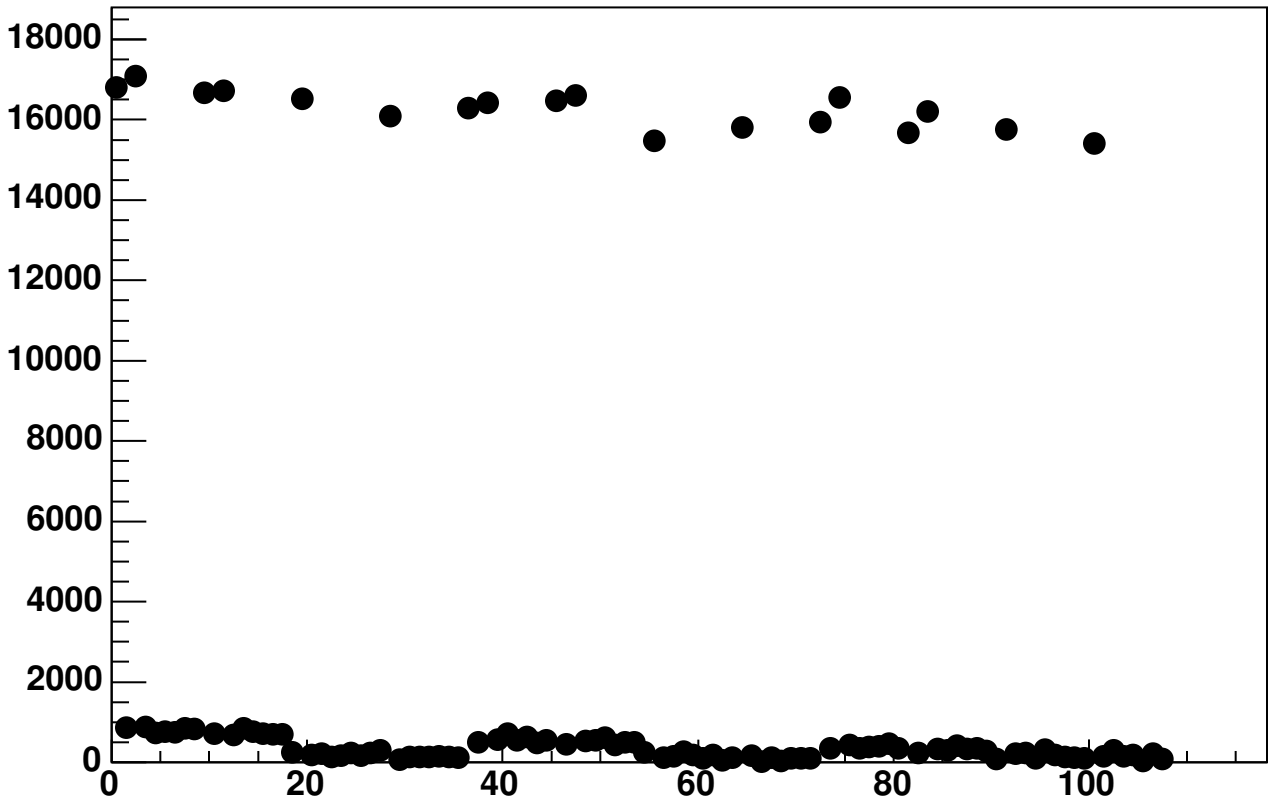
Enable 0, Hold=35, DAC=30000, ADC Mean vs 18*Chip+Chan



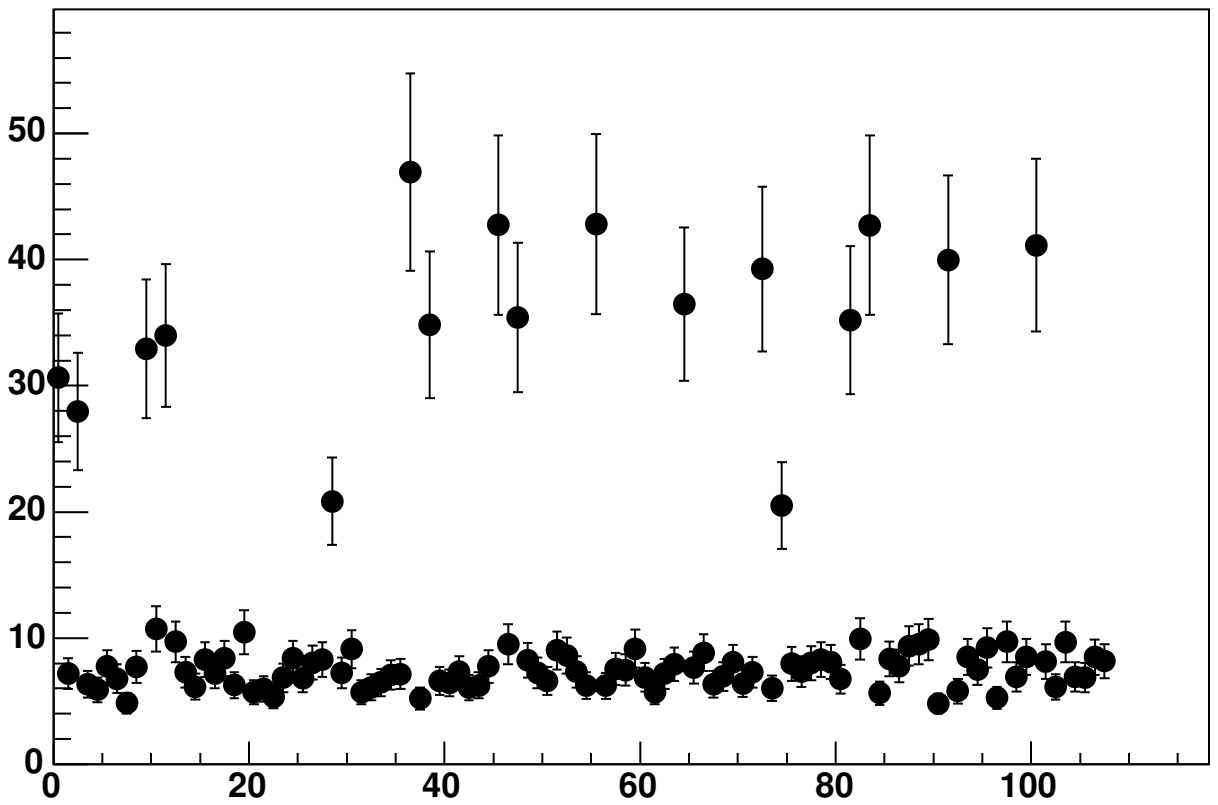
Enable 0, Hold=35, DAC=30000, ADC Noise vs 18*Chip+Chan



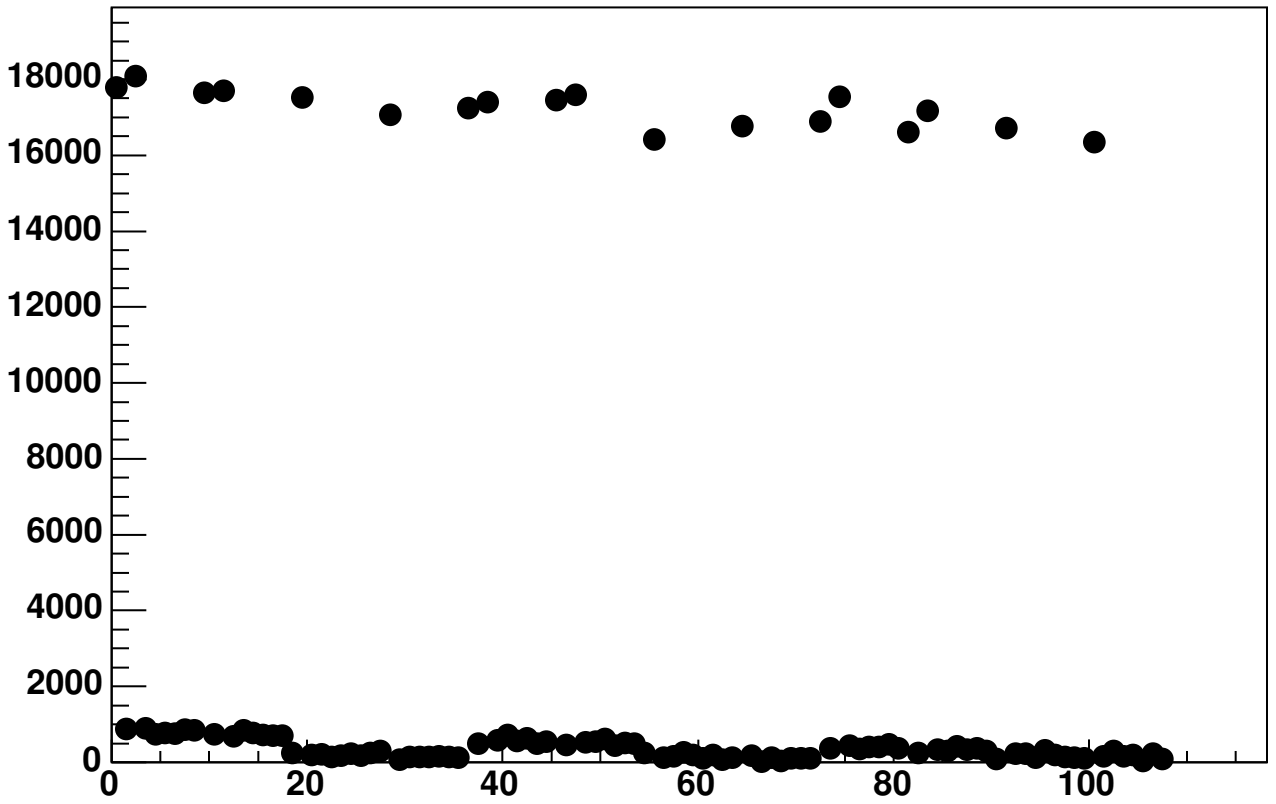
Enable 0, Hold=35, DAC=32000, ADC Mean vs 18*Chip+Chan



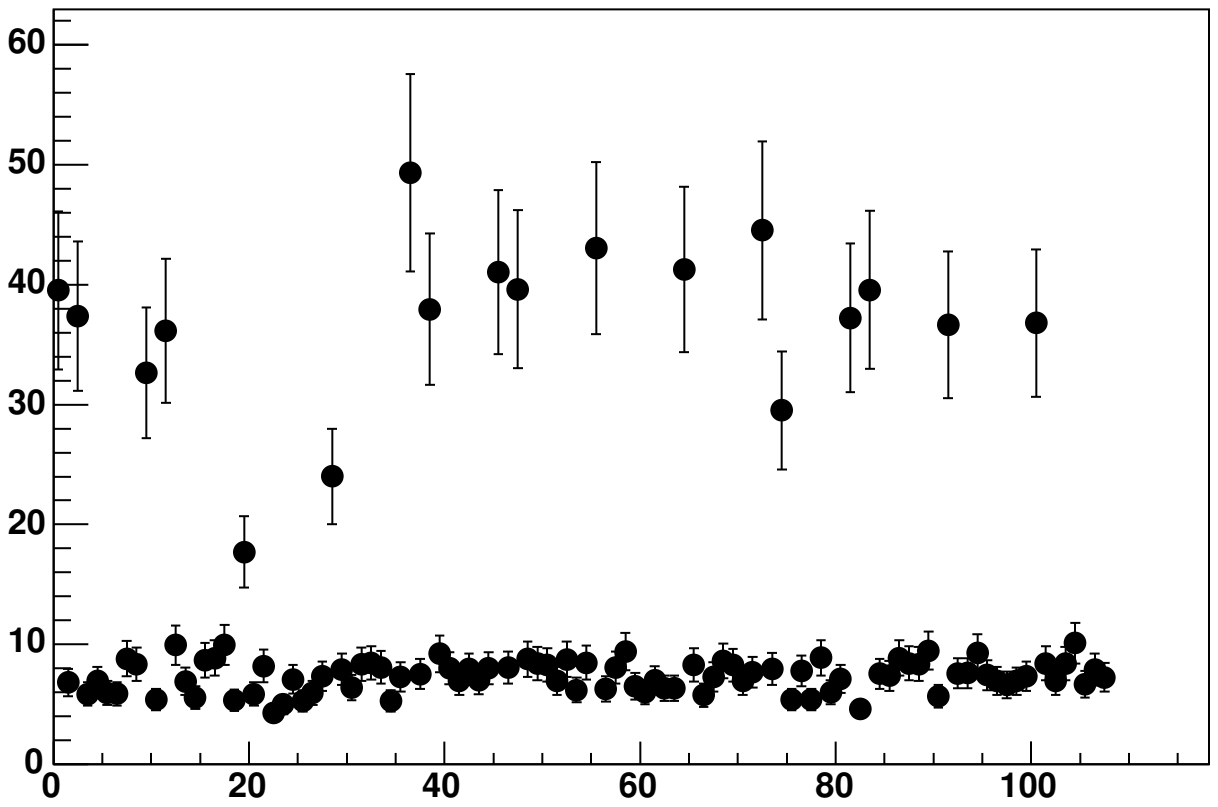
Enable 0, Hold=35, DAC=32000, ADC Noise vs 18*Chip+Chan



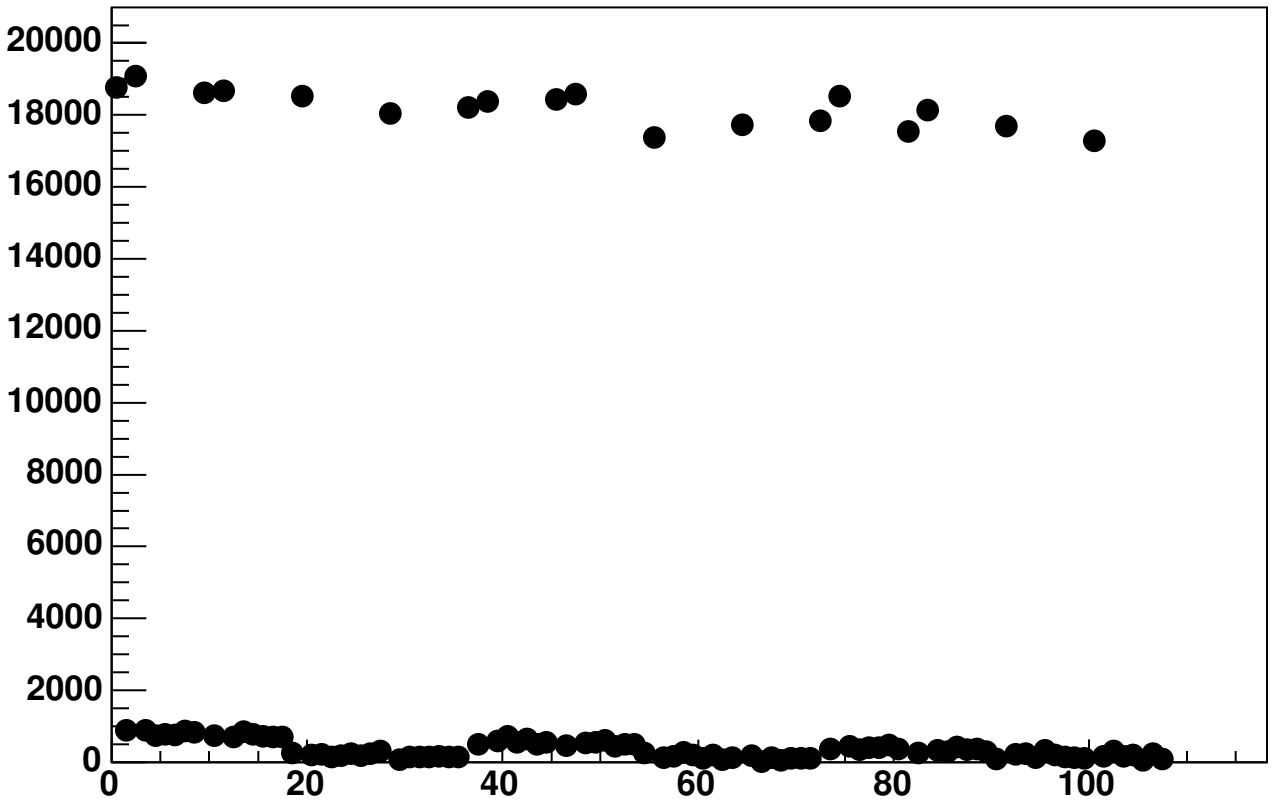
Enable 0, Hold=35, DAC=34000, ADC Mean vs 18*Chip+Chan



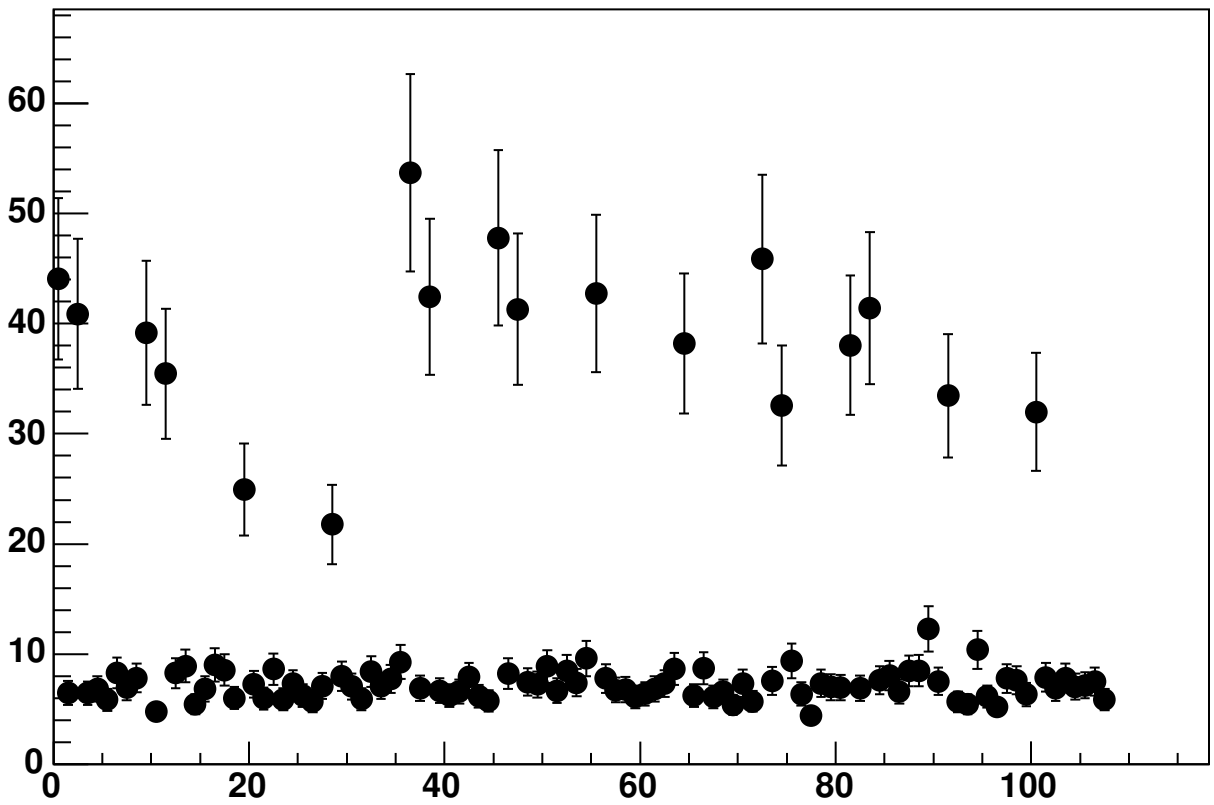
Enable 0, Hold=35, DAC=34000, ADC Noise vs 18*Chip+Chan



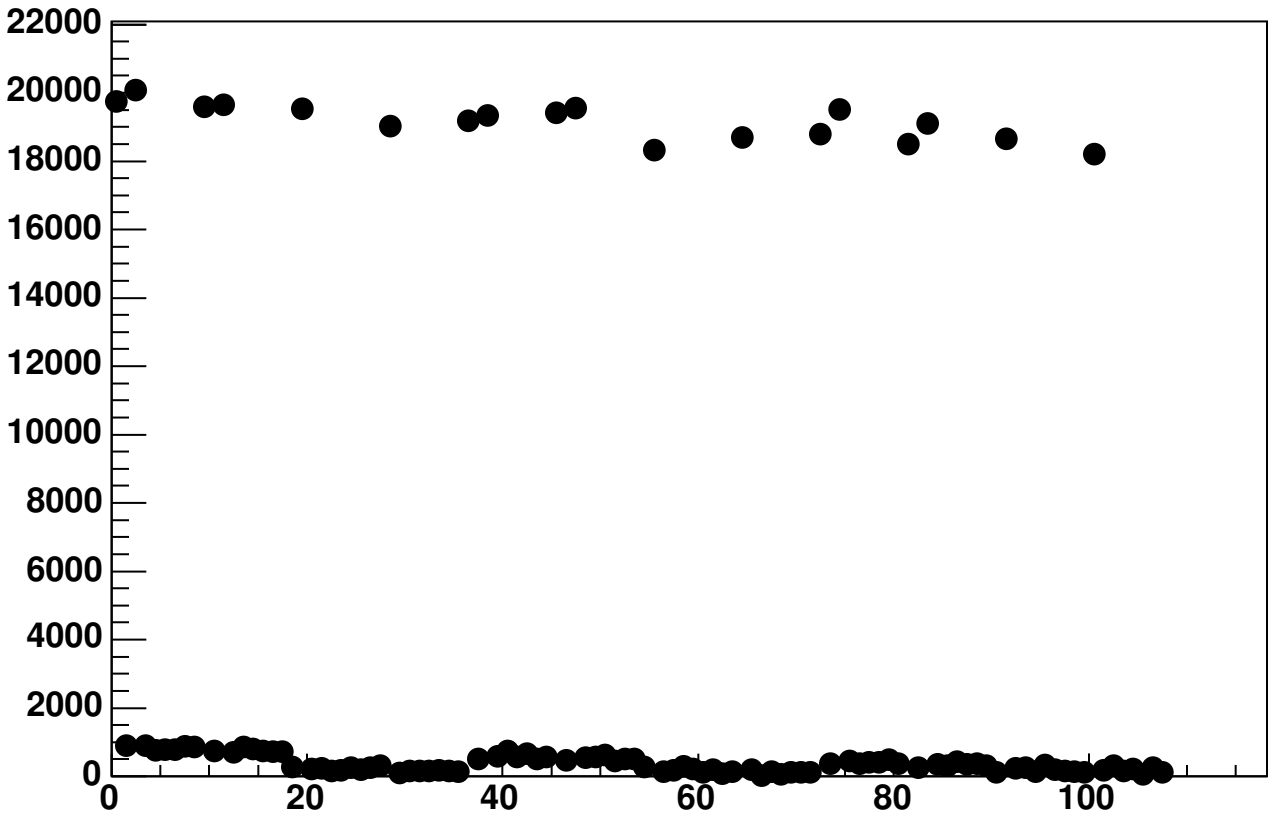
Enable 0, Hold=35, DAC=36000, ADC Mean vs 18*Chip+Chan



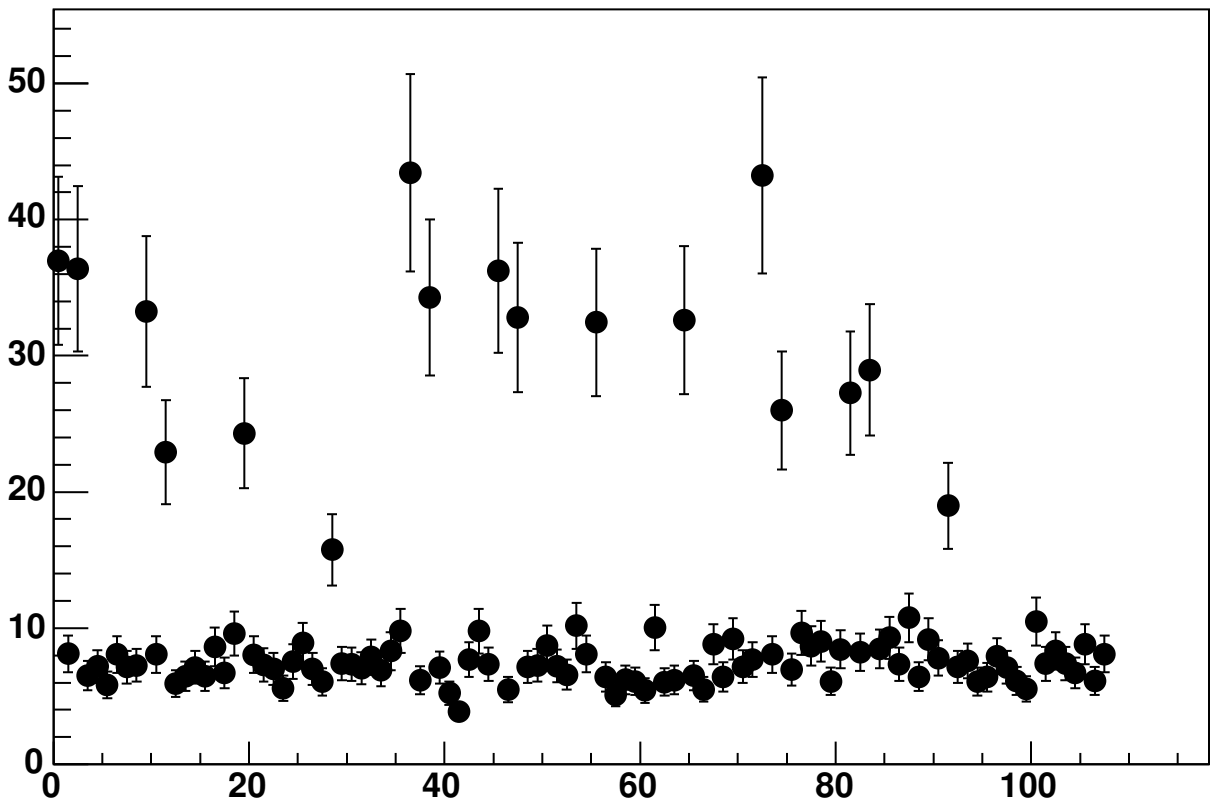
Enable 0, Hold=35, DAC=36000, ADC Noise vs 18*Chip+Chan



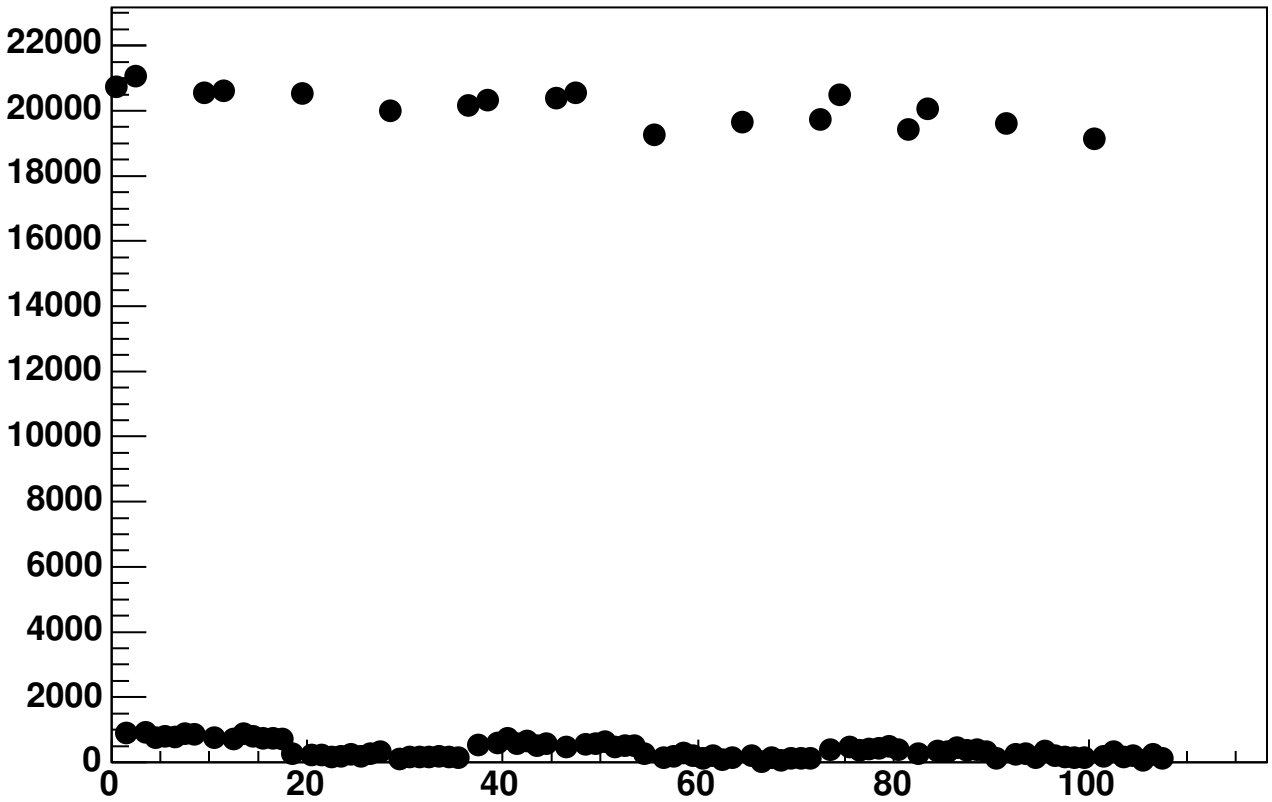
Enable 0, Hold=35, DAC=38000, ADC Mean vs 18*Chip+Chan



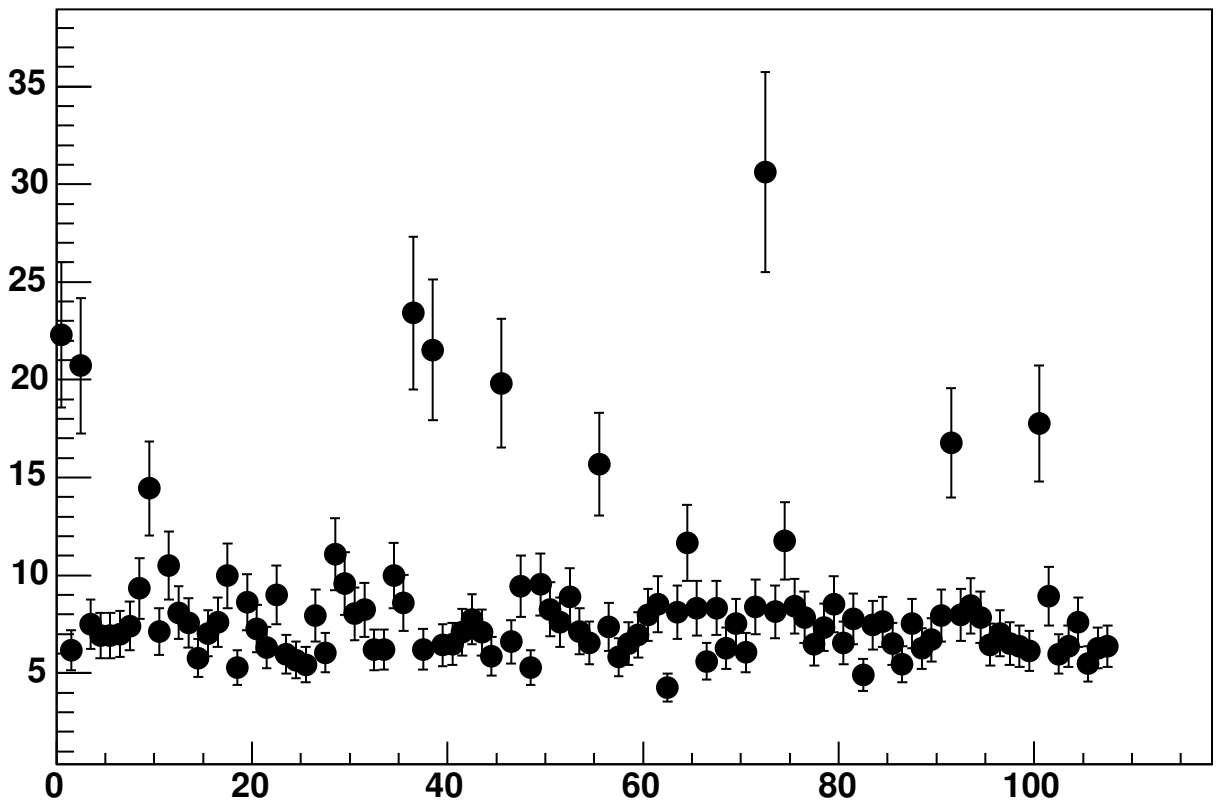
Enable 0, Hold=35, DAC=38000, ADC Noise vs 18*Chip+Chan



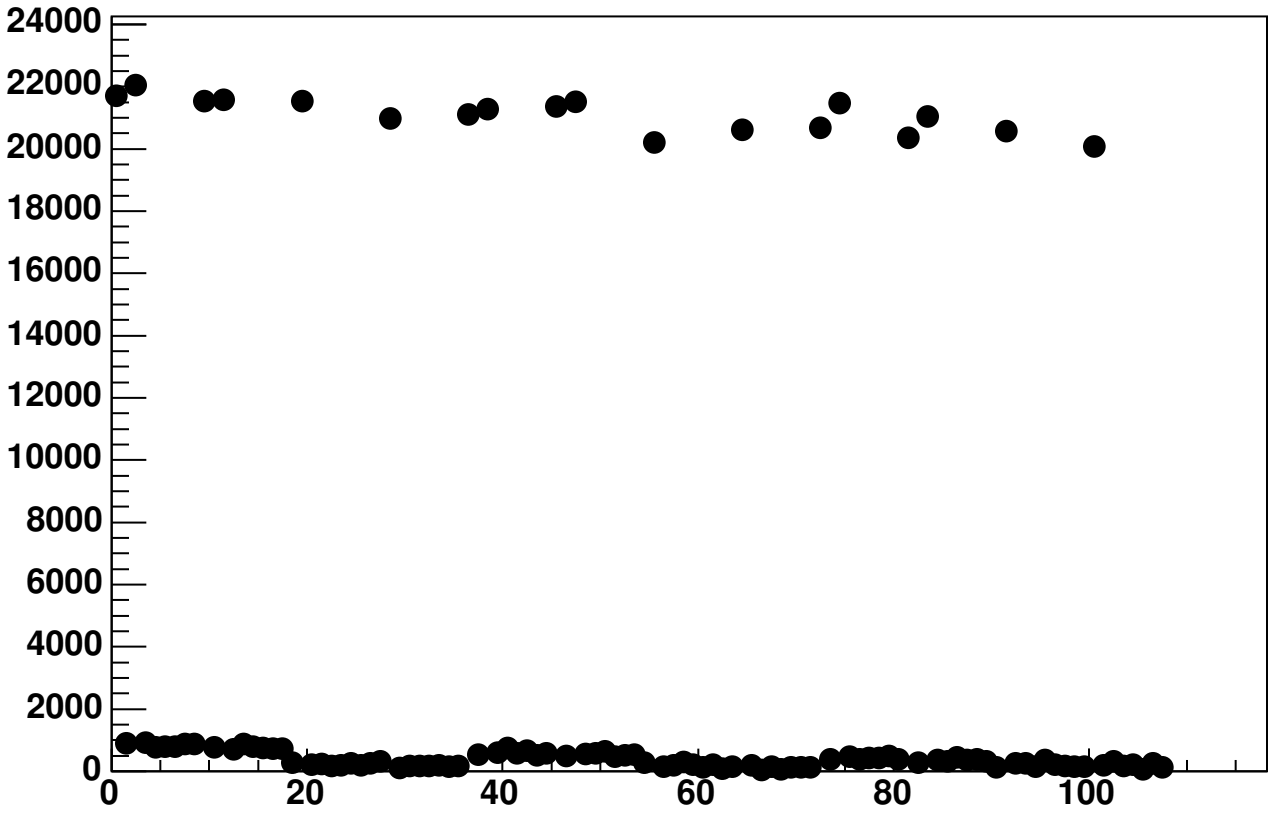
Enable 0, Hold=35, DAC=40000, ADC Mean vs 18*Chip+Chan



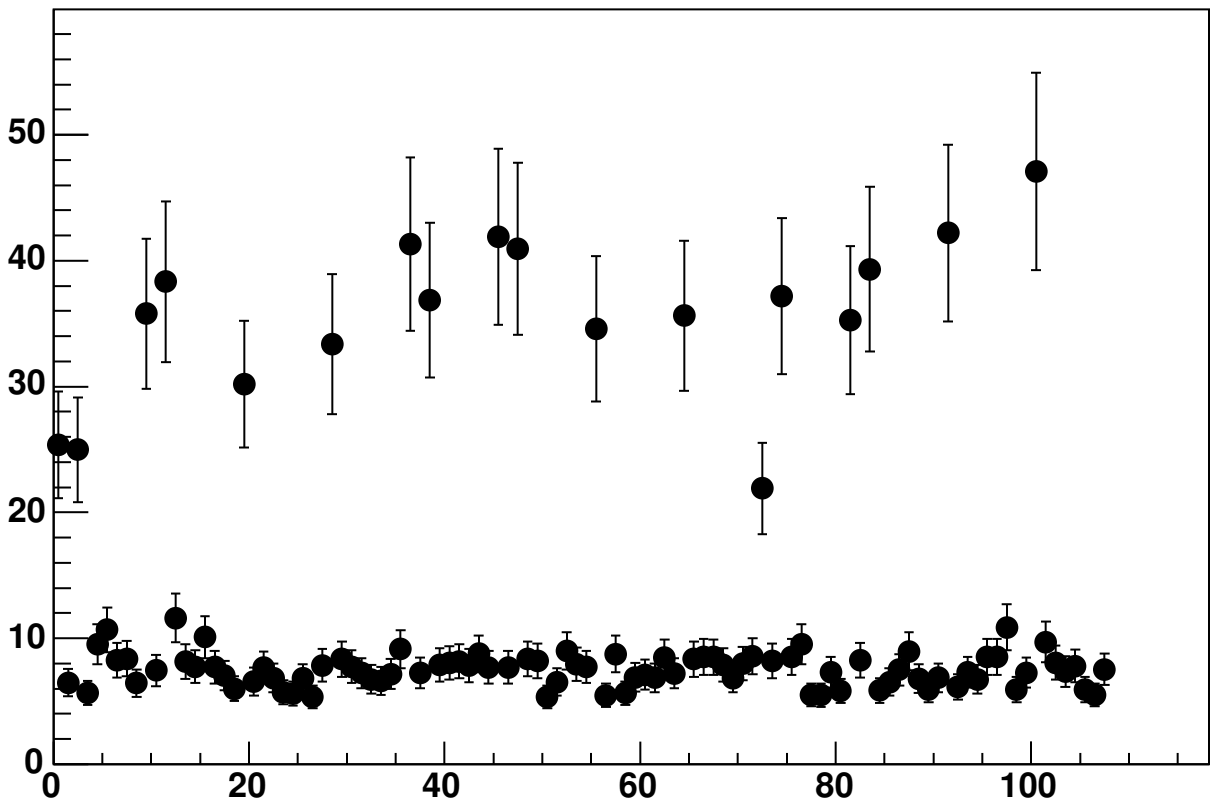
Enable 0, Hold=35, DAC=40000, ADC Noise vs 18*Chip+Chan



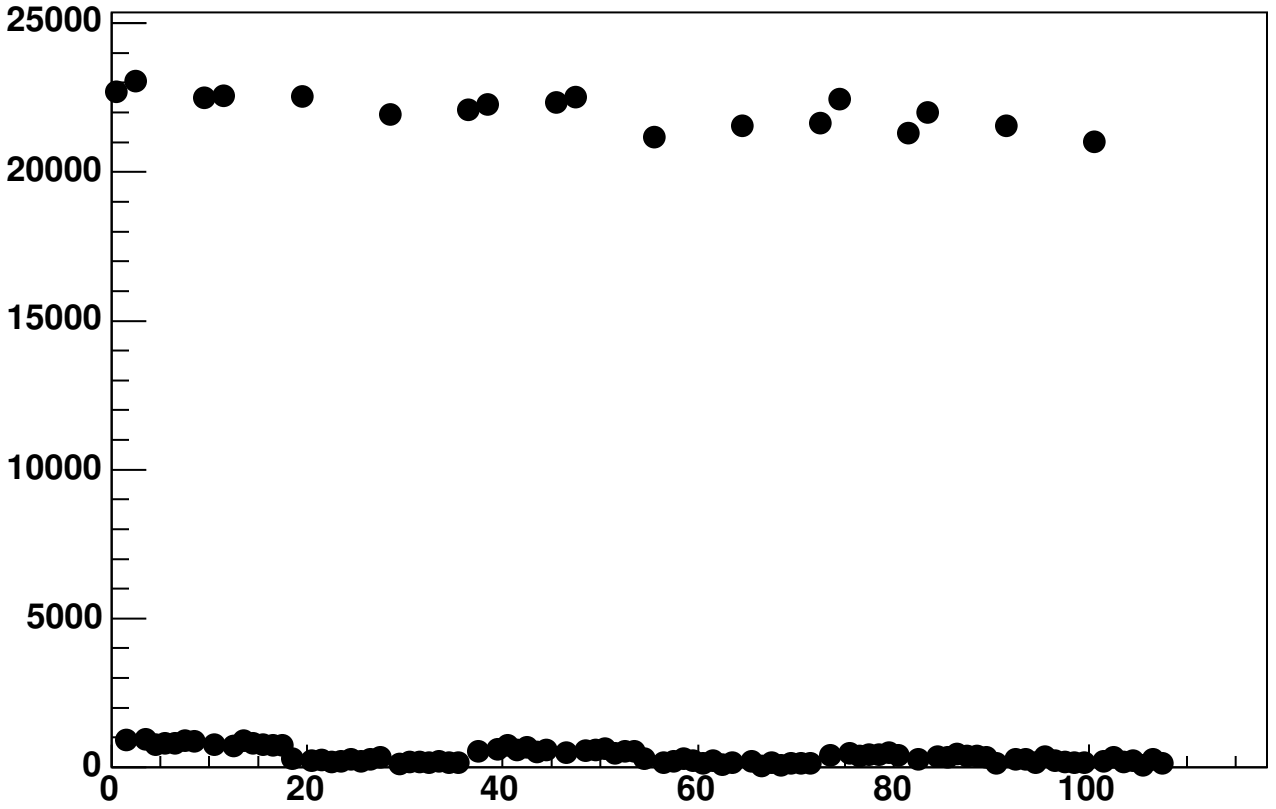
Enable 0, Hold=35, DAC=42000, ADC Mean vs 18*Chip+Chan



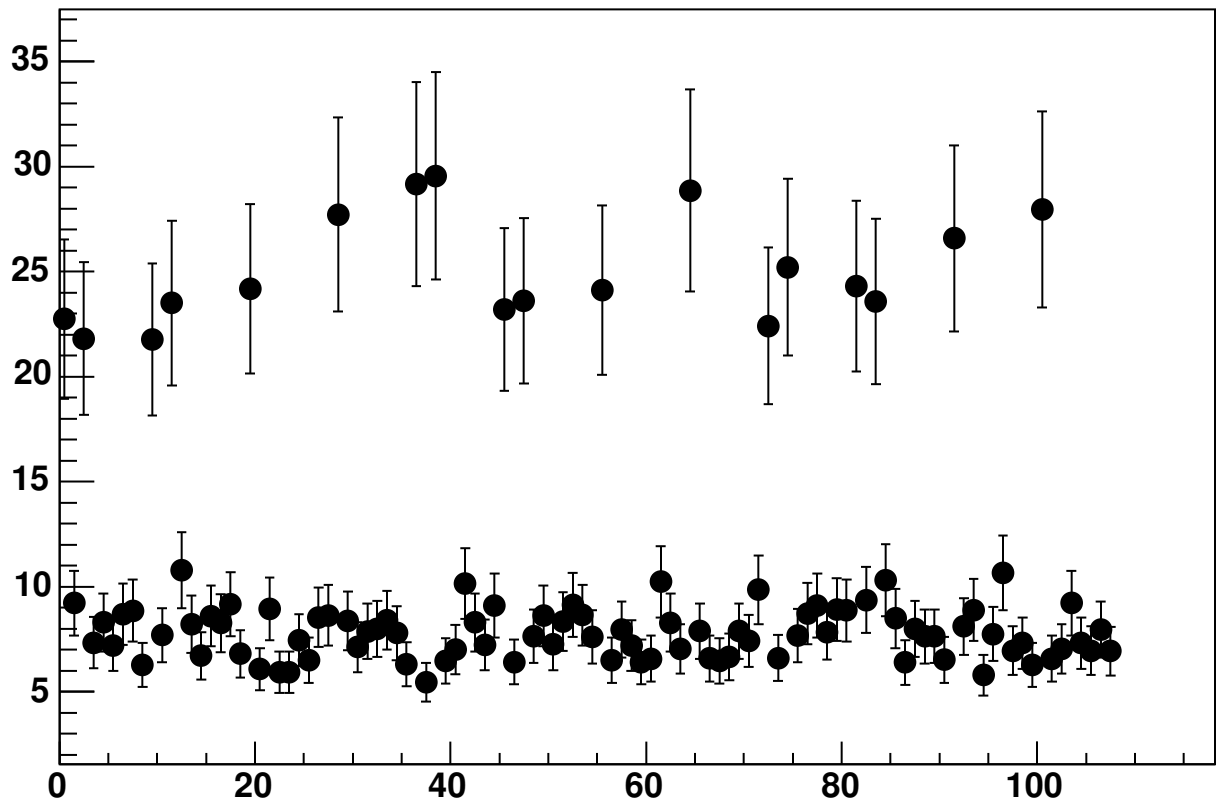
Enable 0, Hold=35, DAC=42000, ADC Noise vs 18*Chip+Chan



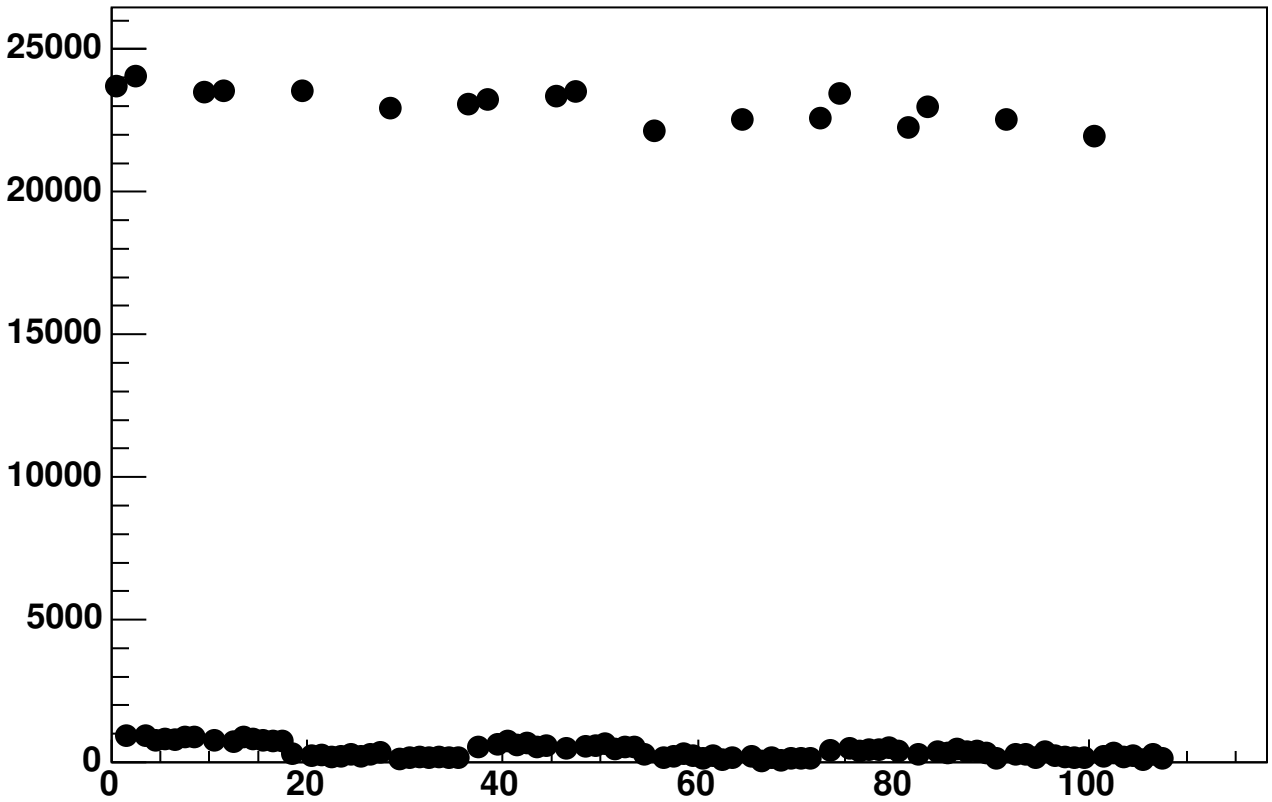
Enable 0, Hold=35, DAC=44000, ADC Mean vs 18*Chip+Chan



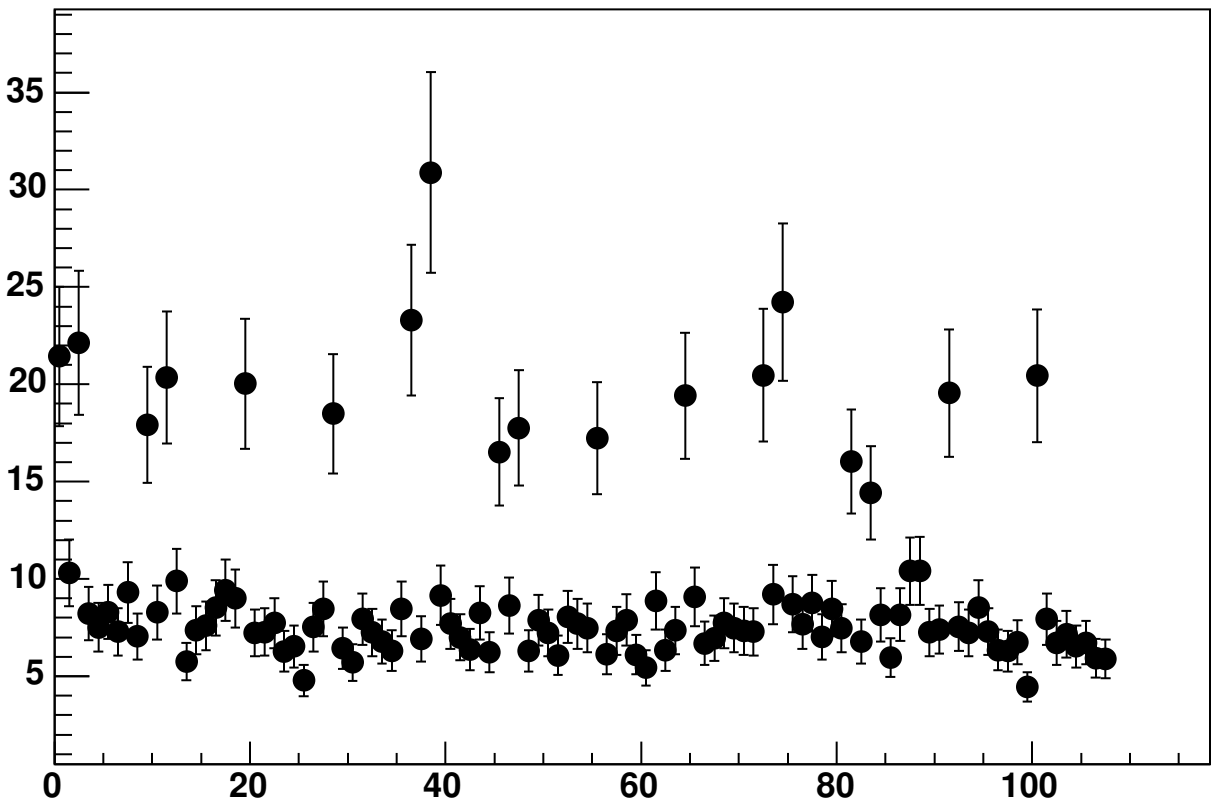
Enable 0, Hold=35, DAC=44000, ADC Noise vs 18*Chip+Chan



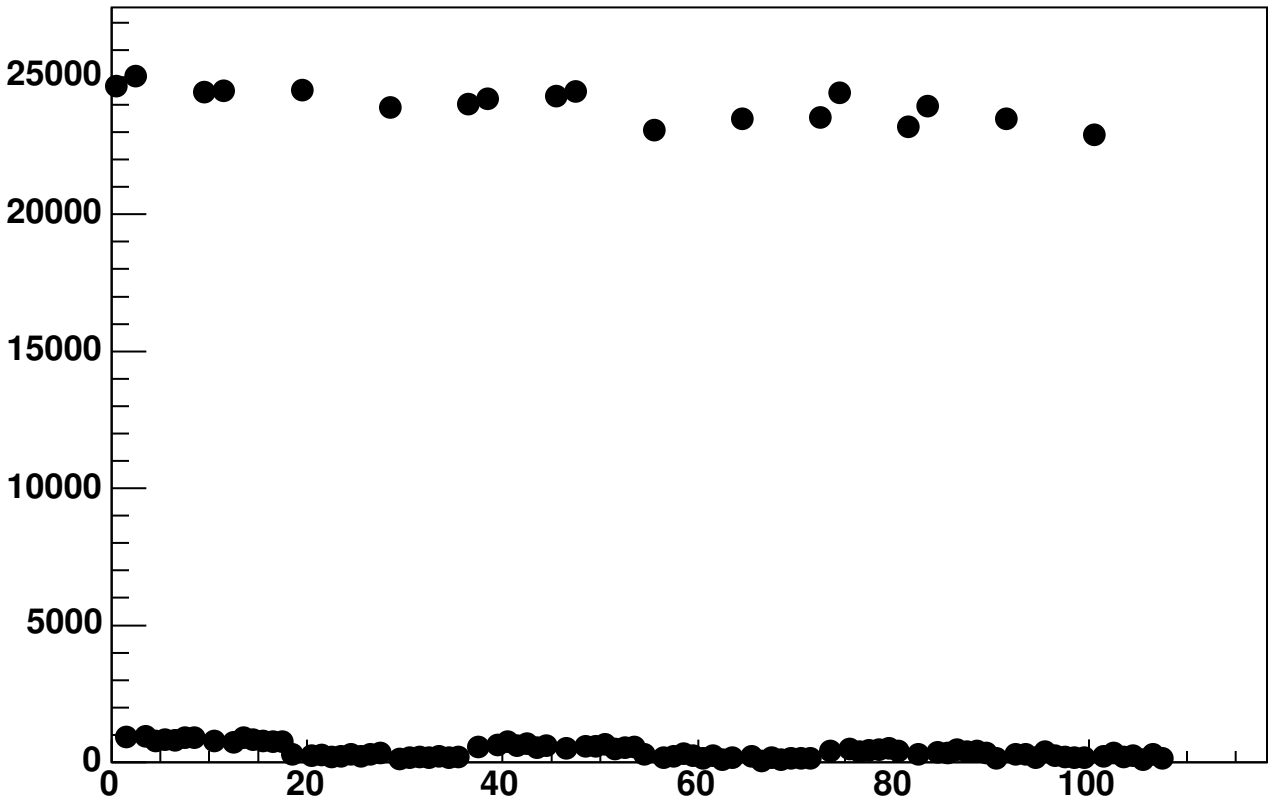
Enable 0, Hold=35, DAC=46000, ADC Mean vs 18*Chip+Chan



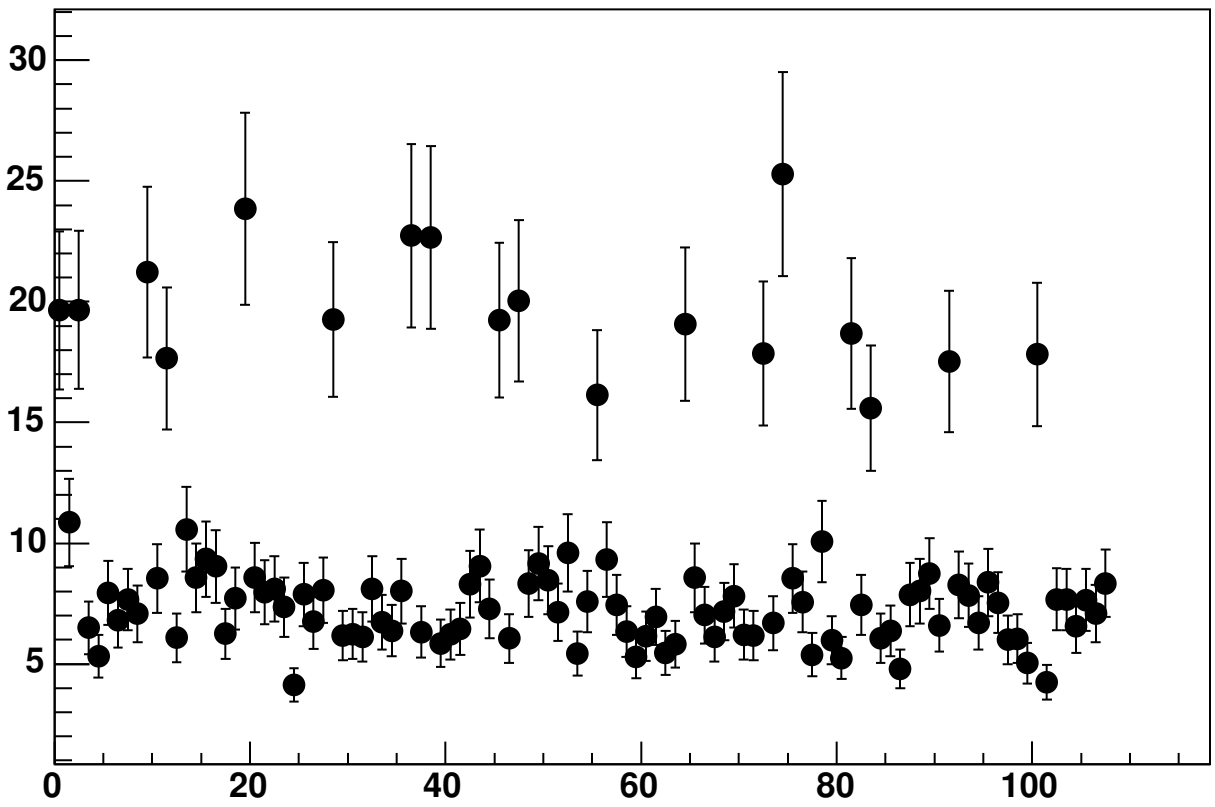
Enable 0, Hold=35, DAC=46000, ADC Noise vs 18*Chip+Chan



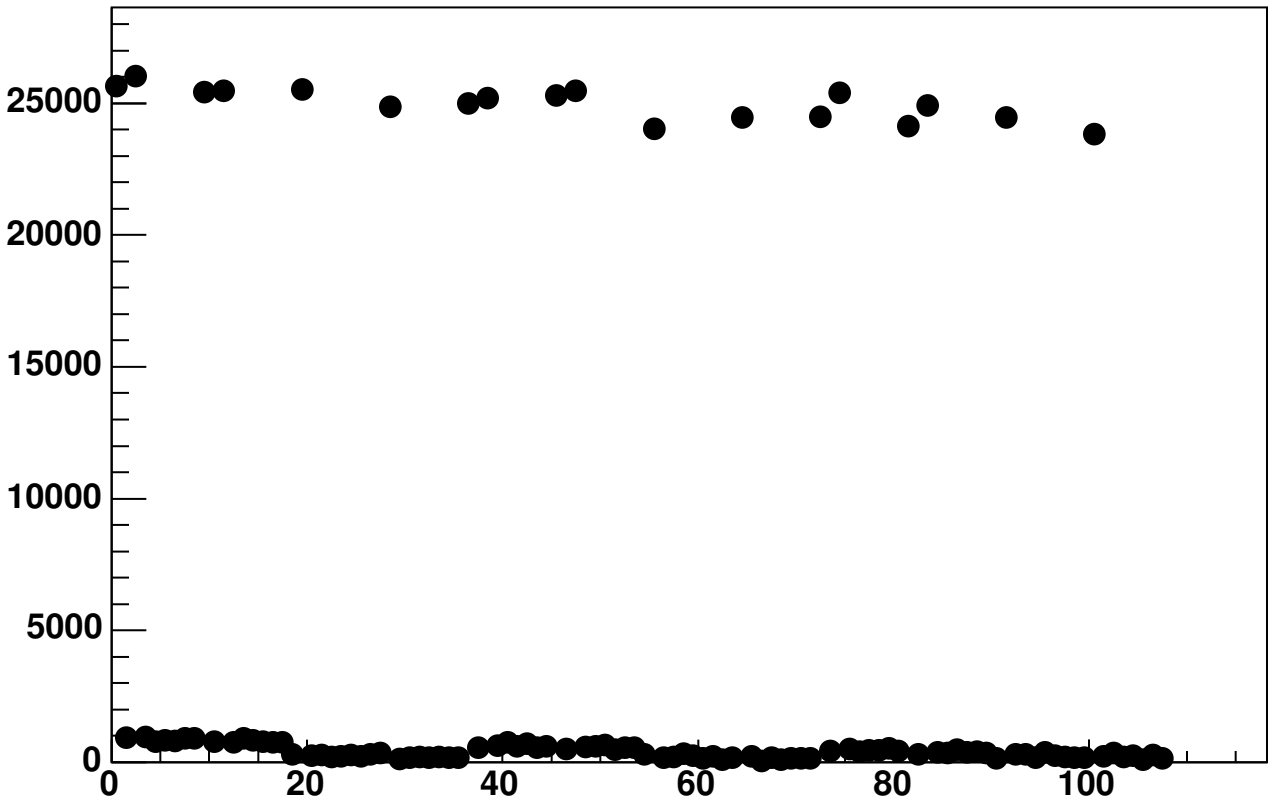
Enable 0, Hold=35, DAC=48000, ADC Mean vs 18*Chip+Chan



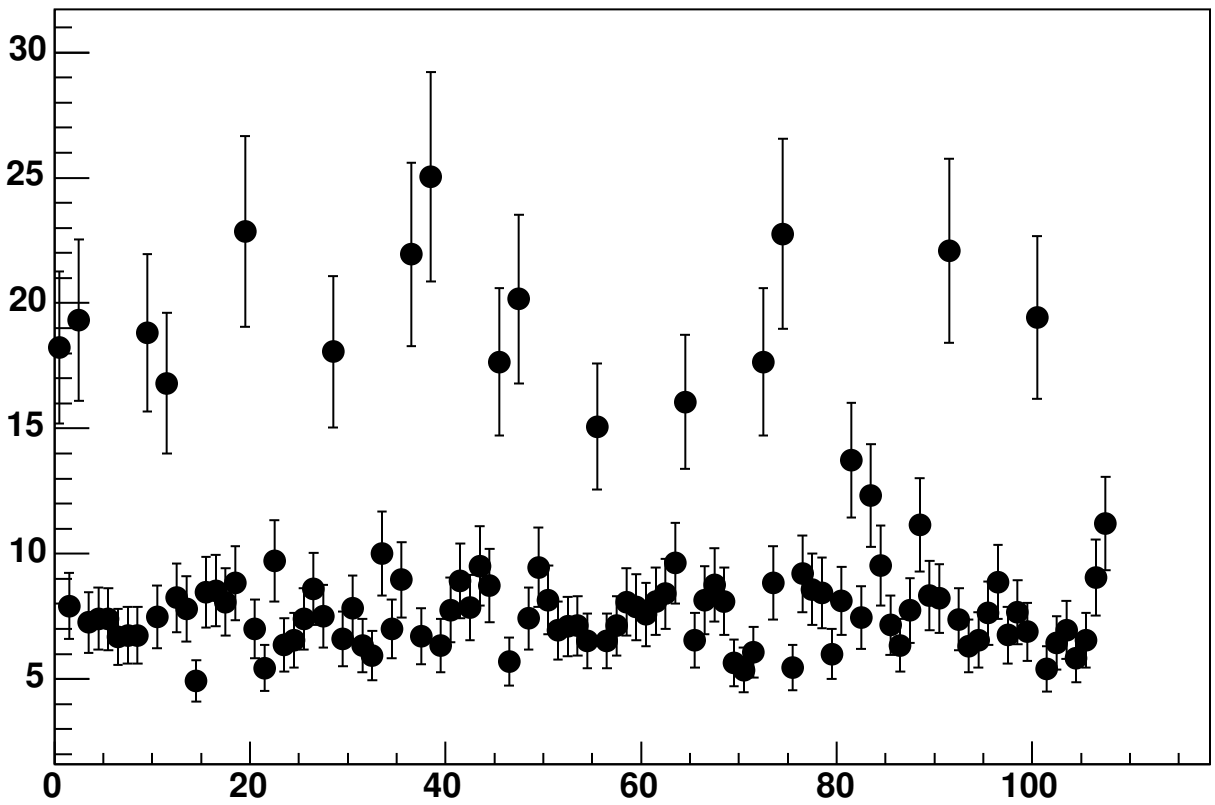
Enable 0, Hold=35, DAC=48000, ADC Noise vs 18*Chip+Chan



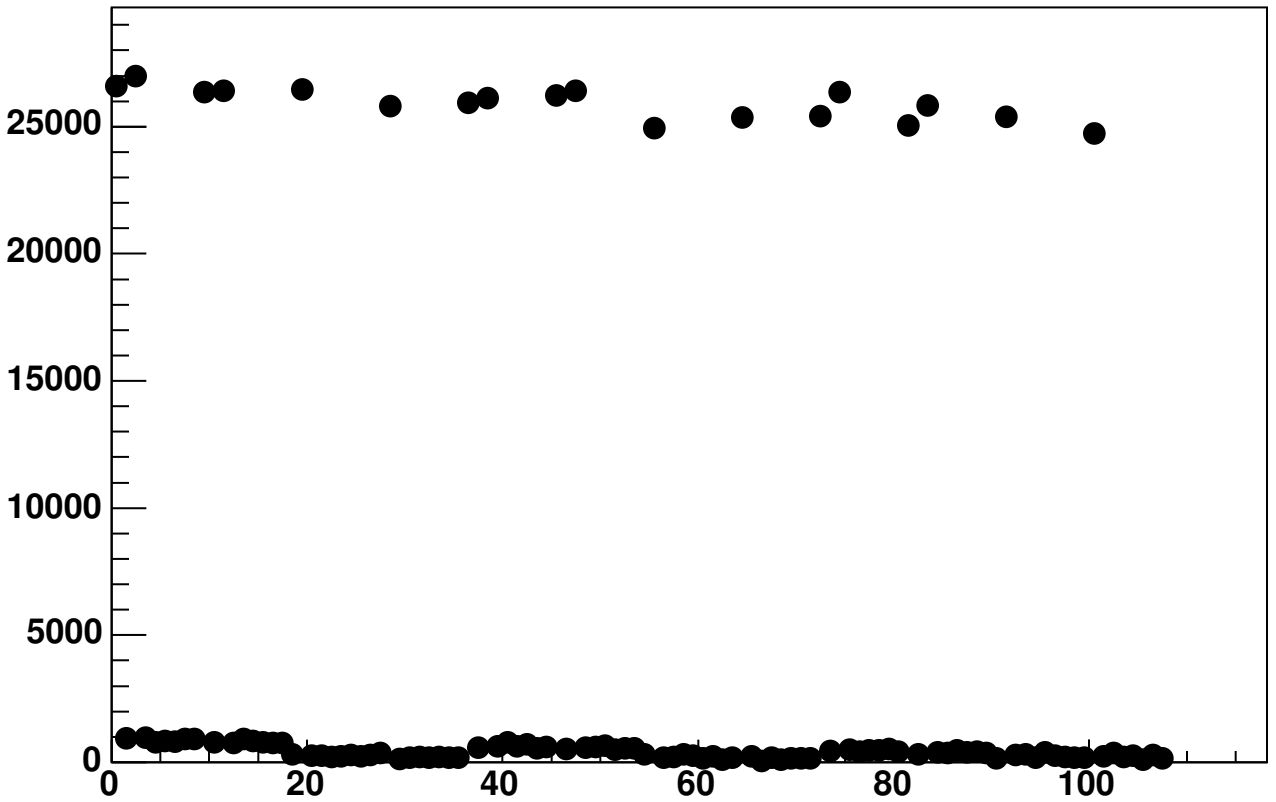
Enable 0, Hold=35, DAC=50000, ADC Mean vs 18*Chip+Chan



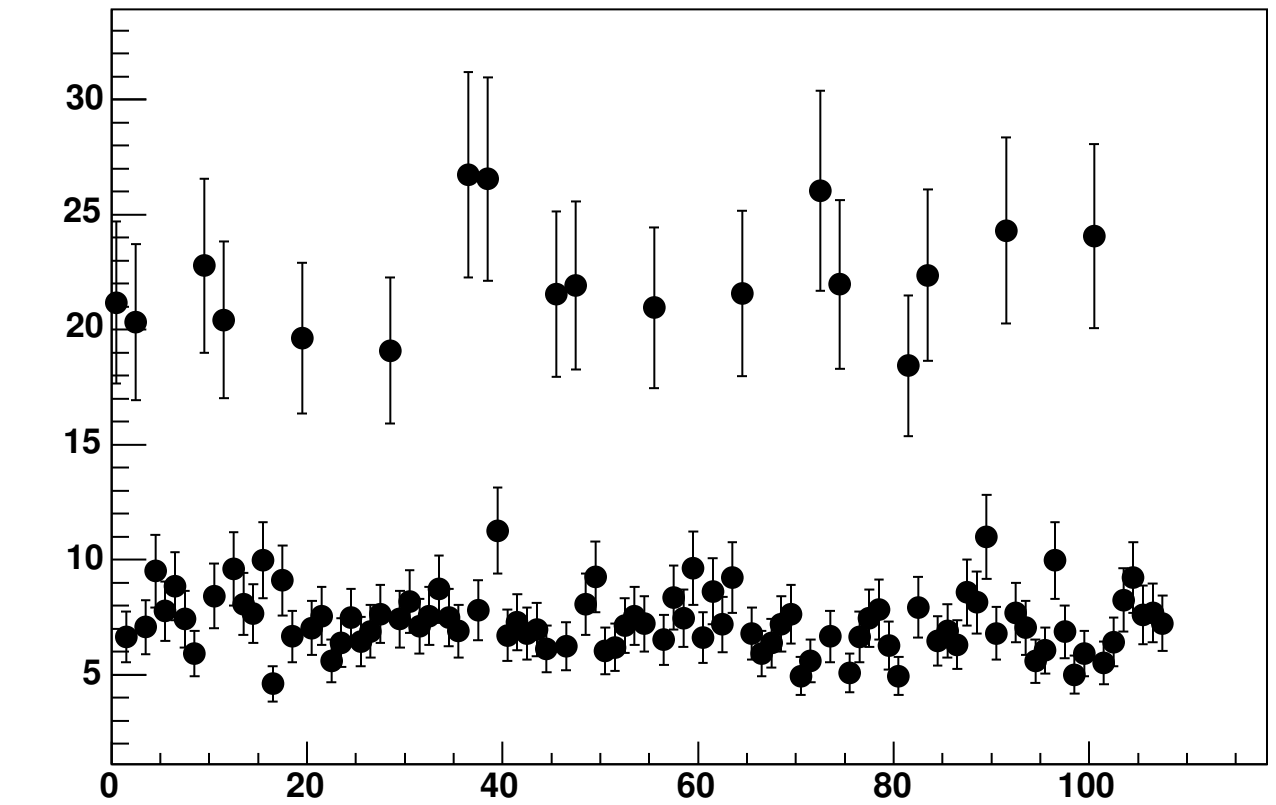
Enable 0, Hold=35, DAC=50000, ADC Noise vs 18*Chip+Chan



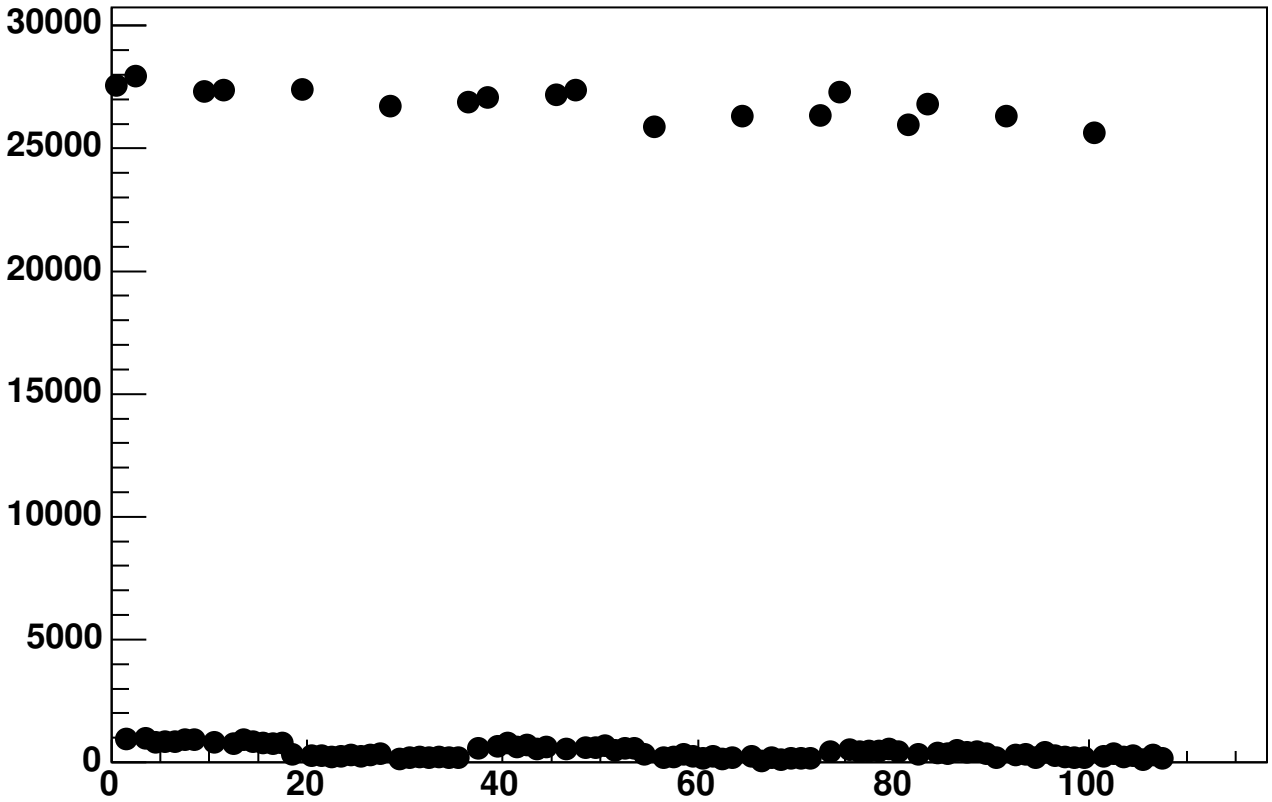
Enable 0, Hold=35, DAC=52000, ADC Mean vs 18*Chip+Chan



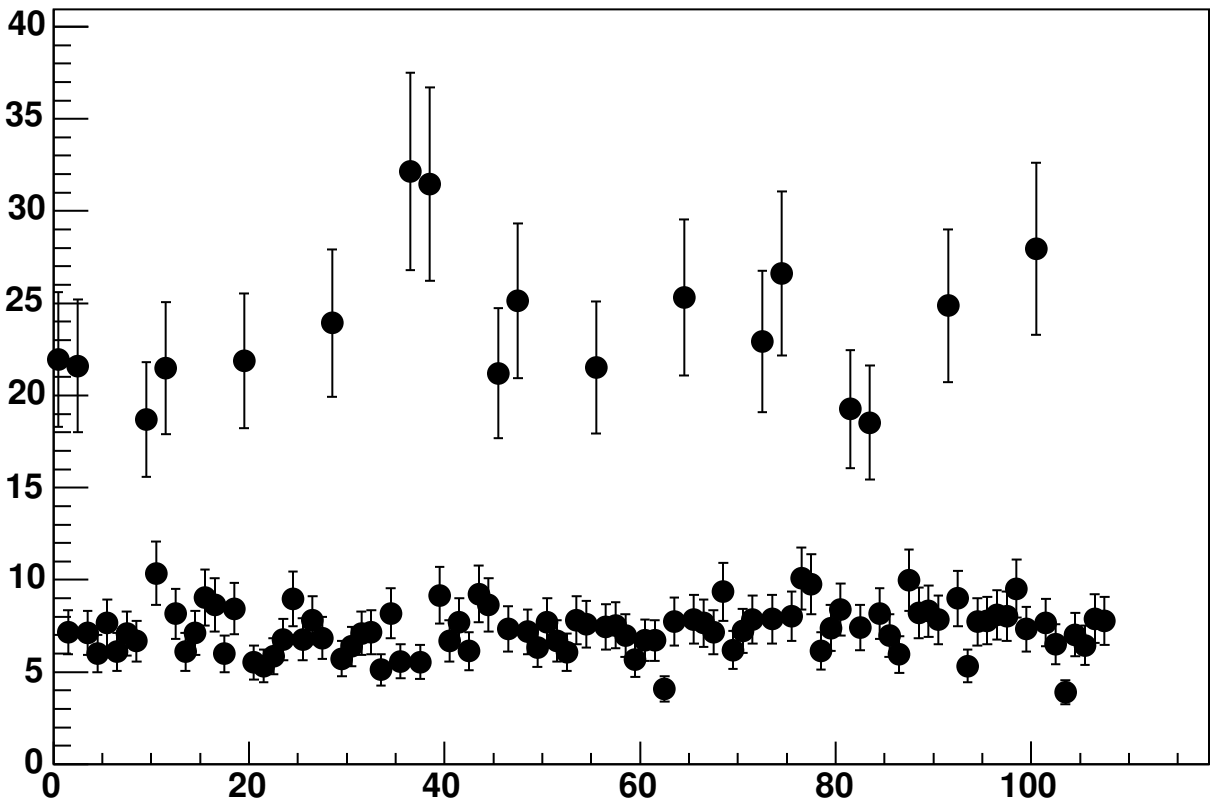
Enable 0, Hold=35, DAC=52000, ADC Noise vs 18*Chip+Chan



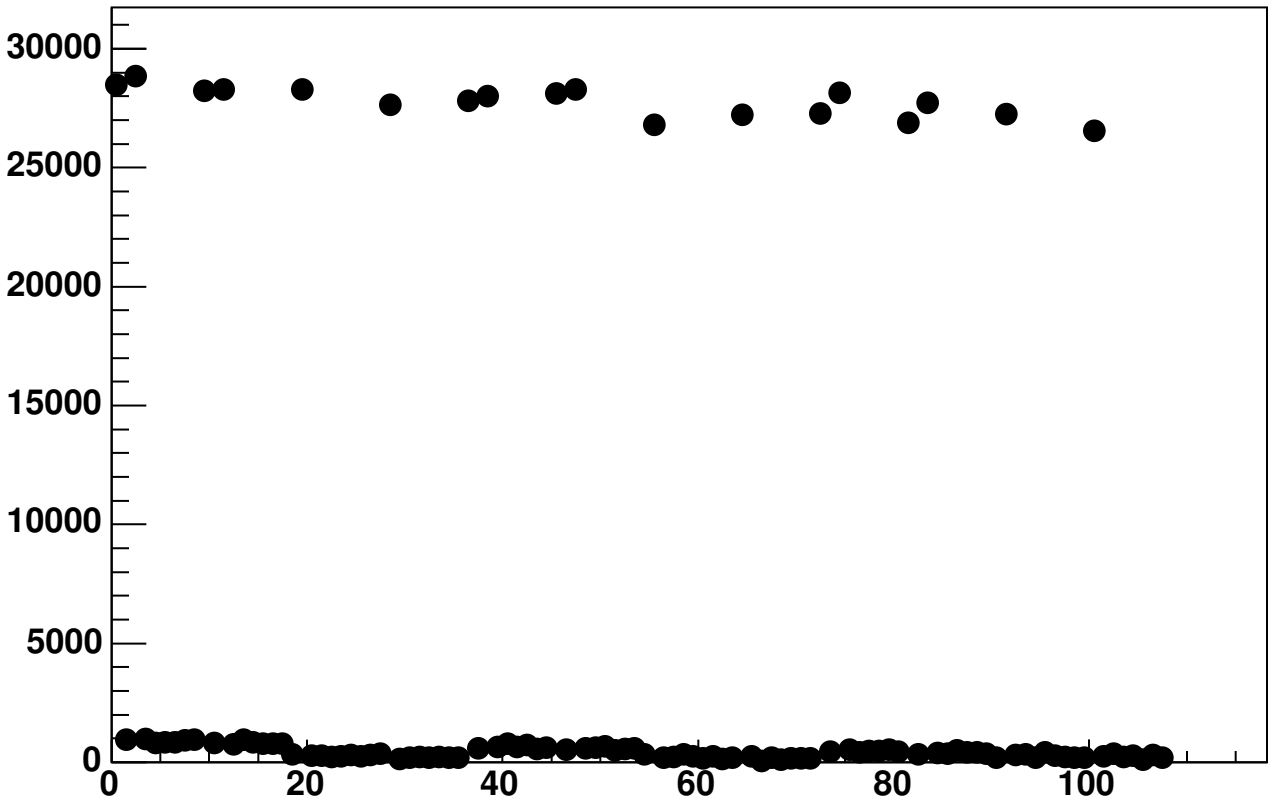
Enable 0, Hold=35, DAC=54000, ADC Mean vs 18*Chip+Chan



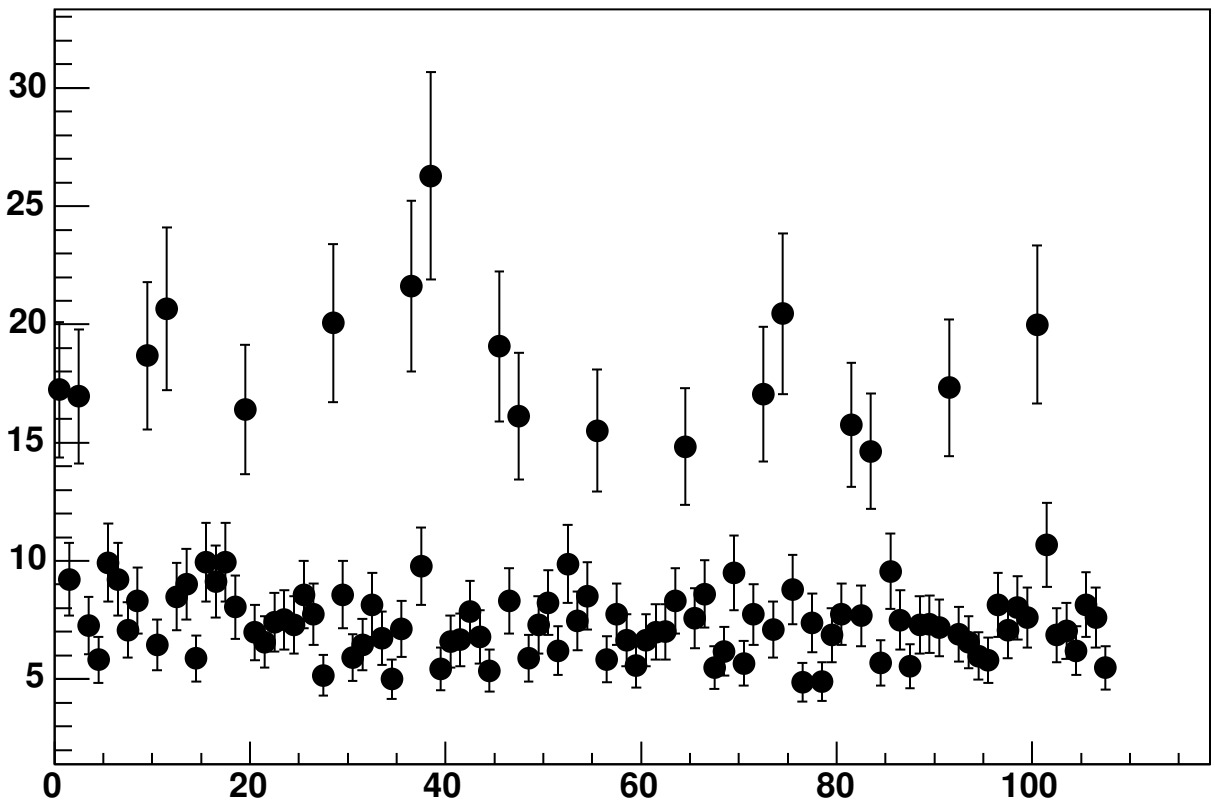
Enable 0, Hold=35, DAC=54000, ADC Noise vs 18*Chip+Chan



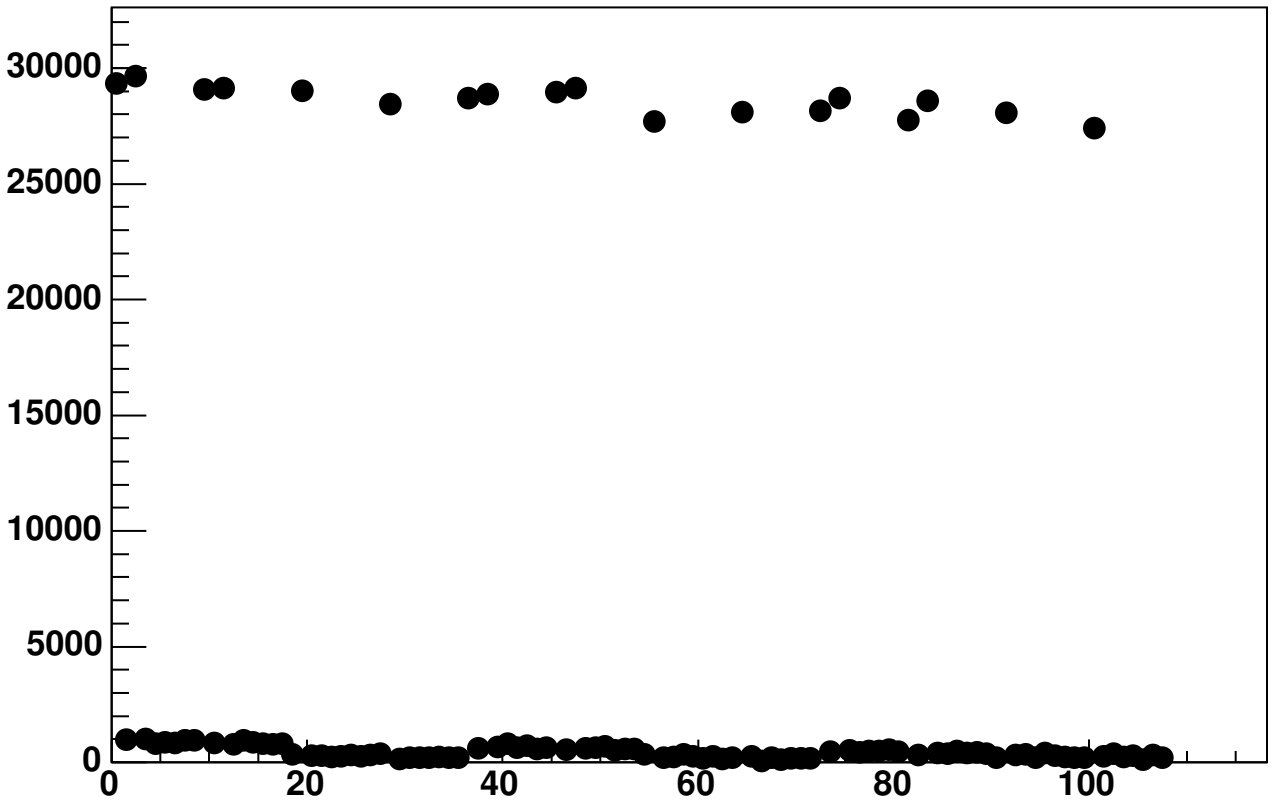
Enable 0, Hold=35, DAC=56000, ADC Mean vs 18*Chip+Chan



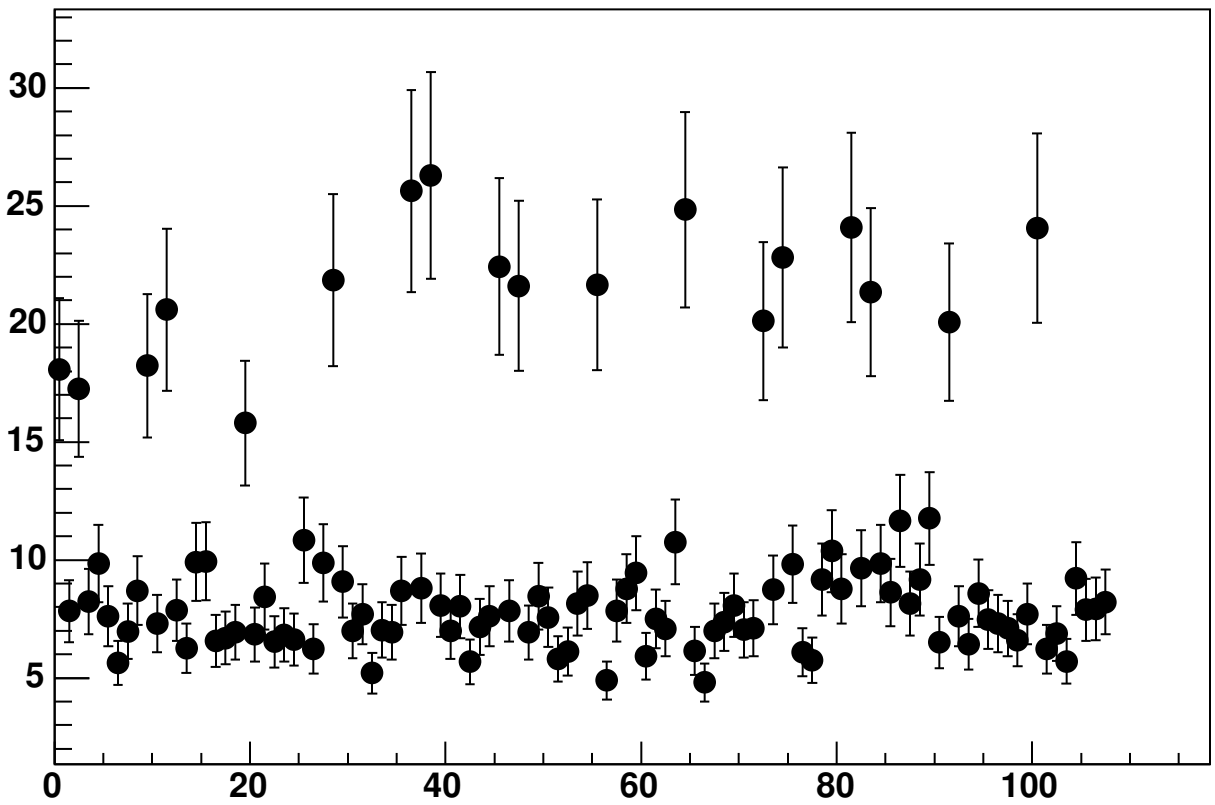
Enable 0, Hold=35, DAC=56000, ADC Noise vs 18*Chip+Chan



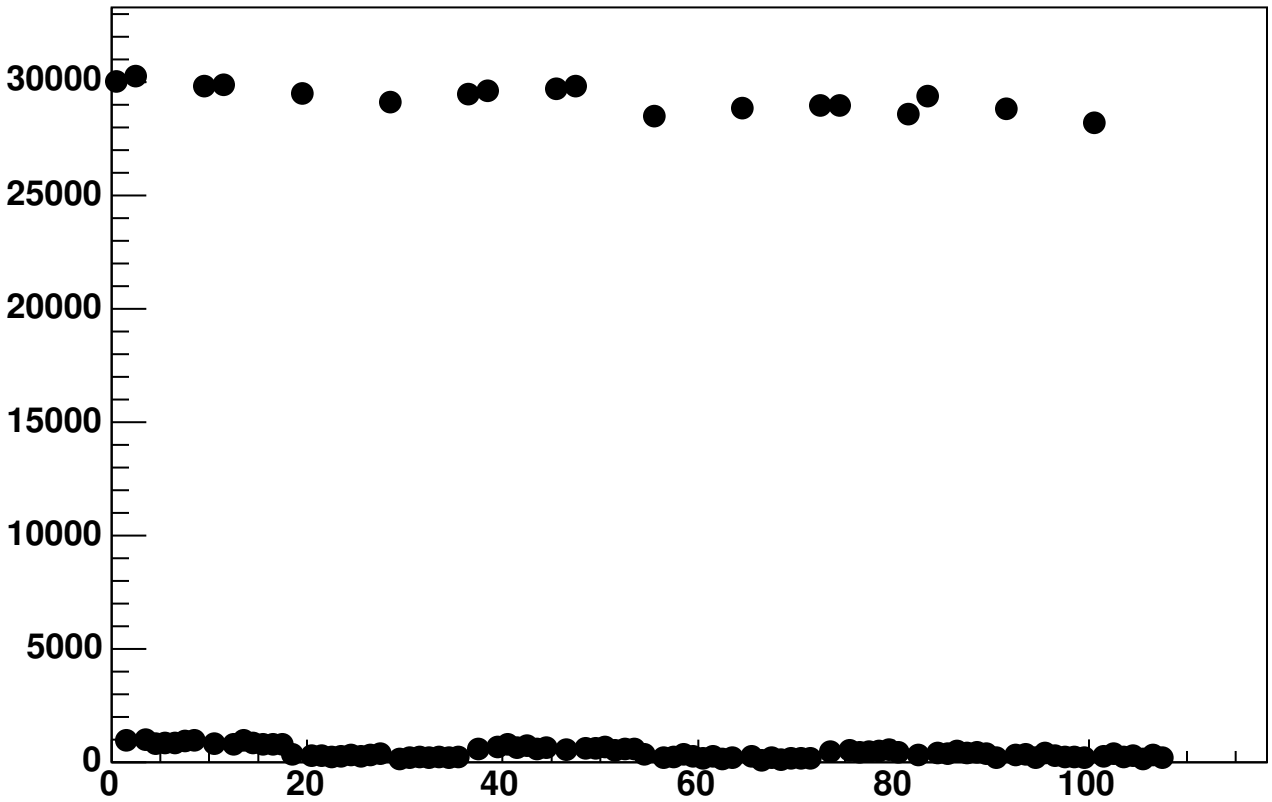
Enable 0, Hold=35, DAC=58000, ADC Mean vs 18*Chip+Chan



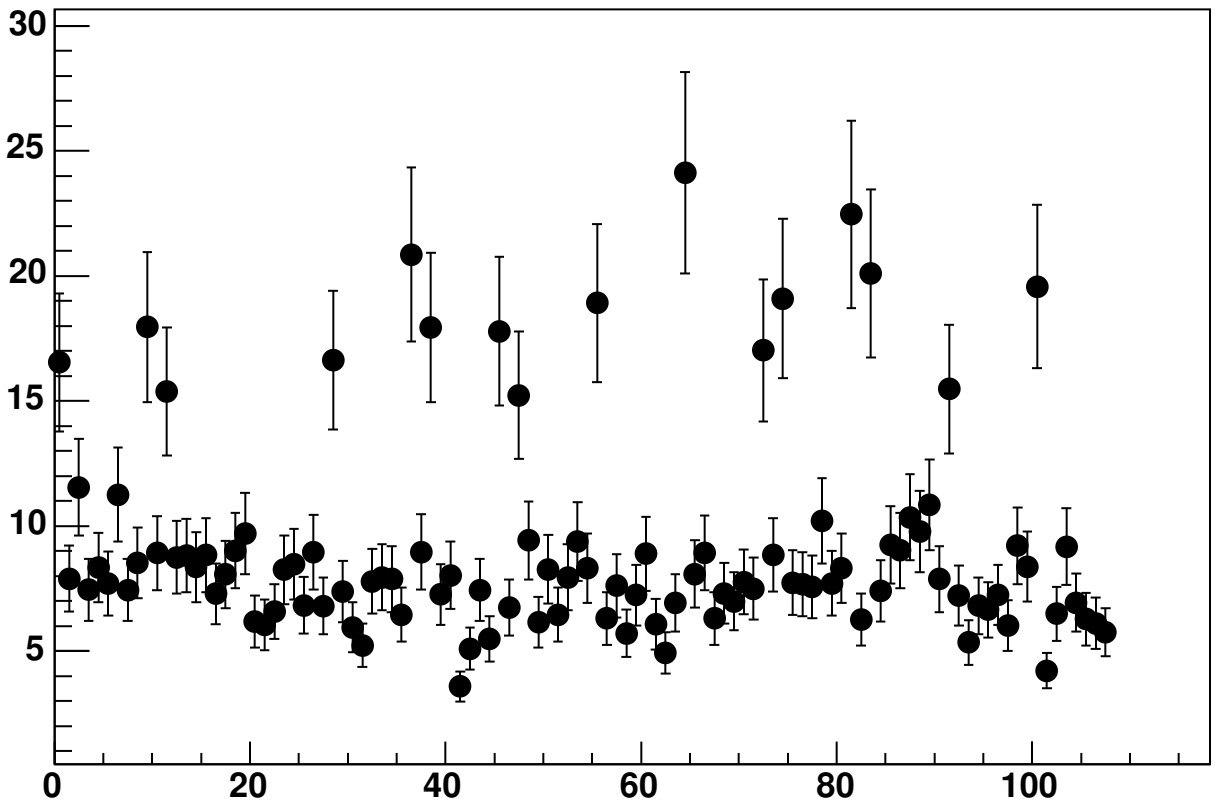
Enable 0, Hold=35, DAC=58000, ADC Noise vs 18*Chip+Chan



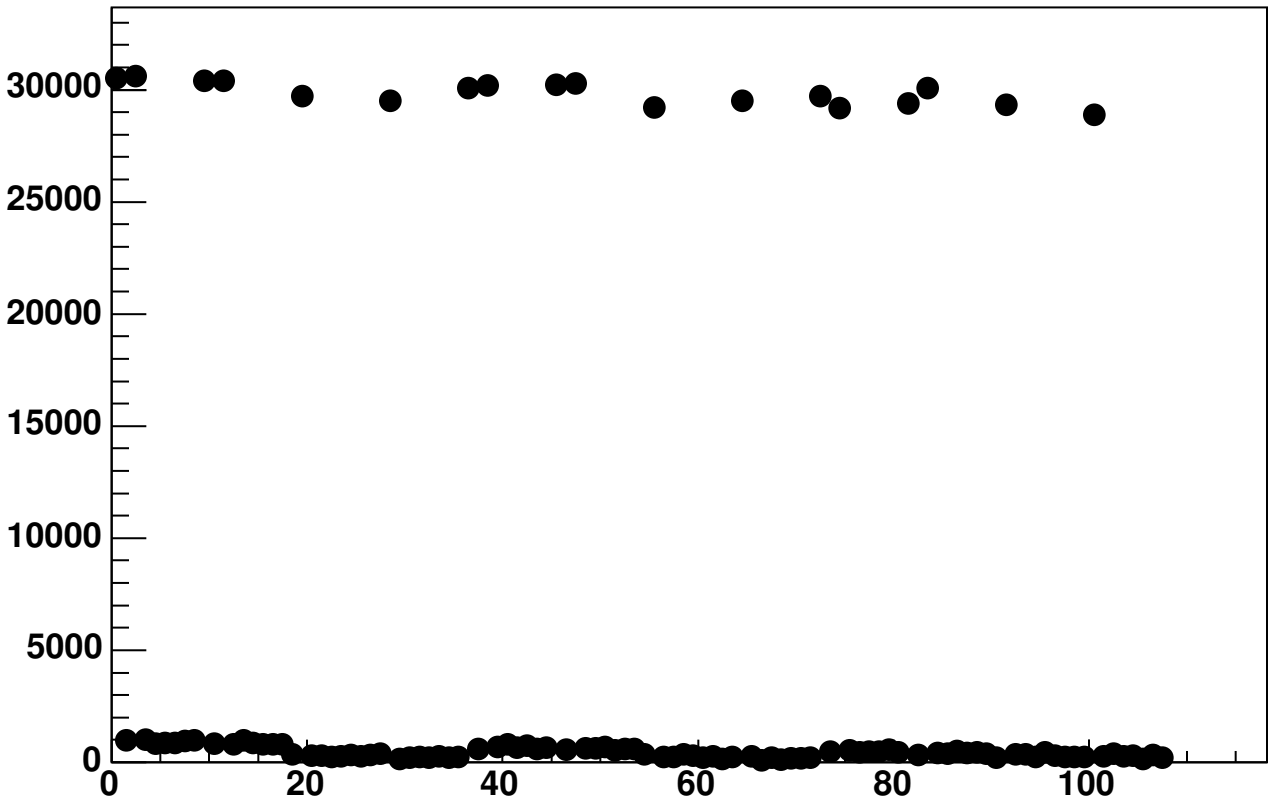
Enable 0, Hold=35, DAC=60000, ADC Mean vs 18*Chip+Chan



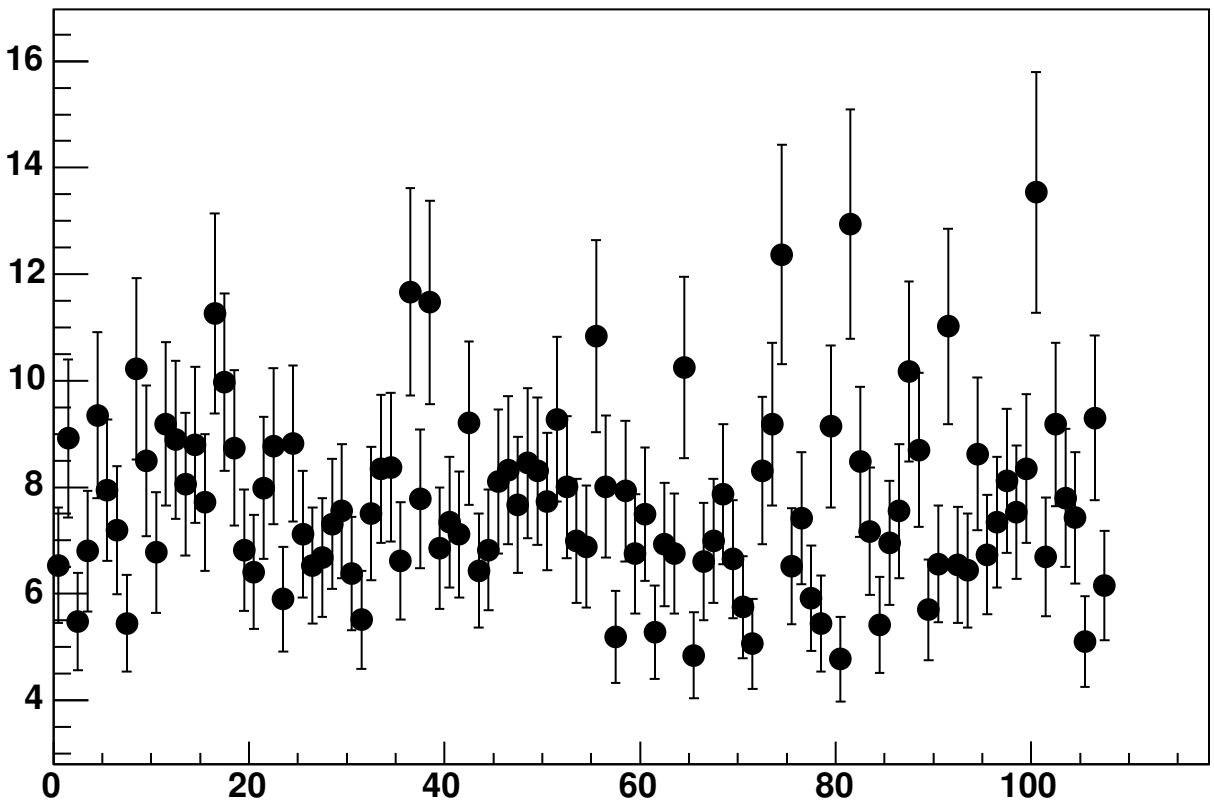
Enable 0, Hold=35, DAC=60000, ADC Noise vs 18*Chip+Chan



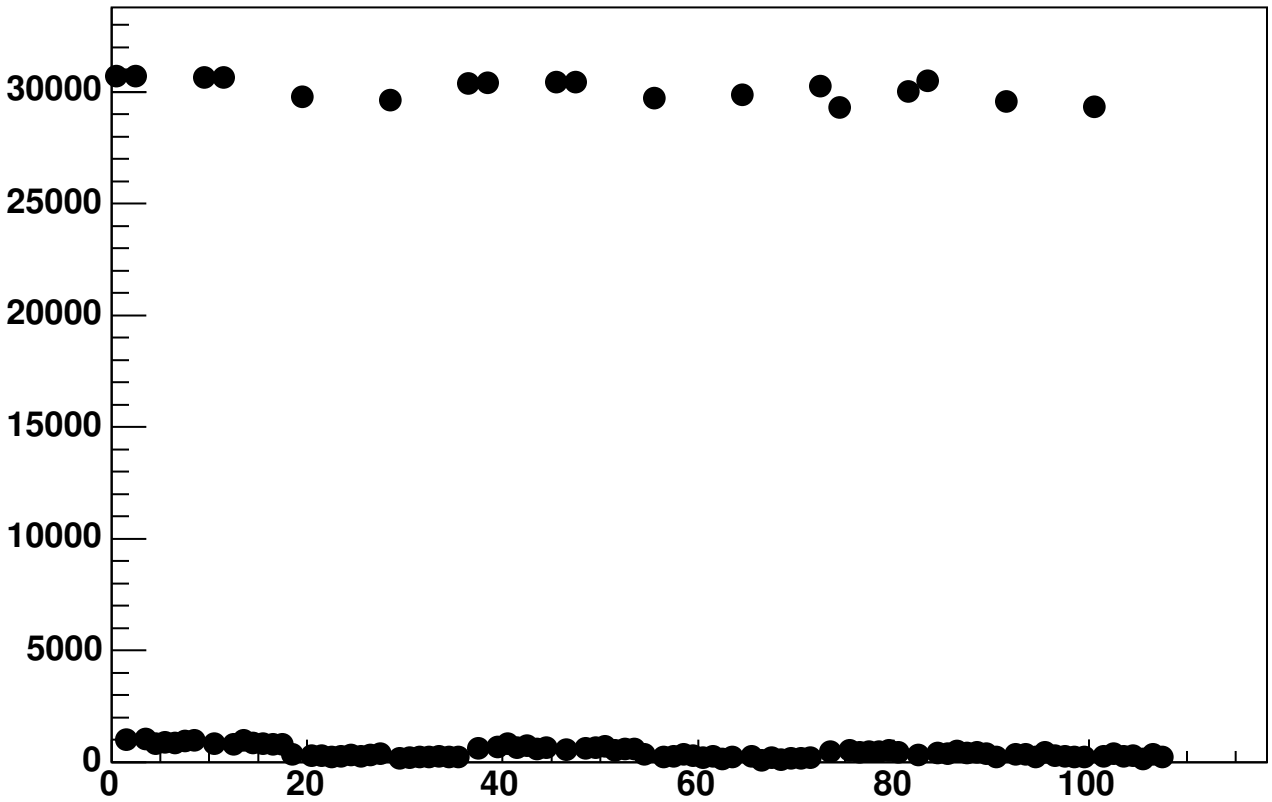
Enable 0, Hold=35, DAC=62000, ADC Mean vs 18*Chip+Chan



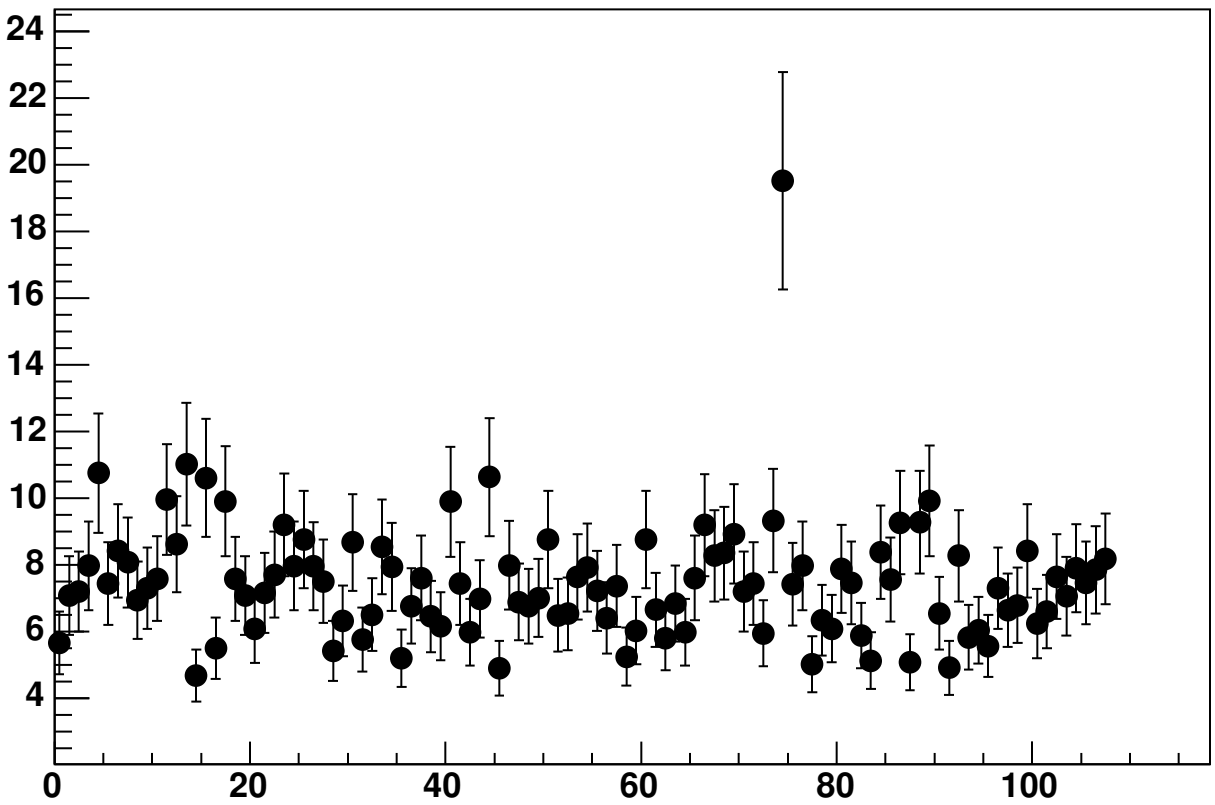
Enable 0, Hold=35, DAC=62000, ADC Noise vs 18*Chip+Chan



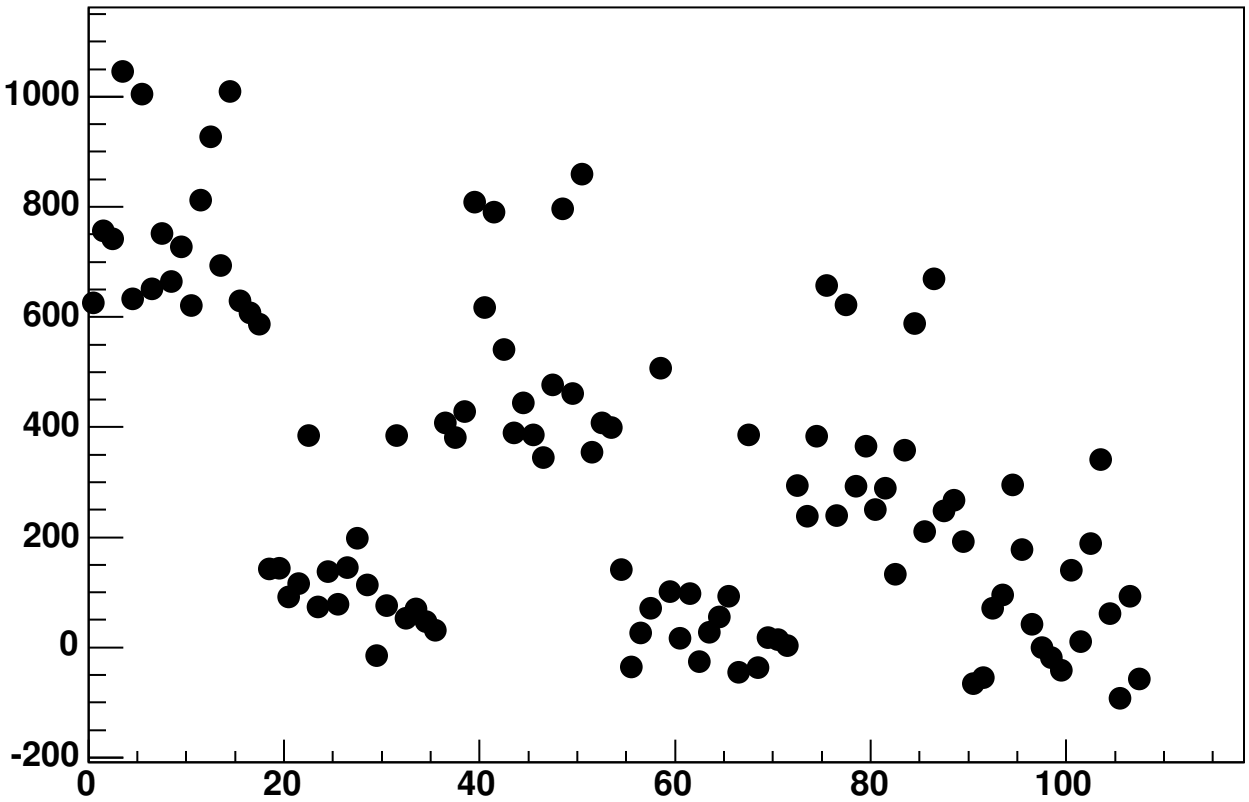
Enable 0, Hold=35, DAC=64000, ADC Mean vs 18*Chip+Chan



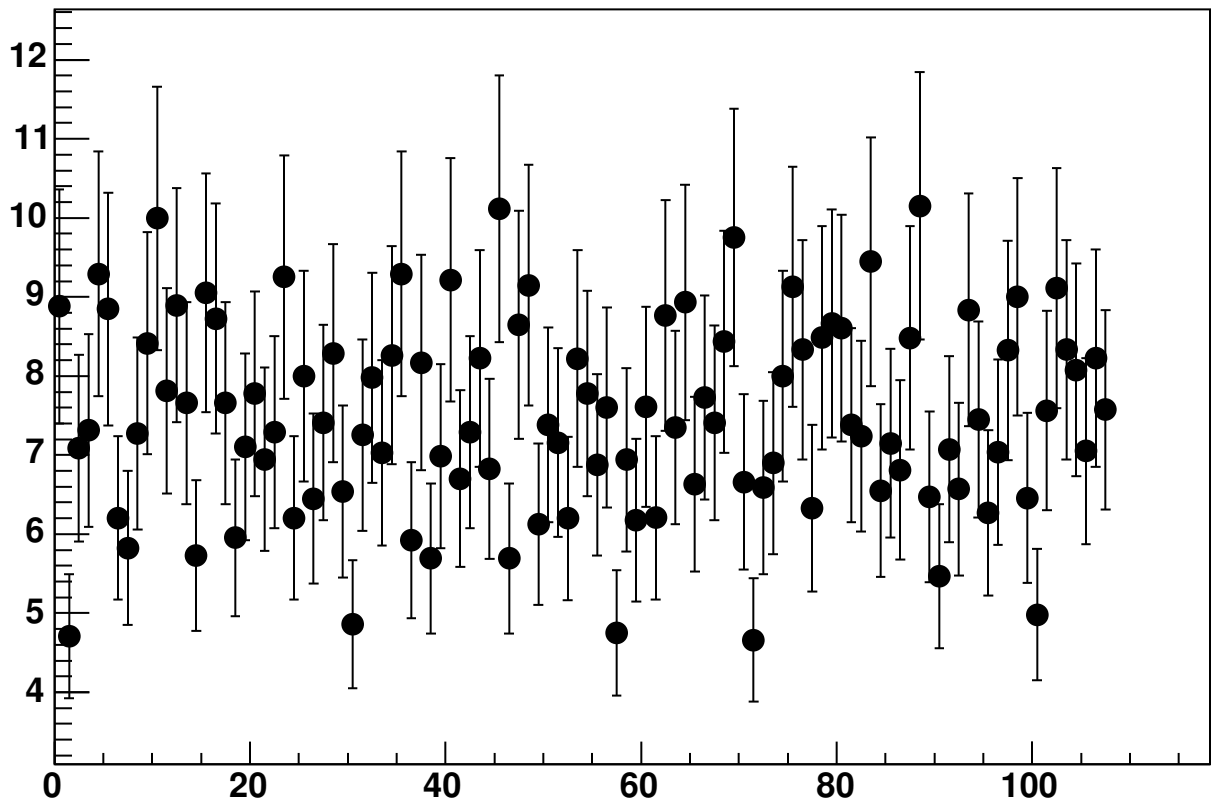
Enable 0, Hold=35, DAC=64000, ADC Noise vs 18*Chip+Chan



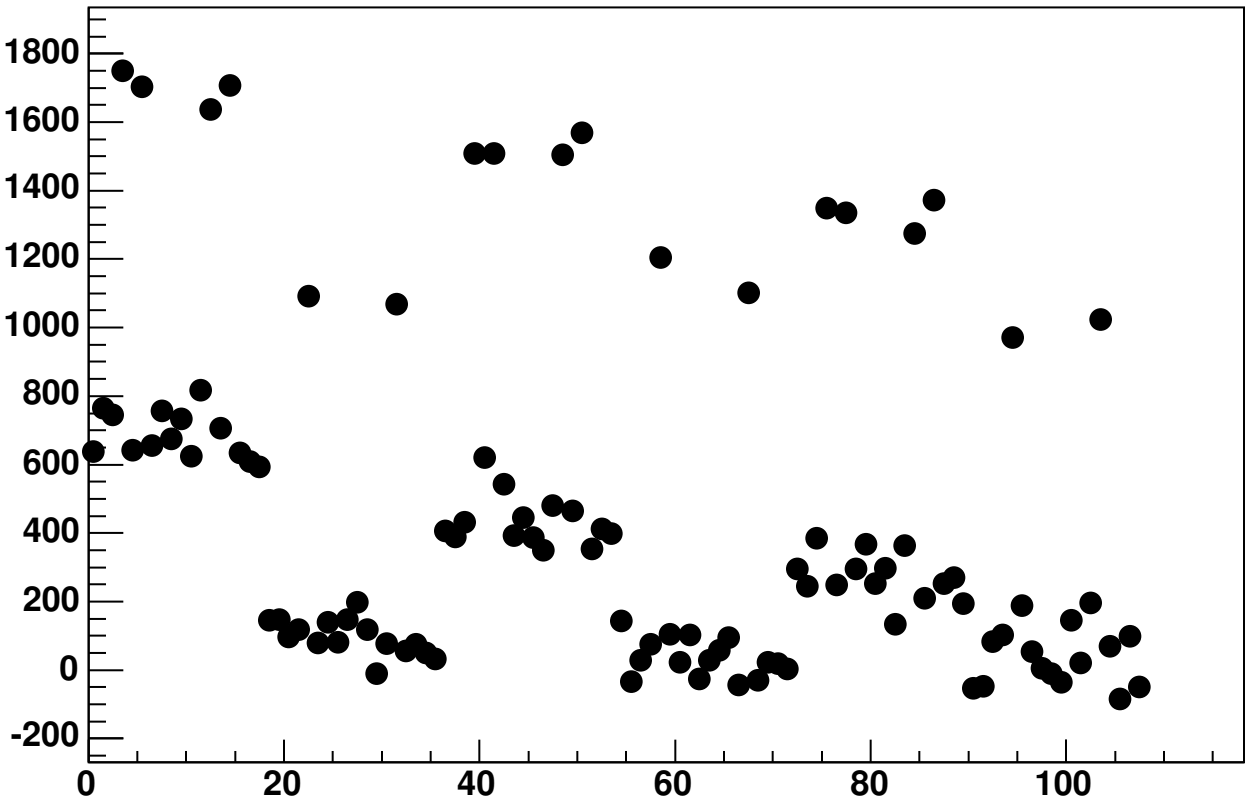
Enable 1, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



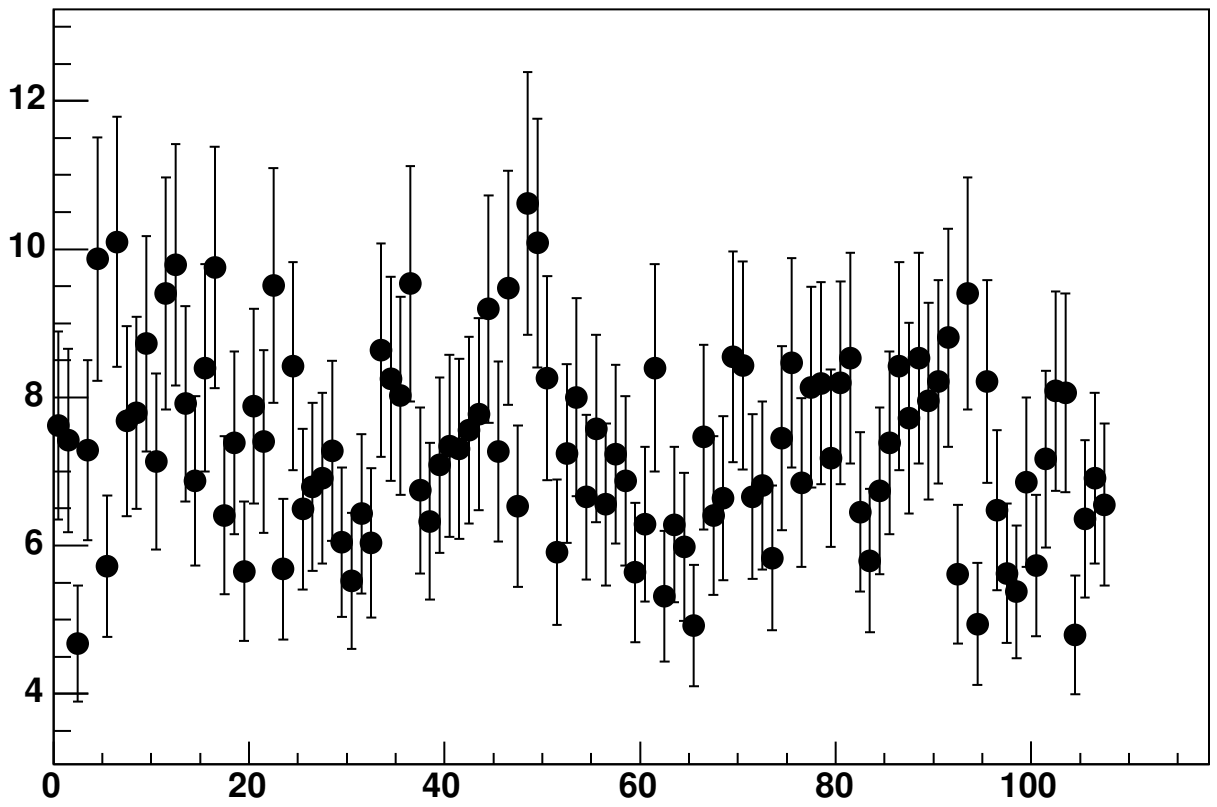
Enable 1, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



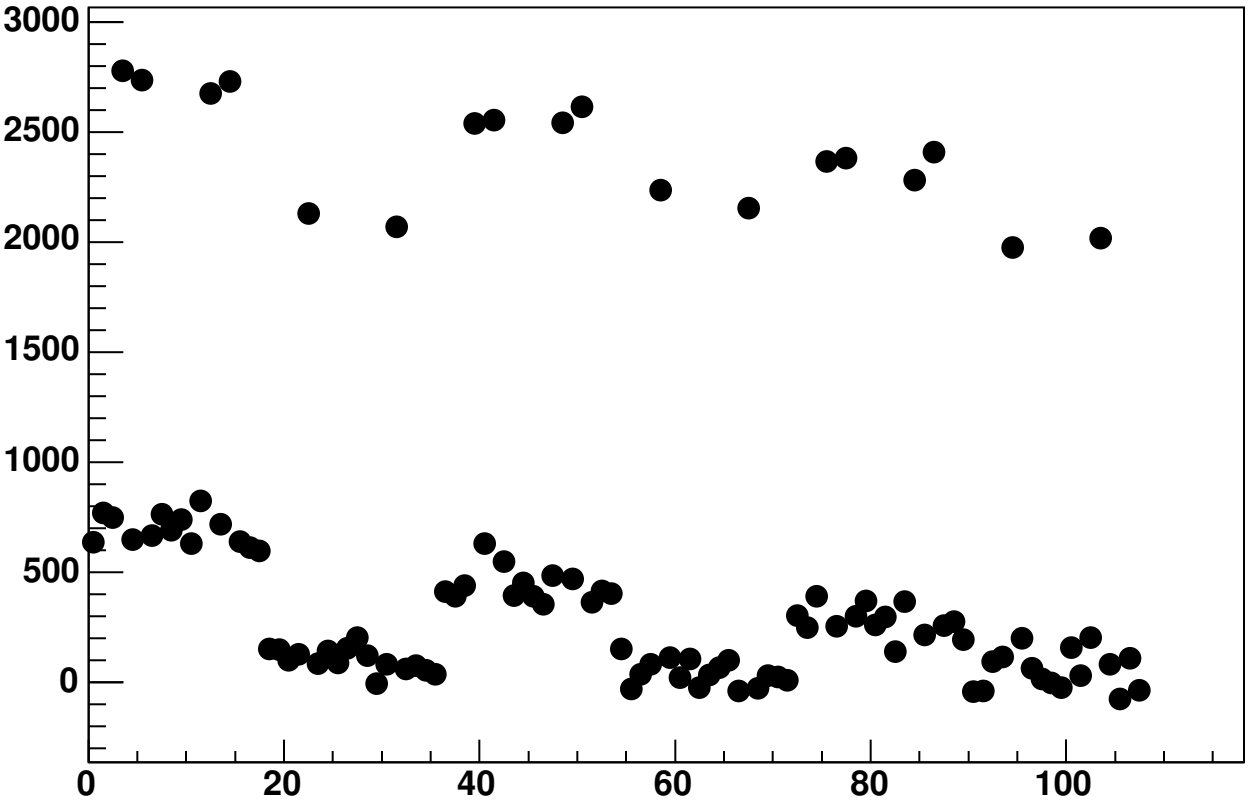
Enable 1, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



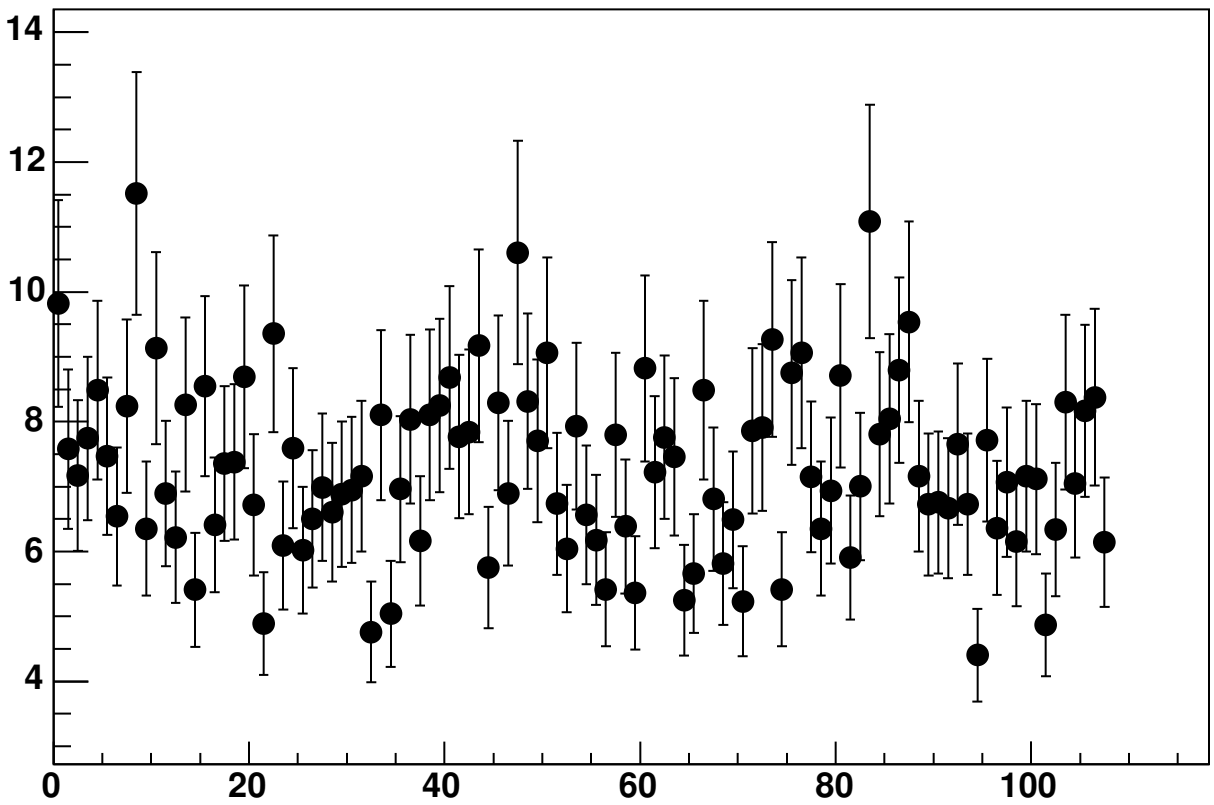
Enable 1, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



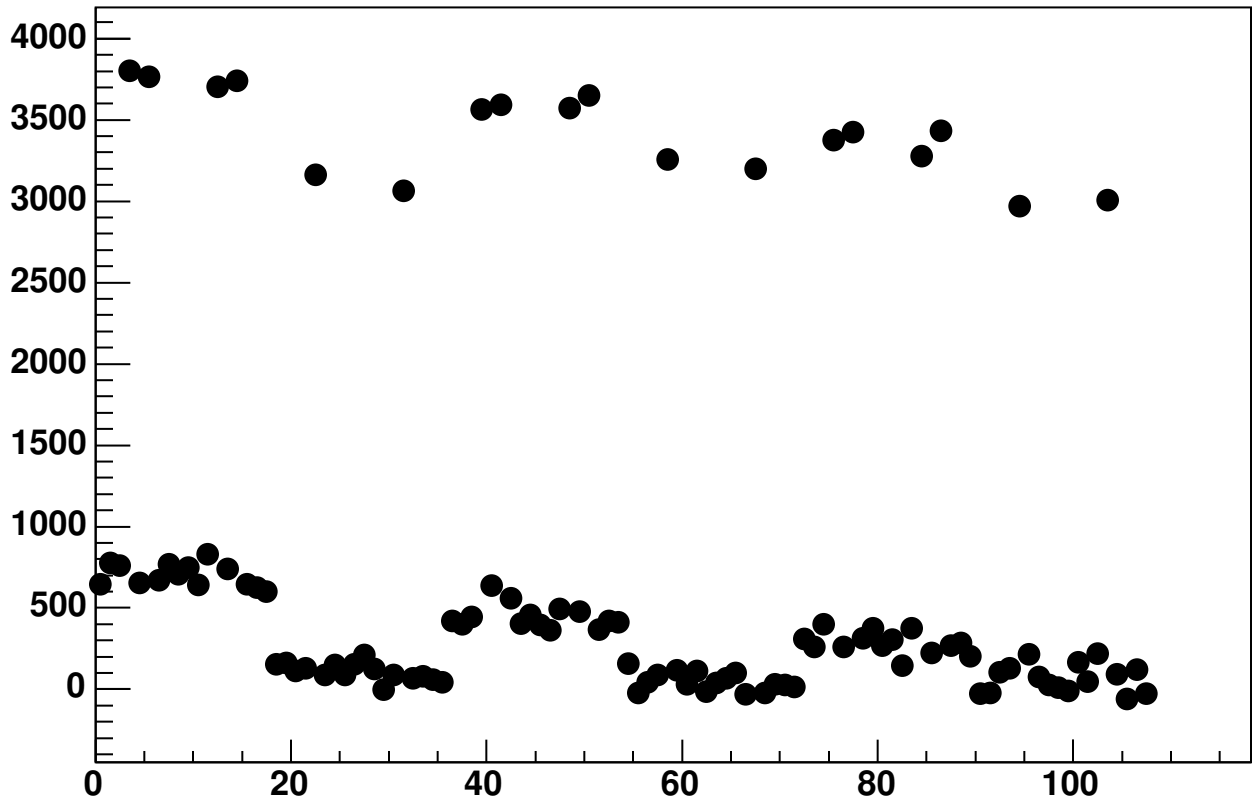
Enable 1, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



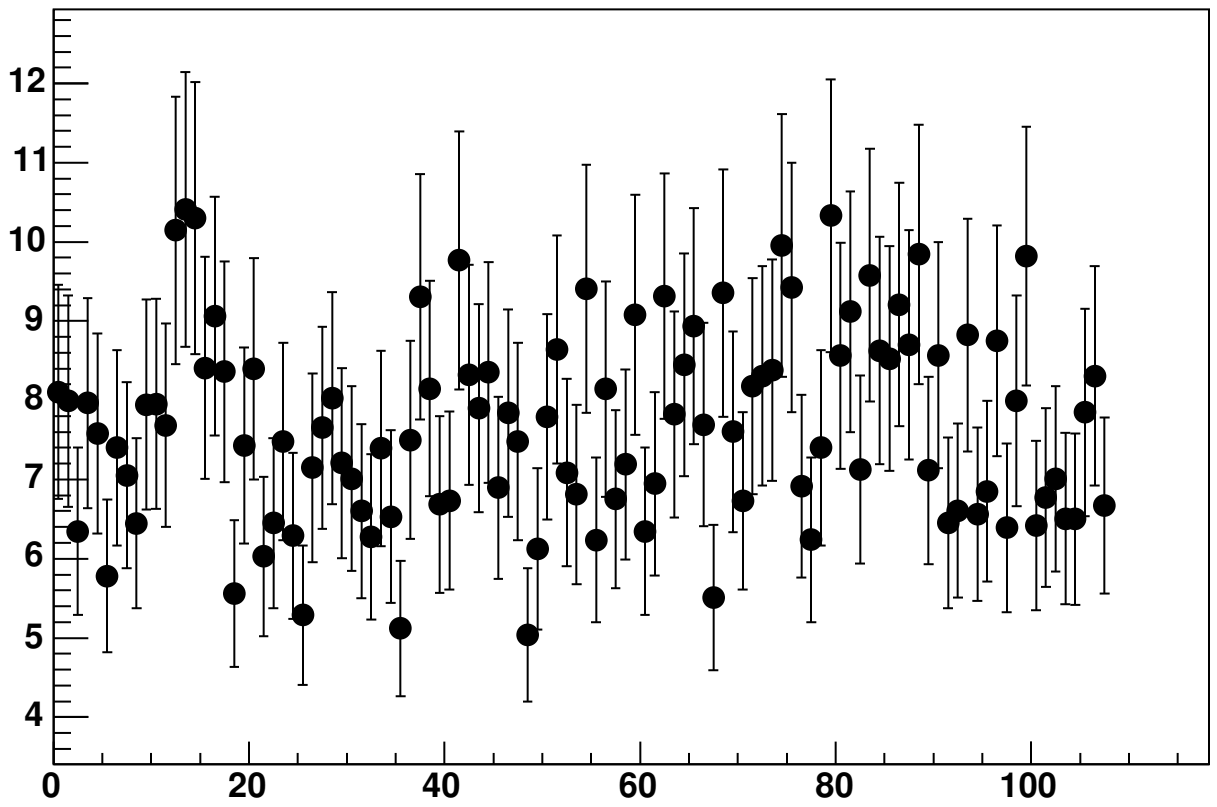
Enable 1, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



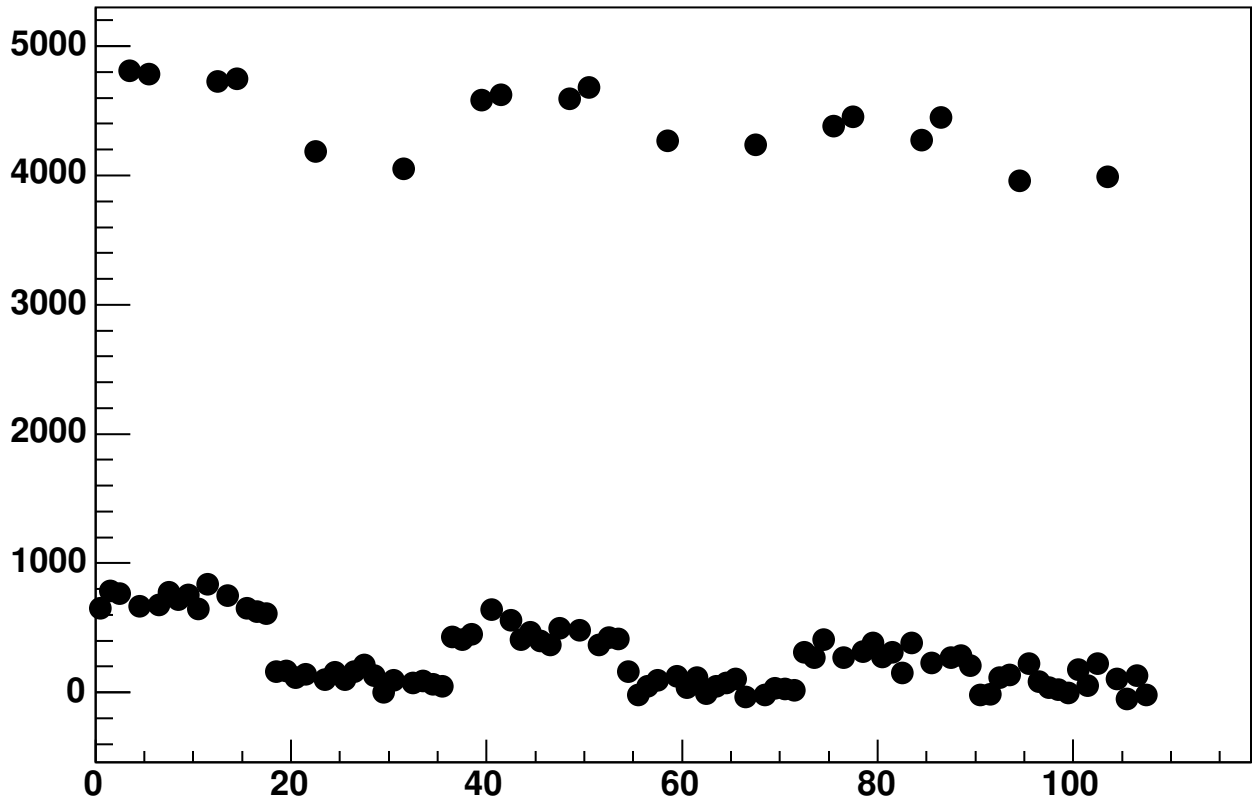
Enable 1, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



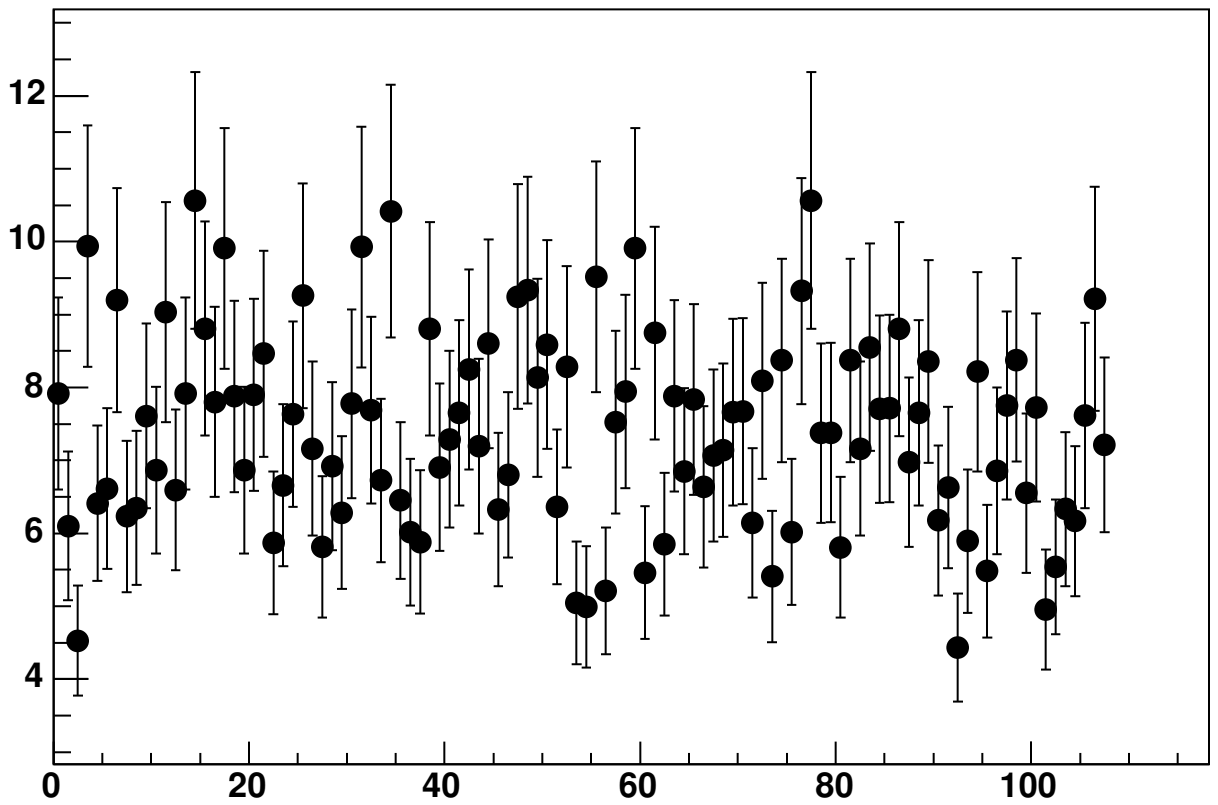
Enable 1, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



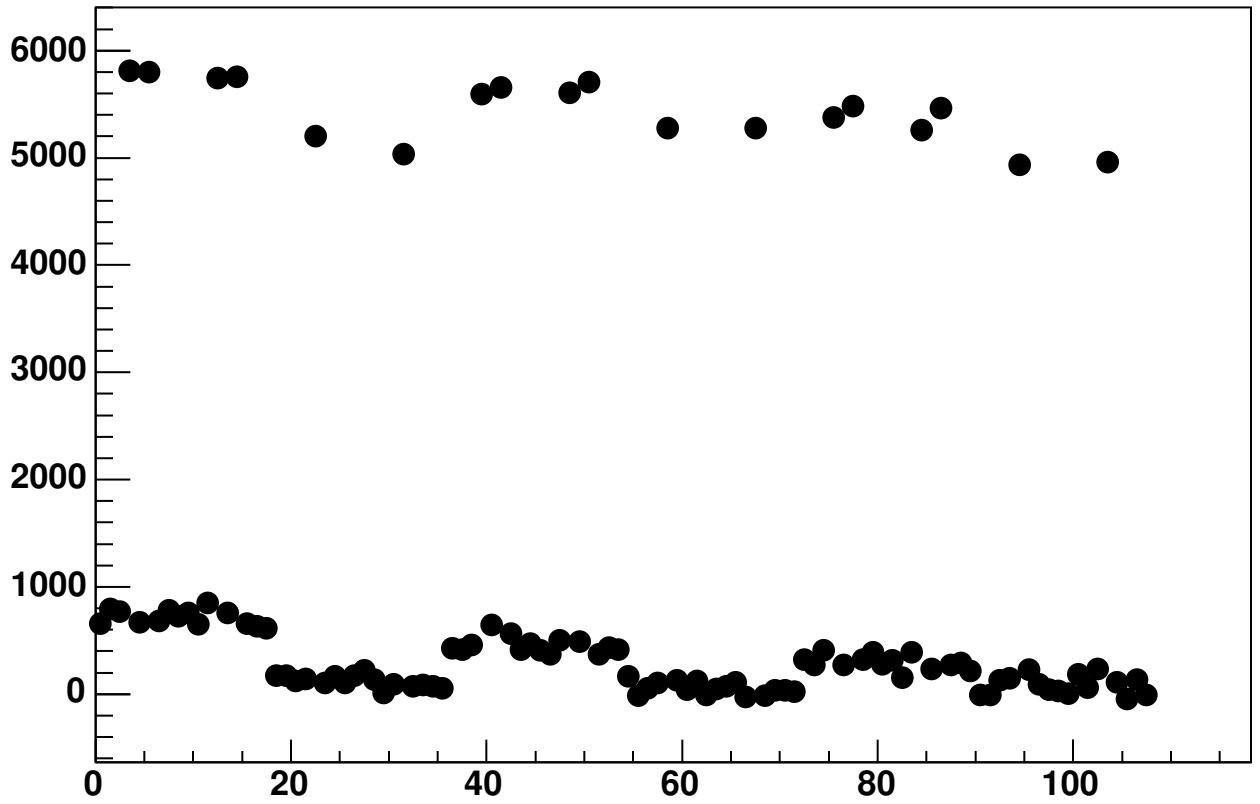
Enable 1, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



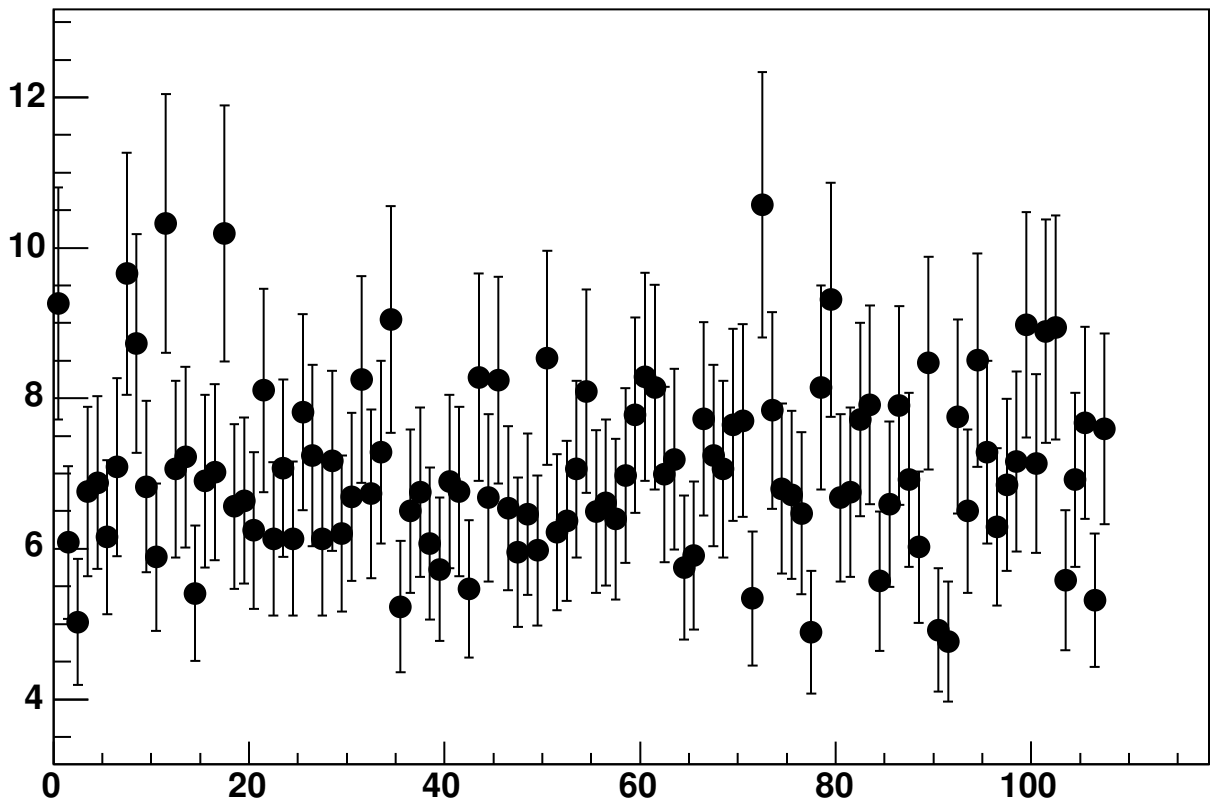
Enable 1, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



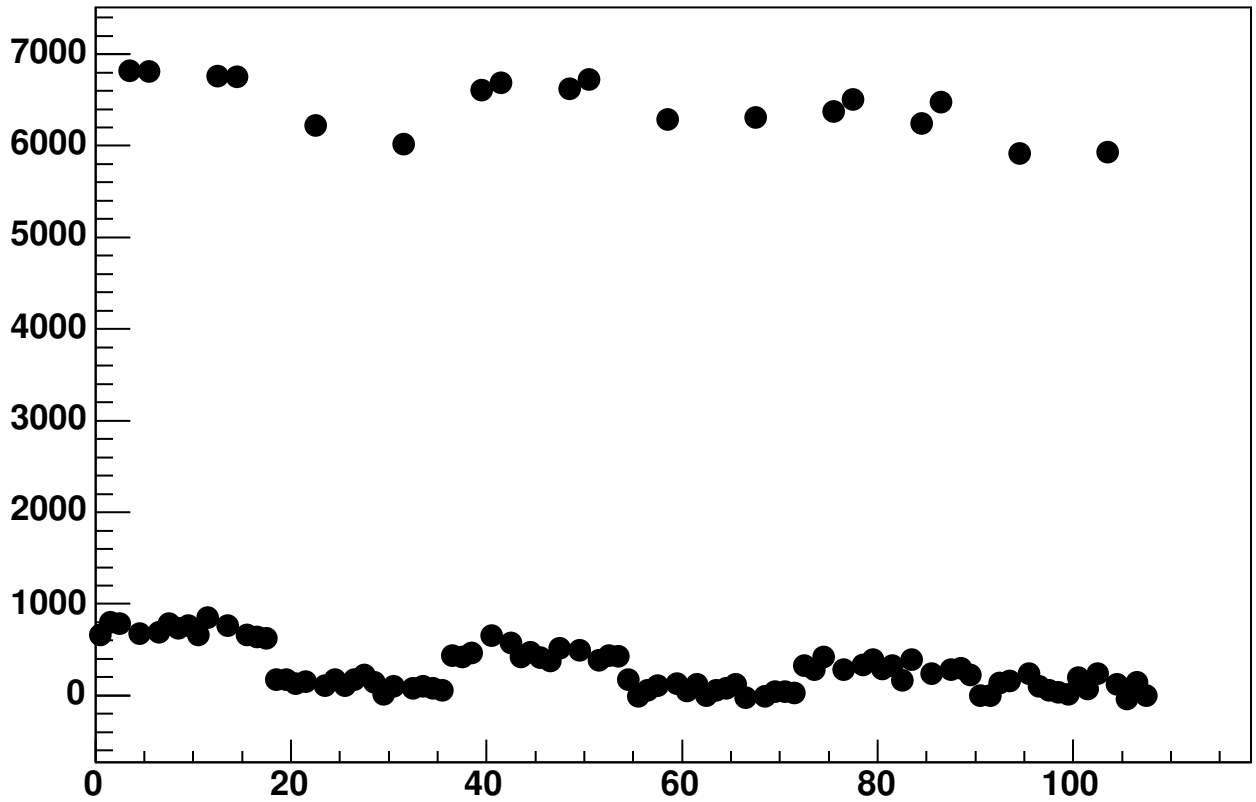
Enable 1, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



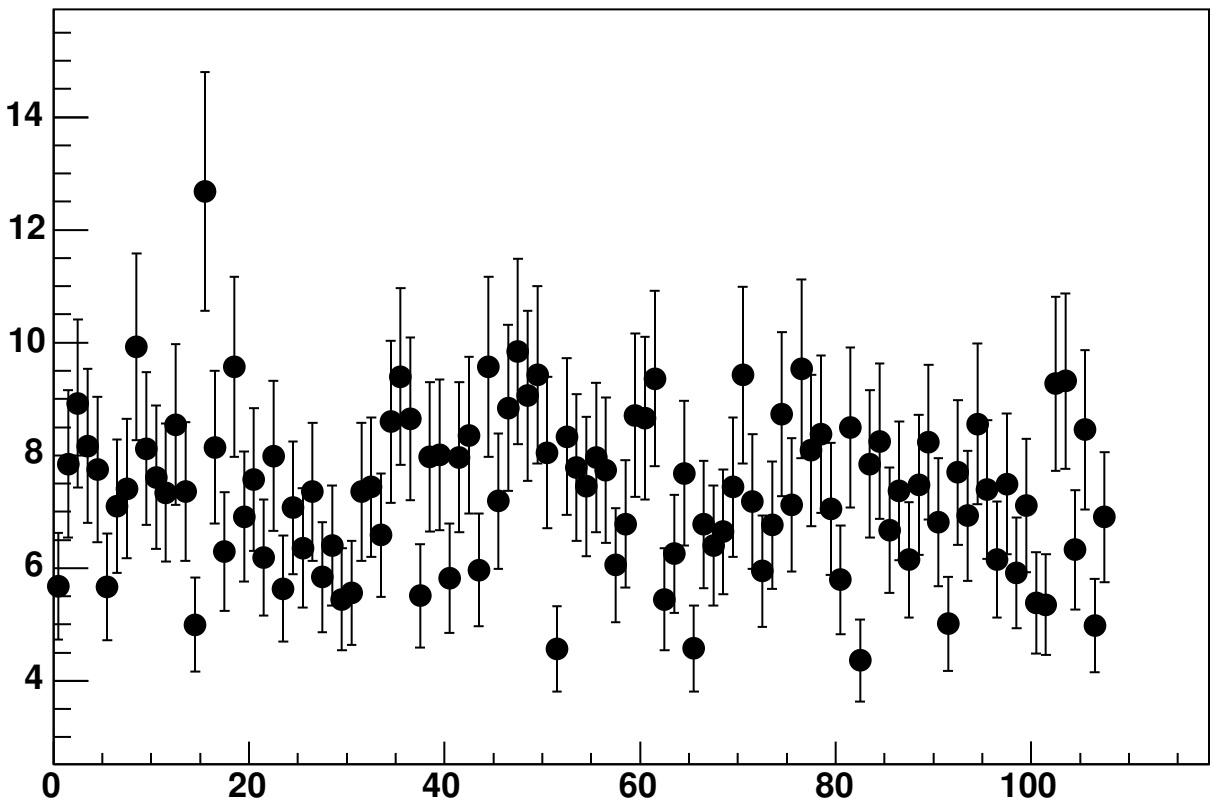
Enable 1, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



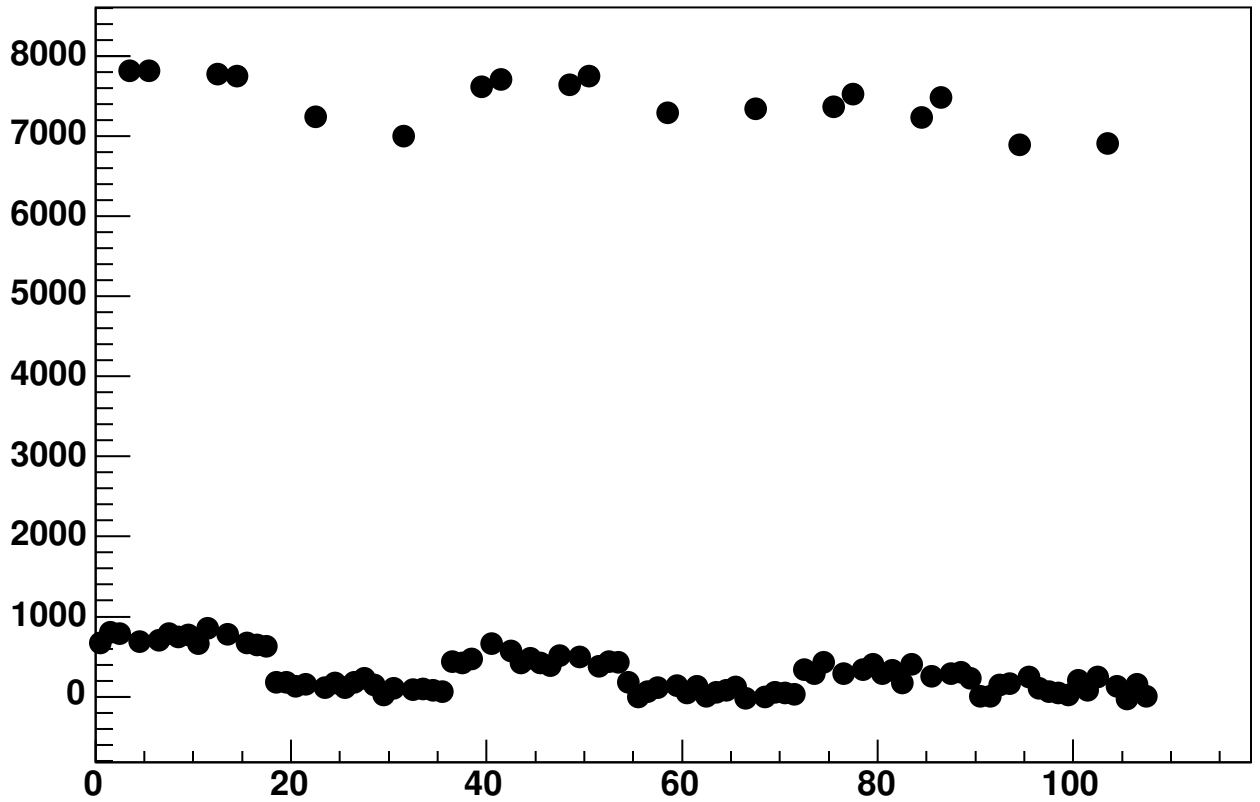
Enable 1, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



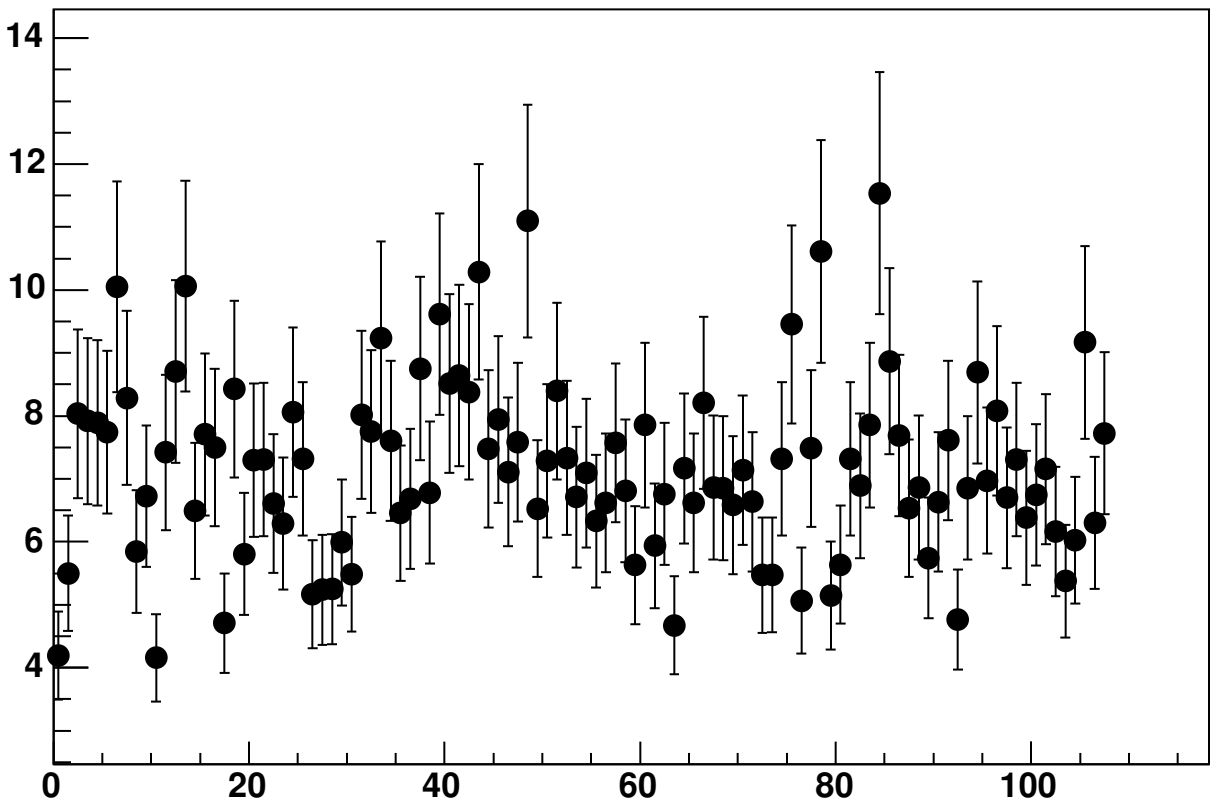
Enable 1, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



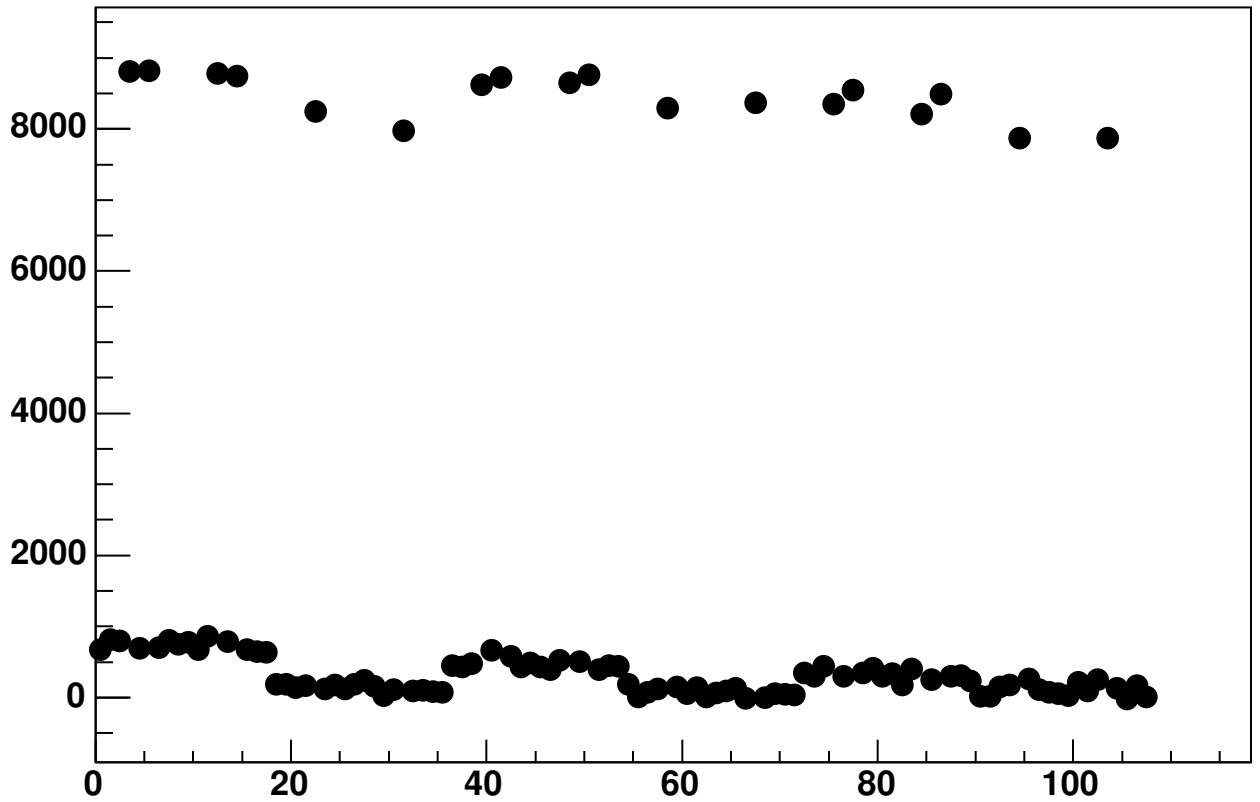
Enable 1, Hold=35, DAC=14000, ADC Mean vs 18*Chip+Chan



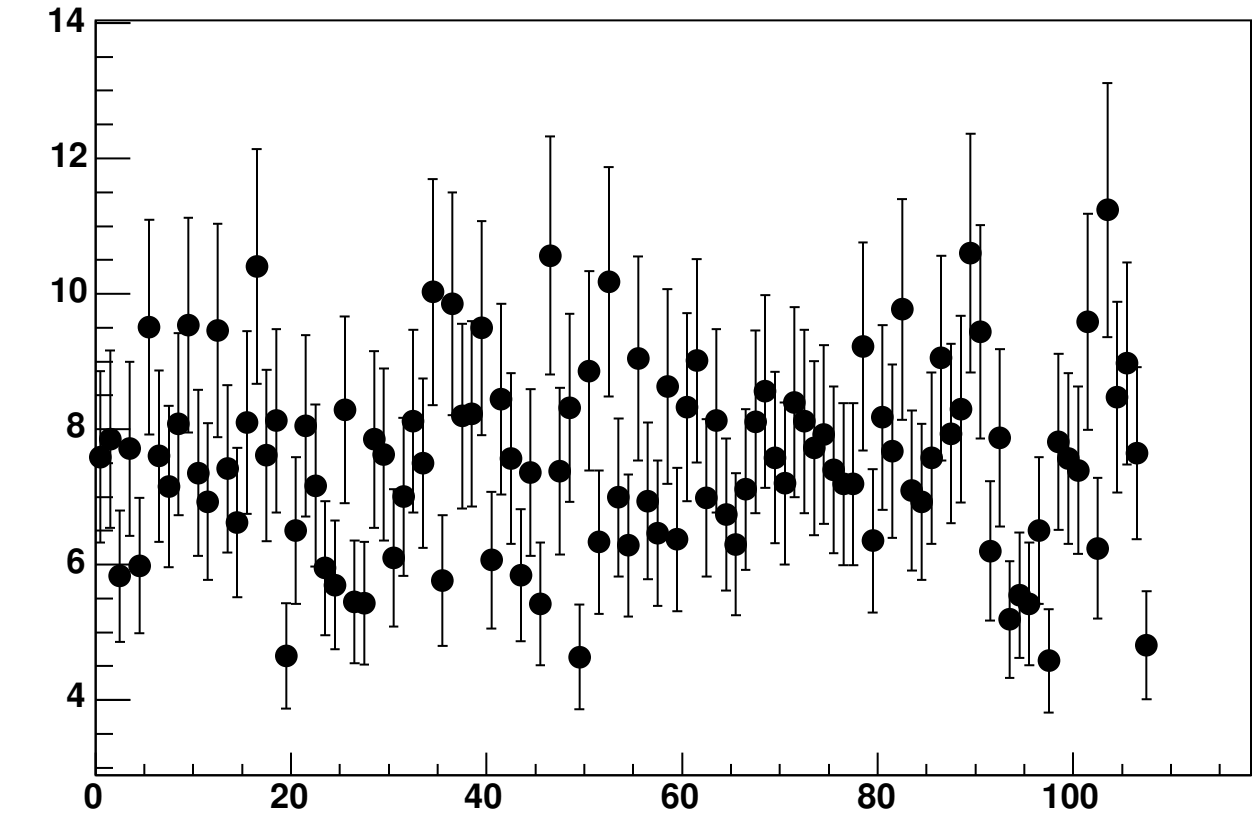
Enable 1, Hold=35, DAC=14000, ADC Noise vs 18*Chip+Chan



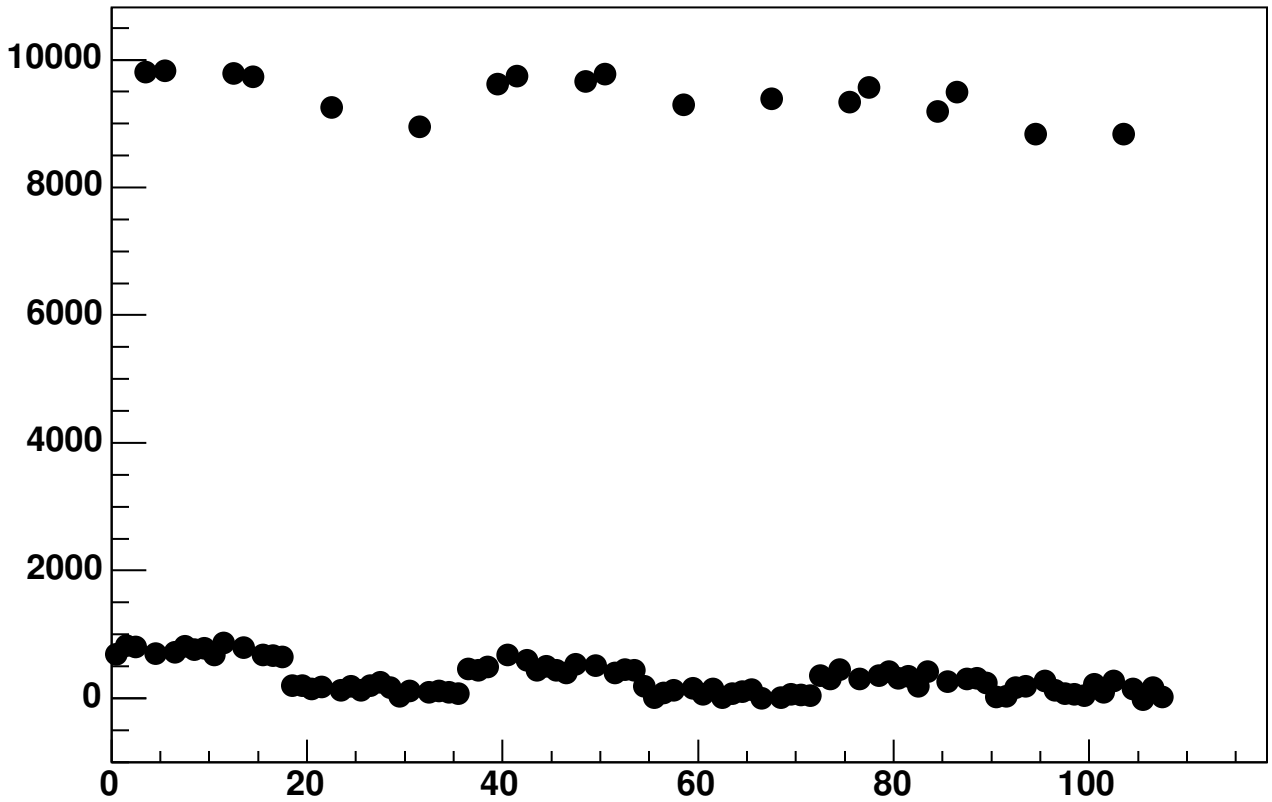
Enable 1, Hold=35, DAC=16000, ADC Mean vs 18*Chip+Chan



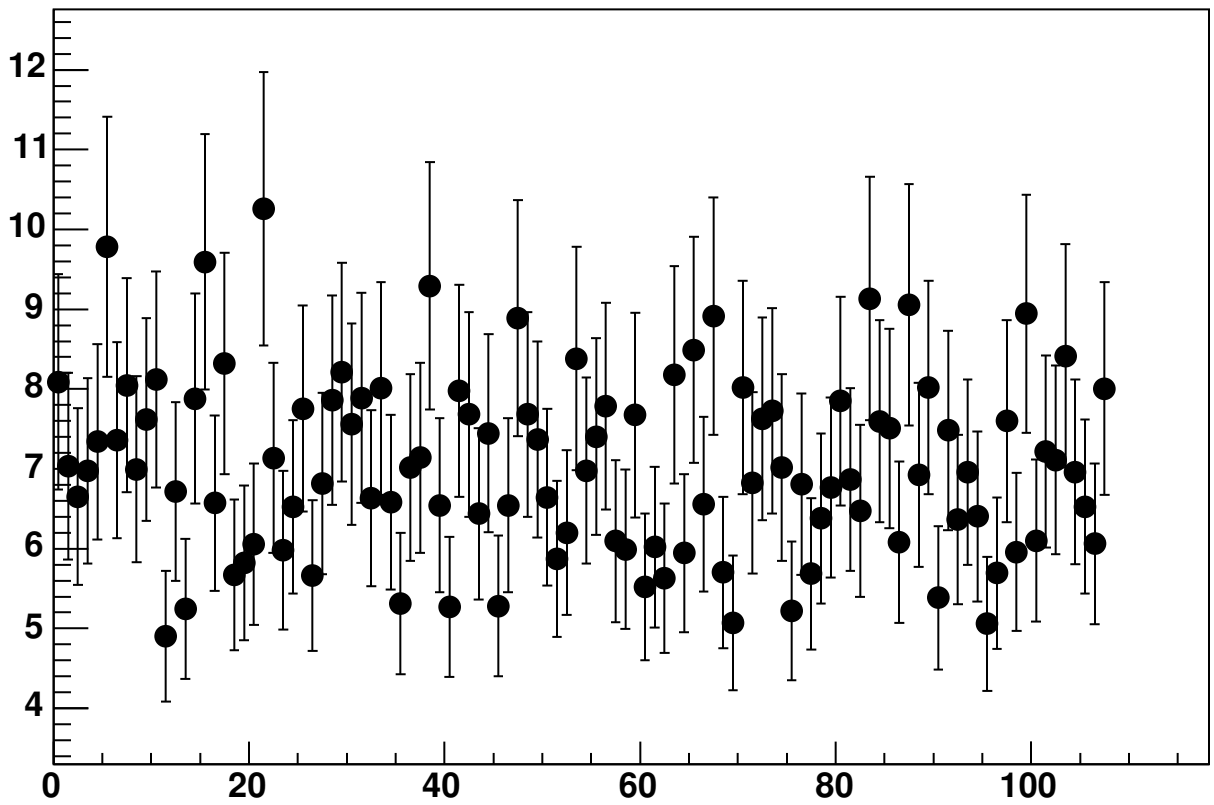
Enable 1, Hold=35, DAC=16000, ADC Noise vs 18*Chip+Chan



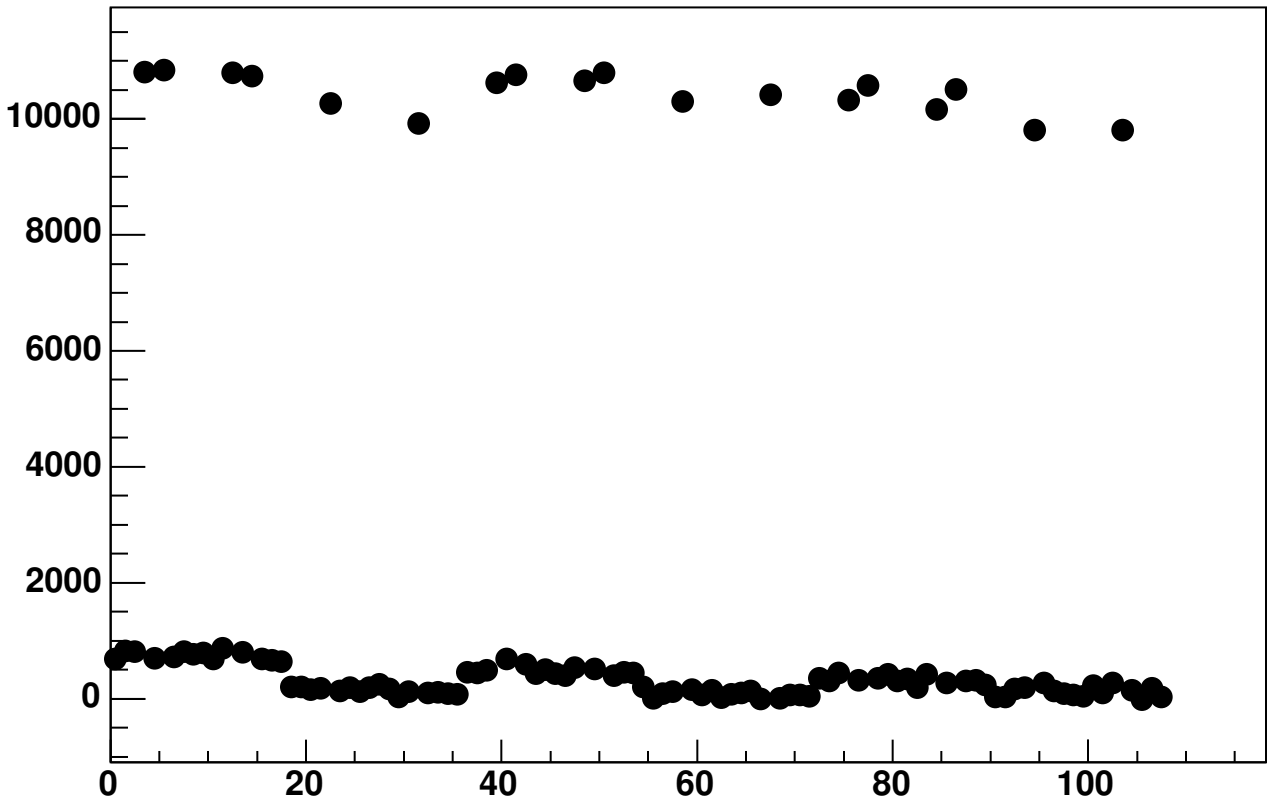
Enable 1, Hold=35, DAC=18000, ADC Mean vs 18*Chip+Chan



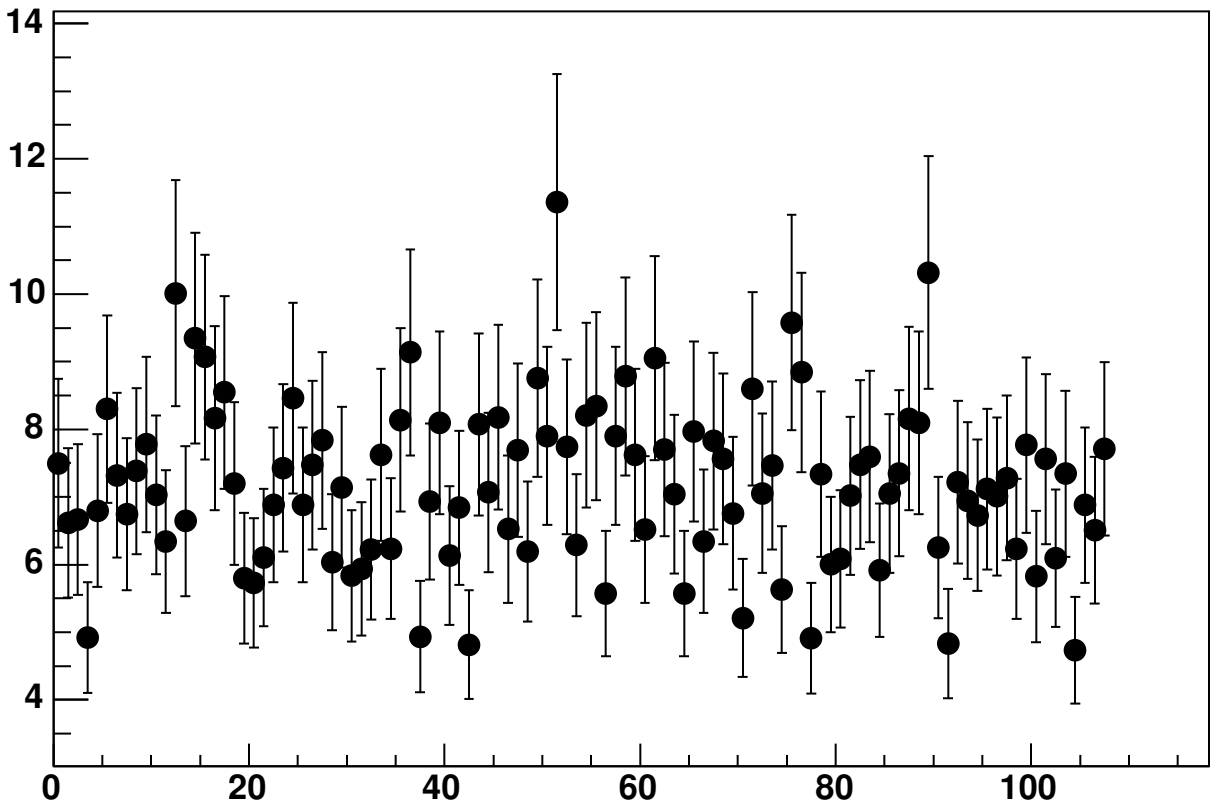
Enable 1, Hold=35, DAC=18000, ADC Noise vs 18*Chip+Chan



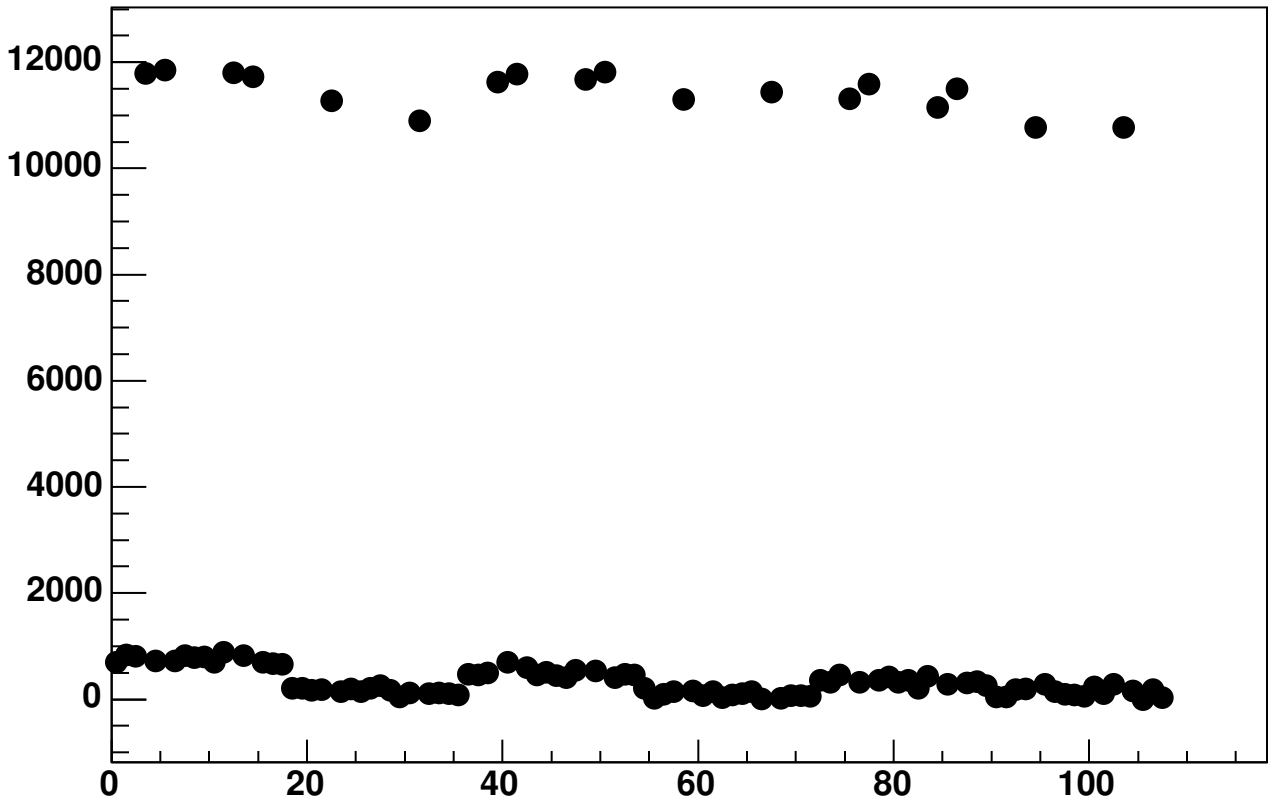
Enable 1, Hold=35, DAC=20000, ADC Mean vs 18*Chip+Chan



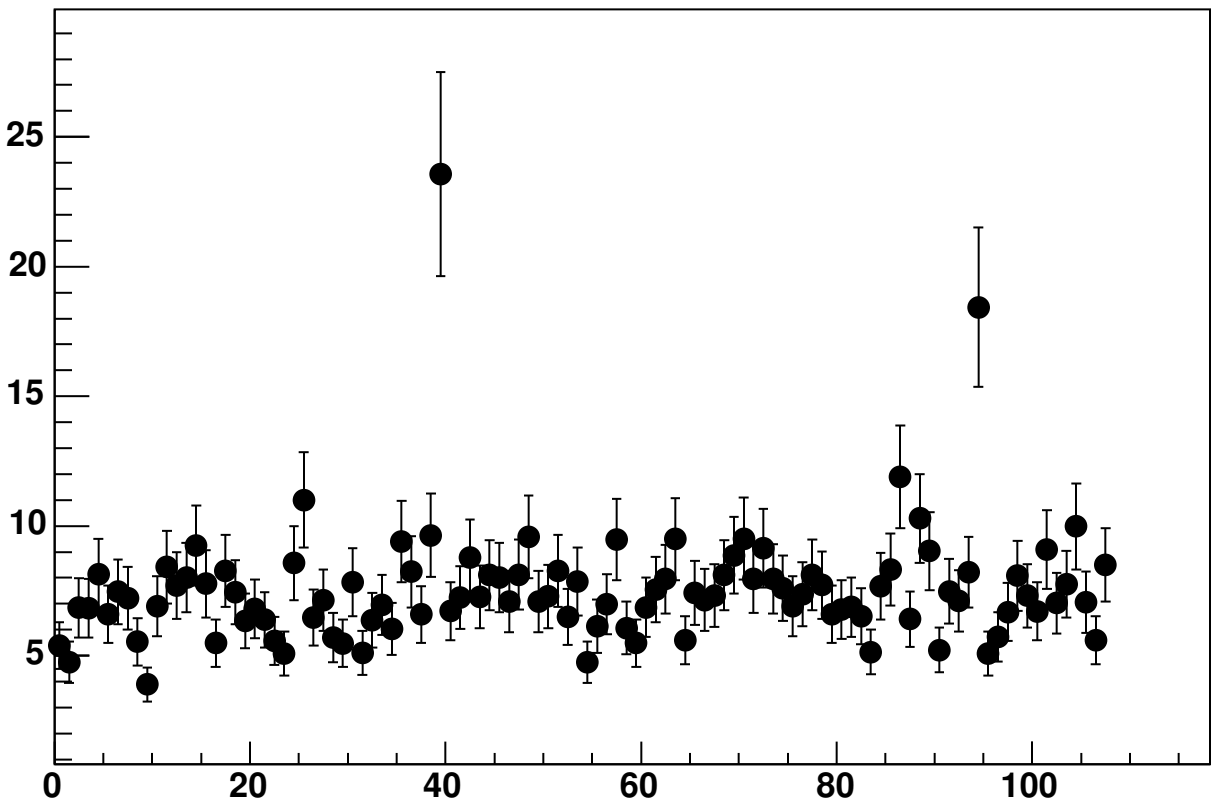
Enable 1, Hold=35, DAC=20000, ADC Noise vs 18*Chip+Chan



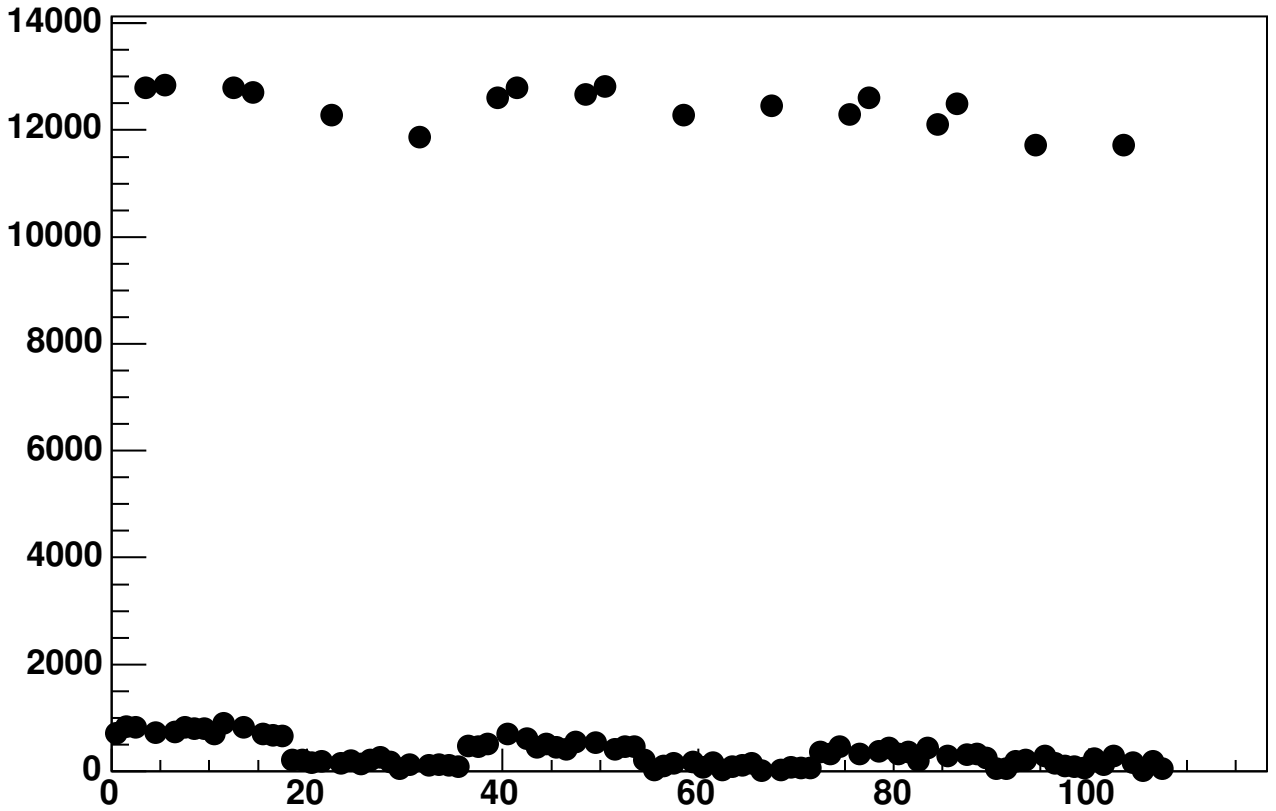
Enable 1, Hold=35, DAC=22000, ADC Mean vs 18*Chip+Chan



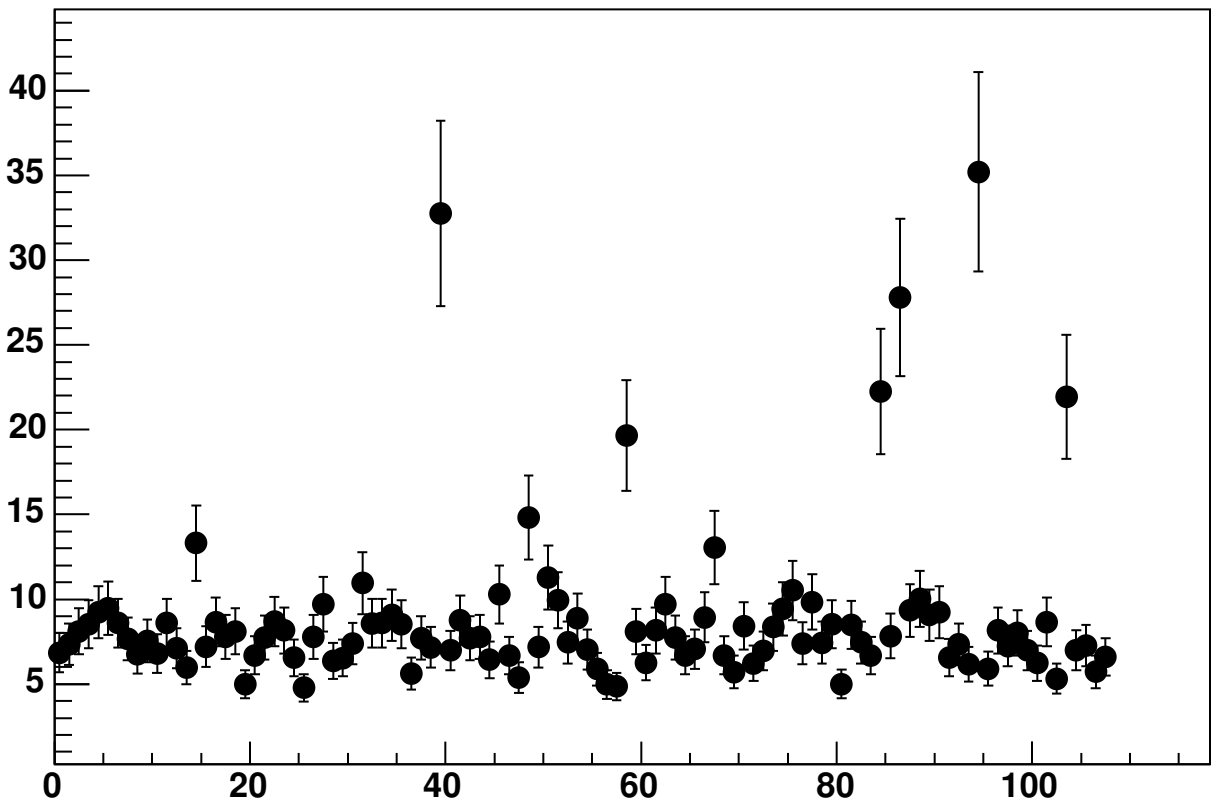
Enable 1, Hold=35, DAC=22000, ADC Noise vs 18*Chip+Chan



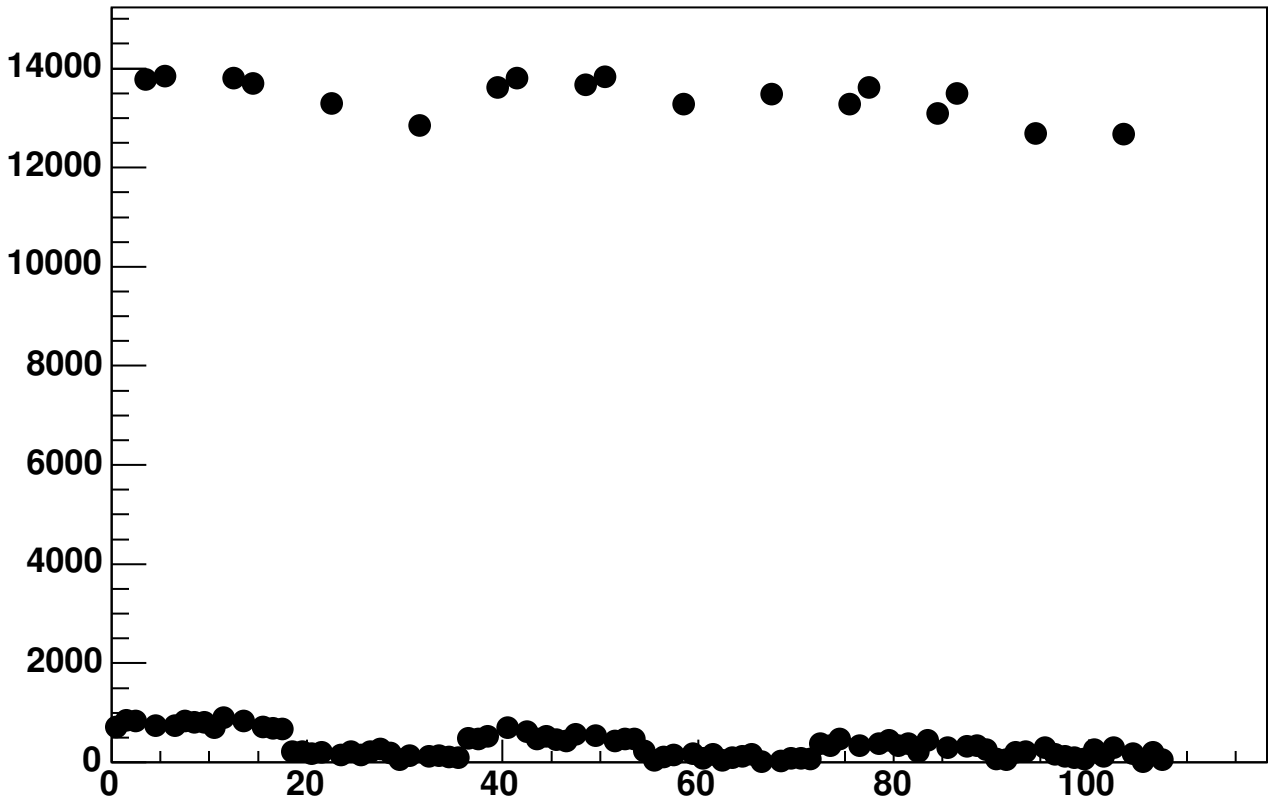
Enable 1, Hold=35, DAC=24000, ADC Mean vs 18*Chip+Chan



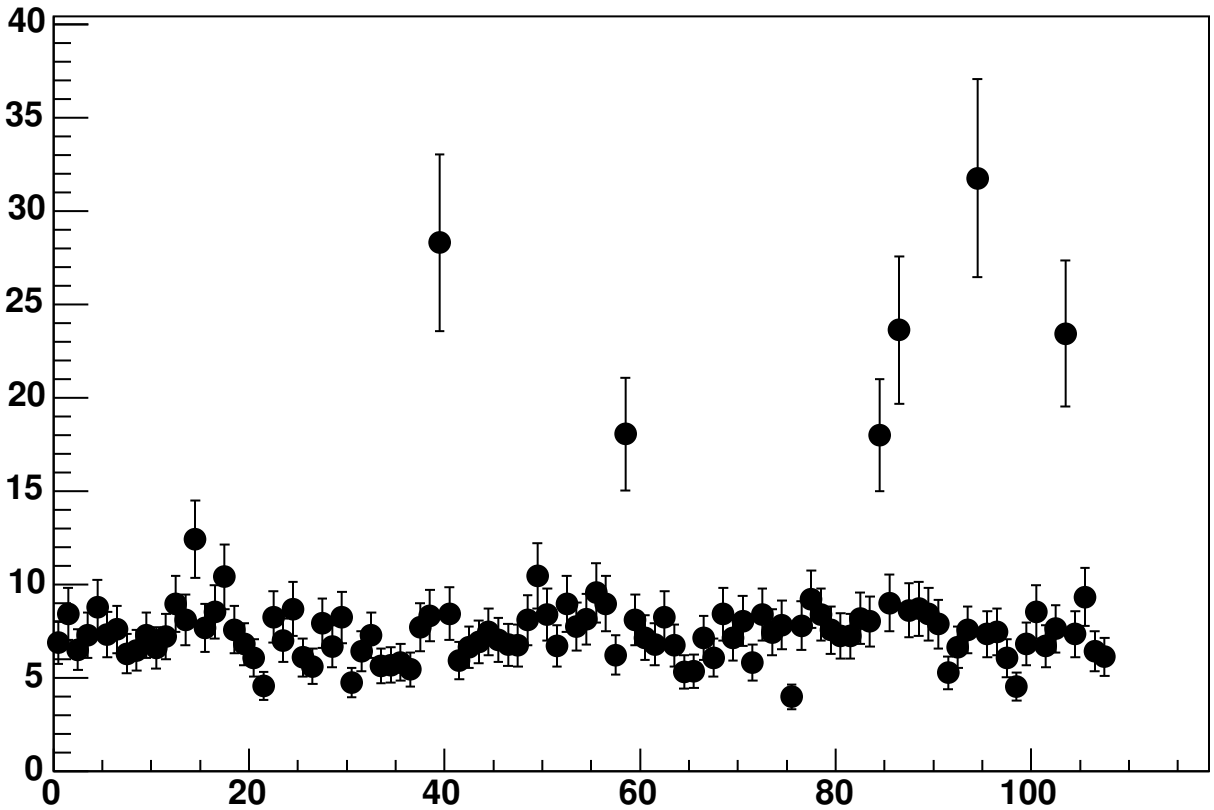
Enable 1, Hold=35, DAC=24000, ADC Noise vs 18*Chip+Chan



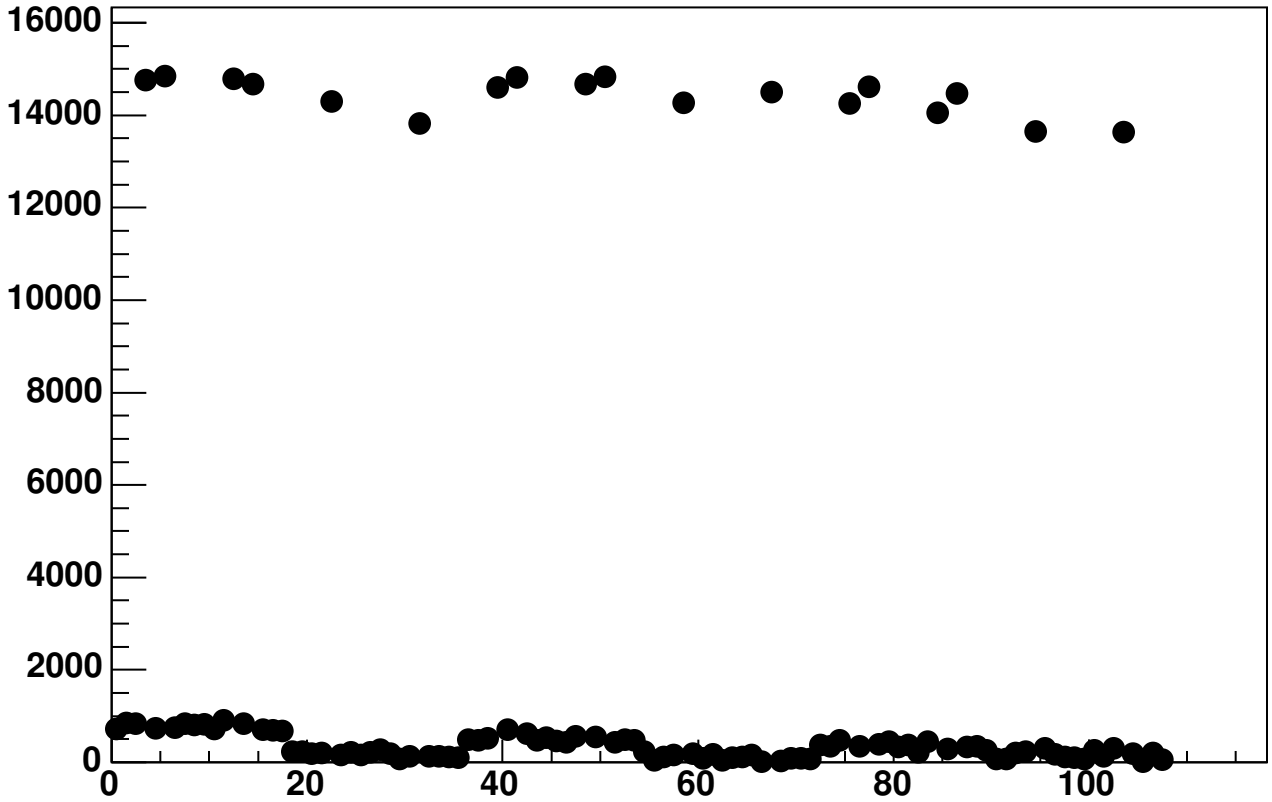
Enable 1, Hold=35, DAC=26000, ADC Mean vs 18*Chip+Chan



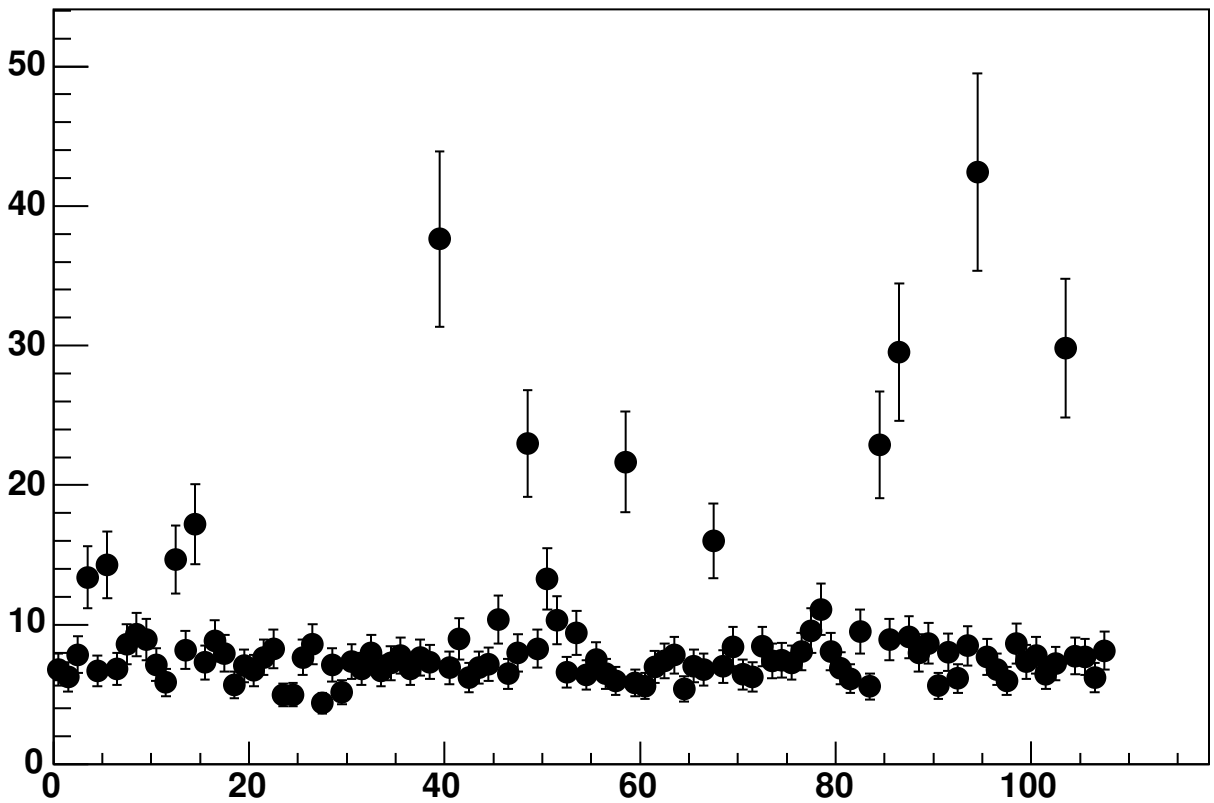
Enable 1, Hold=35, DAC=26000, ADC Noise vs 18*Chip+Chan



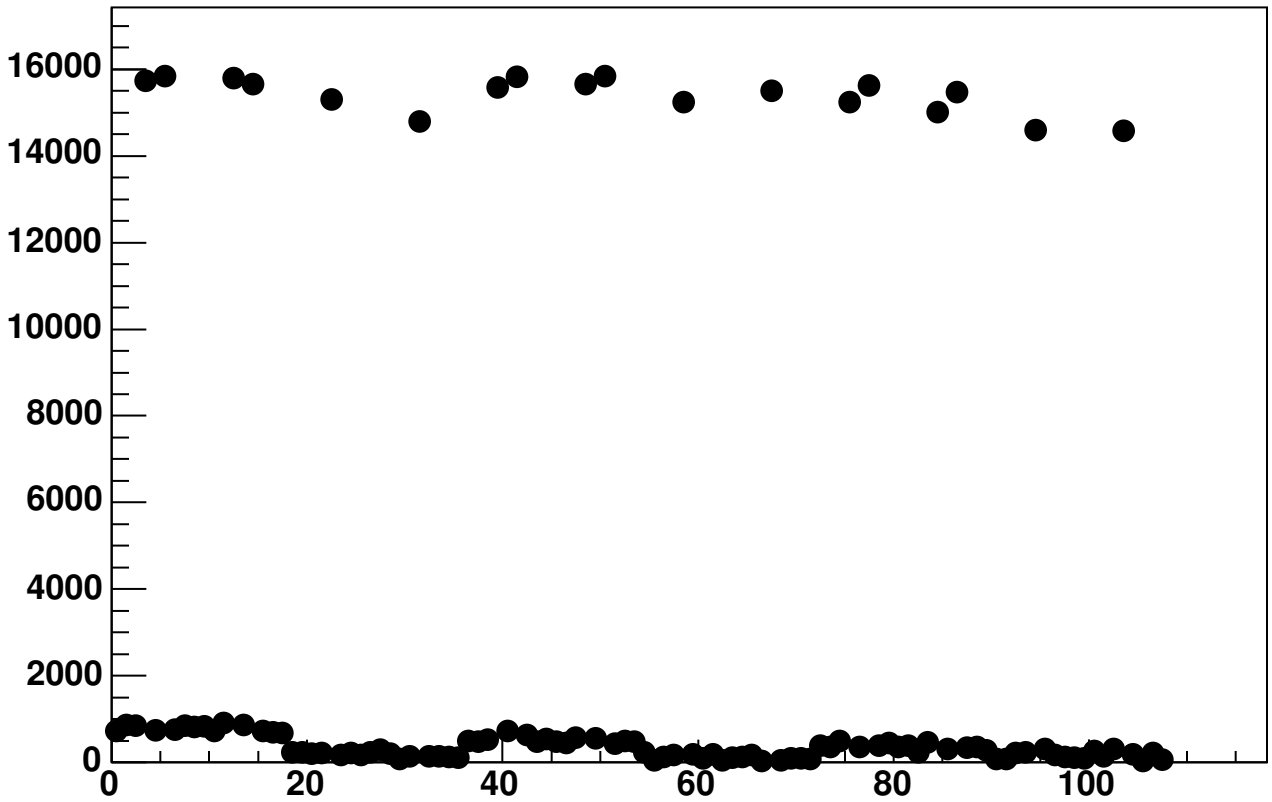
Enable 1, Hold=35, DAC=28000, ADC Mean vs 18*Chip+Chan



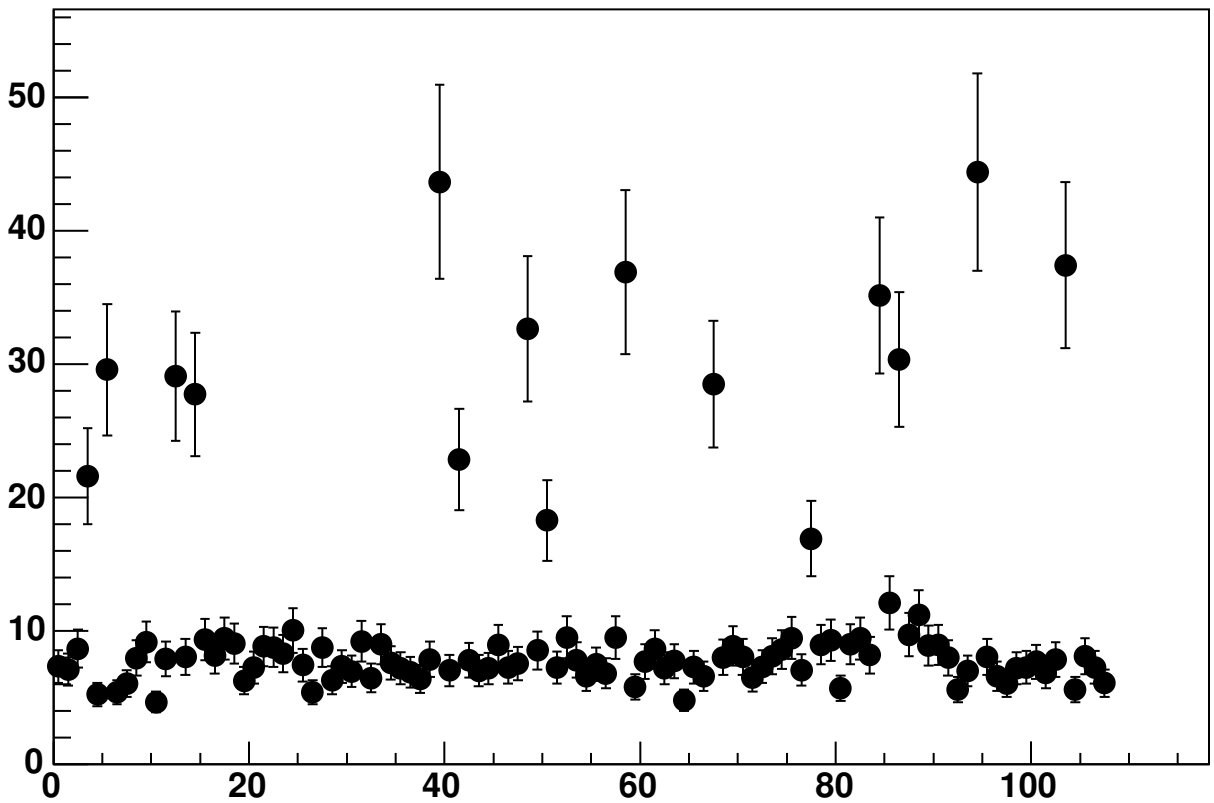
Enable 1, Hold=35, DAC=28000, ADC Noise vs 18*Chip+Chan



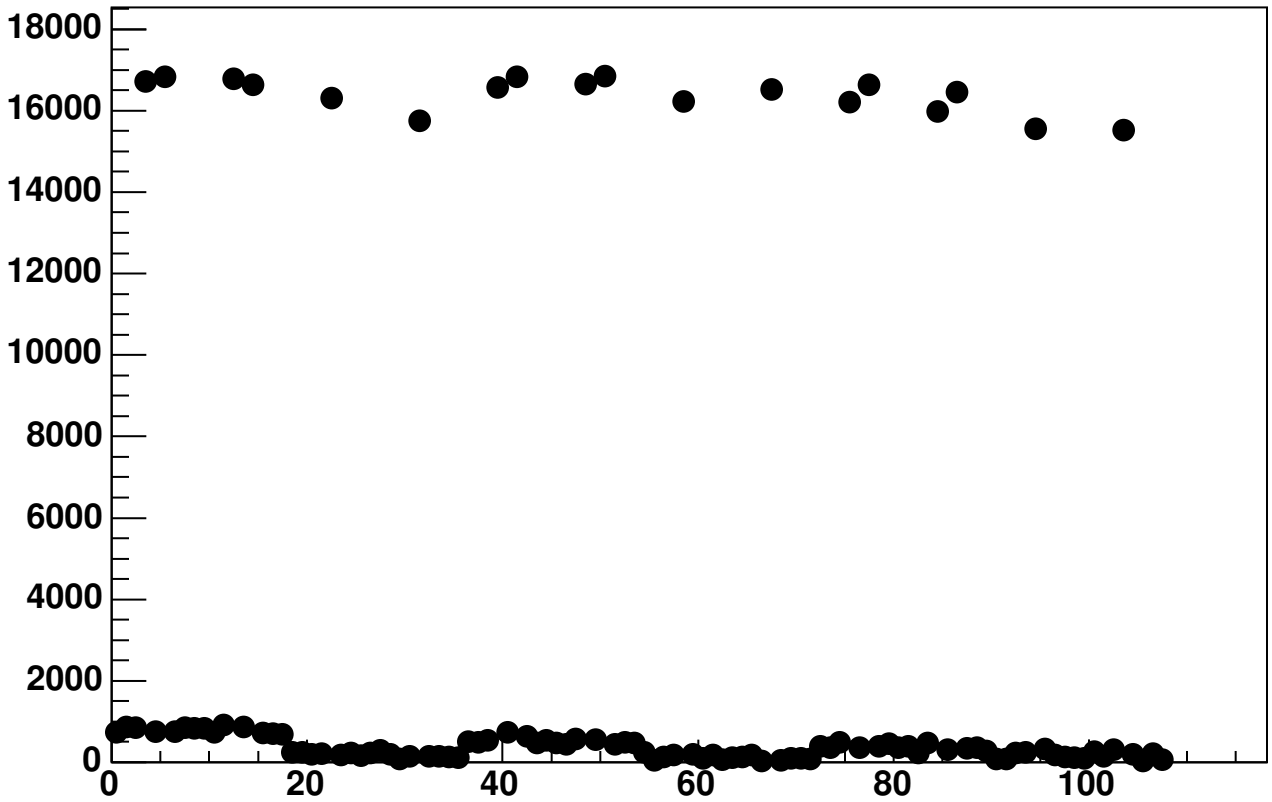
Enable 1, Hold=35, DAC=30000, ADC Mean vs 18*Chip+Chan



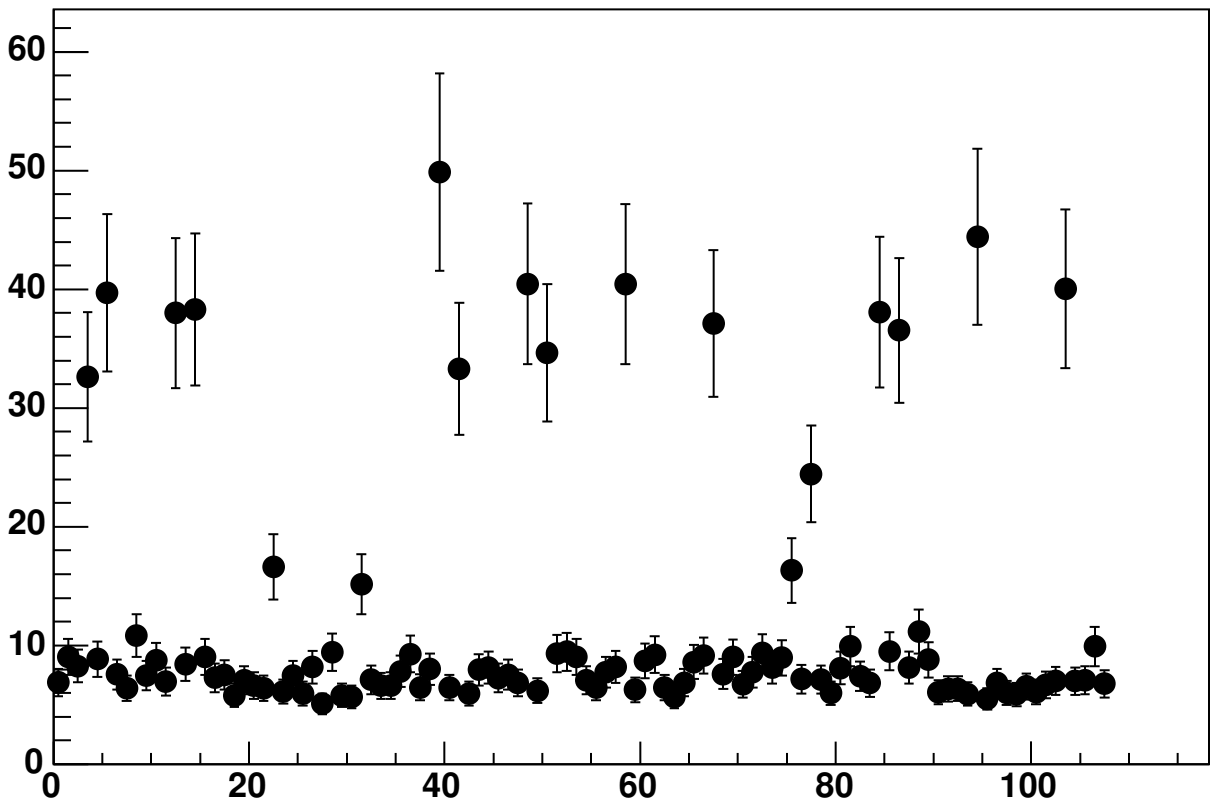
Enable 1, Hold=35, DAC=30000, ADC Noise vs 18*Chip+Chan



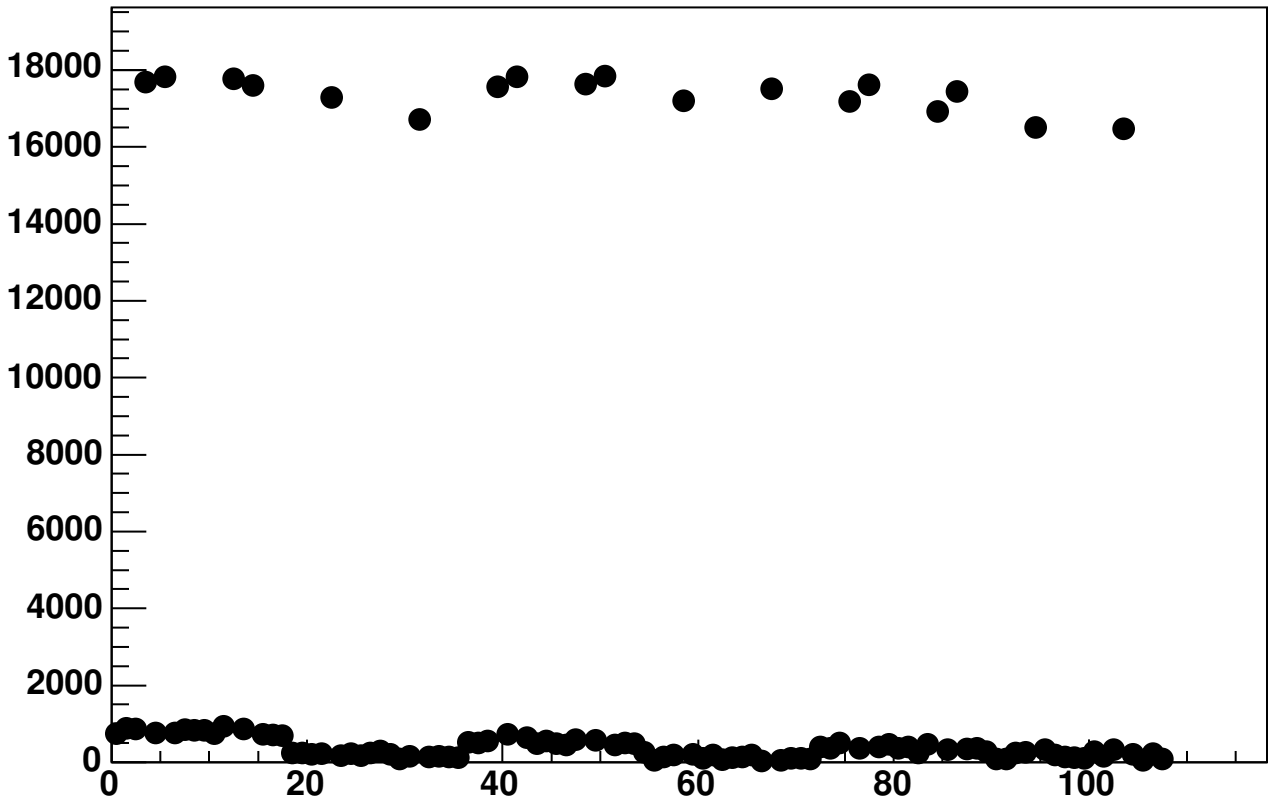
Enable 1, Hold=35, DAC=32000, ADC Mean vs 18*Chip+Chan



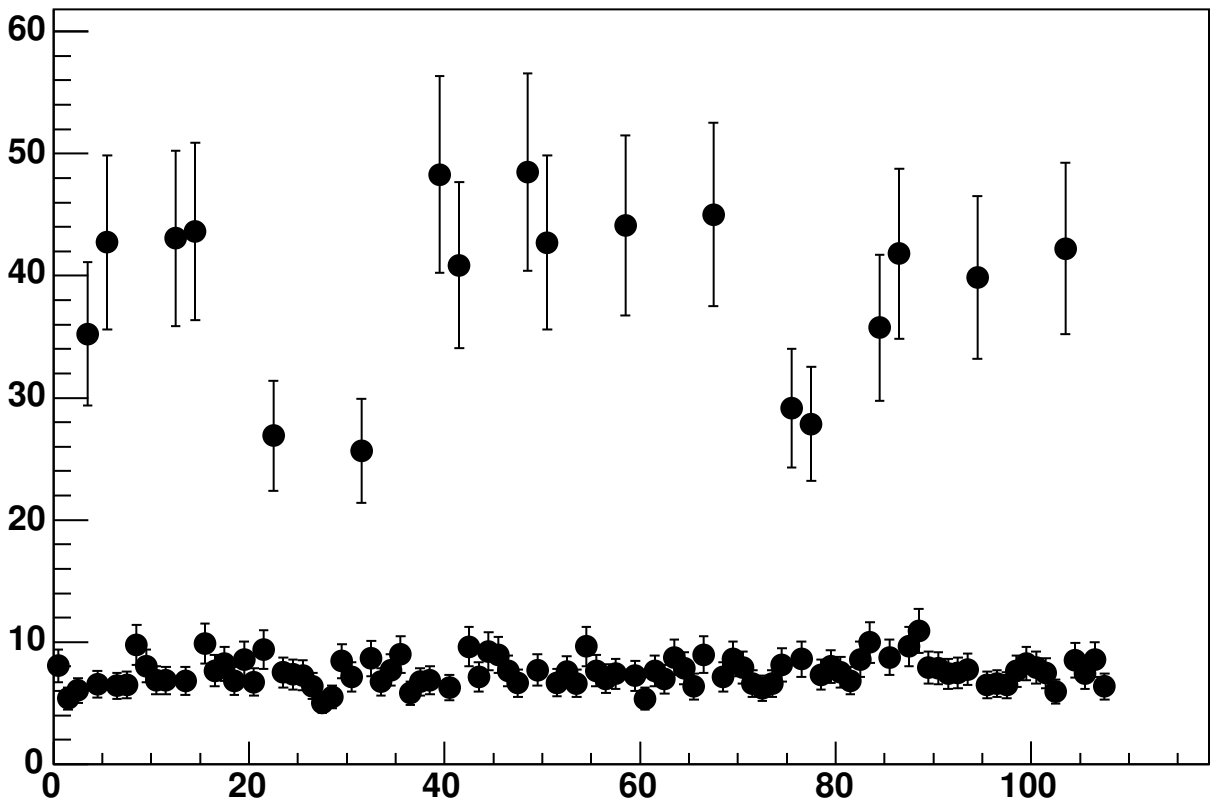
Enable 1, Hold=35, DAC=32000, ADC Noise vs 18*Chip+Chan



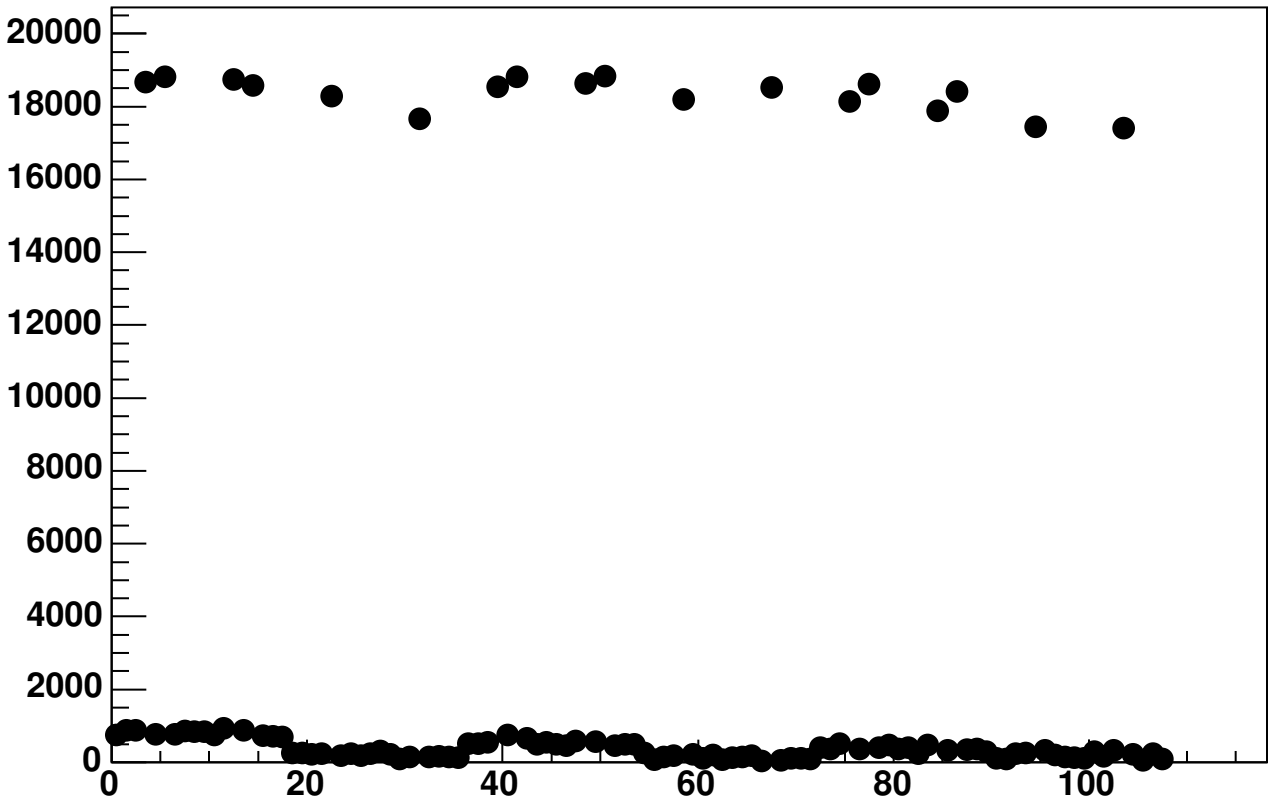
Enable 1, Hold=35, DAC=34000, ADC Mean vs 18*Chip+Chan



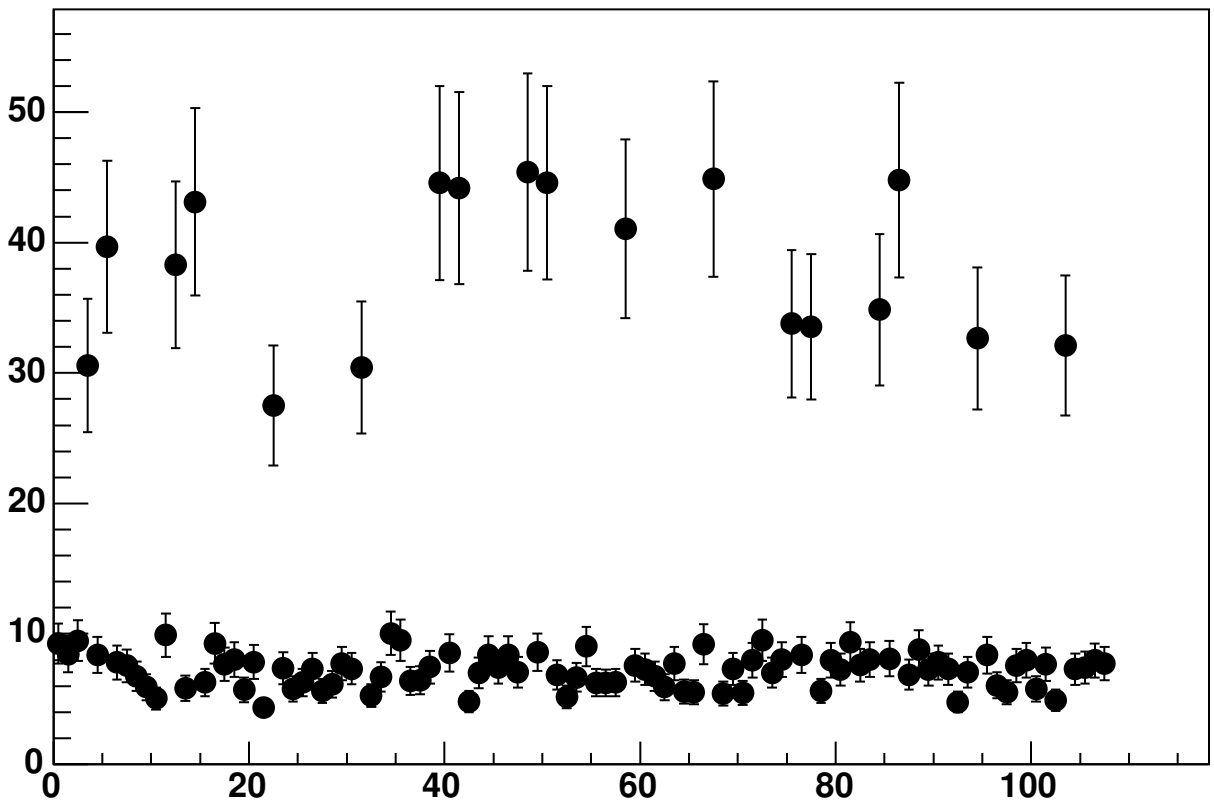
Enable 1, Hold=35, DAC=34000, ADC Noise vs 18*Chip+Chan



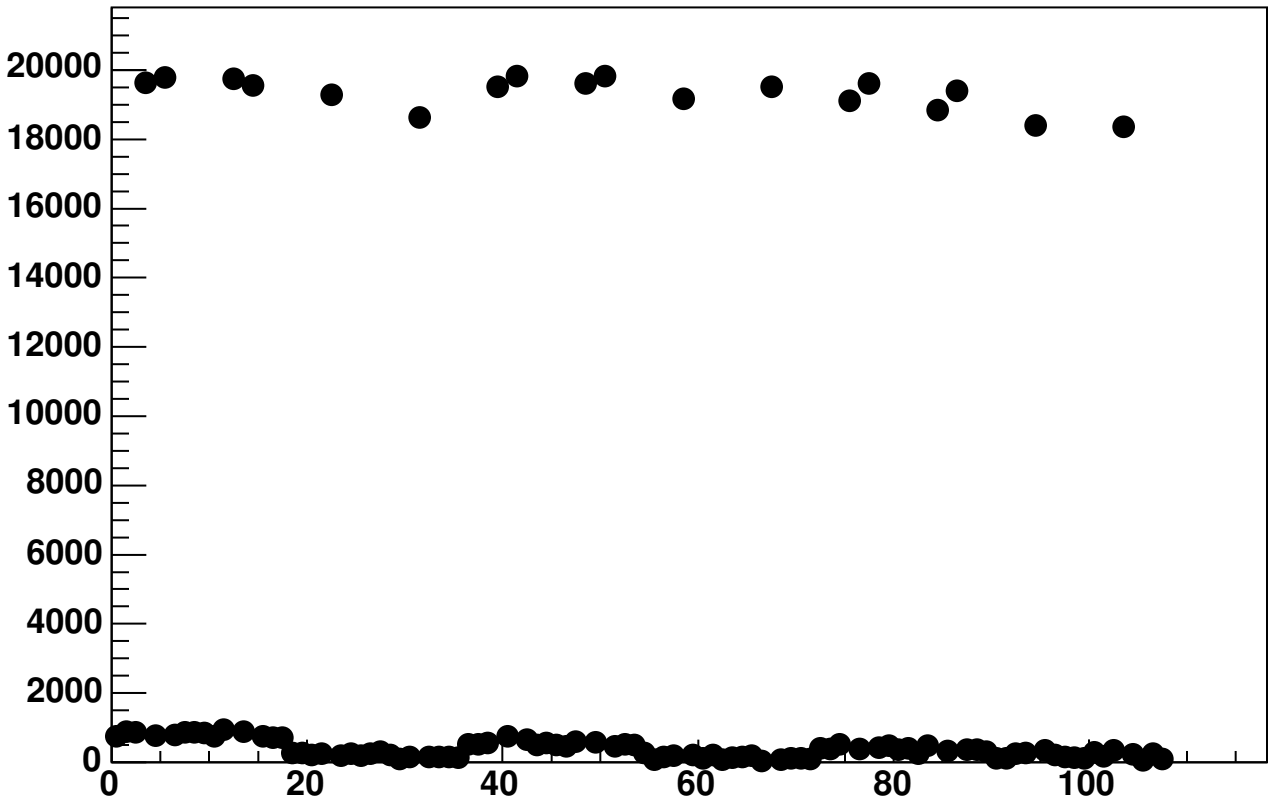
Enable 1, Hold=35, DAC=36000, ADC Mean vs 18*Chip+Chan



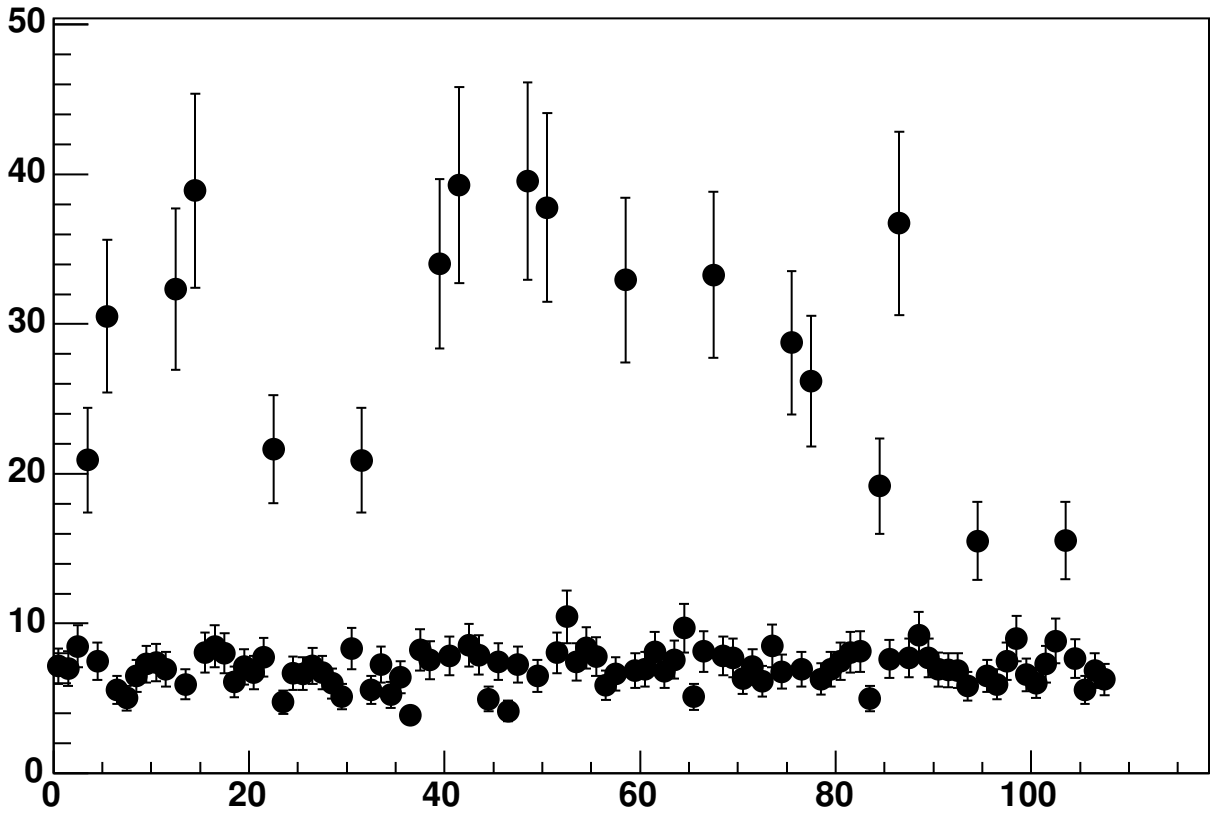
Enable 1, Hold=35, DAC=36000, ADC Noise vs 18*Chip+Chan



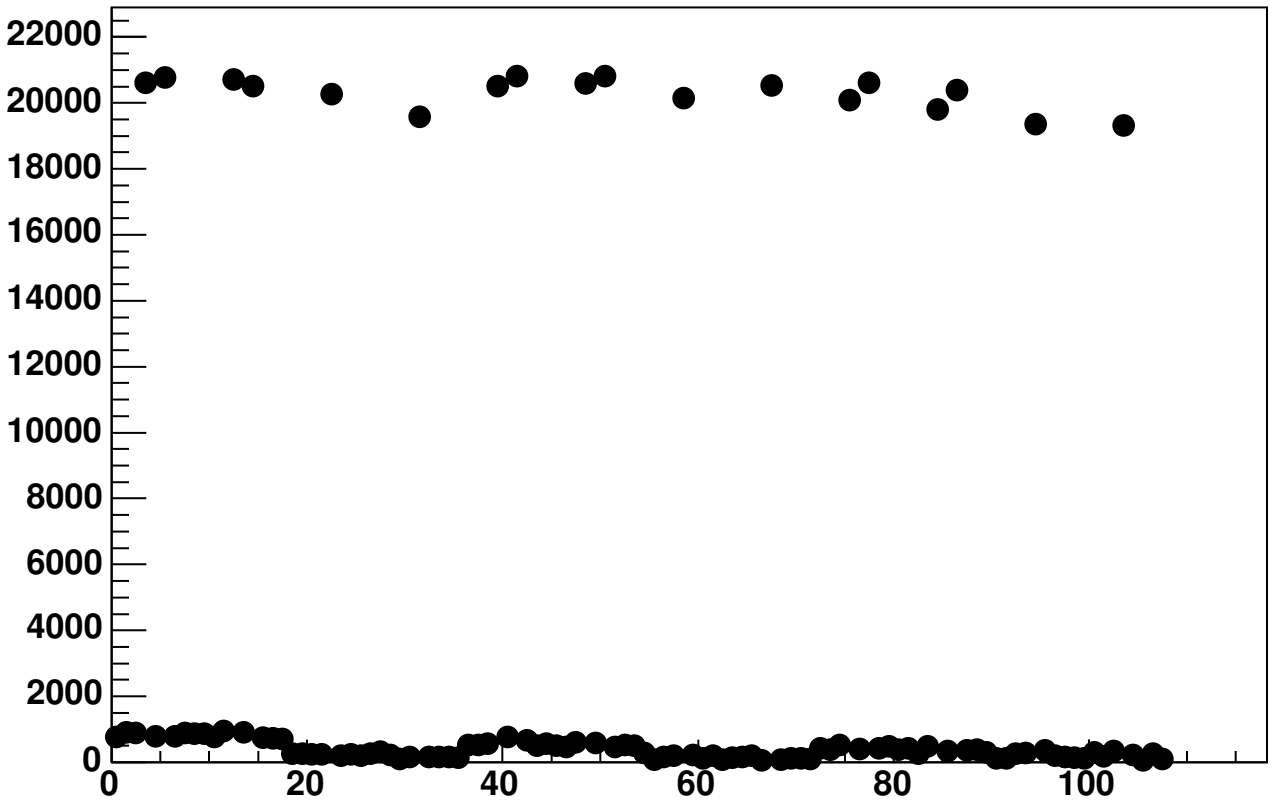
Enable 1, Hold=35, DAC=38000, ADC Mean vs 18*Chip+Chan



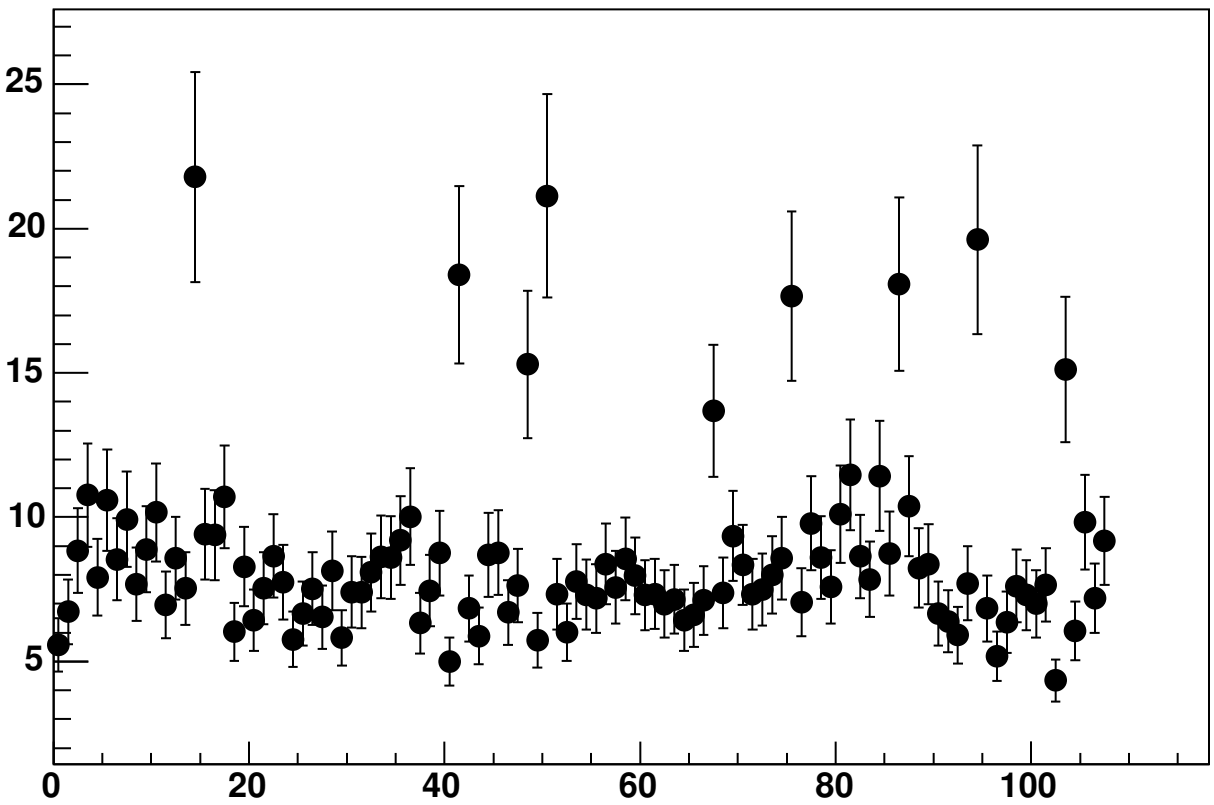
Enable 1, Hold=35, DAC=38000, ADC Noise vs 18*Chip+Chan



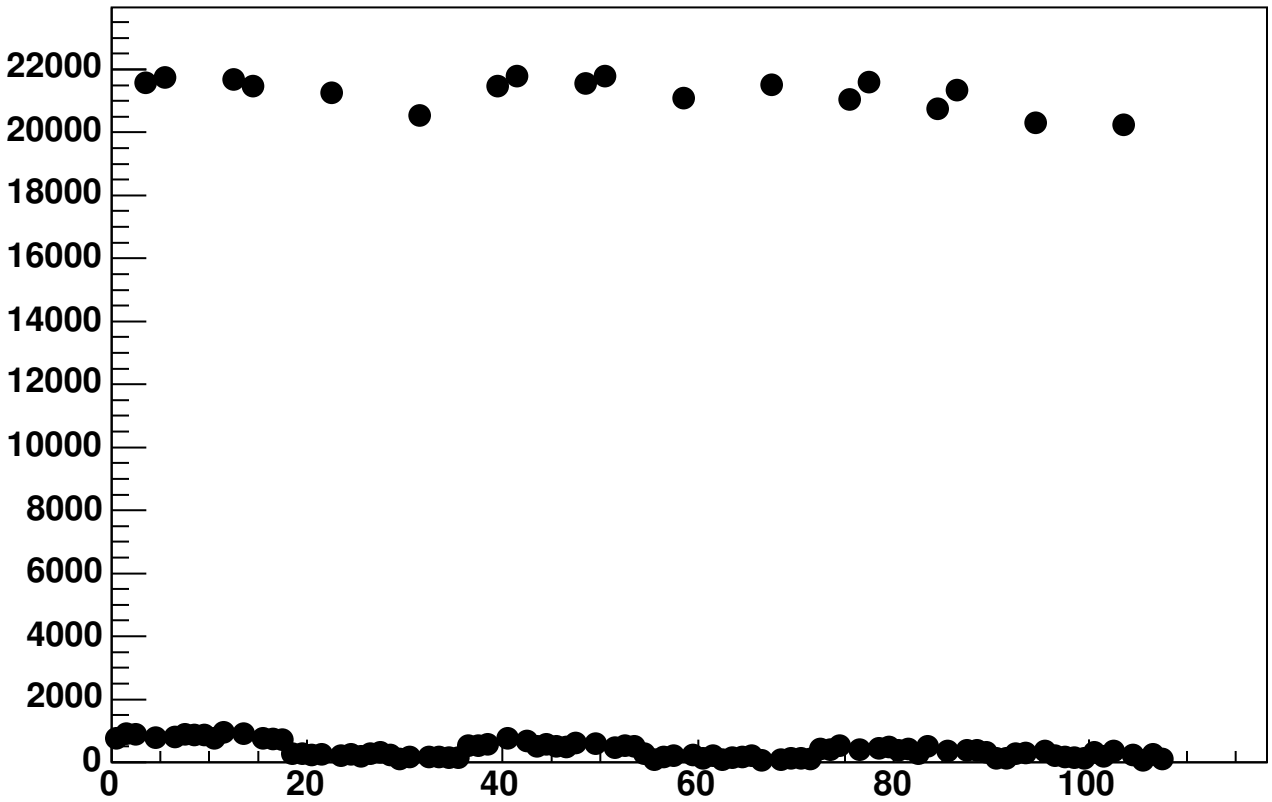
Enable 1, Hold=35, DAC=40000, ADC Mean vs 18*Chip+Chan



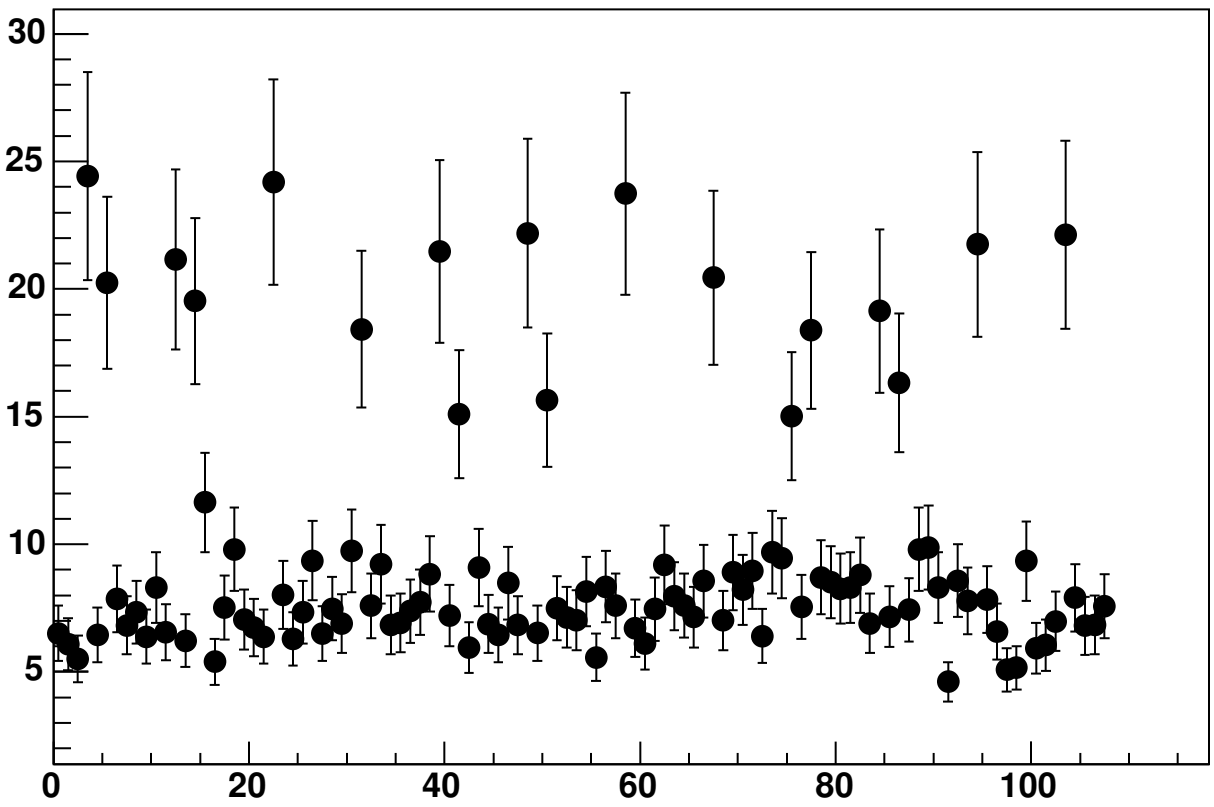
Enable 1, Hold=35, DAC=40000, ADC Noise vs 18*Chip+Chan



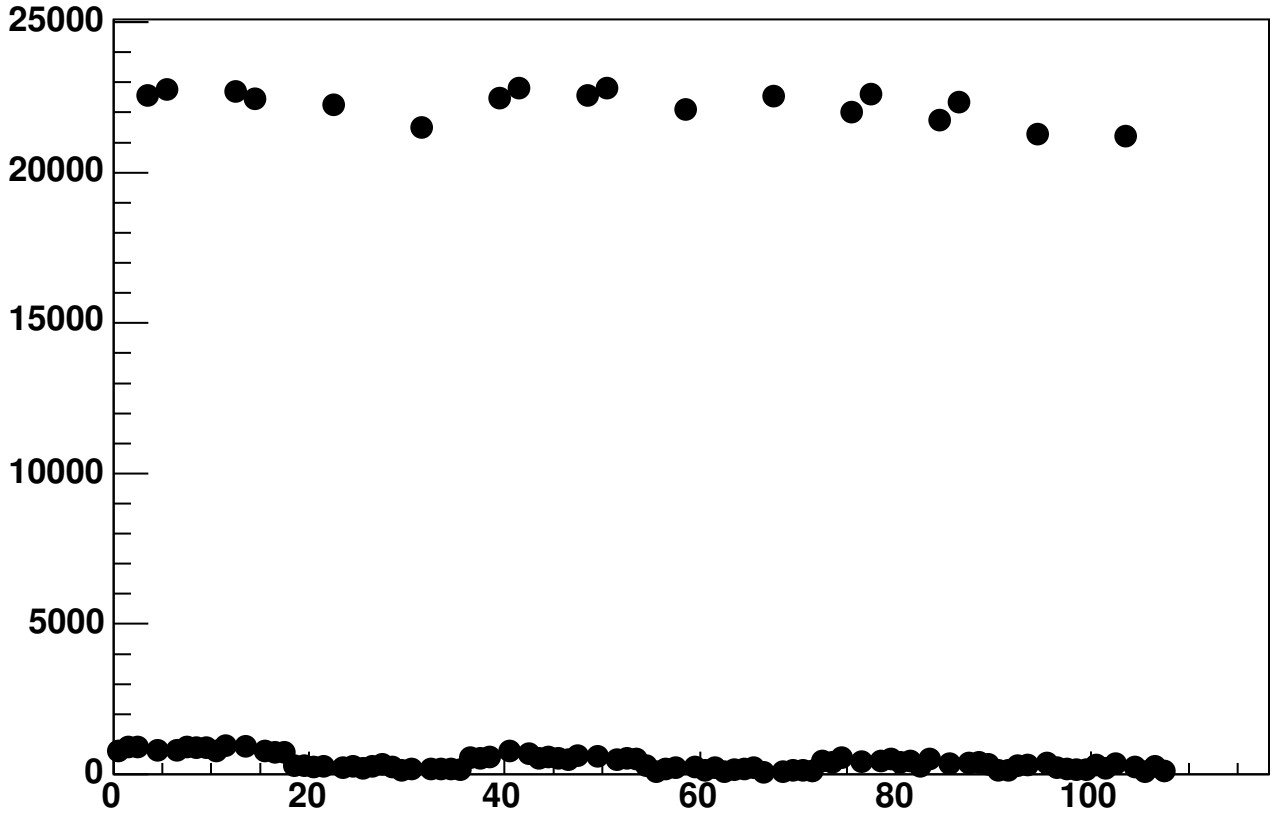
Enable 1, Hold=35, DAC=42000, ADC Mean vs 18*Chip+Chan



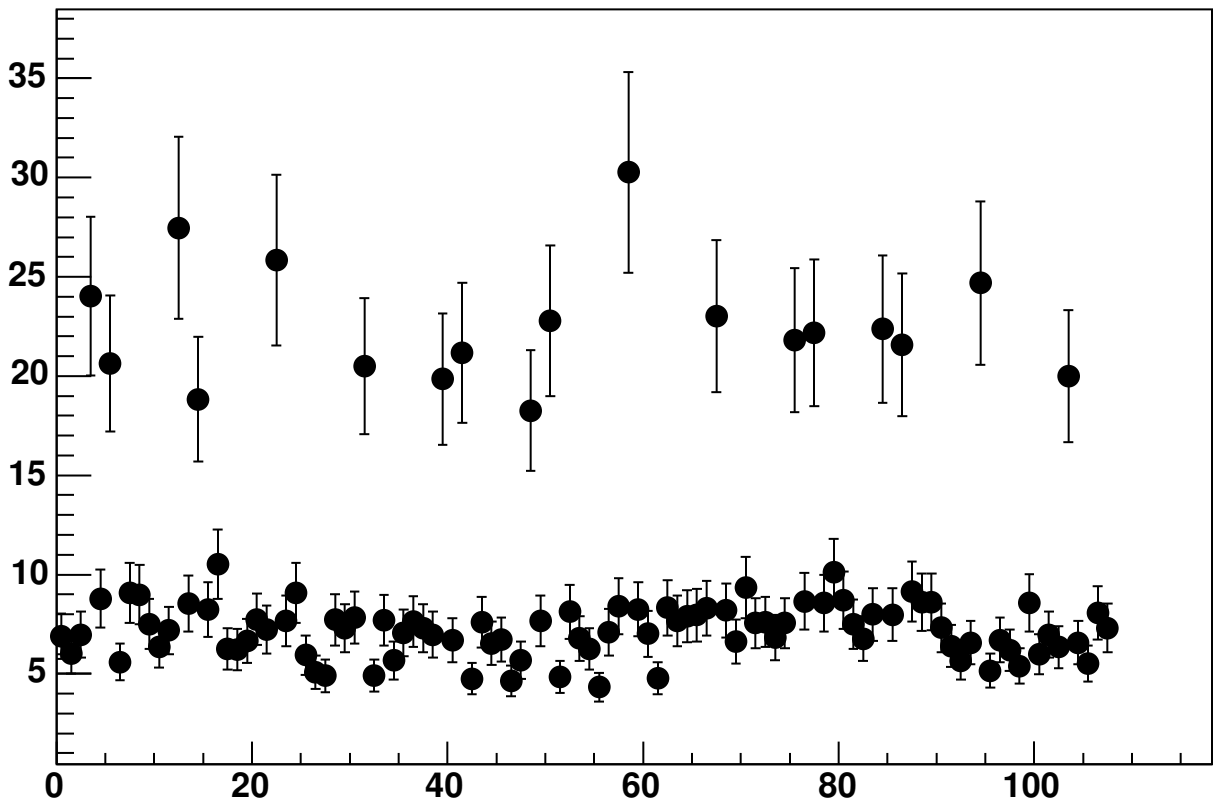
Enable 1, Hold=35, DAC=42000, ADC Noise vs 18*Chip+Chan



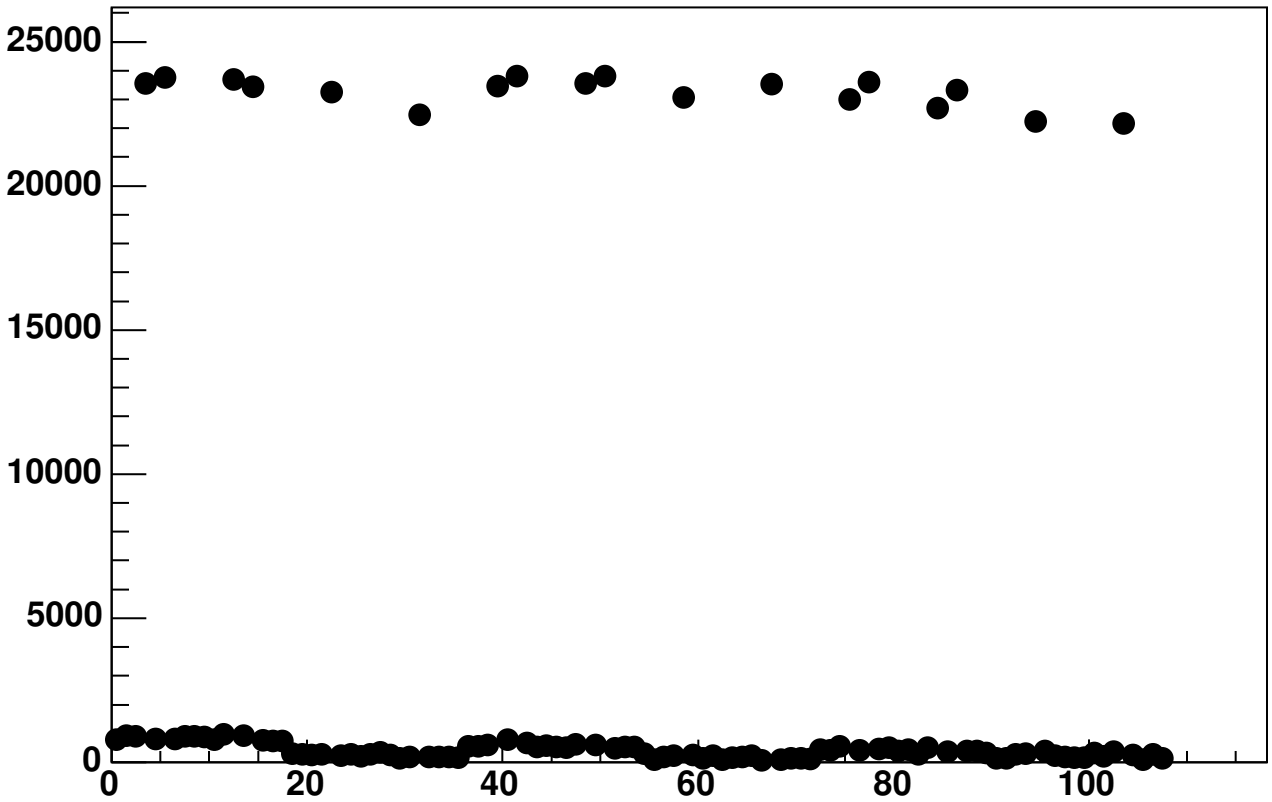
Enable 1, Hold=35, DAC=44000, ADC Mean vs 18*Chip+Chan



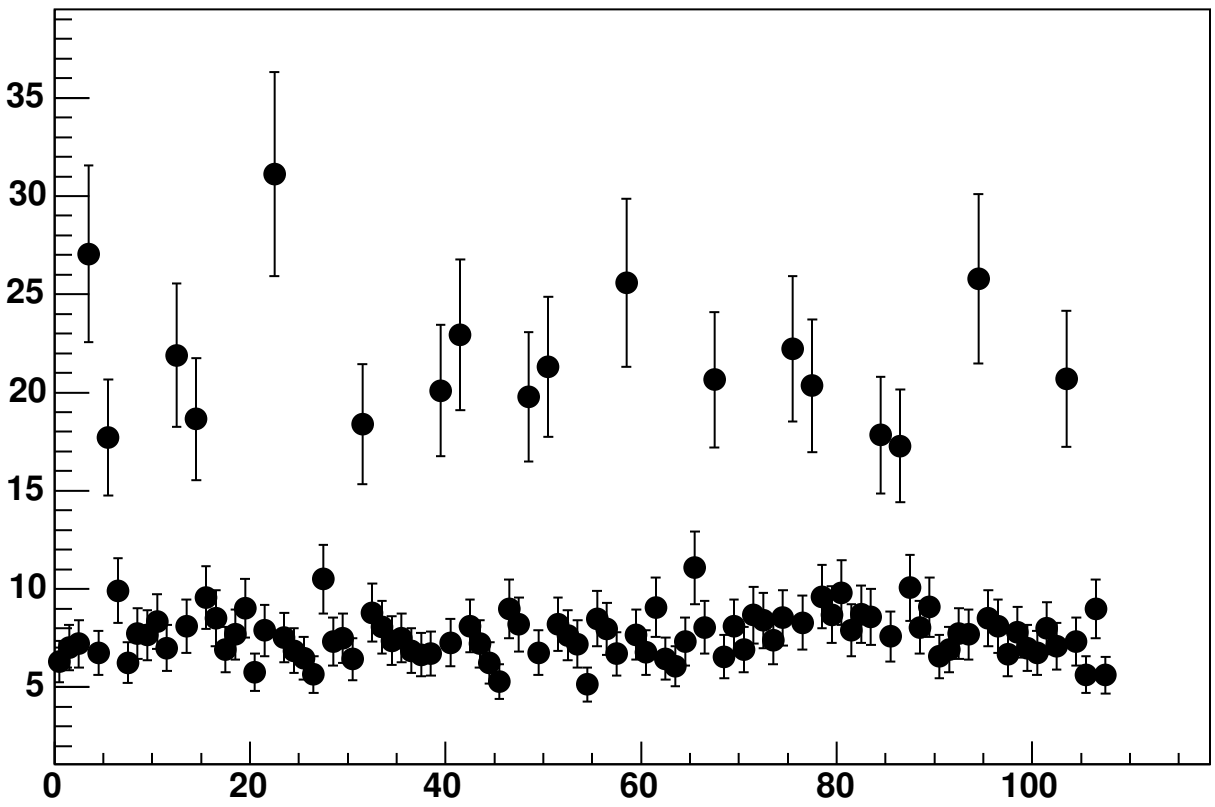
Enable 1, Hold=35, DAC=44000, ADC Noise vs 18*Chip+Chan



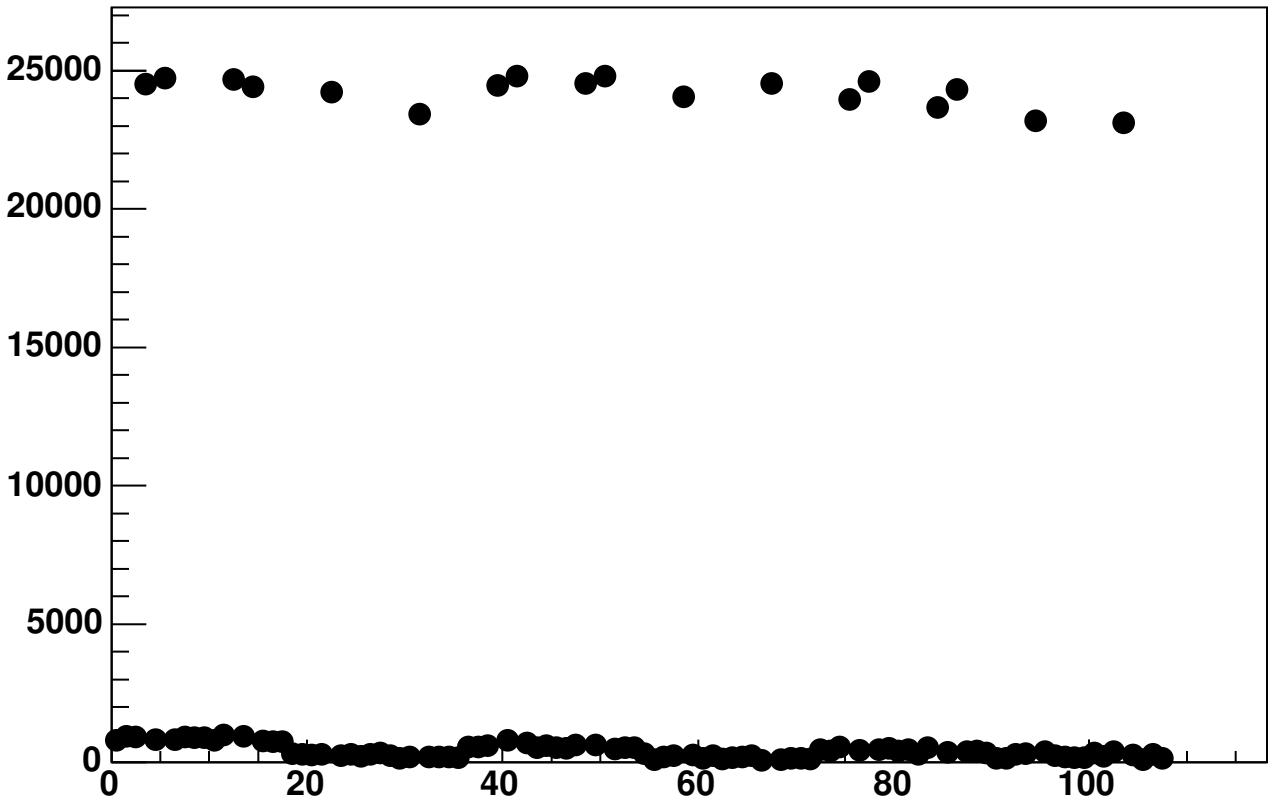
Enable 1, Hold=35, DAC=46000, ADC Mean vs 18*Chip+Chan



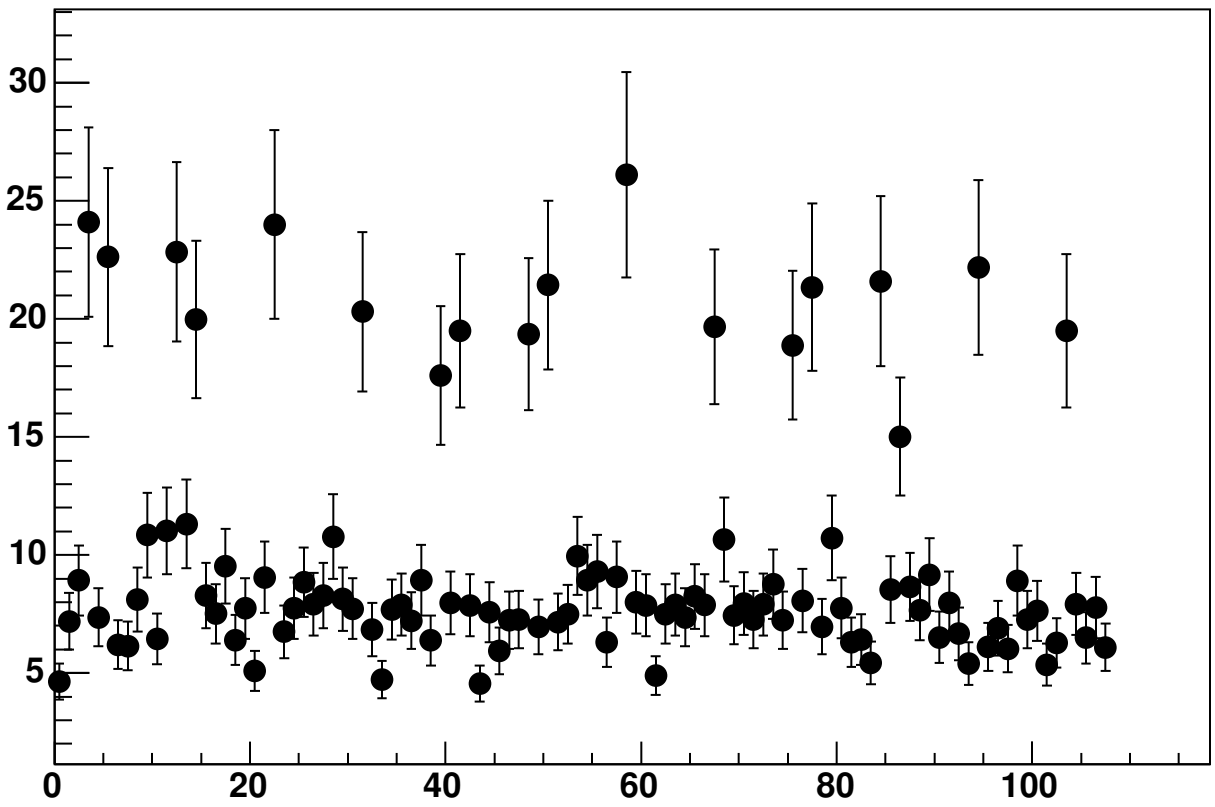
Enable 1, Hold=35, DAC=46000, ADC Noise vs 18*Chip+Chan



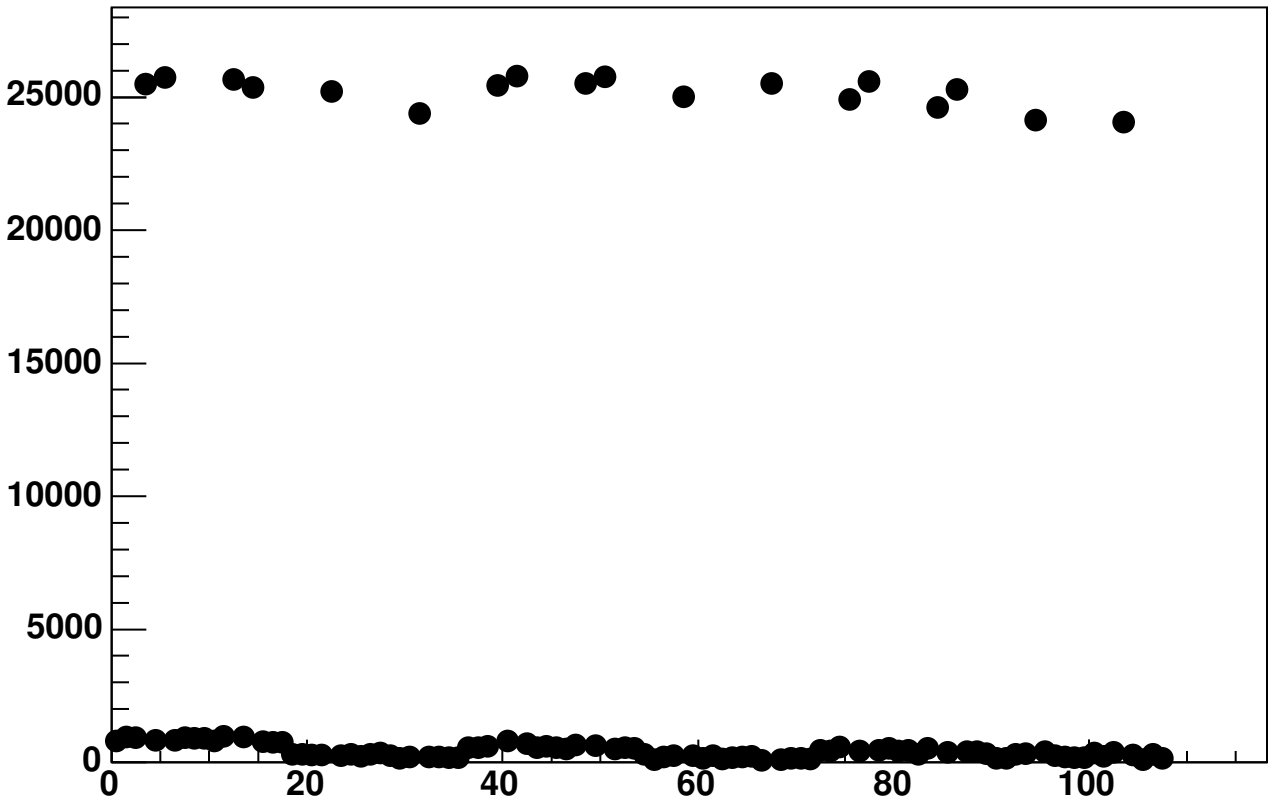
Enable 1, Hold=35, DAC=48000, ADC Mean vs 18*Chip+Chan



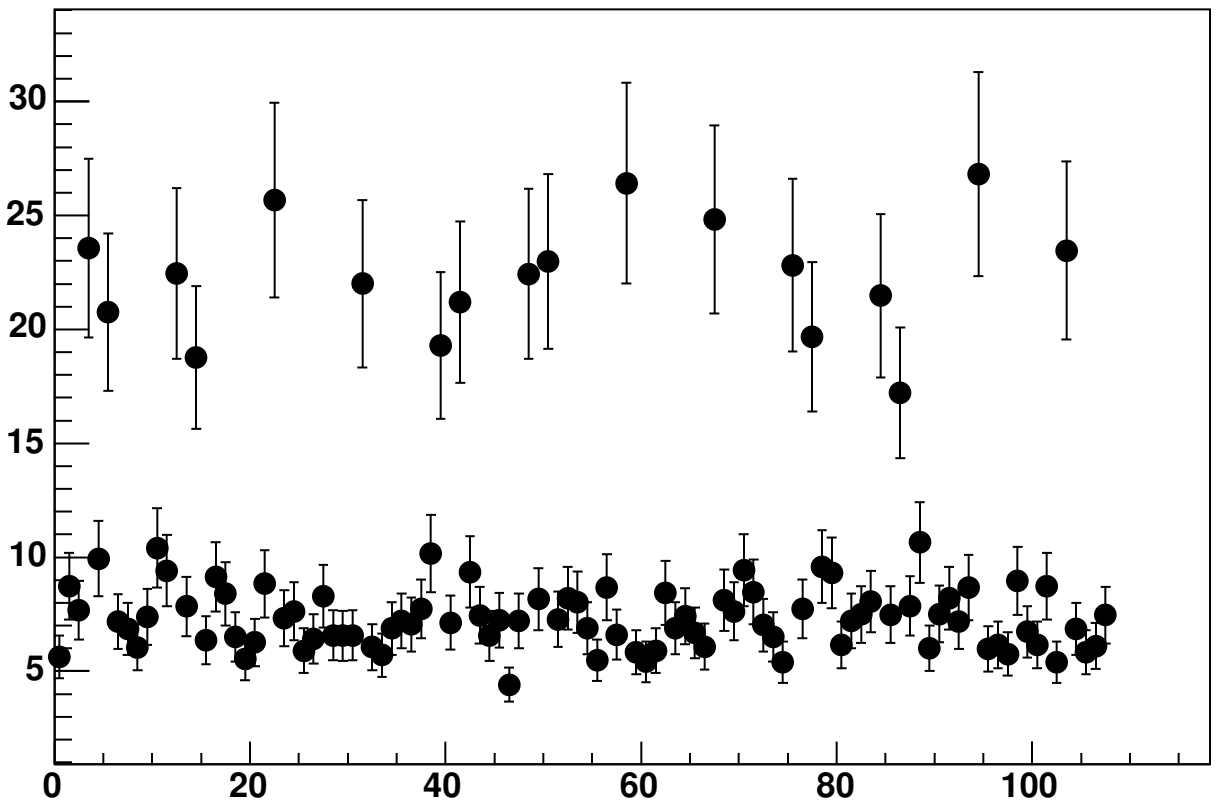
Enable 1, Hold=35, DAC=48000, ADC Noise vs 18*Chip+Chan



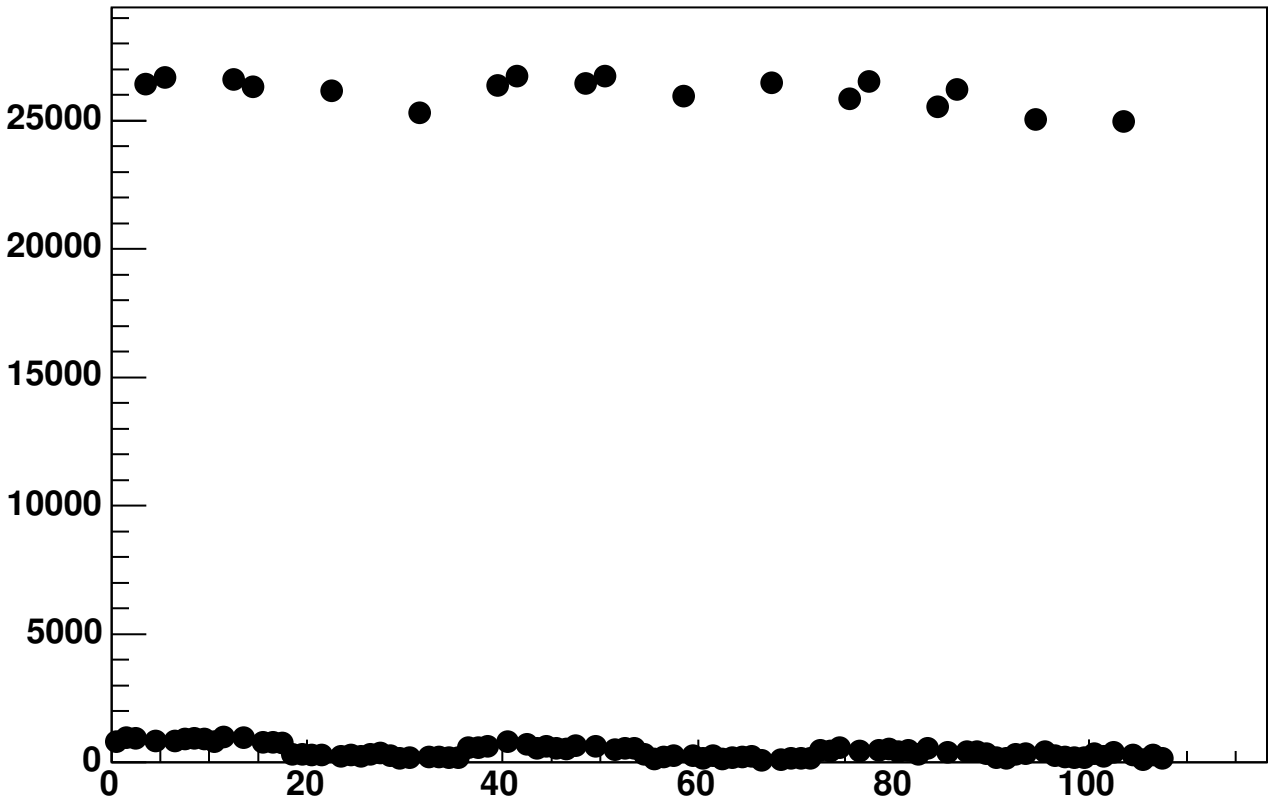
Enable 1, Hold=35, DAC=50000, ADC Mean vs 18*Chip+Chan



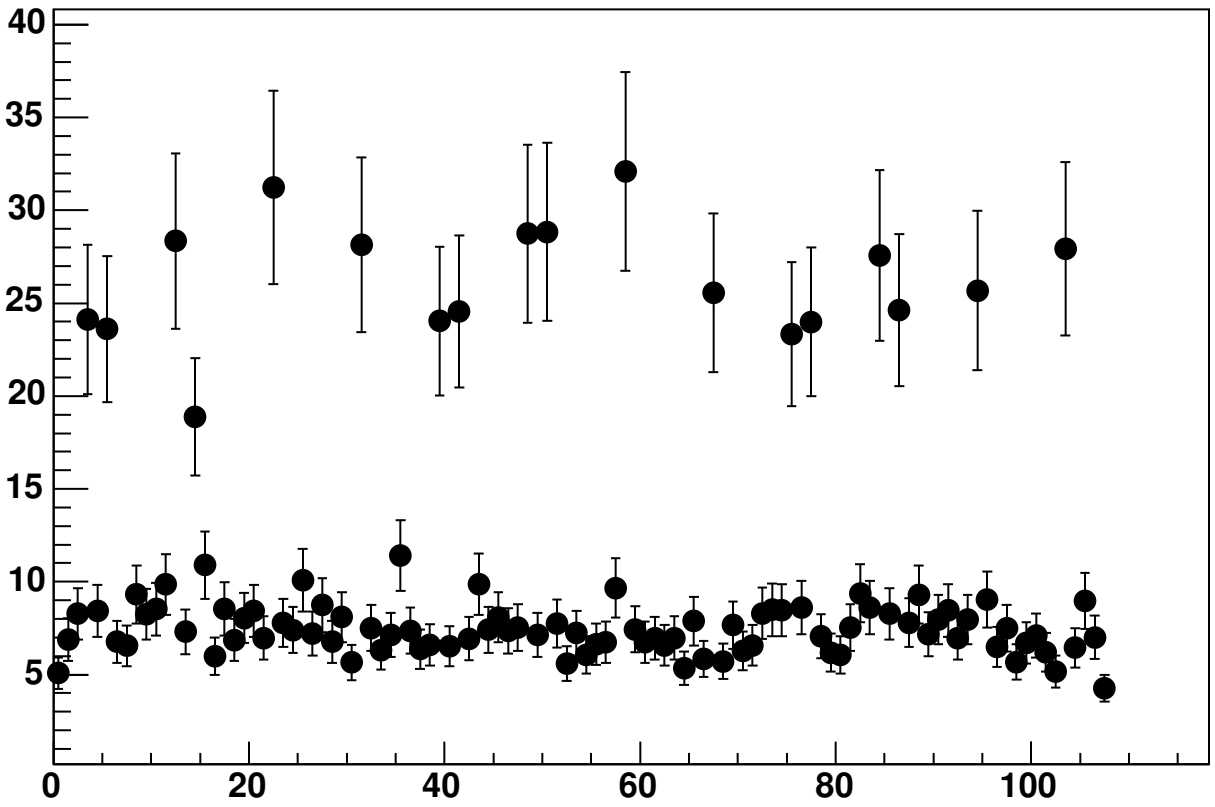
Enable 1, Hold=35, DAC=50000, ADC Noise vs 18*Chip+Chan



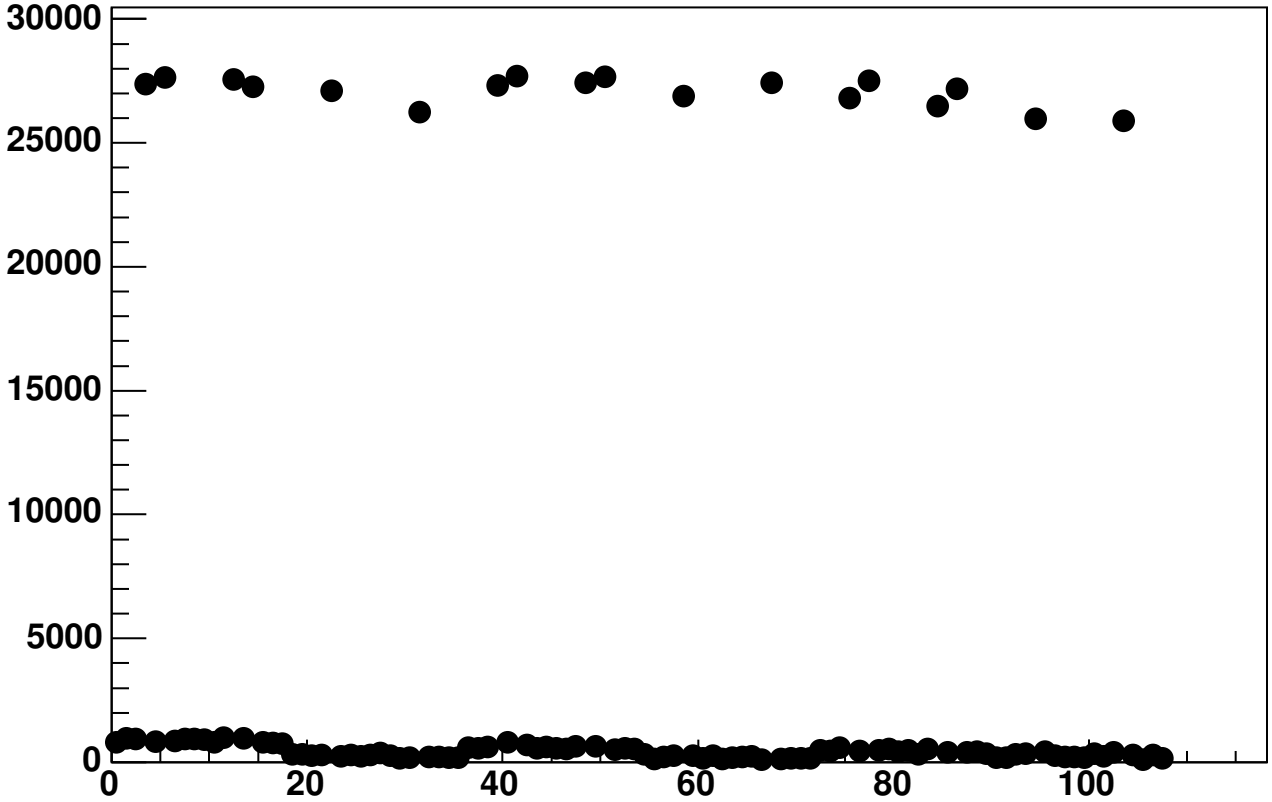
Enable 1, Hold=35, DAC=52000, ADC Mean vs 18*Chip+Chan



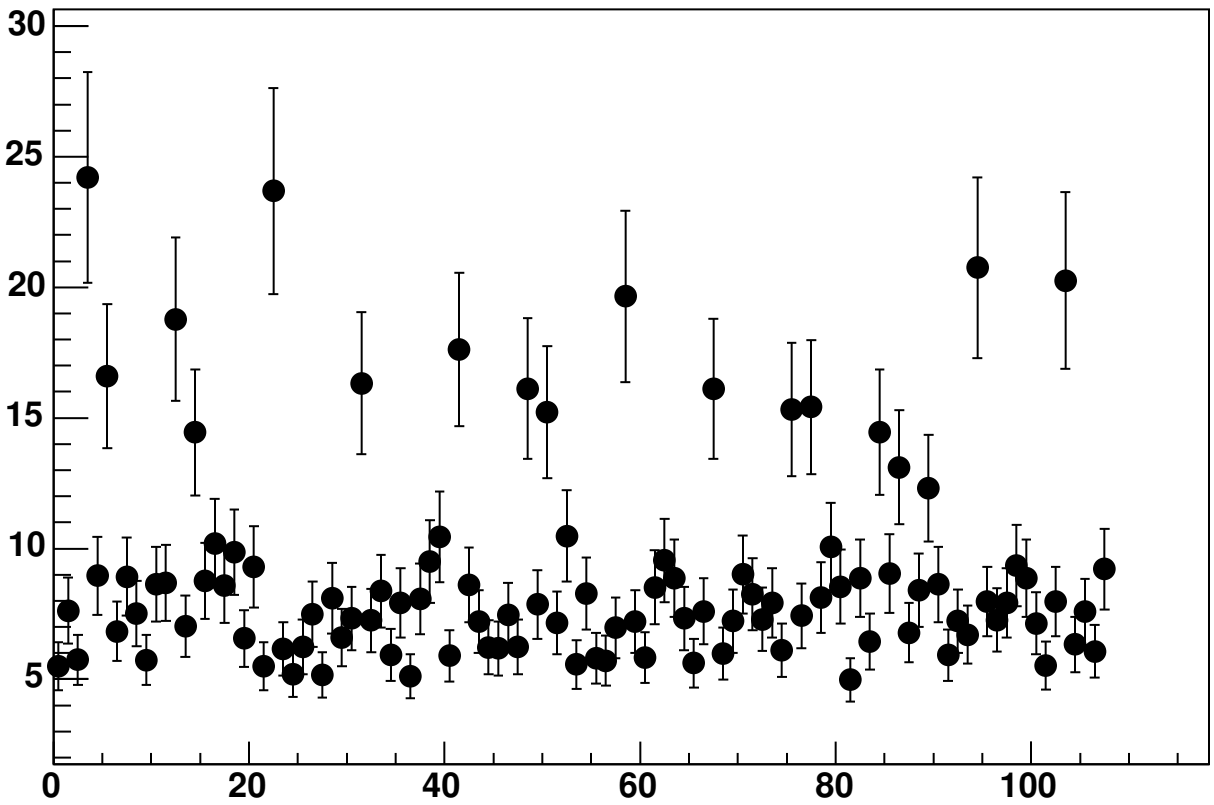
Enable 1, Hold=35, DAC=52000, ADC Noise vs 18*Chip+Chan



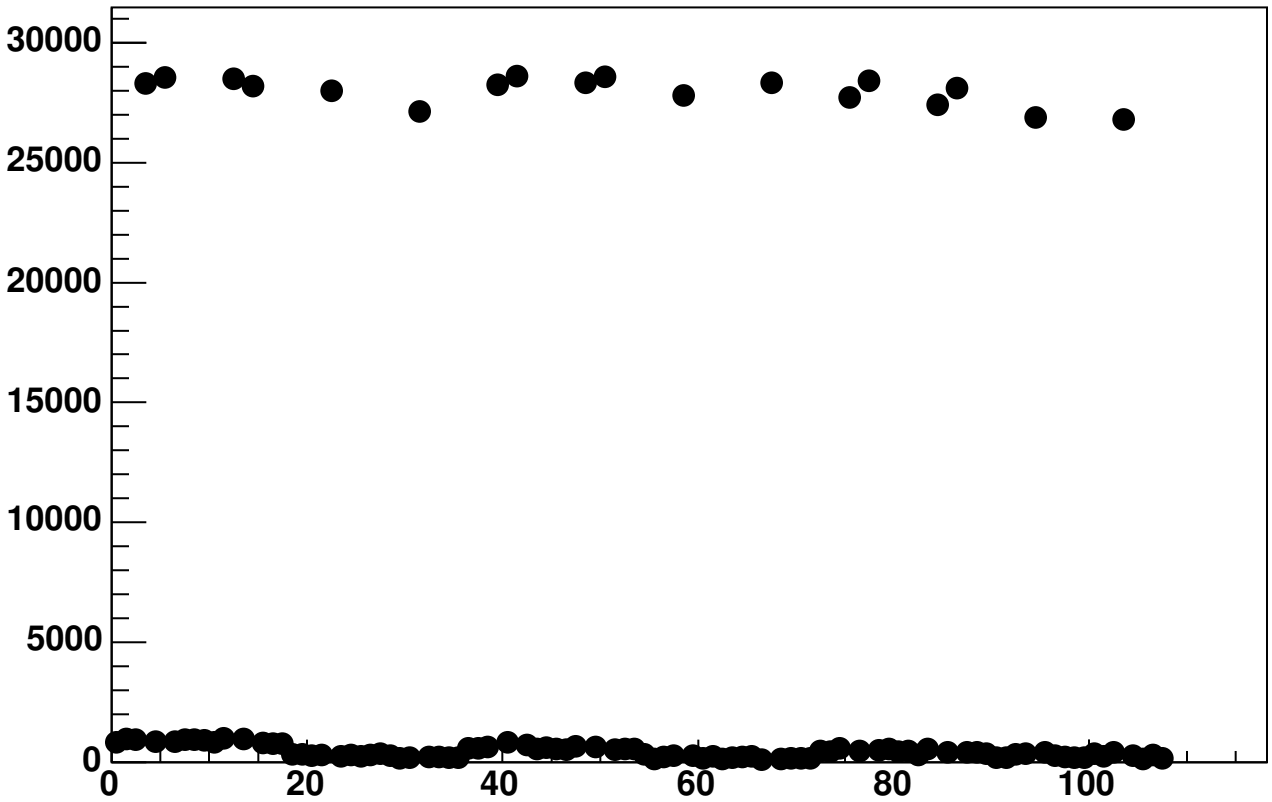
Enable 1, Hold=35, DAC=54000, ADC Mean vs 18*Chip+Chan



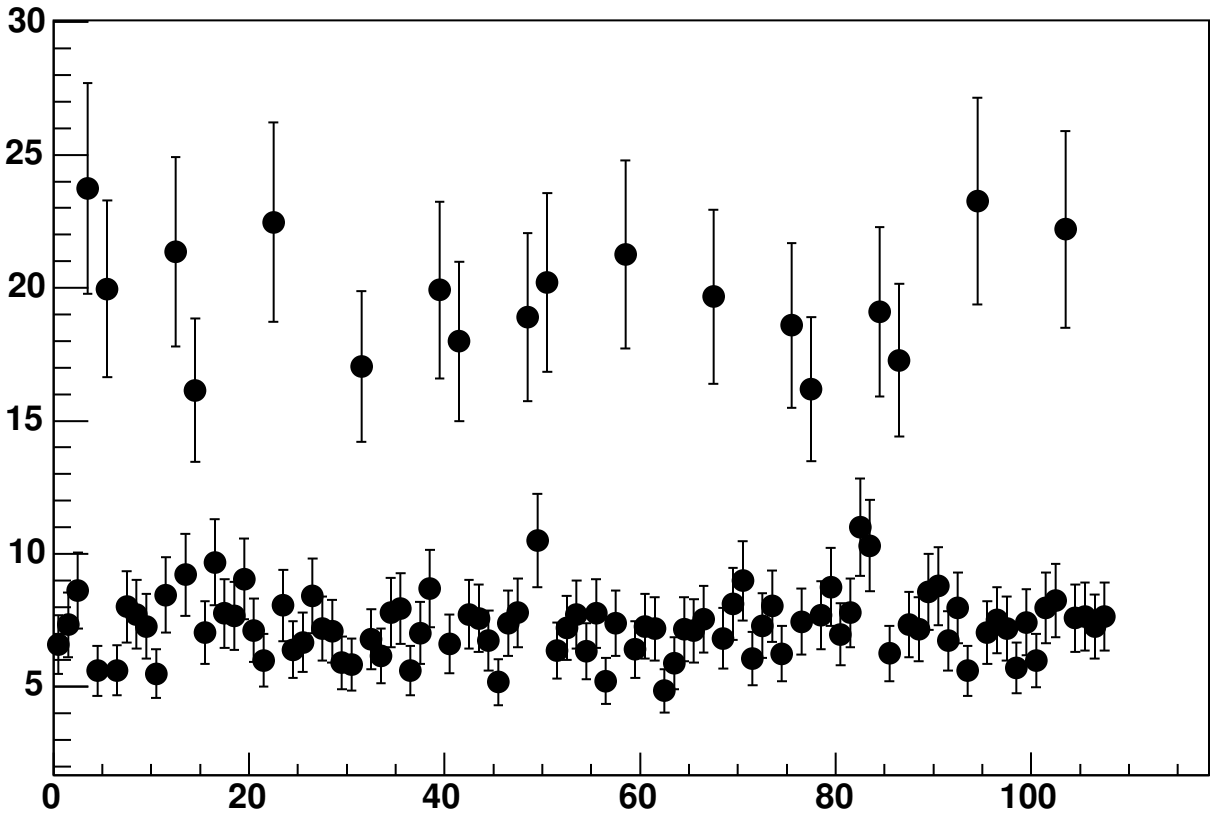
Enable 1, Hold=35, DAC=54000, ADC Noise vs 18*Chip+Chan



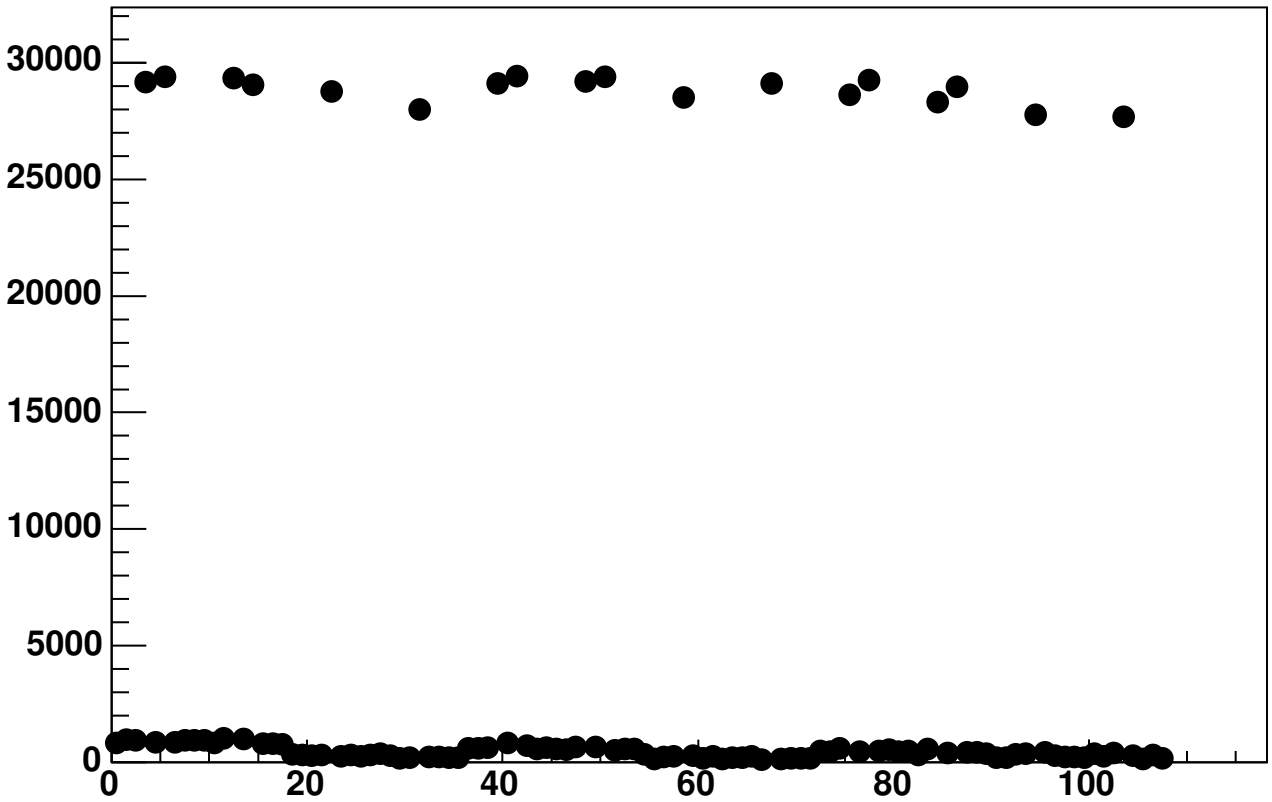
Enable 1, Hold=35, DAC=56000, ADC Mean vs 18*Chip+Chan



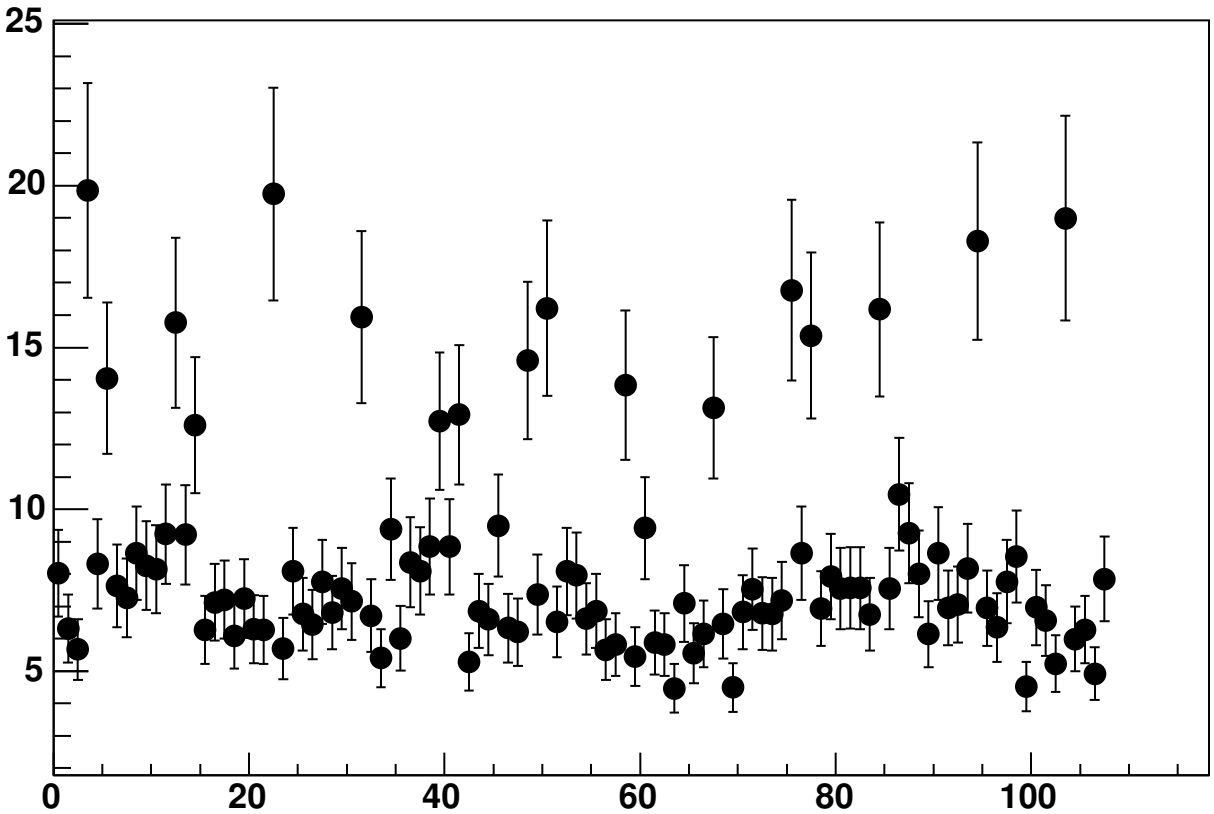
Enable 1, Hold=35, DAC=56000, ADC Noise vs 18*Chip+Chan



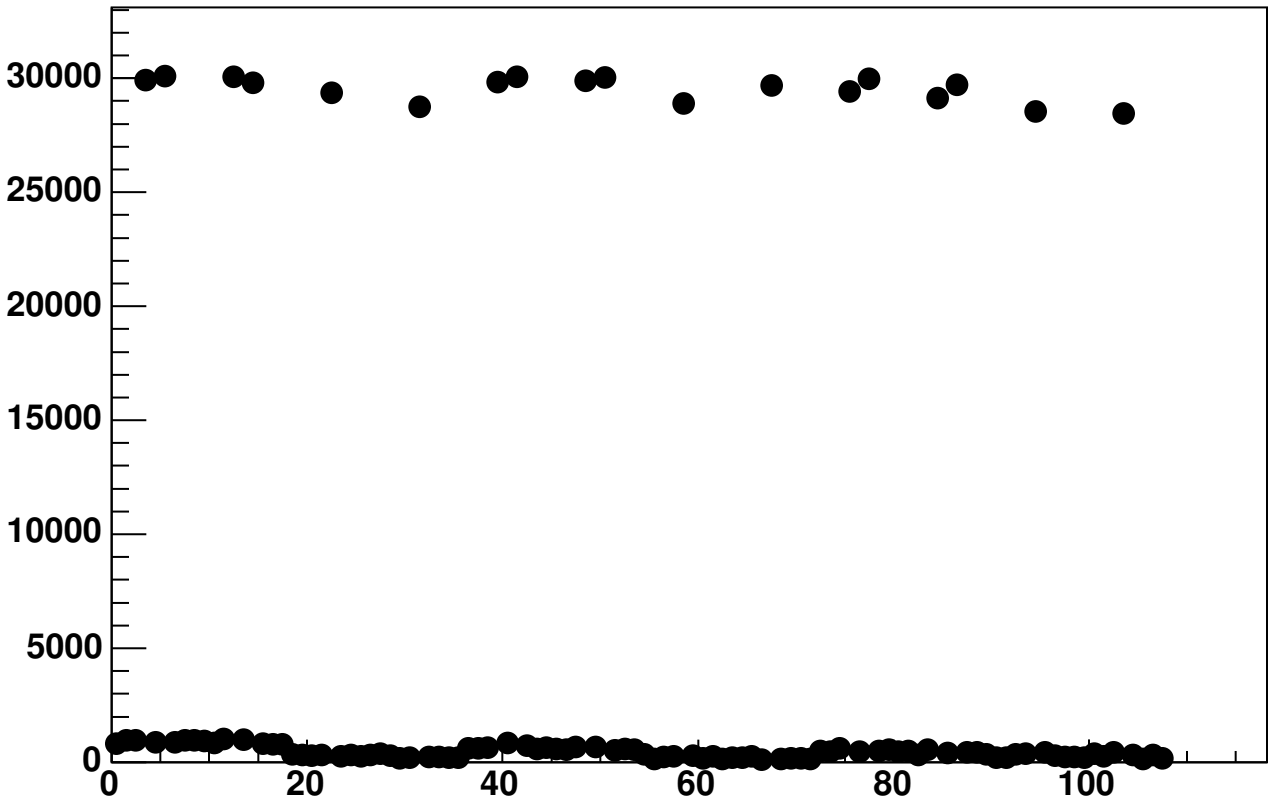
Enable 1, Hold=35, DAC=58000, ADC Mean vs 18*Chip+Chan



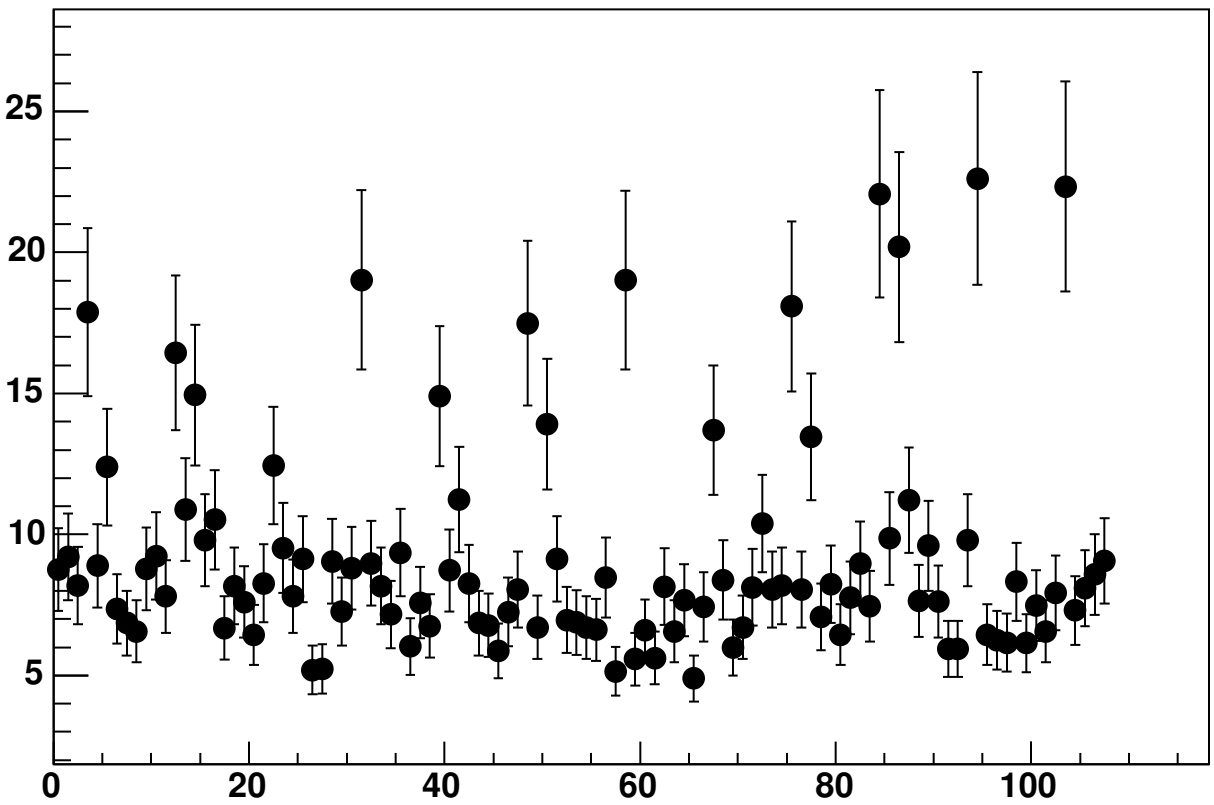
Enable 1, Hold=35, DAC=58000, ADC Noise vs 18*Chip+Chan



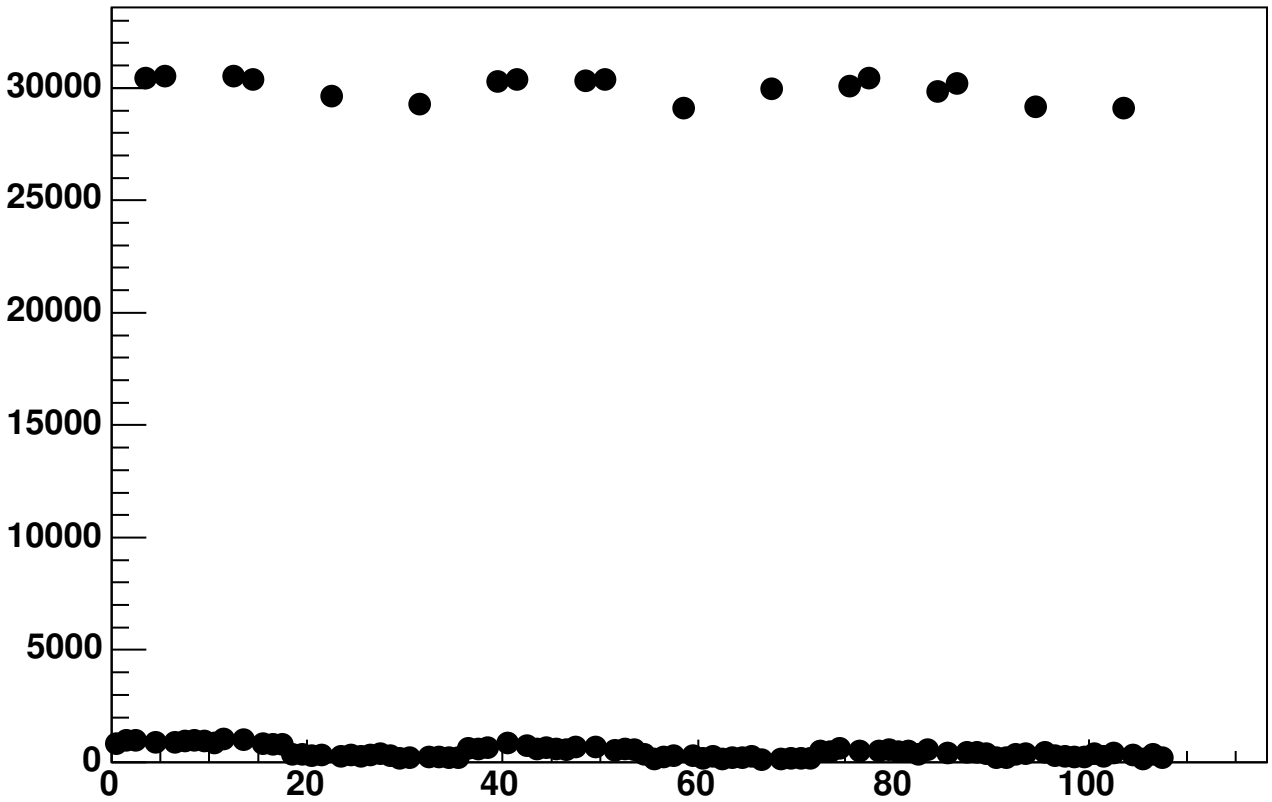
Enable 1, Hold=35, DAC=60000, ADC Mean vs 18*Chip+Chan



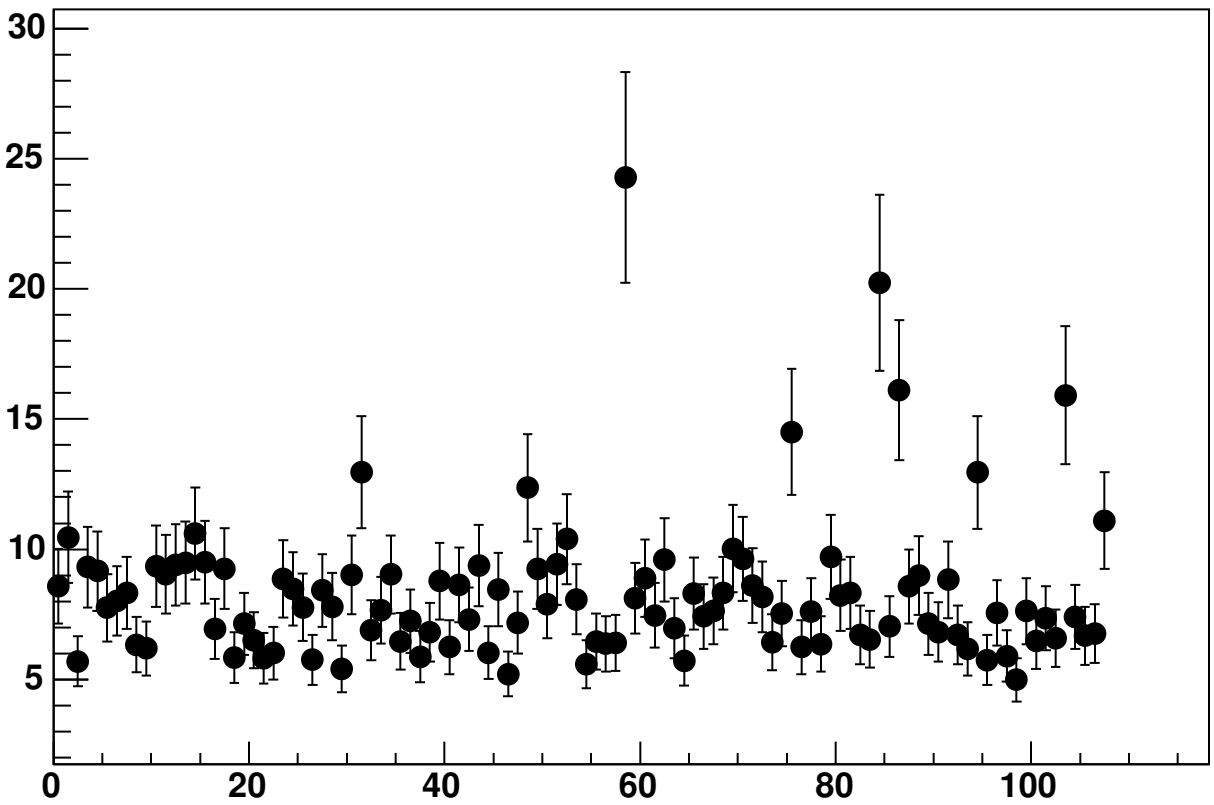
Enable 1, Hold=35, DAC=60000, ADC Noise vs 18*Chip+Chan



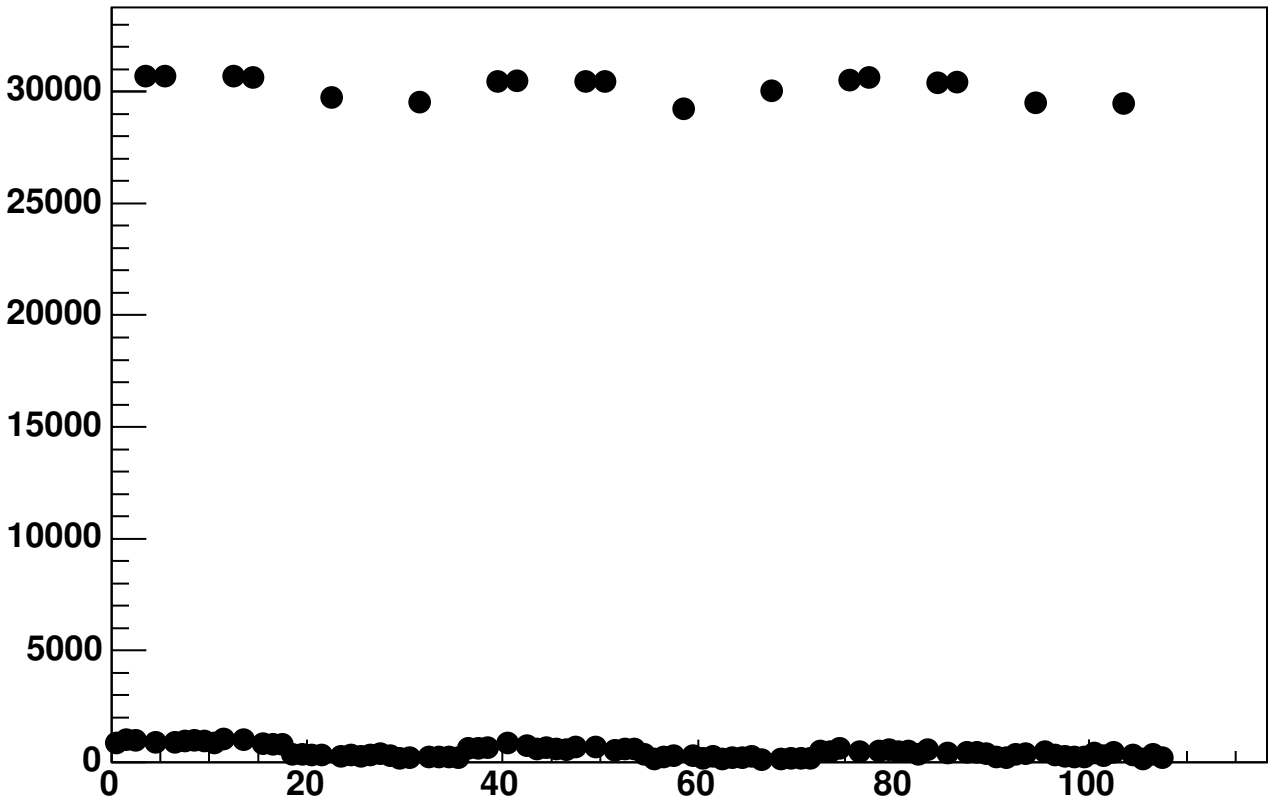
Enable 1, Hold=35, DAC=62000, ADC Mean vs 18*Chip+Chan



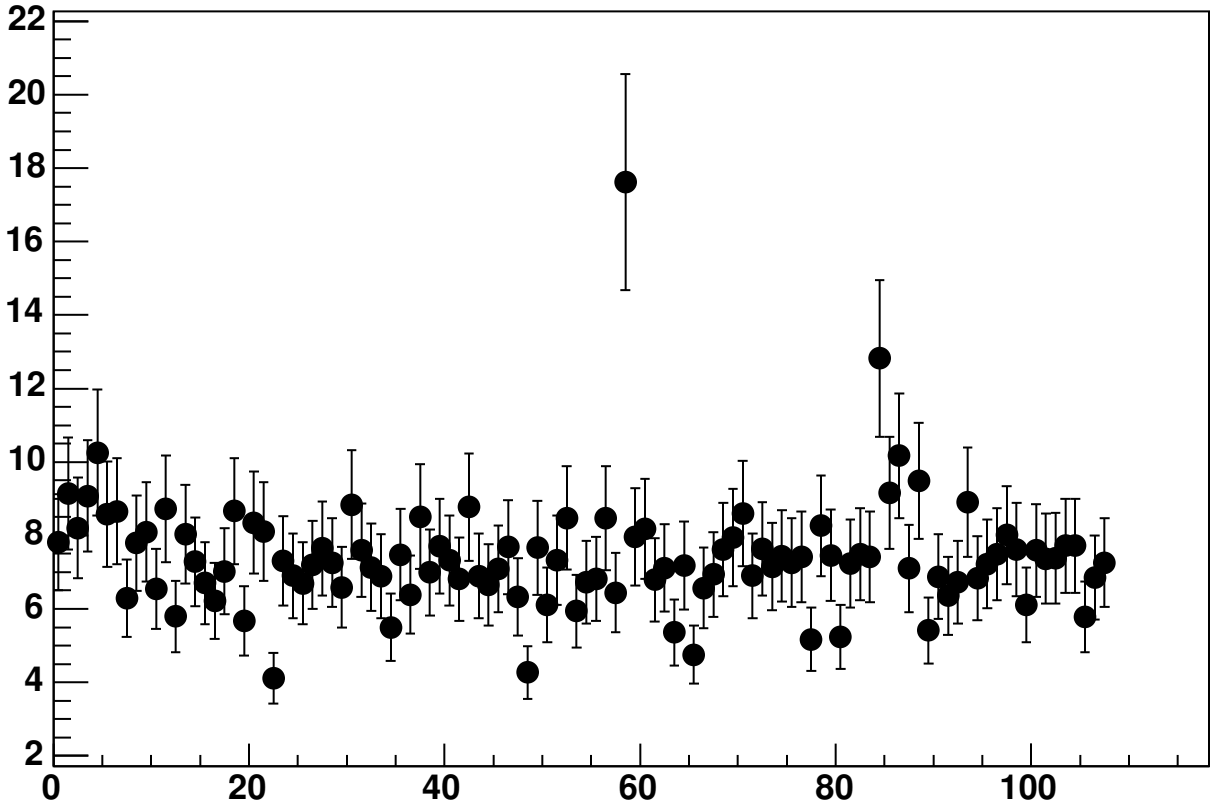
Enable 1, Hold=35, DAC=62000, ADC Noise vs 18*Chip+Chan



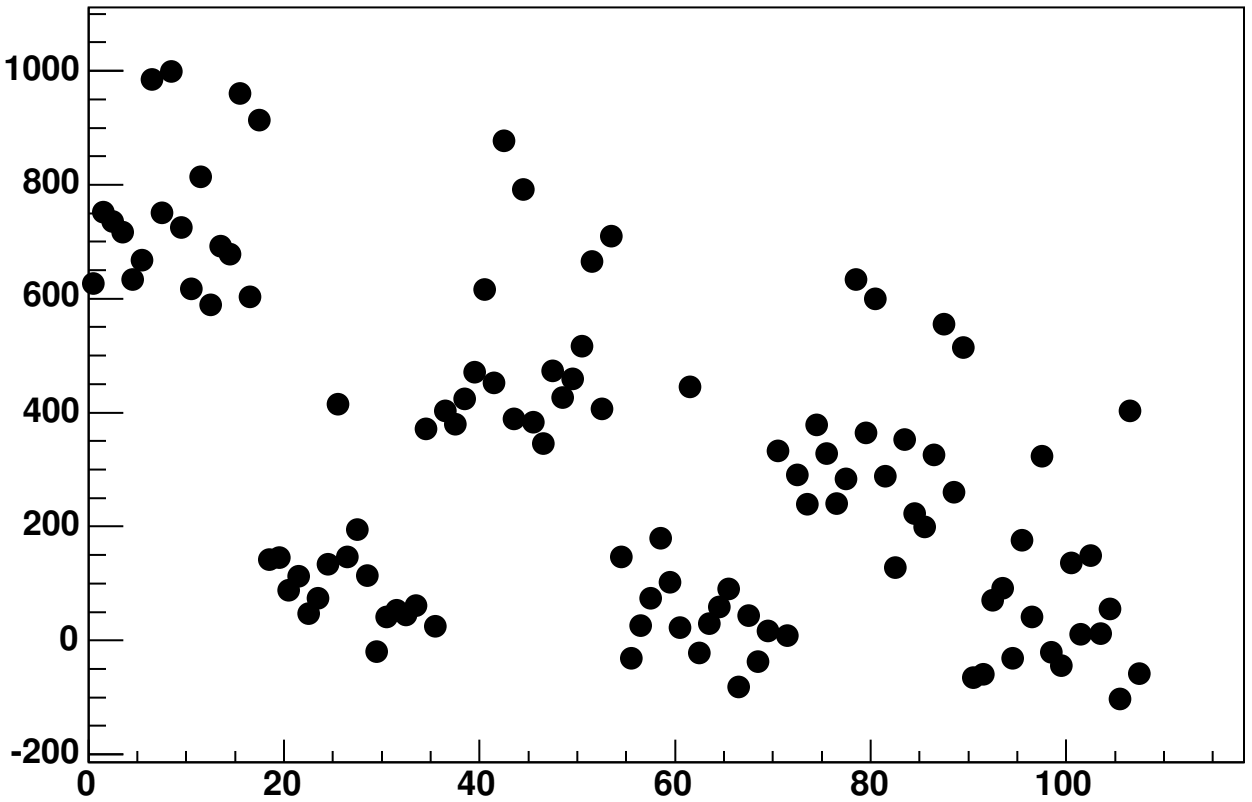
Enable 1, Hold=35, DAC=64000, ADC Mean vs 18*Chip+Chan



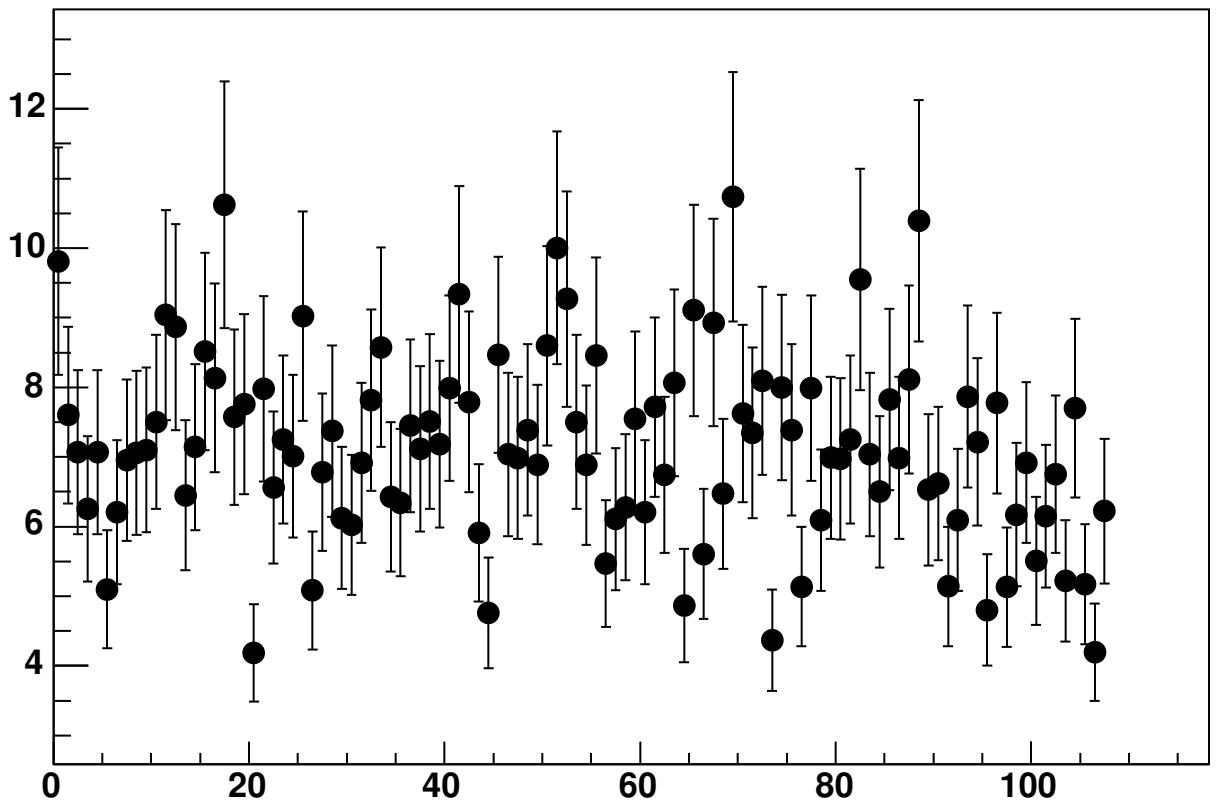
Enable 1, Hold=35, DAC=64000, ADC Noise vs 18*Chip+Chan



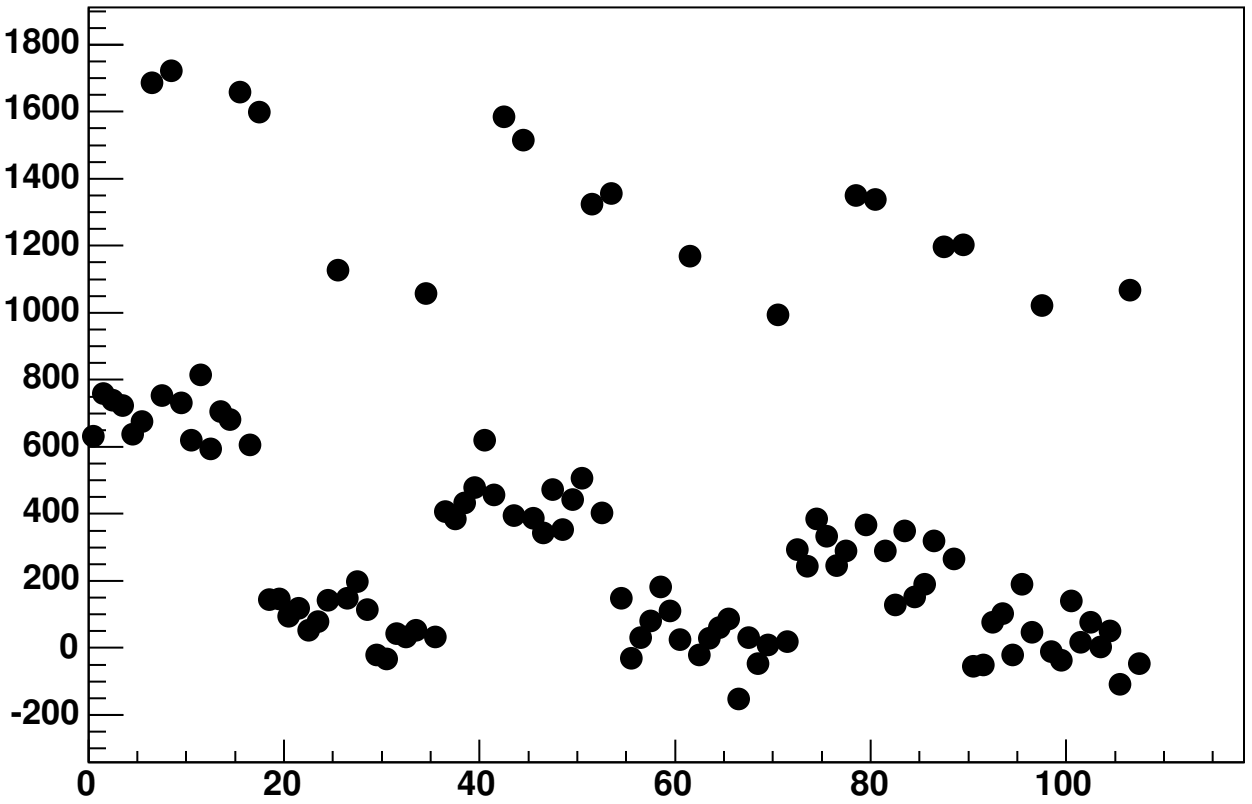
Enable 2, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



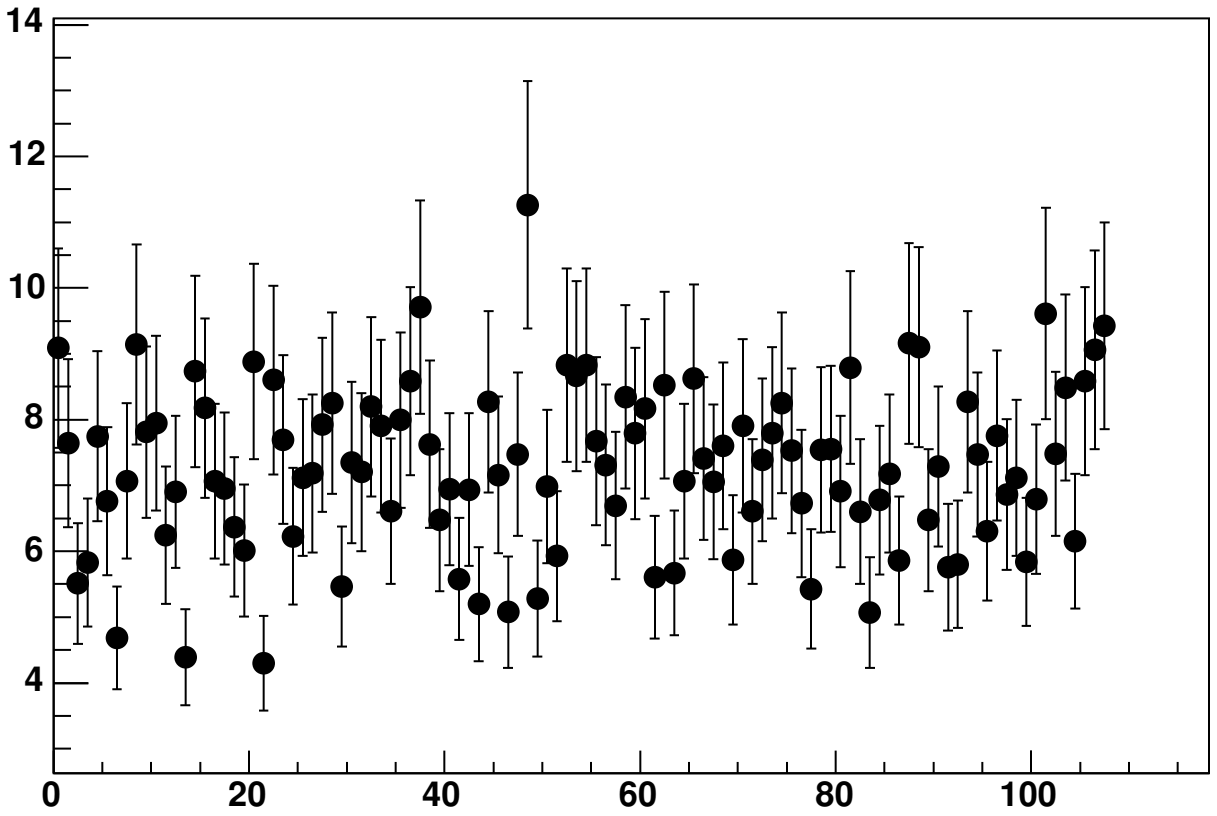
Enable 2, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



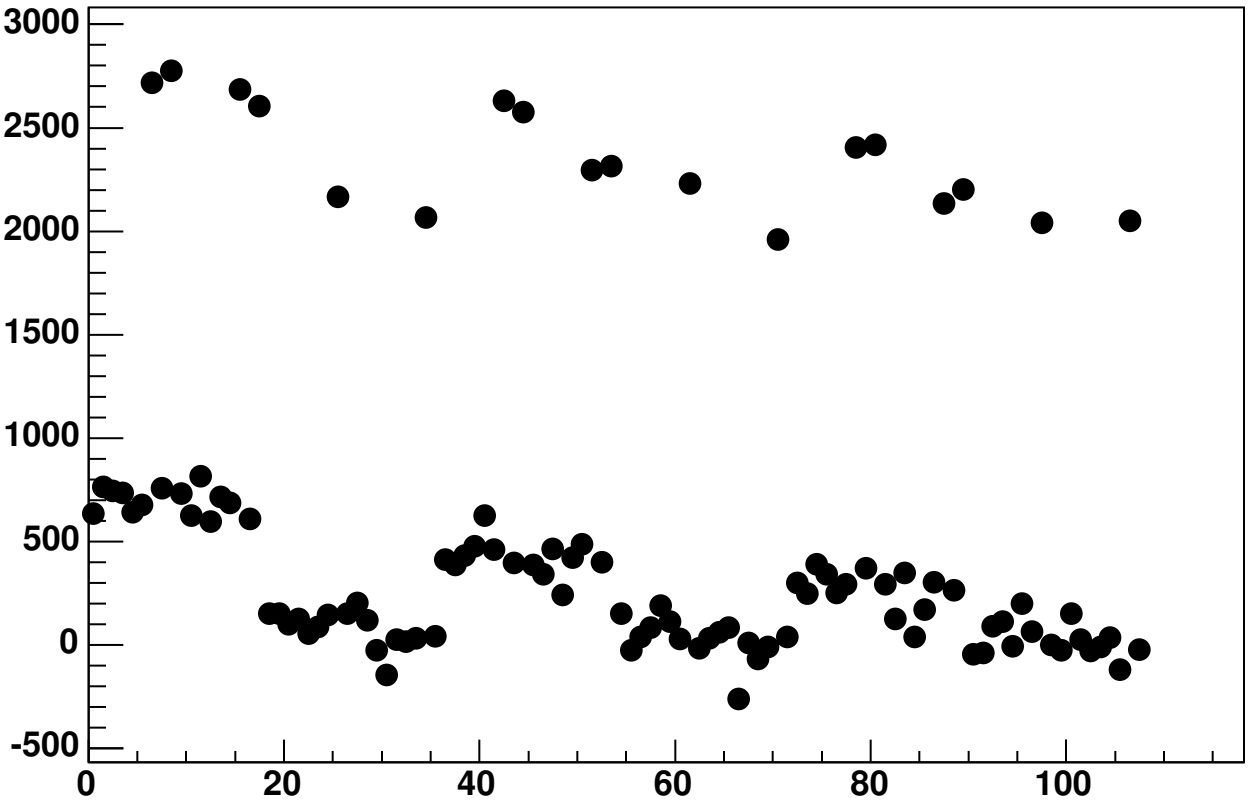
Enable 2, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



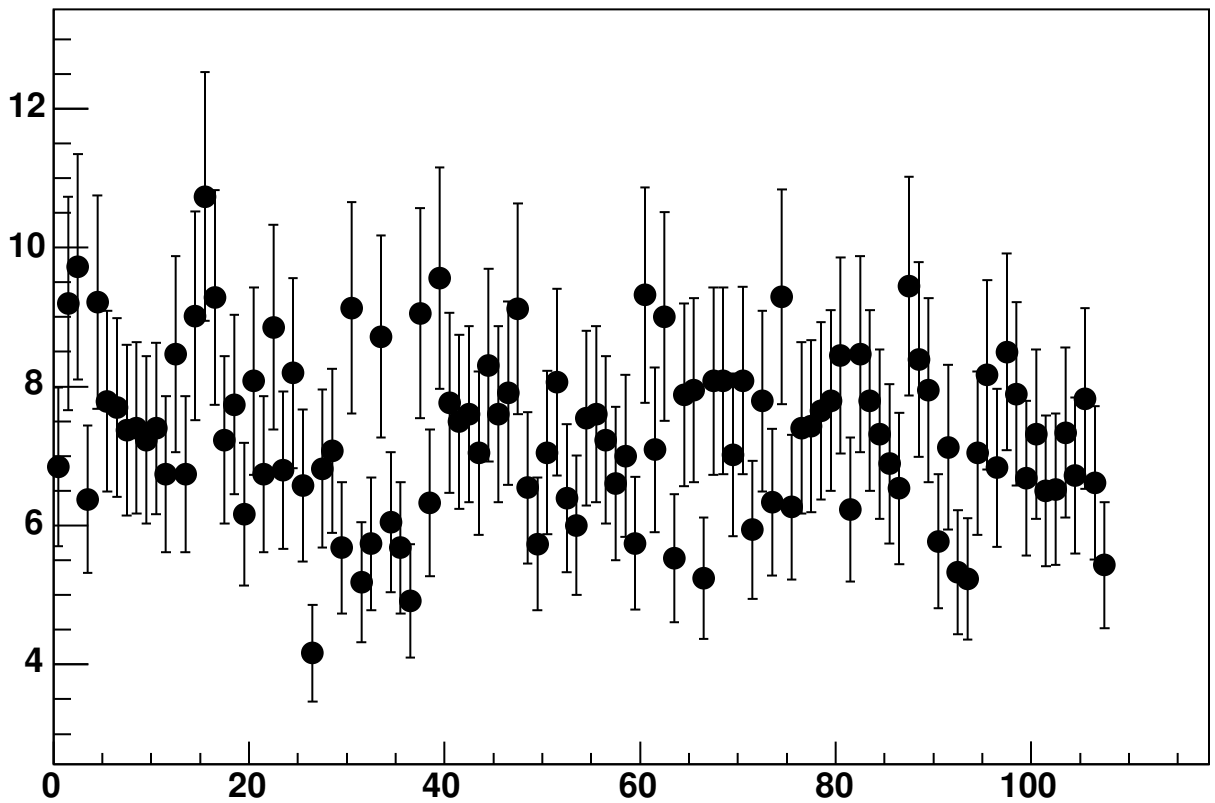
Enable 2, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



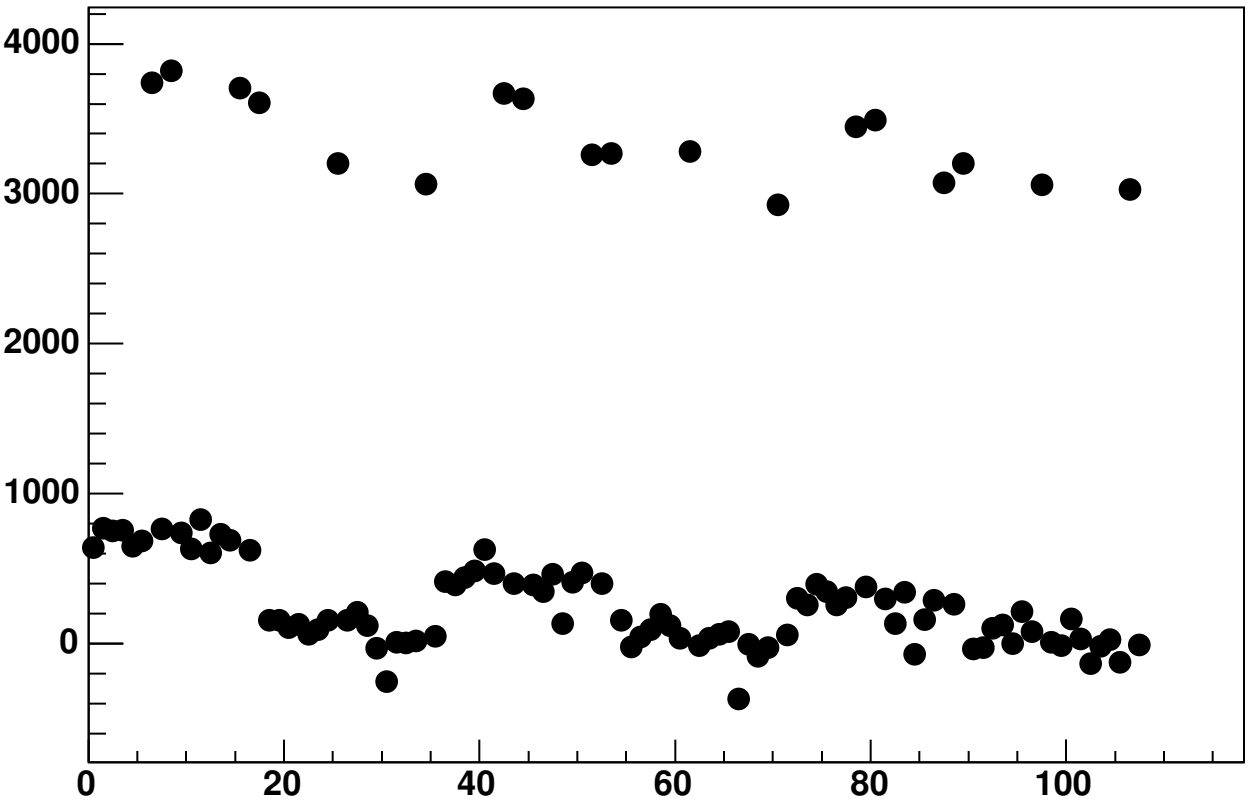
Enable 2, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



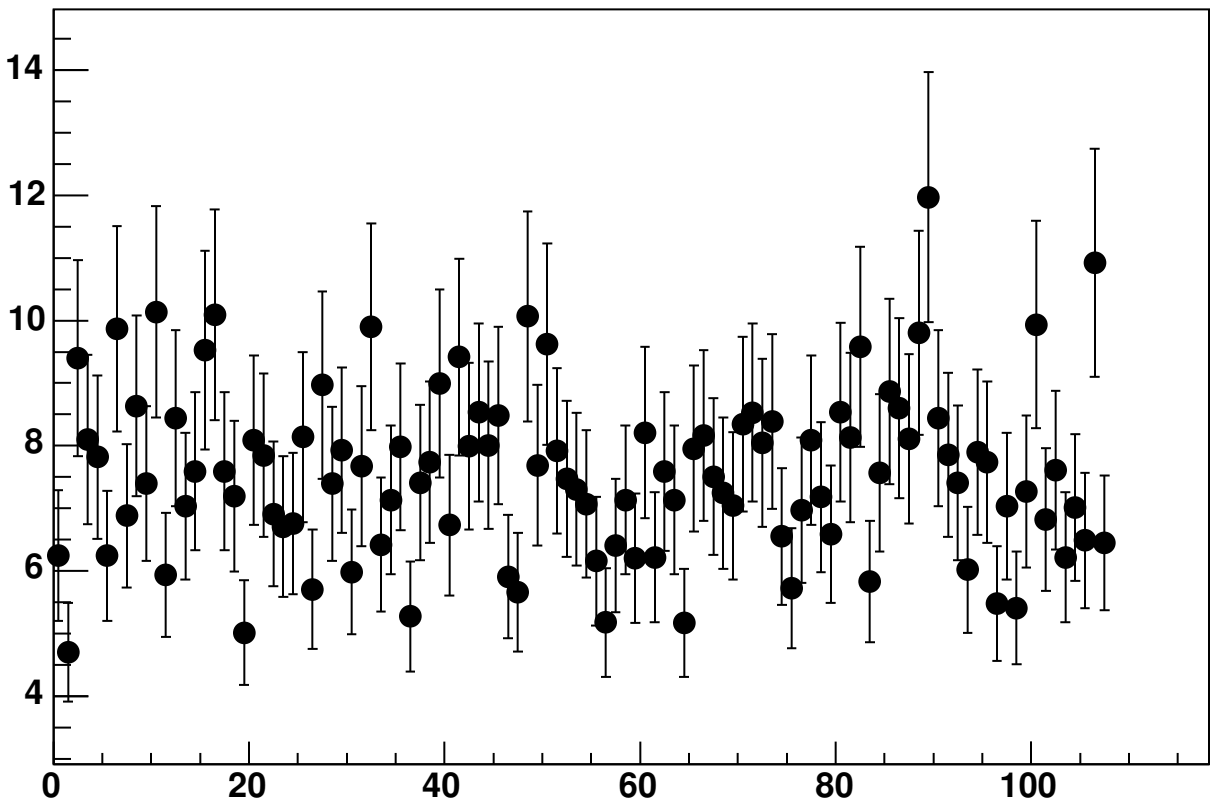
Enable 2, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



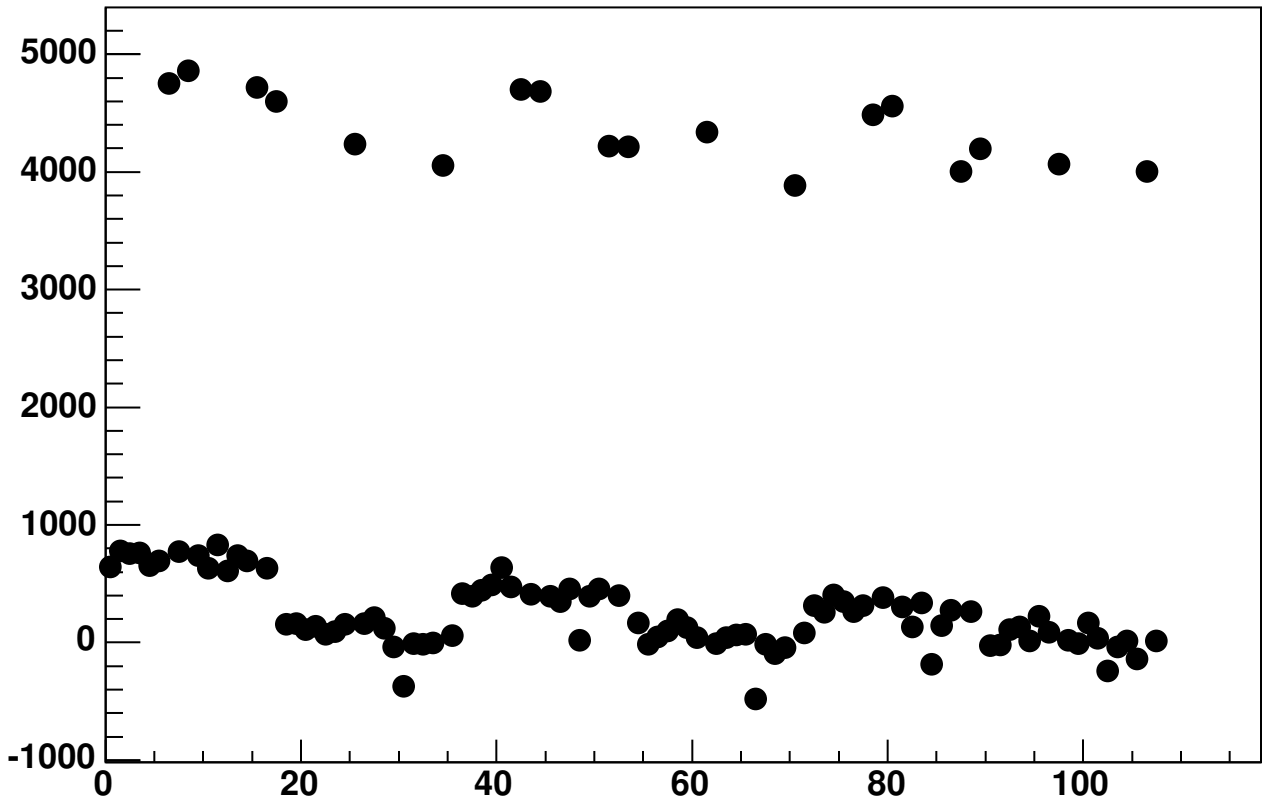
Enable 2, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



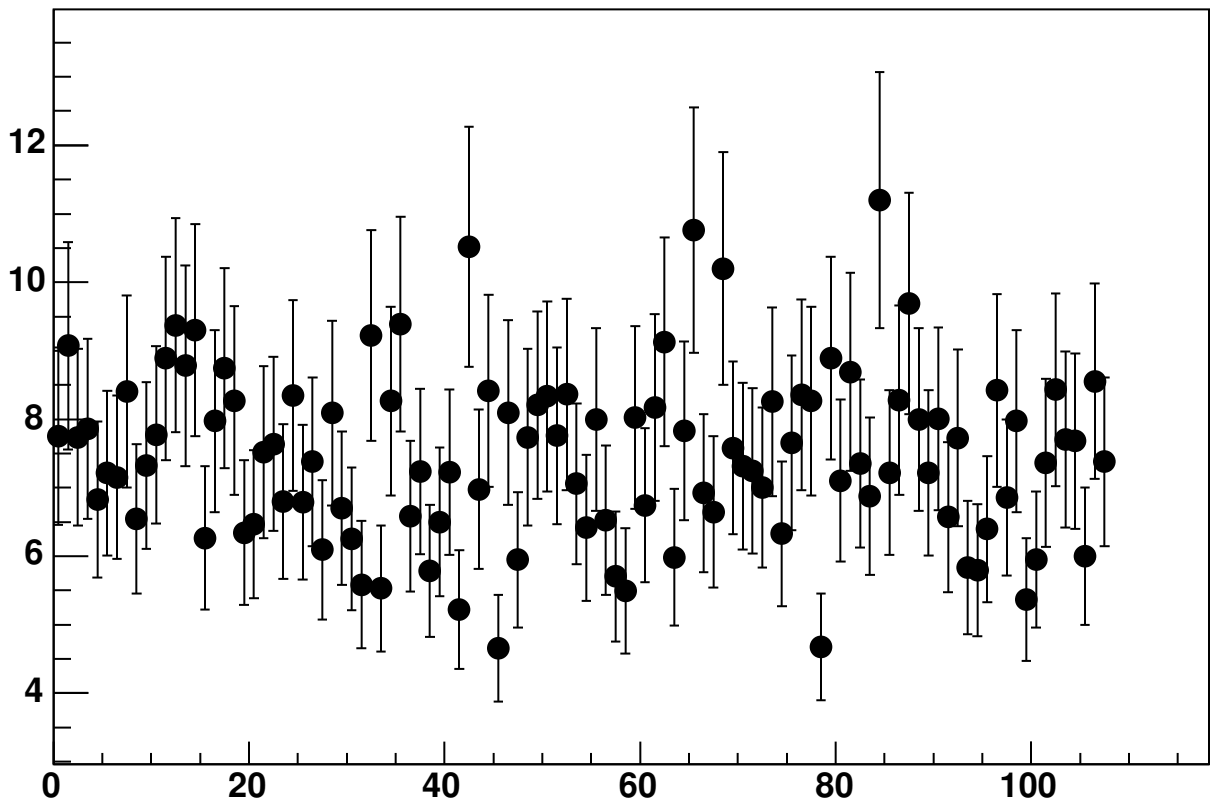
Enable 2, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



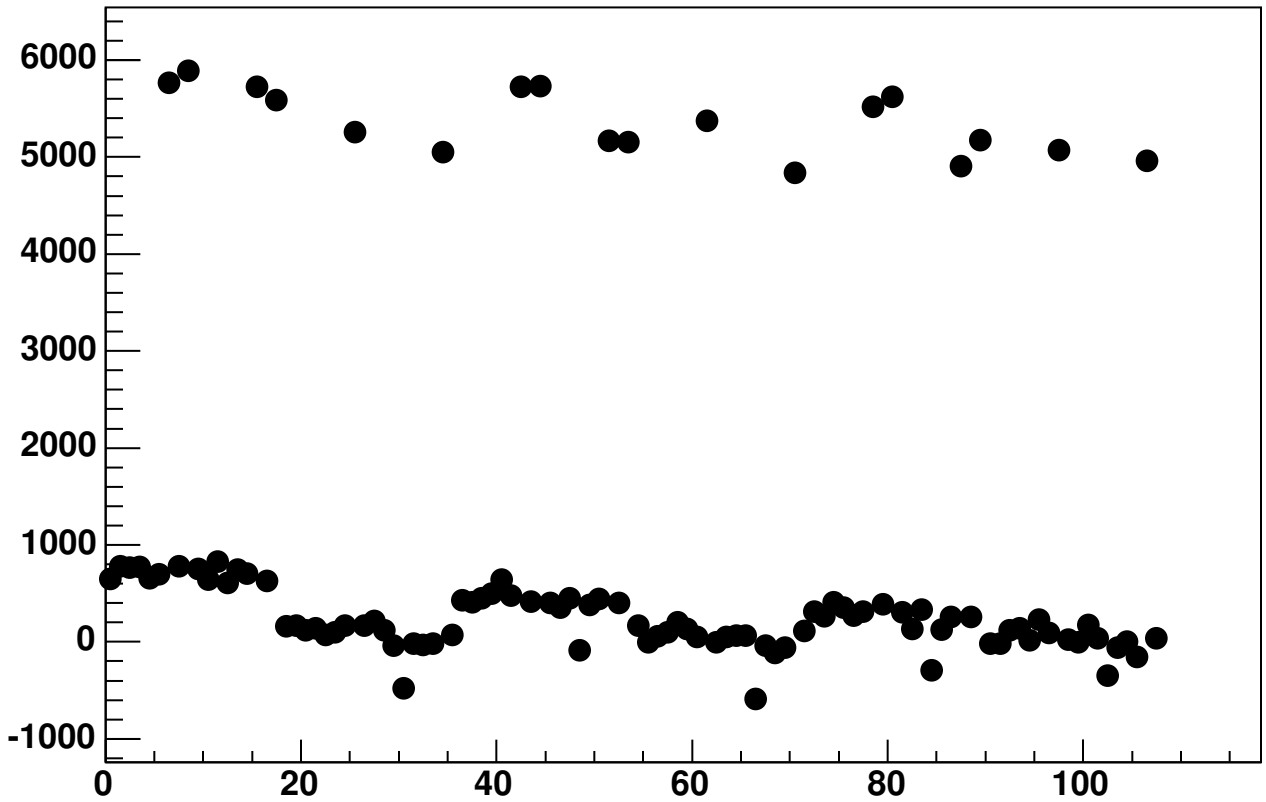
Enable 2, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



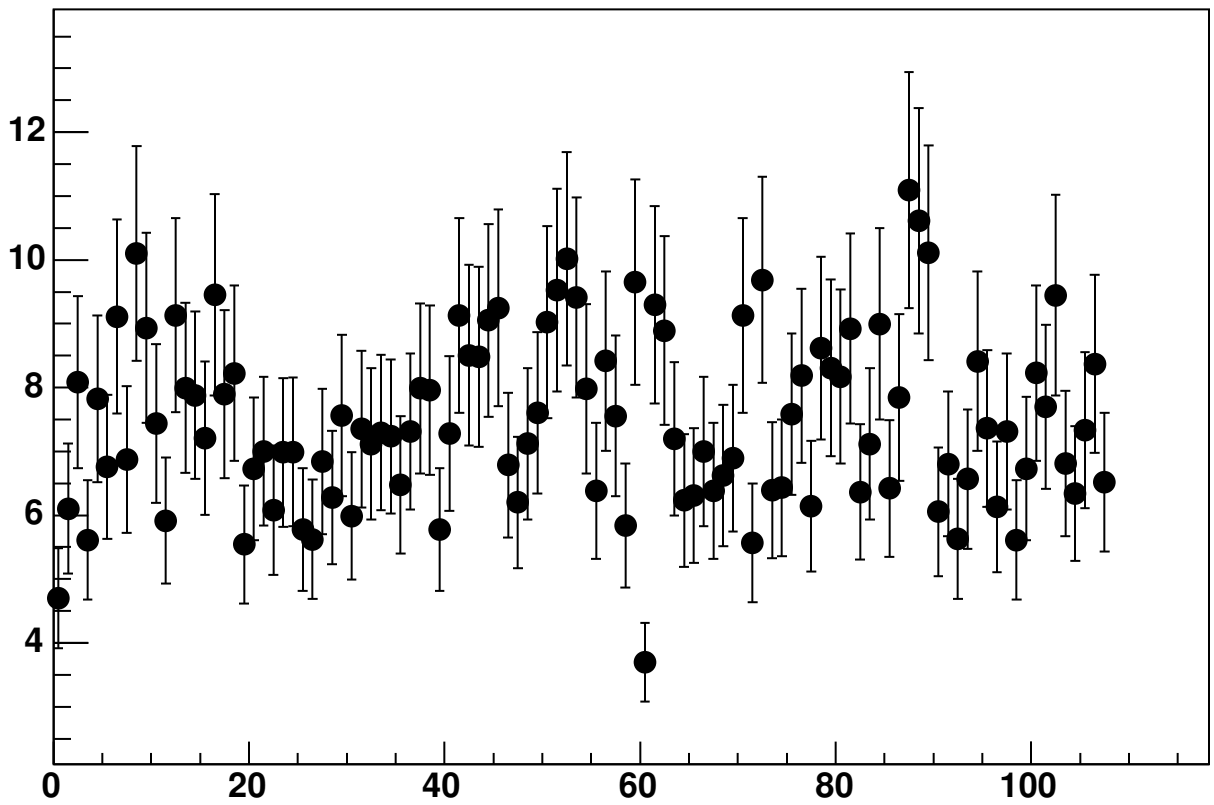
Enable 2, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



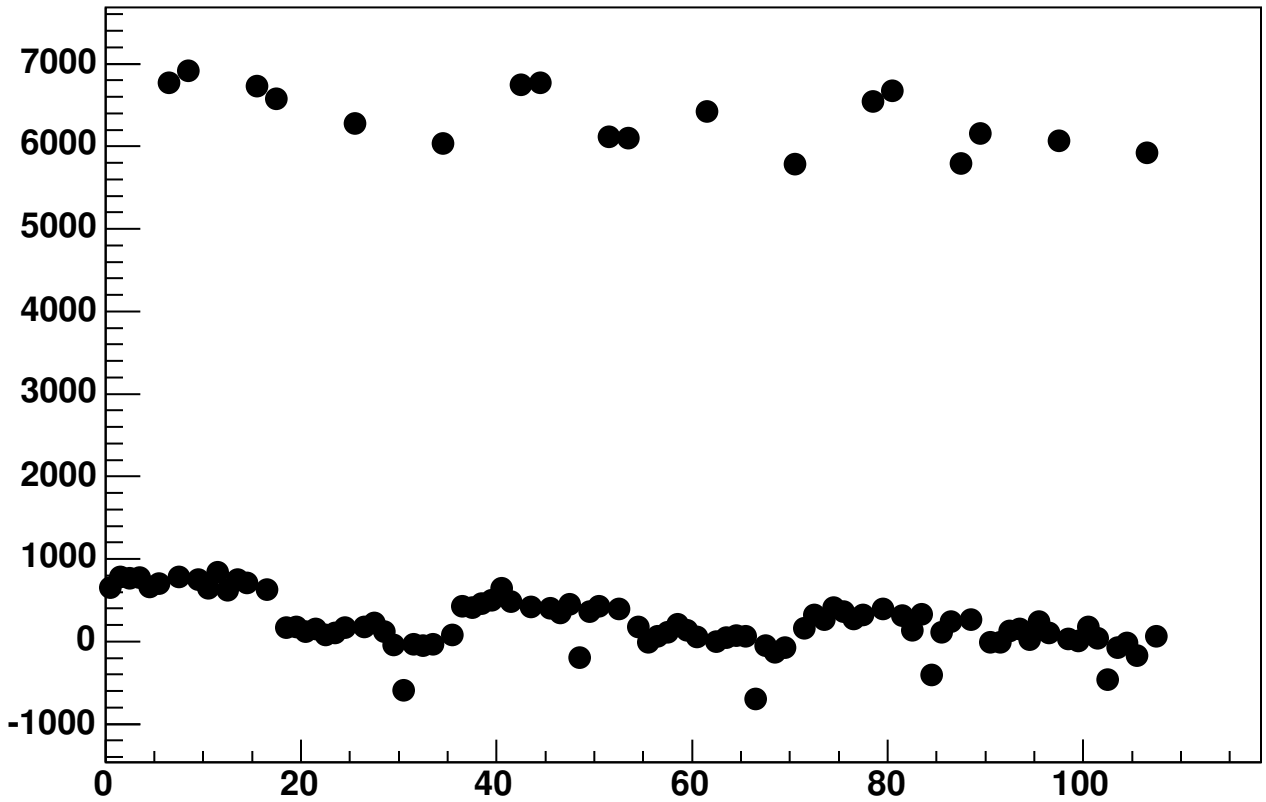
Enable 2, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



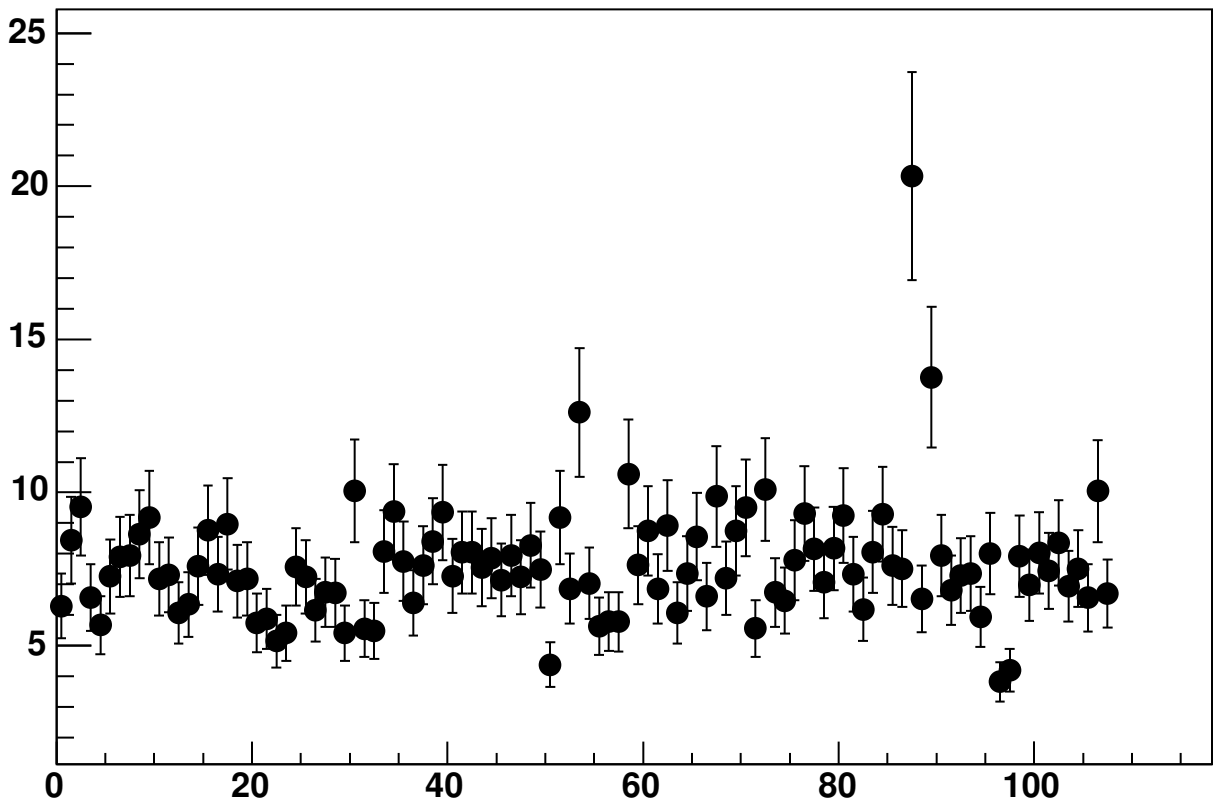
Enable 2, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



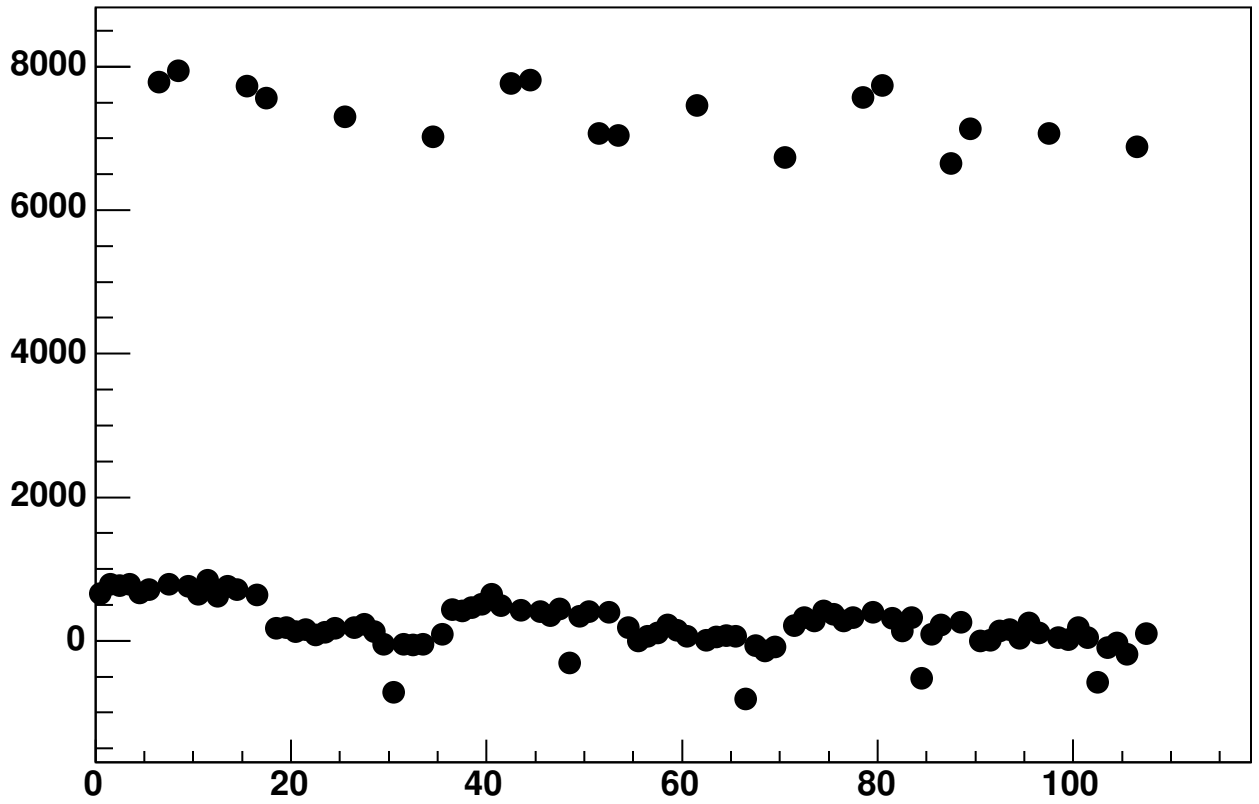
Enable 2, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



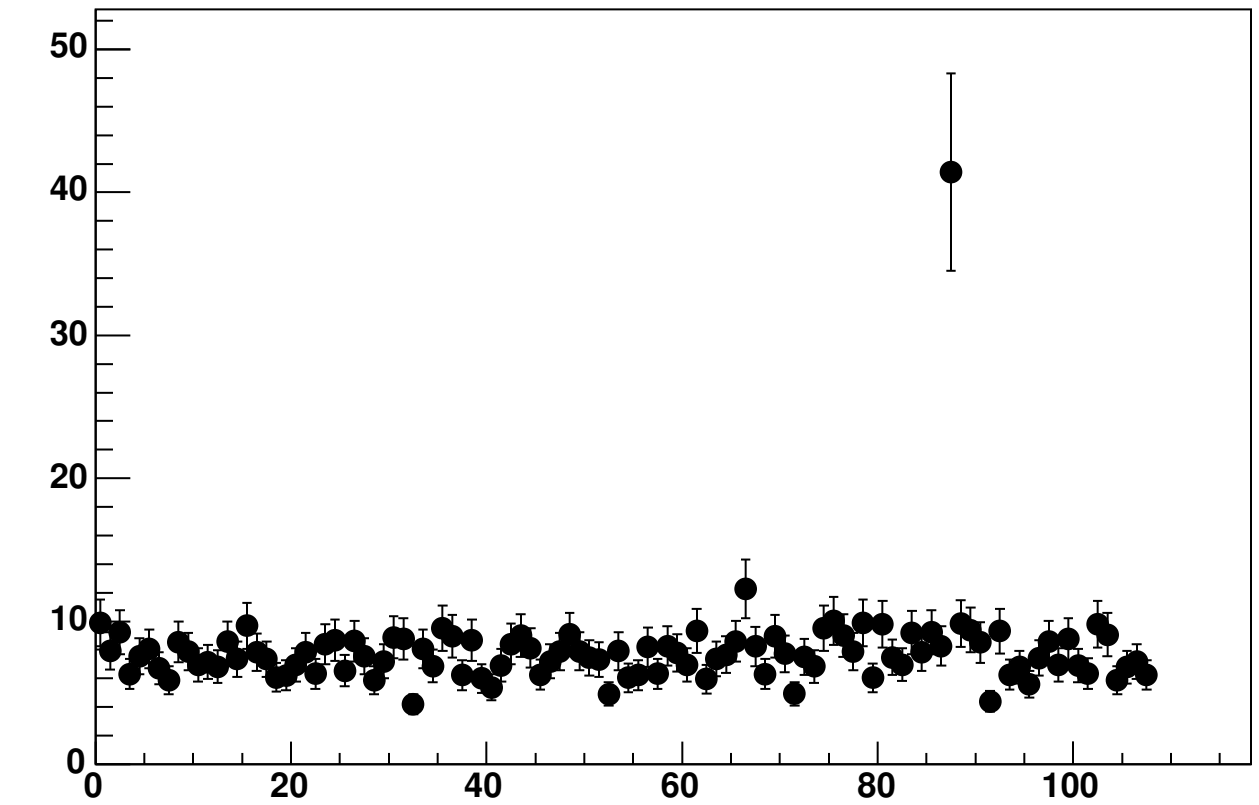
Enable 2, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



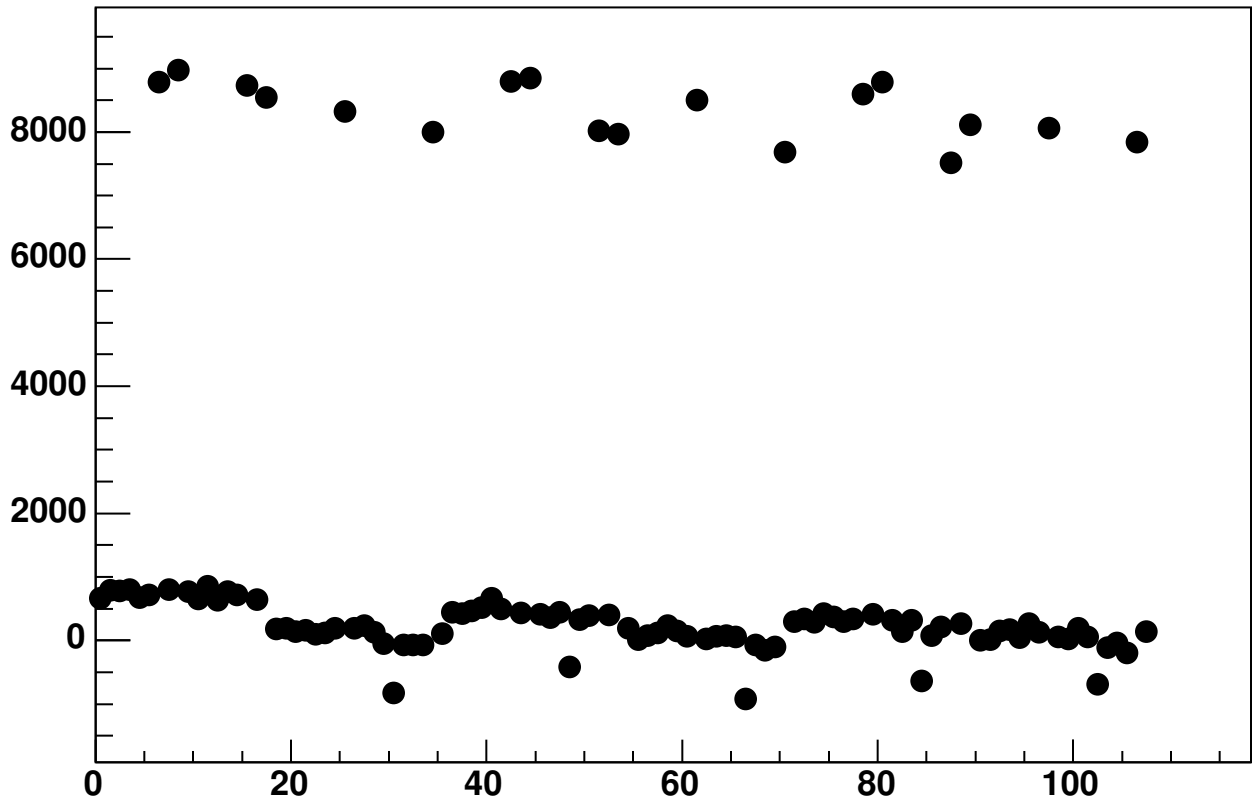
Enable 2, Hold=35, DAC=14000, ADC Mean vs 18*Chip+Chan



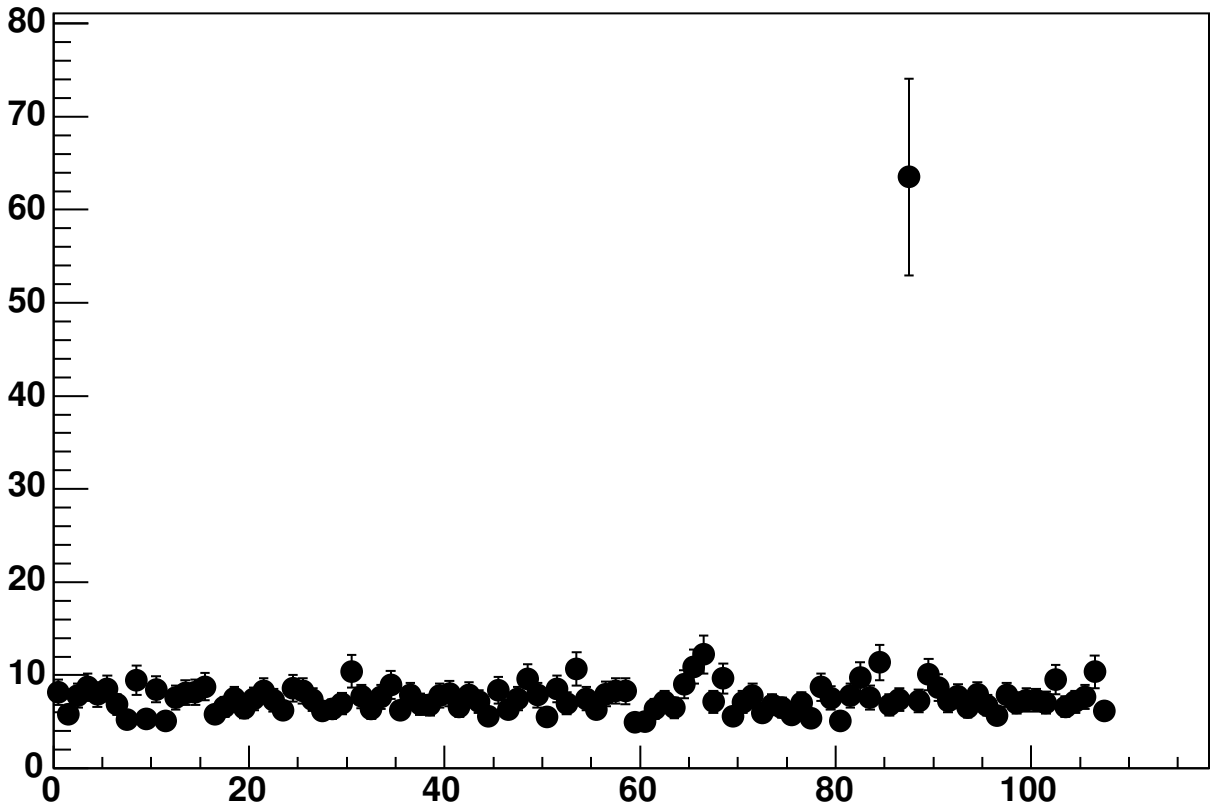
Enable 2, Hold=35, DAC=14000, ADC Noise vs 18*Chip+Chan



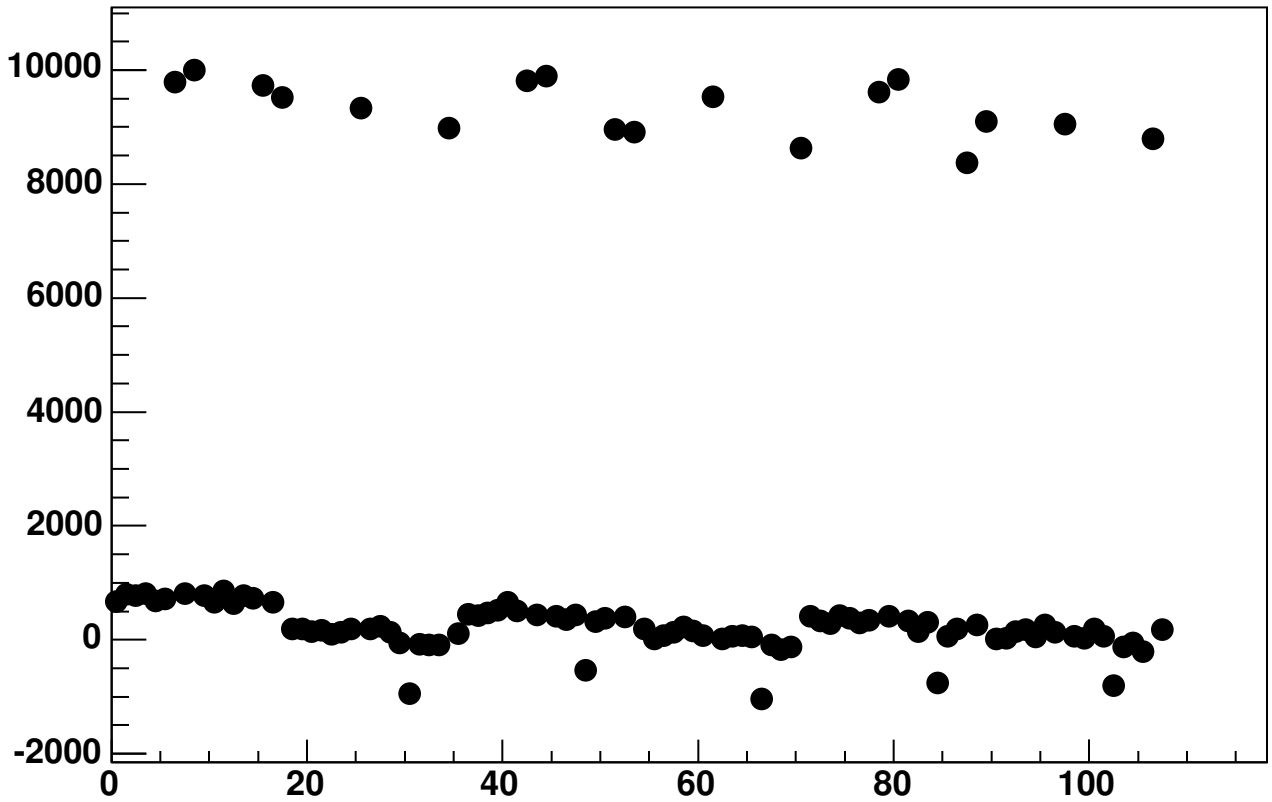
Enable 2, Hold=35, DAC=16000, ADC Mean vs 18*Chip+Chan



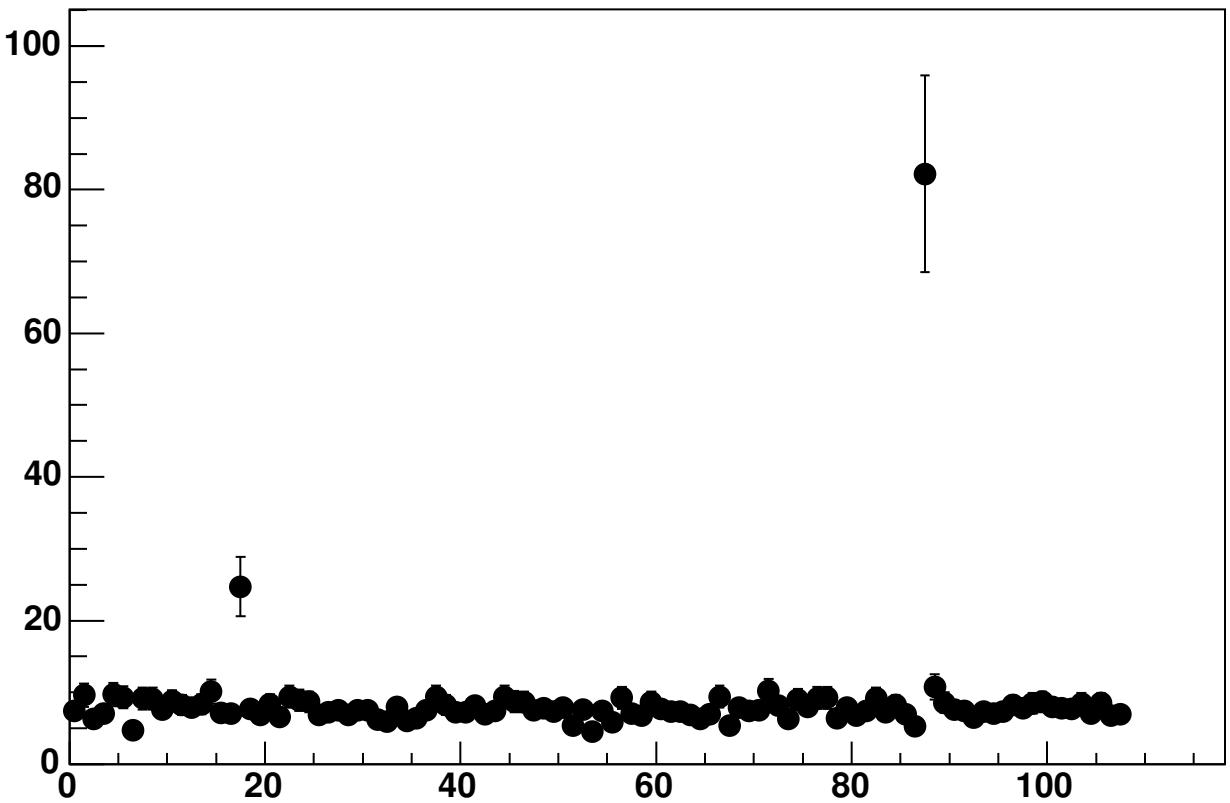
Enable 2, Hold=35, DAC=16000, ADC Noise vs 18*Chip+Chan



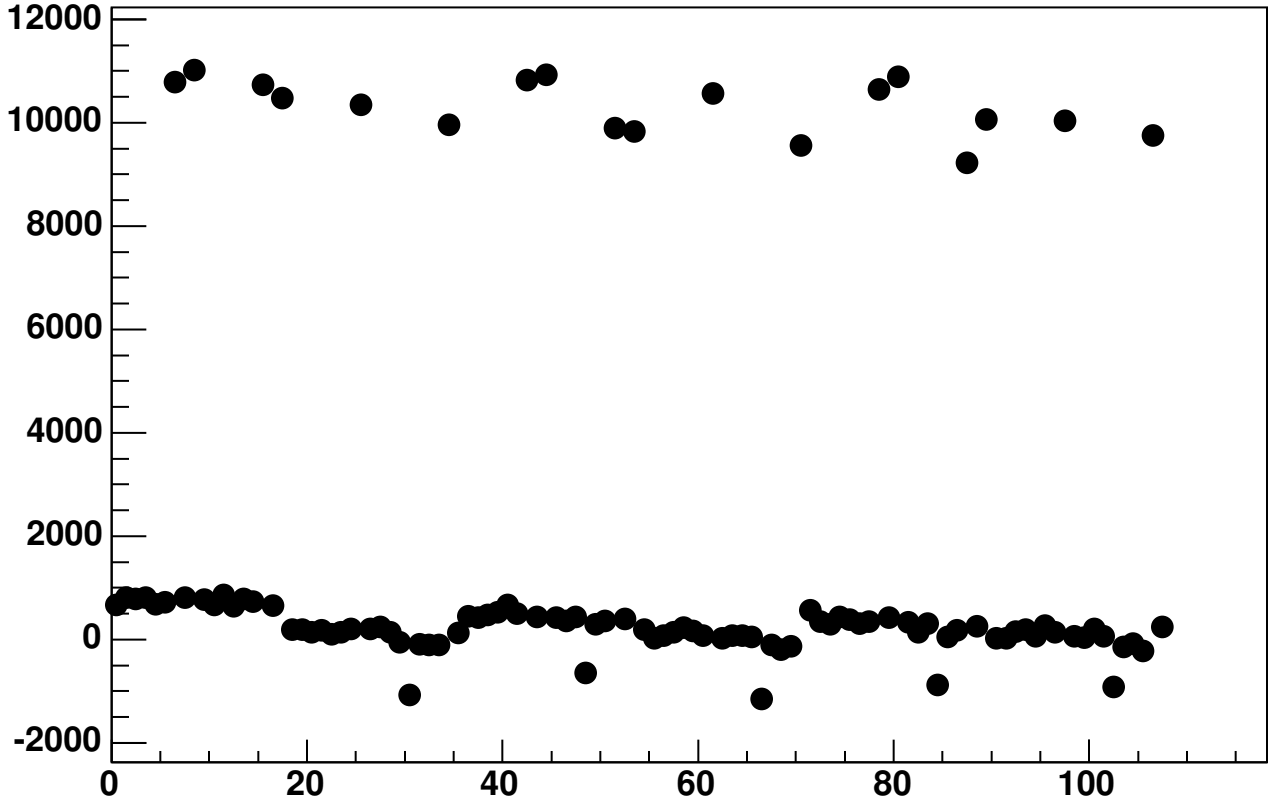
Enable 2, Hold=35, DAC=18000, ADC Mean vs 18*Chip+Chan



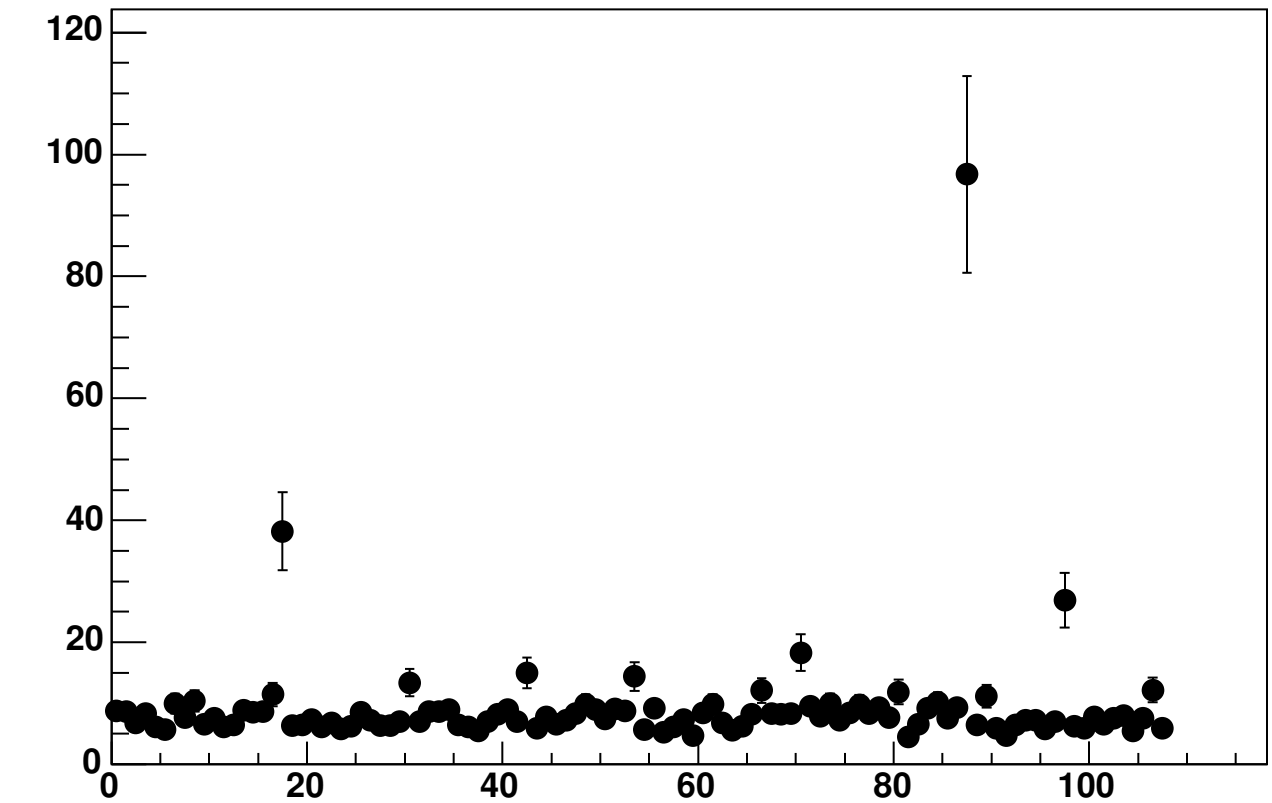
Enable 2, Hold=35, DAC=18000, ADC Noise vs 18*Chip+Chan



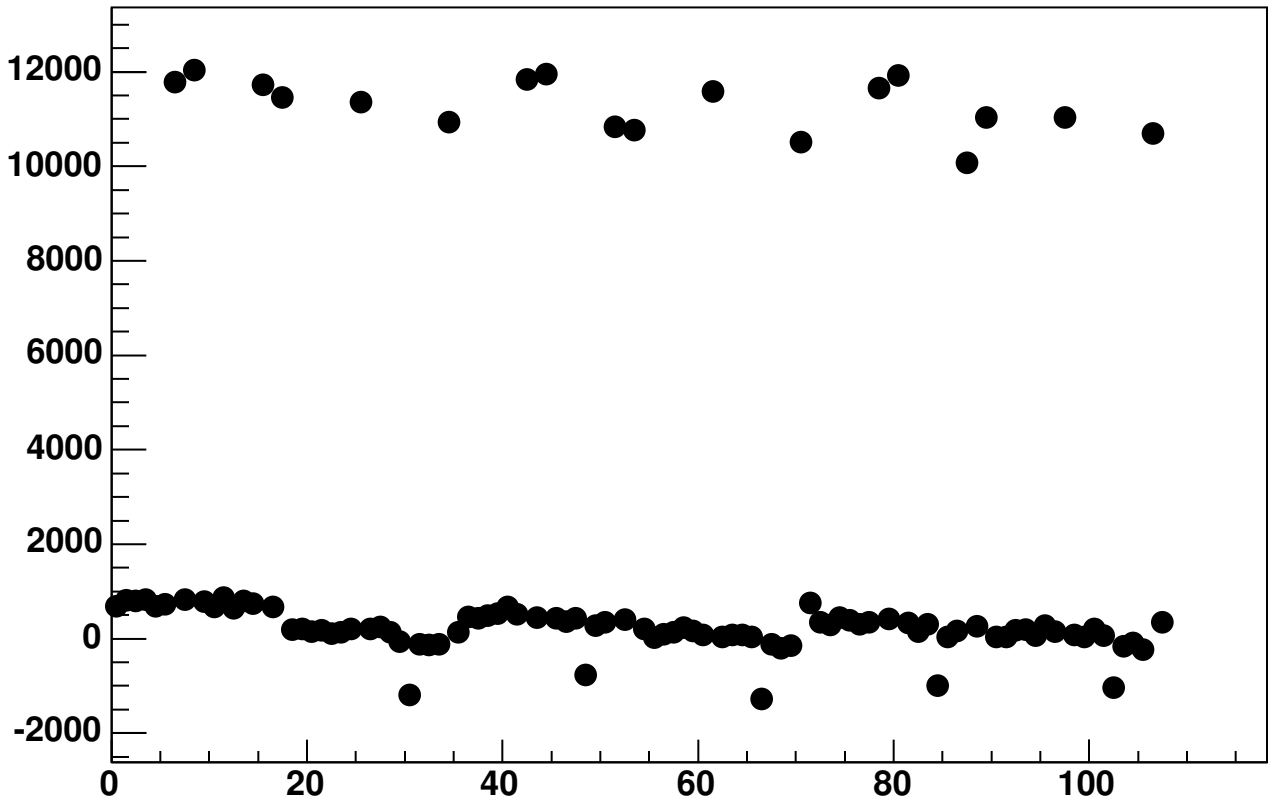
Enable 2, Hold=35, DAC=20000, ADC Mean vs 18*Chip+Chan



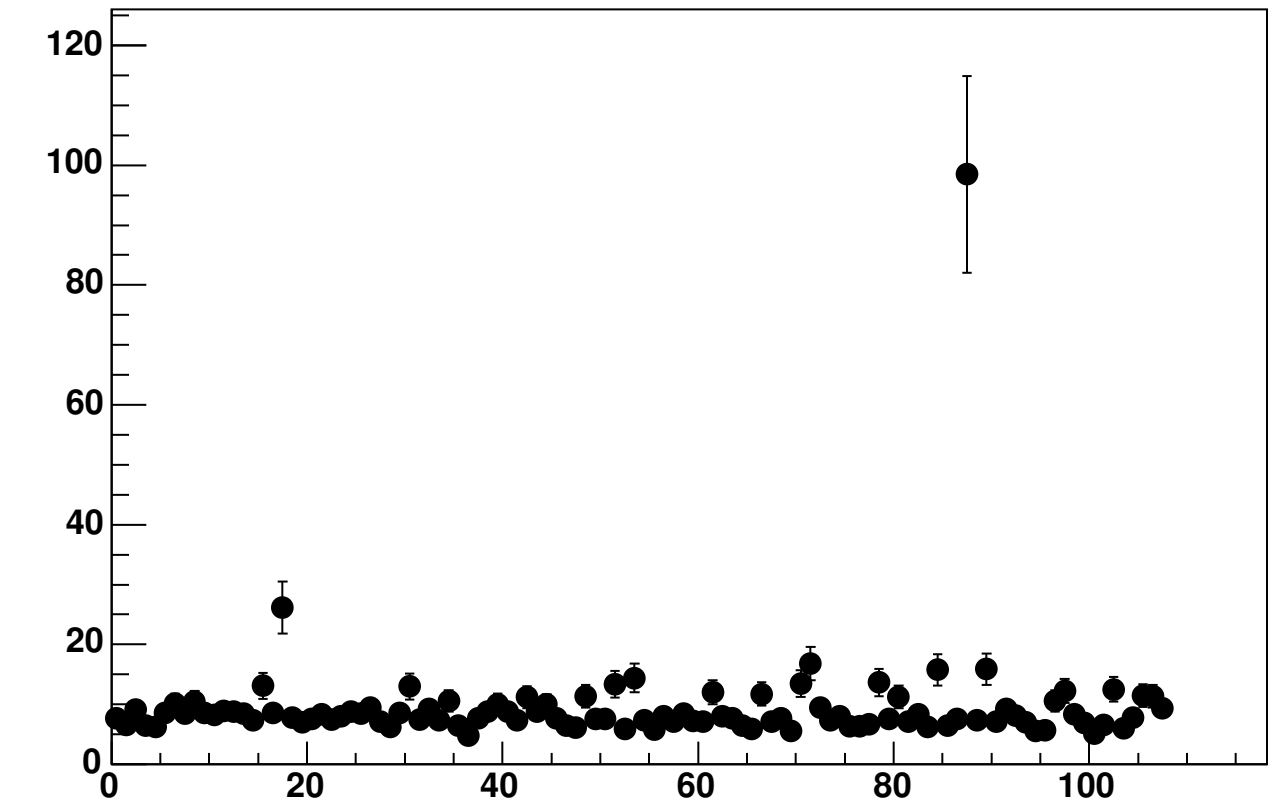
Enable 2, Hold=35, DAC=20000, ADC Noise vs 18*Chip+Chan



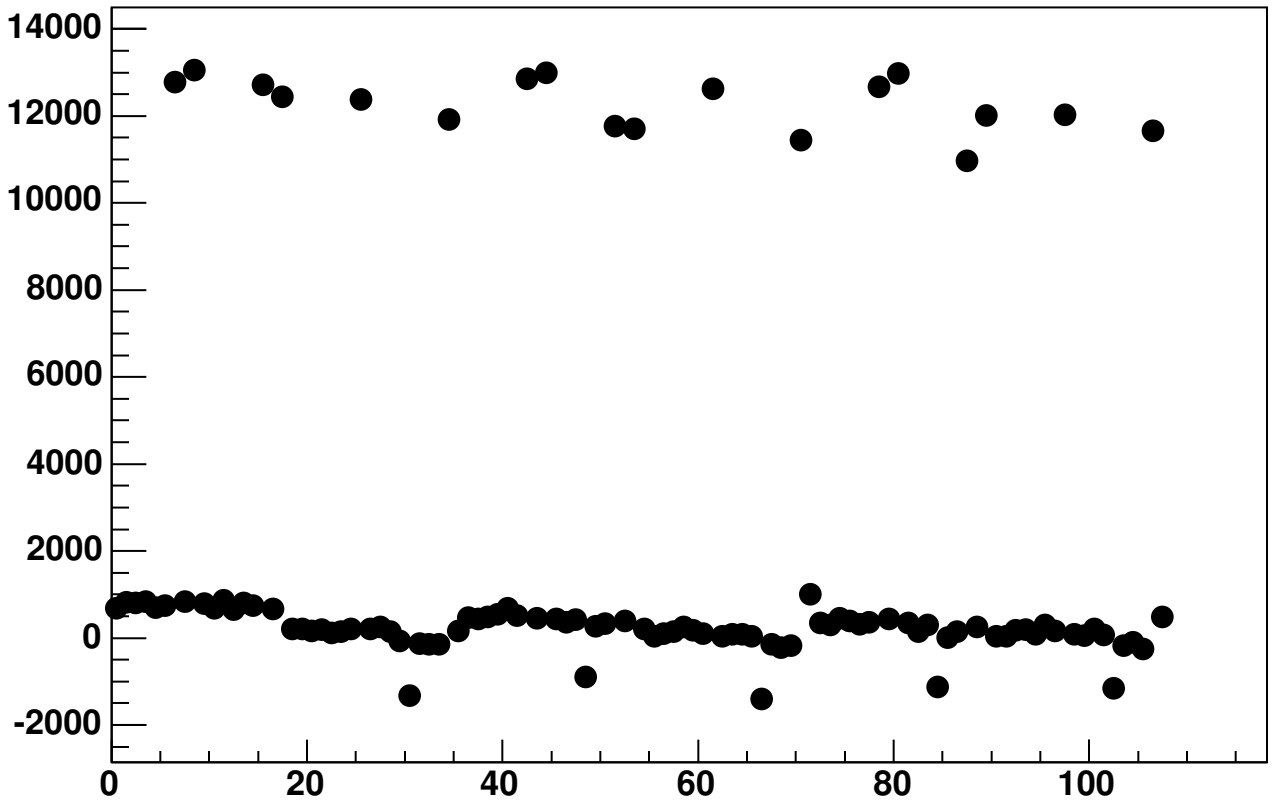
Enable 2, Hold=35, DAC=22000, ADC Mean vs 18*Chip+Chan



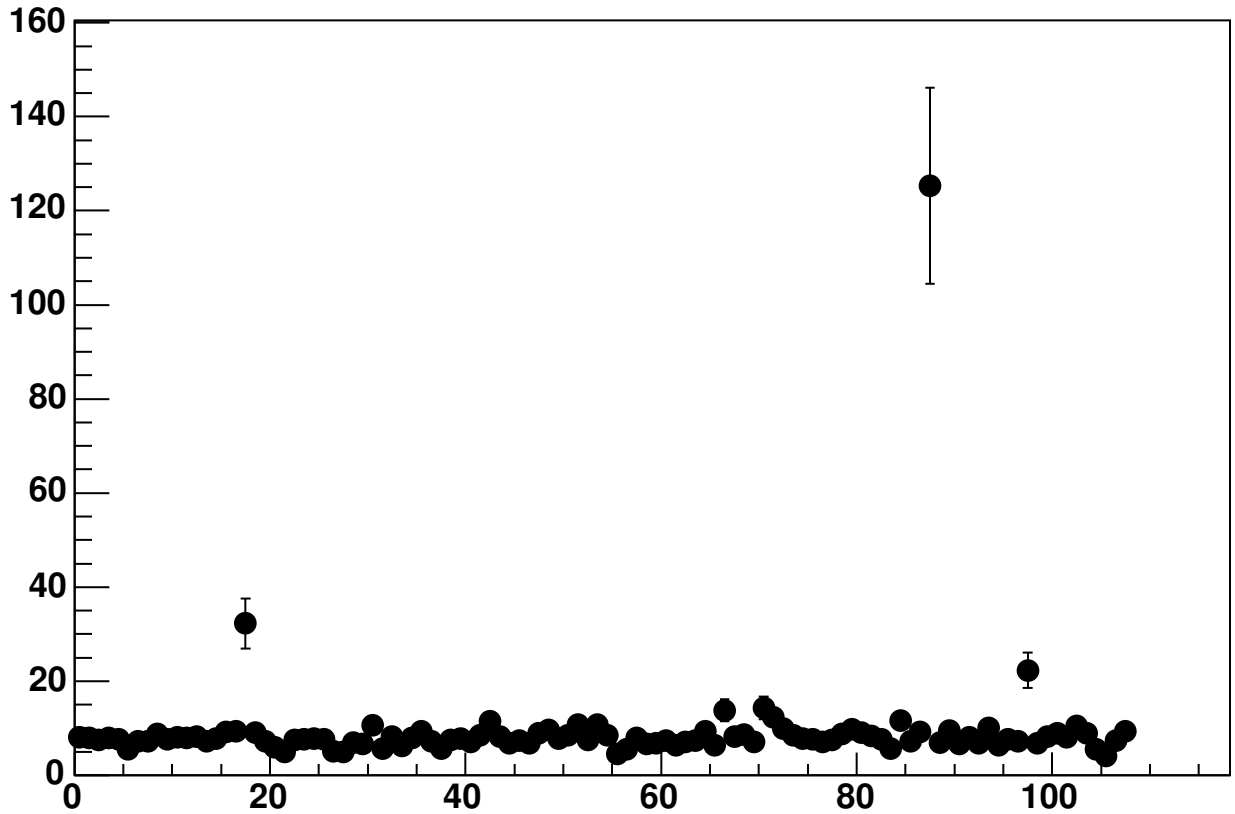
Enable 2, Hold=35, DAC=22000, ADC Noise vs 18*Chip+Chan



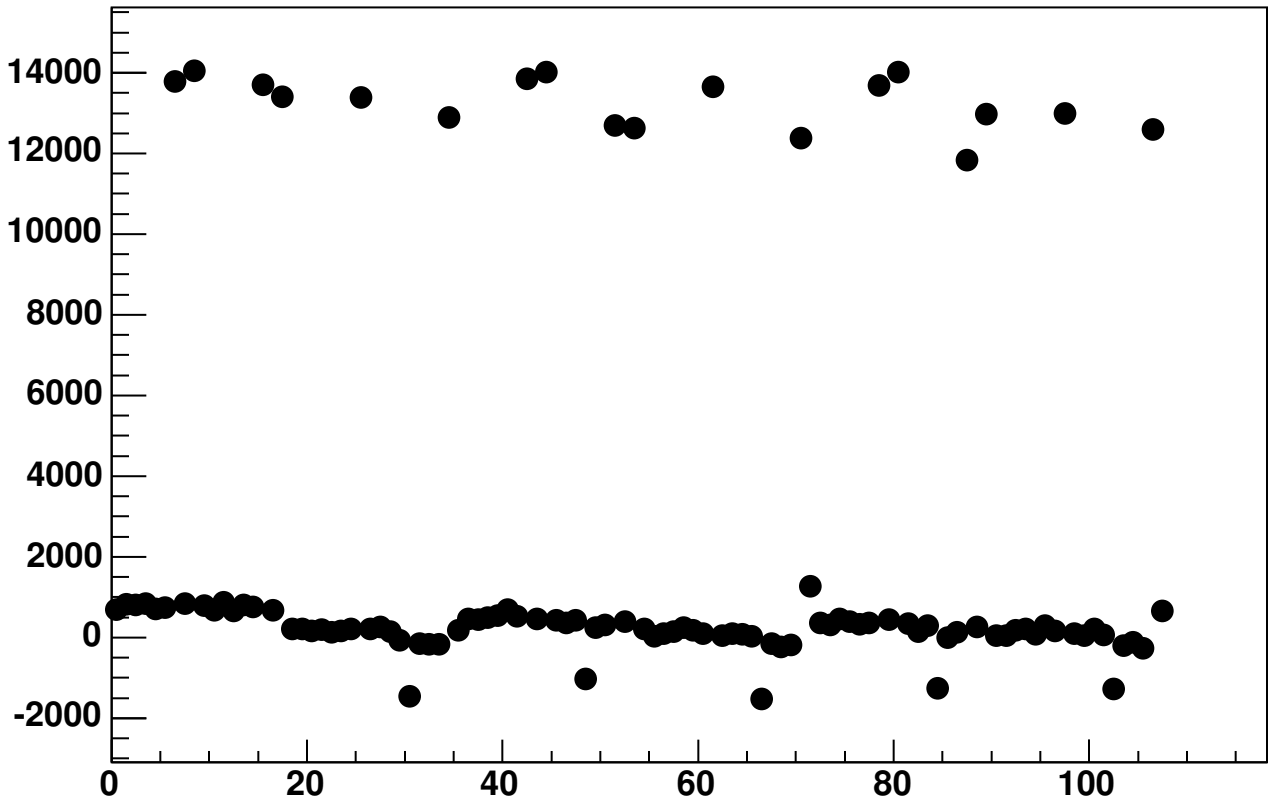
Enable 2, Hold=35, DAC=24000, ADC Mean vs 18*Chip+Chan



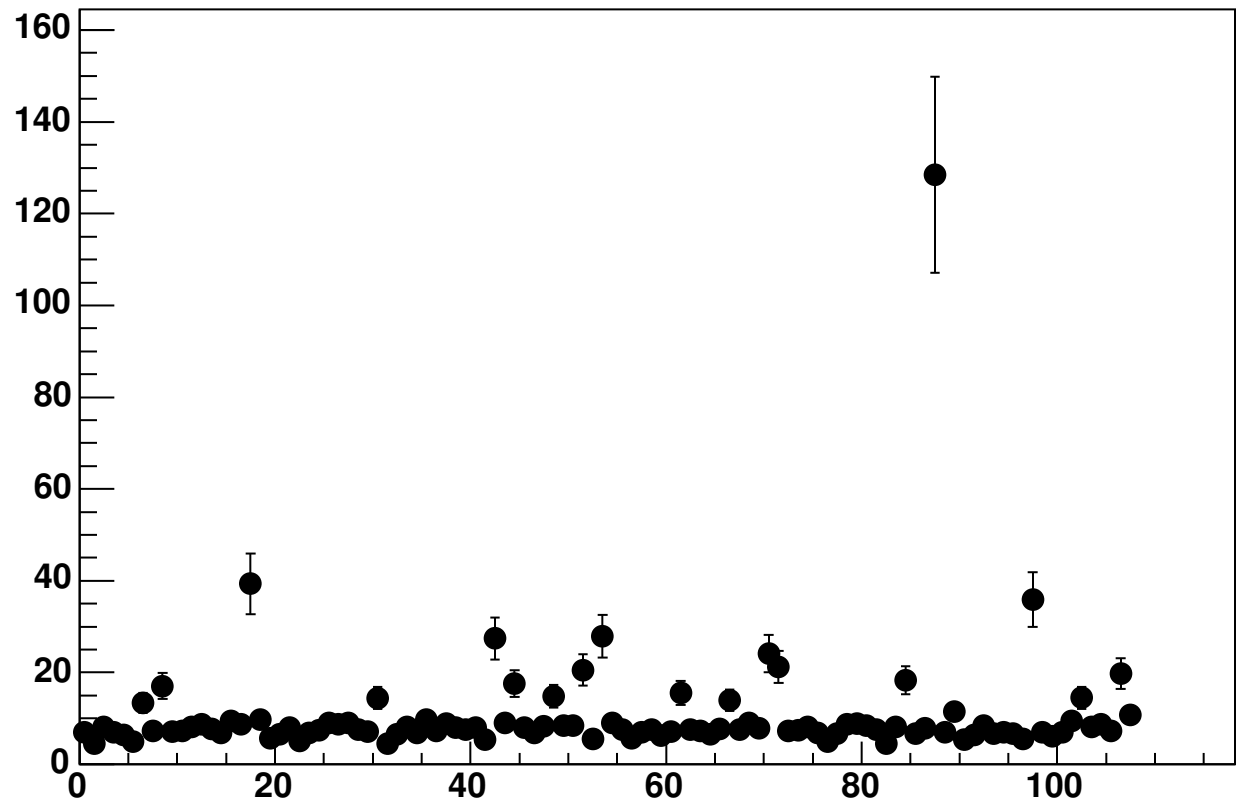
Enable 2, Hold=35, DAC=24000, ADC Noise vs 18*Chip+Chan



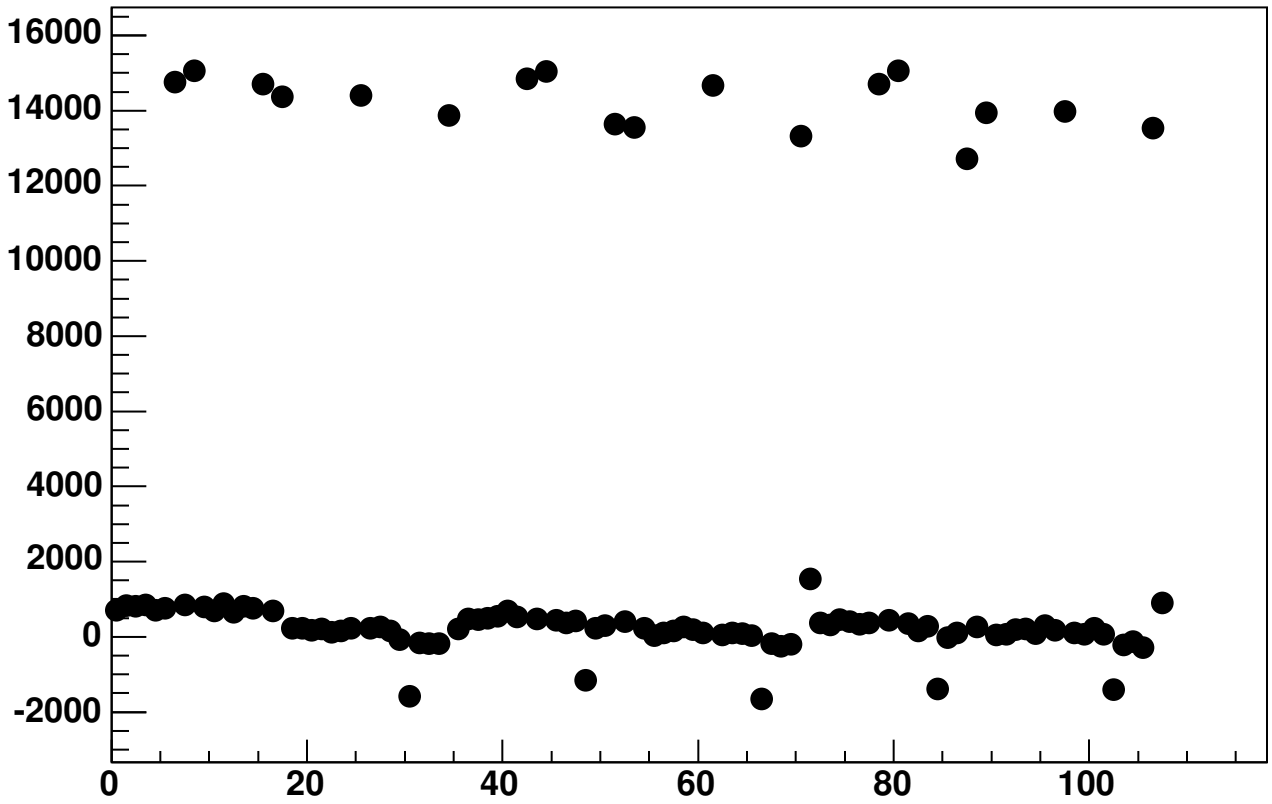
Enable 2, Hold=35, DAC=26000, ADC Mean vs 18*Chip+Chan



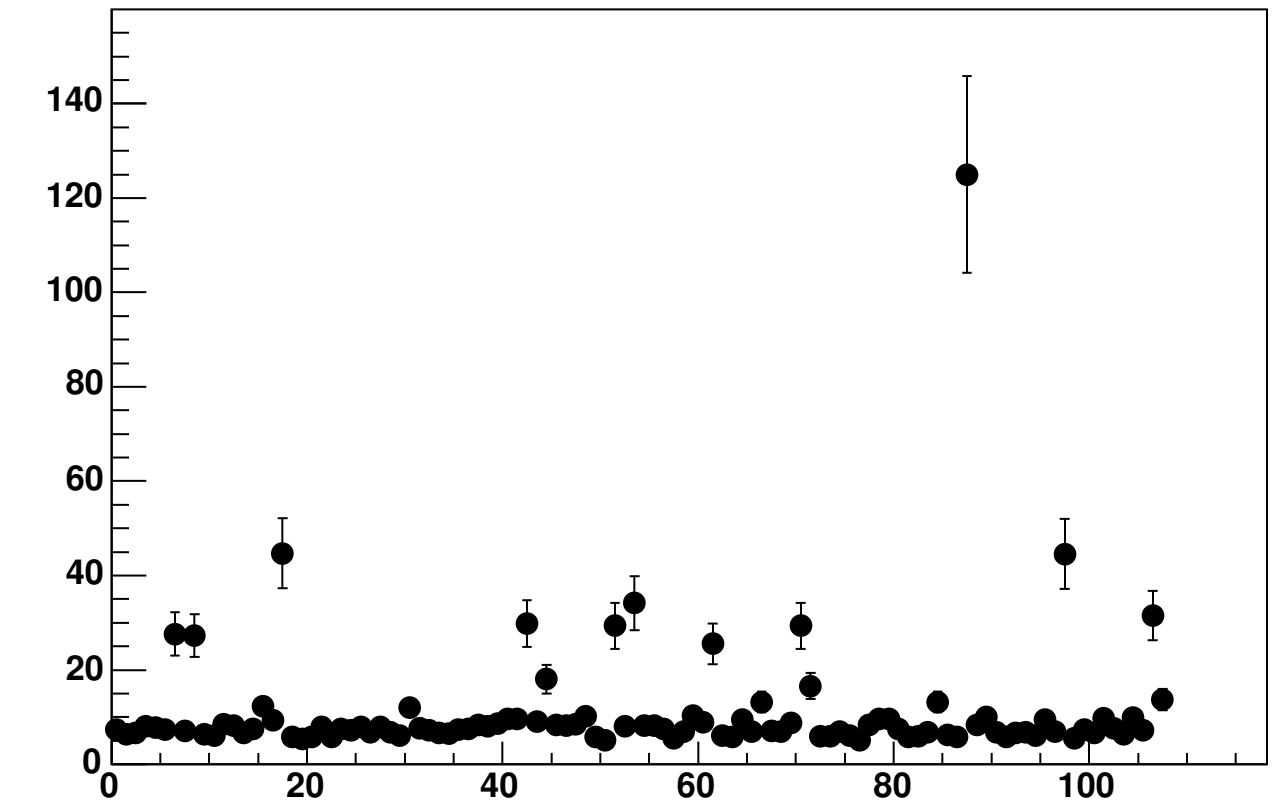
Enable 2, Hold=35, DAC=26000, ADC Noise vs 18*Chip+Chan



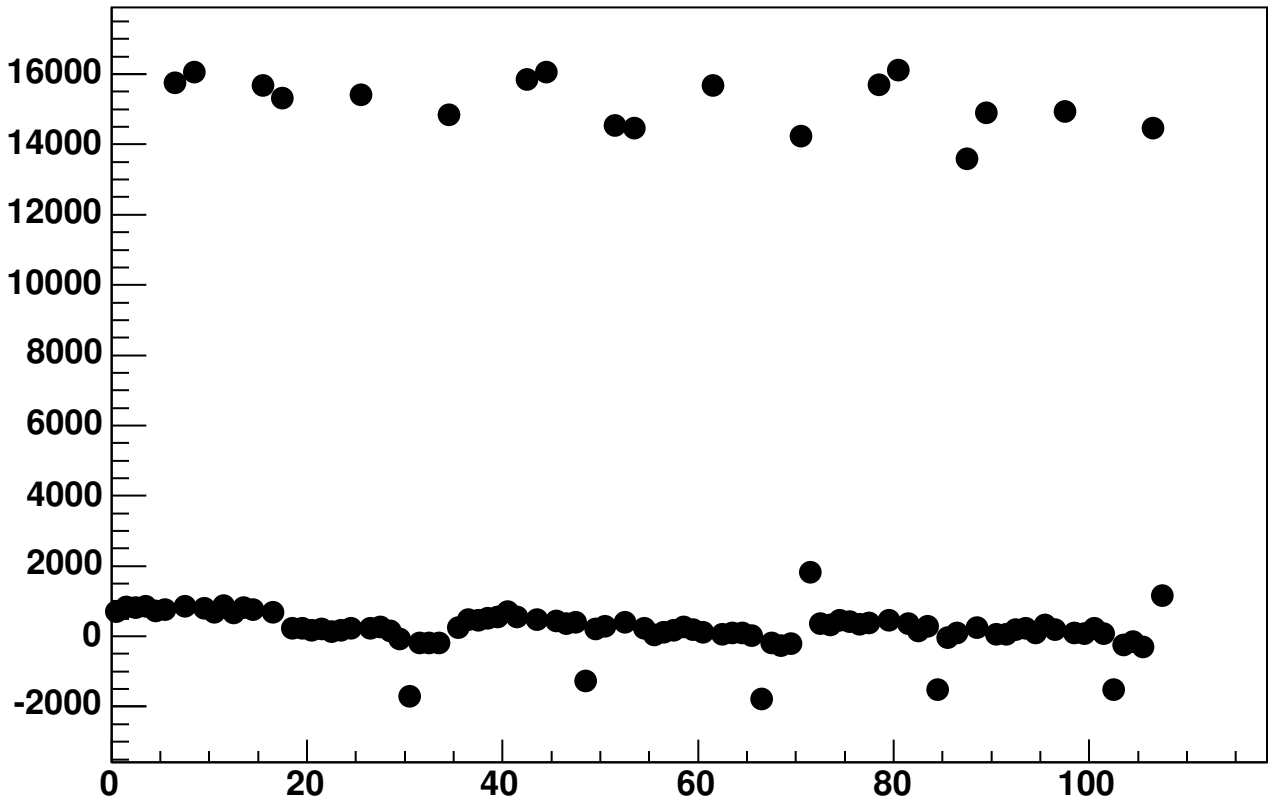
Enable 2, Hold=35, DAC=28000, ADC Mean vs 18*Chip+Chan



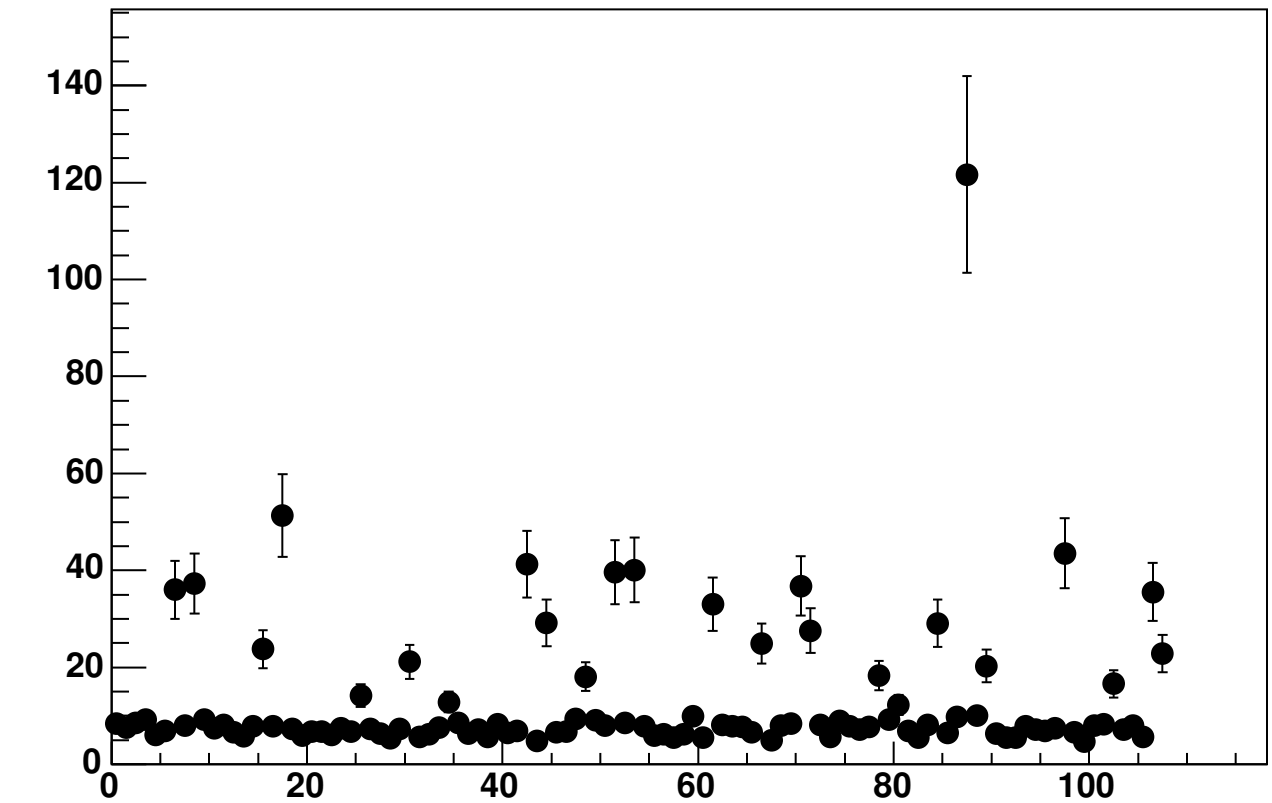
Enable 2, Hold=35, DAC=28000, ADC Noise vs 18*Chip+Chan



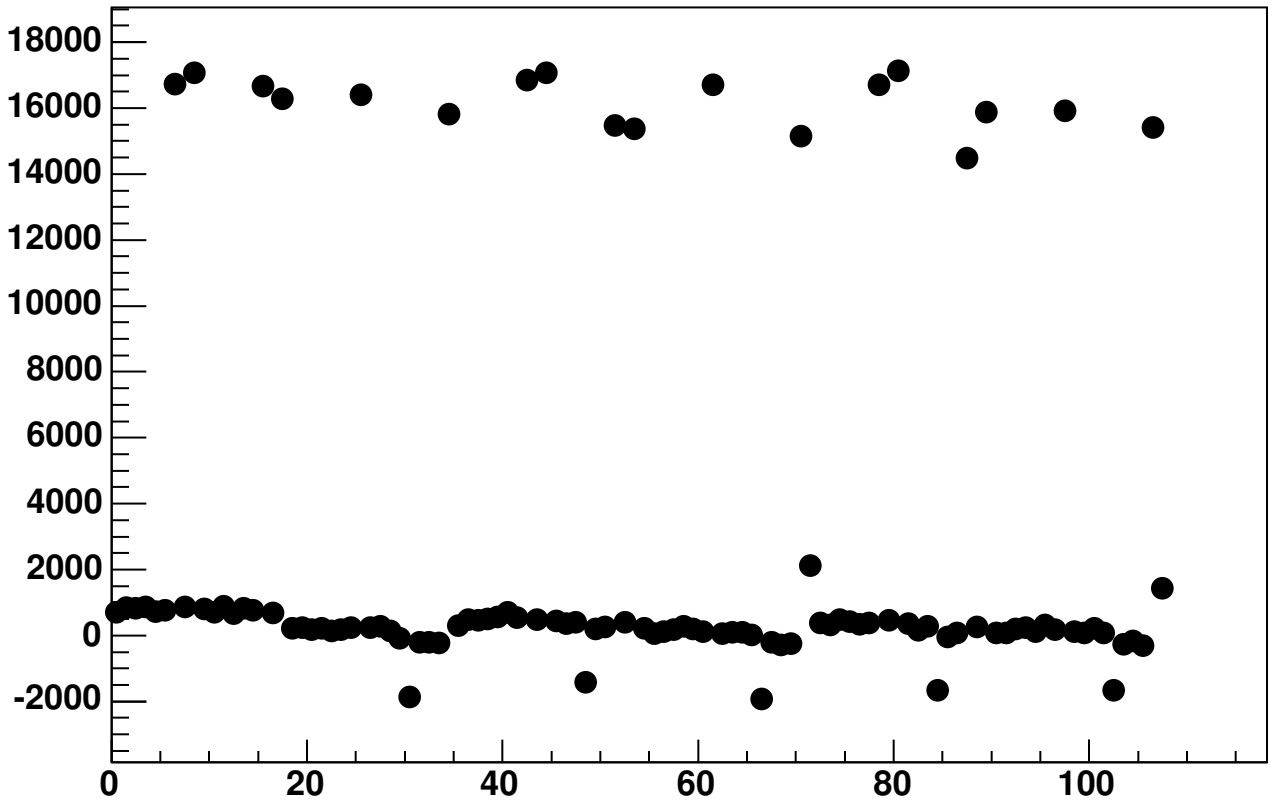
Enable 2, Hold=35, DAC=30000, ADC Mean vs 18*Chip+Chan



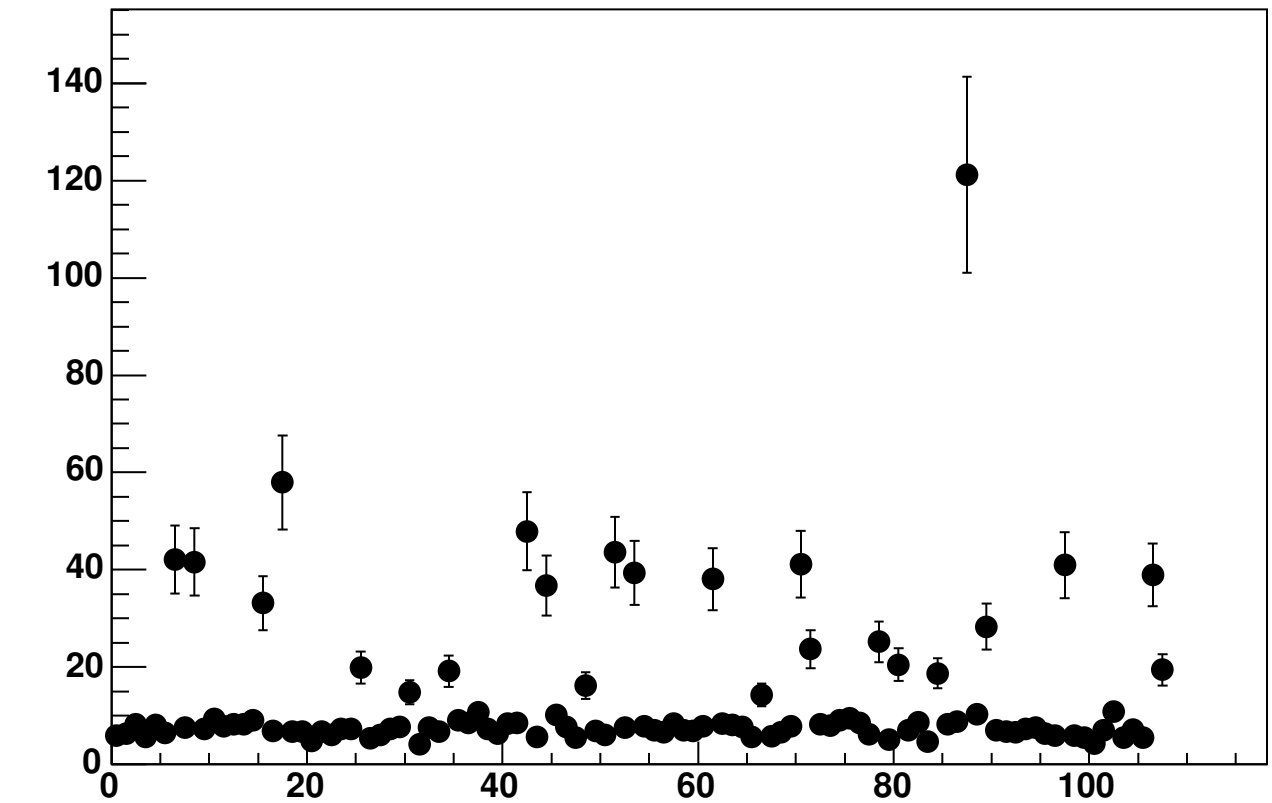
Enable 2, Hold=35, DAC=30000, ADC Noise vs 18*Chip+Chan



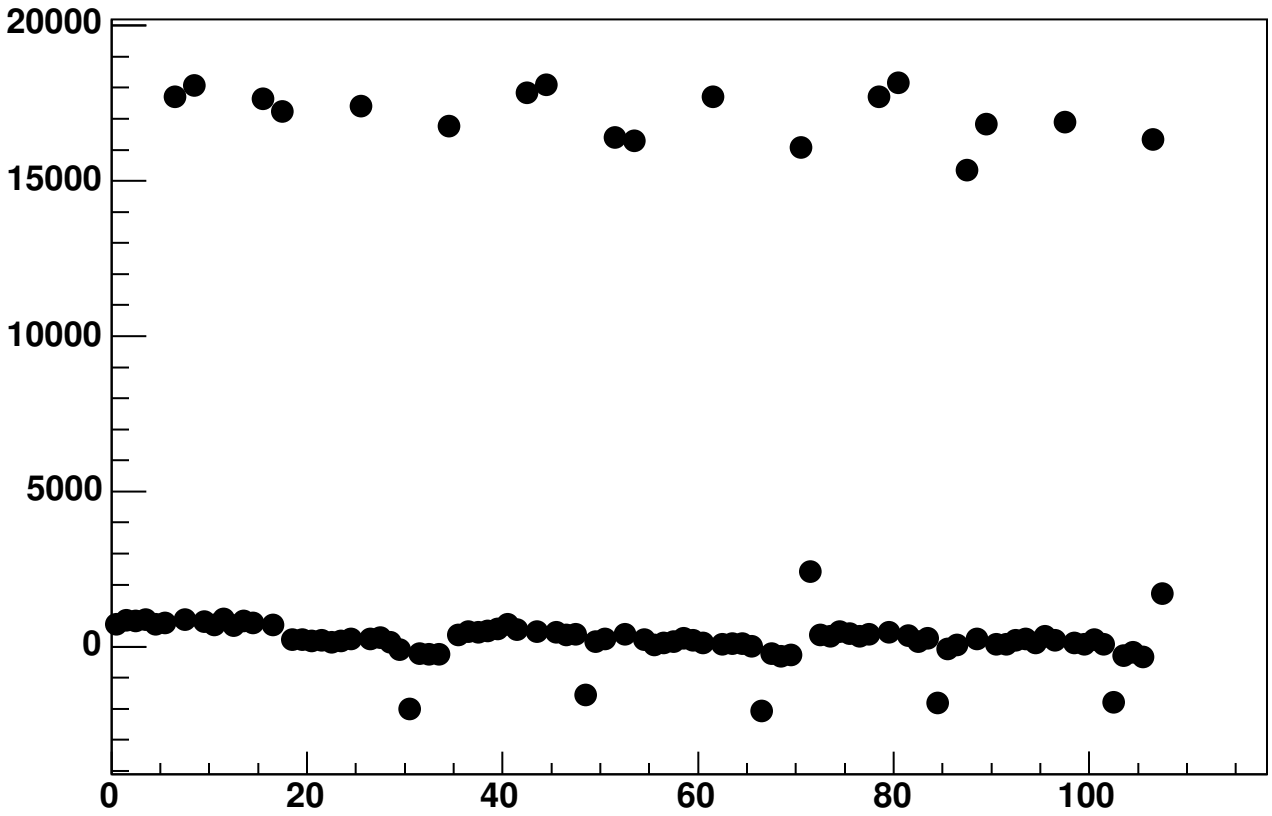
Enable 2, Hold=35, DAC=32000, ADC Mean vs 18*Chip+Chan



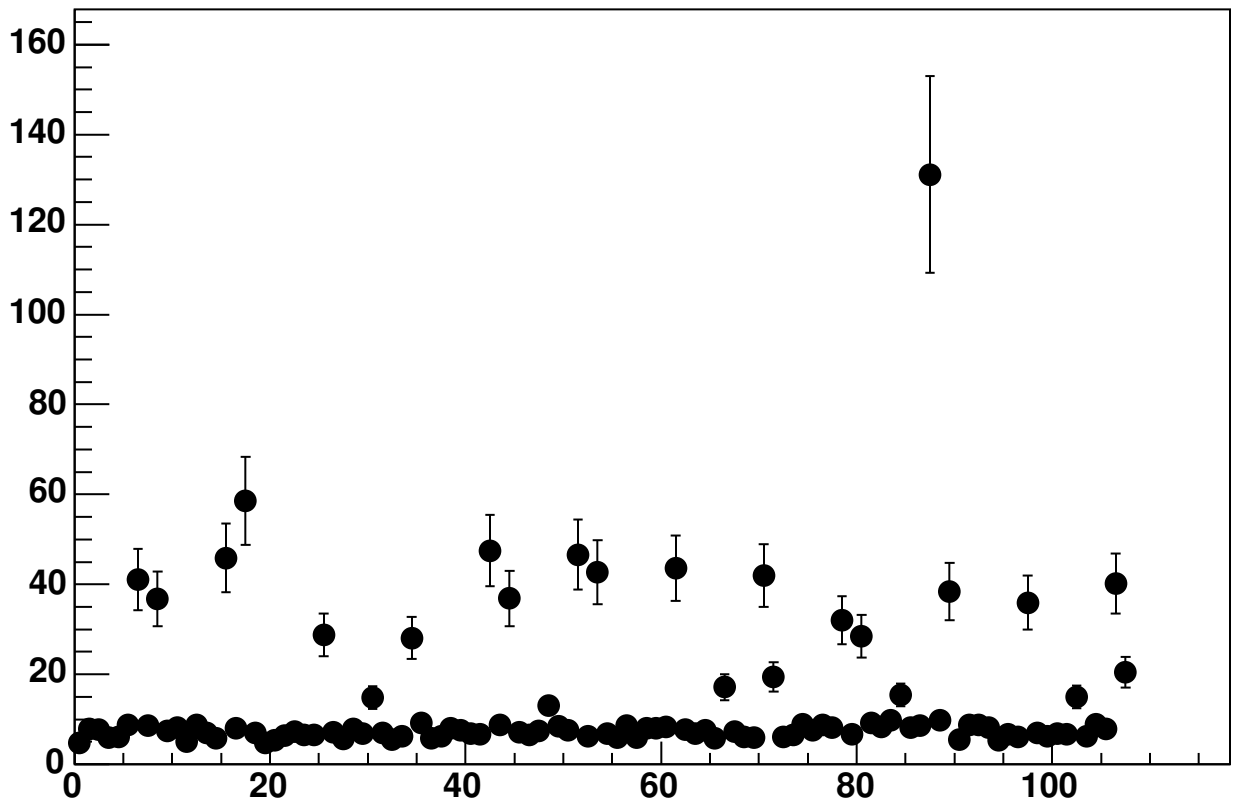
Enable 2, Hold=35, DAC=32000, ADC Noise vs 18*Chip+Chan



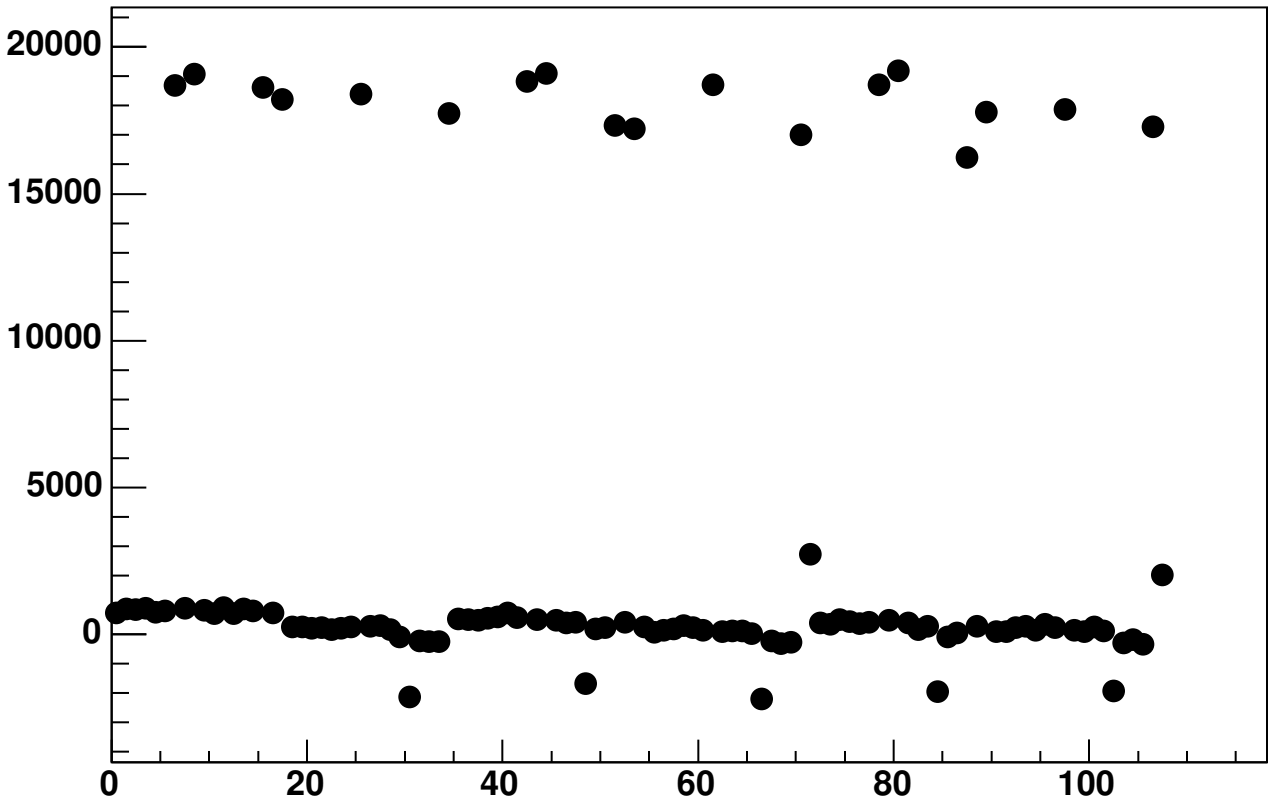
Enable 2, Hold=35, DAC=34000, ADC Mean vs 18*Chip+Chan



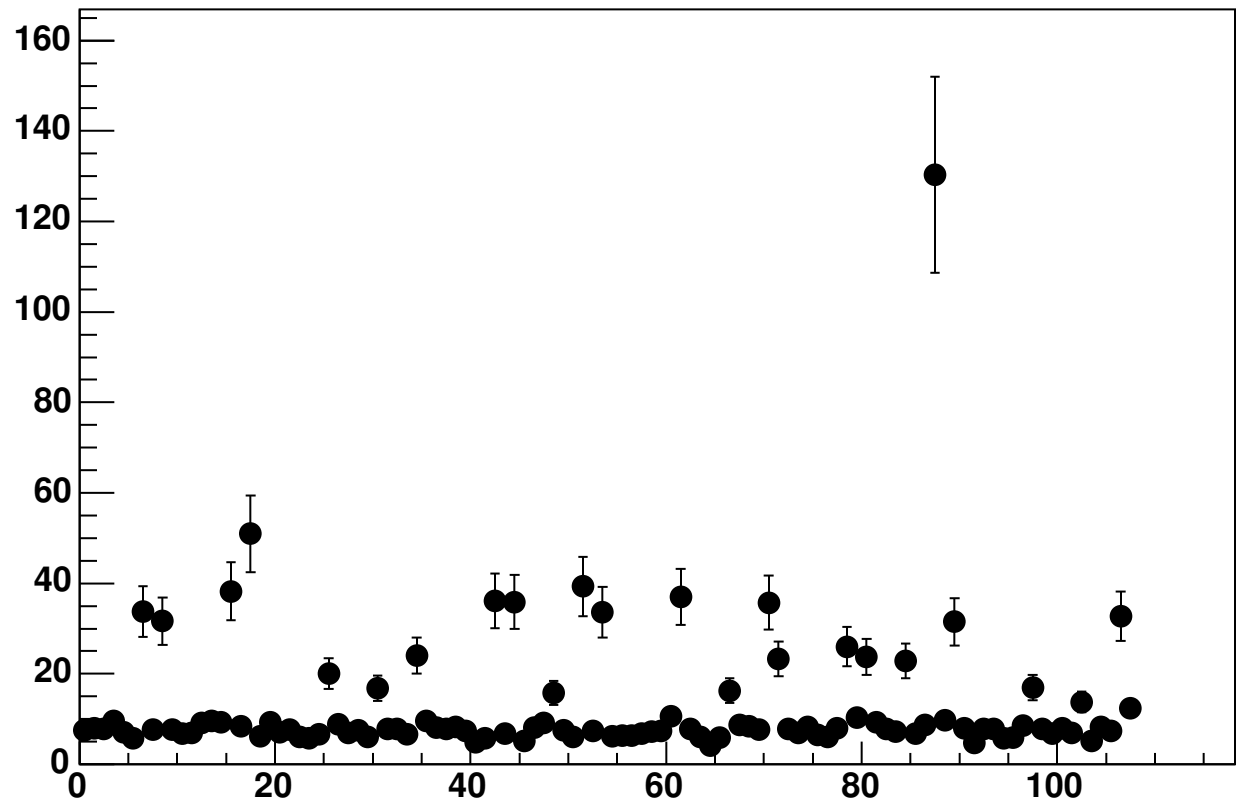
Enable 2, Hold=35, DAC=34000, ADC Noise vs 18*Chip+Chan



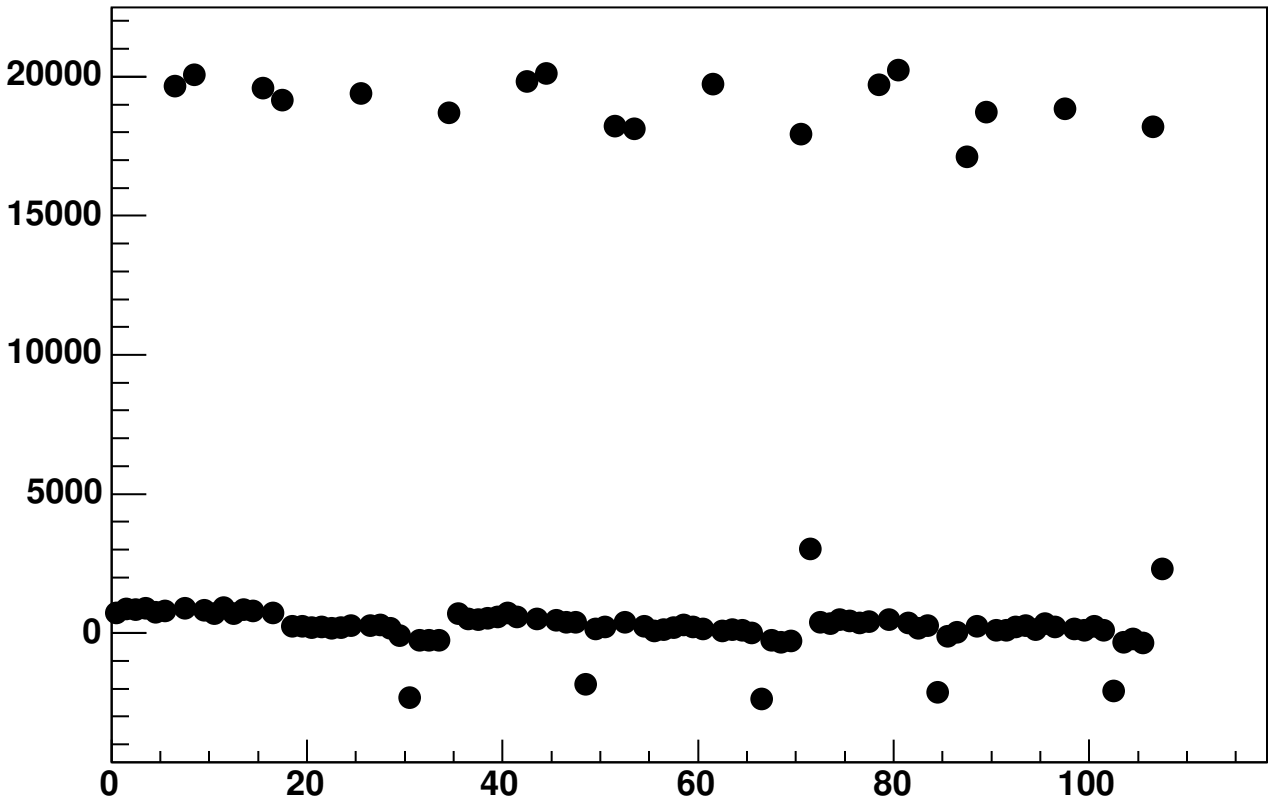
Enable 2, Hold=35, DAC=36000, ADC Mean vs 18*Chip+Chan



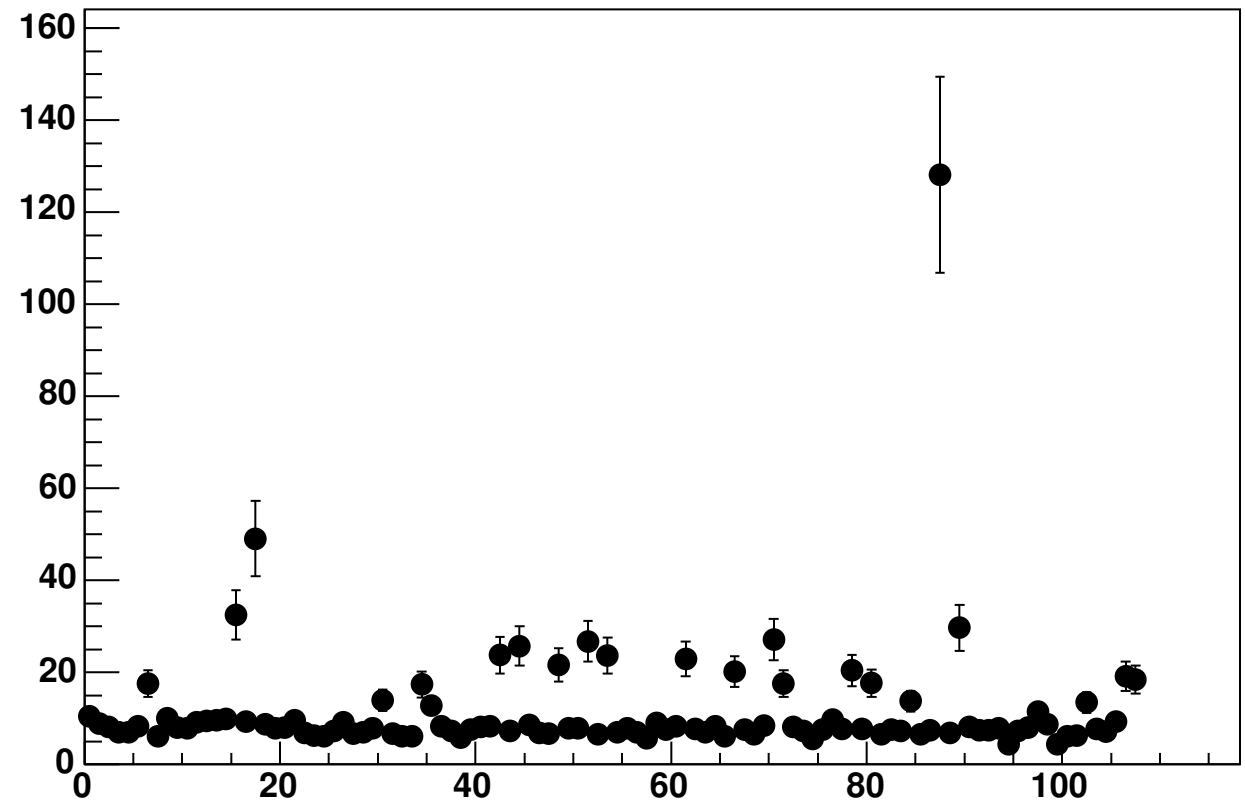
Enable 2, Hold=35, DAC=36000, ADC Noise vs 18*Chip+Chan



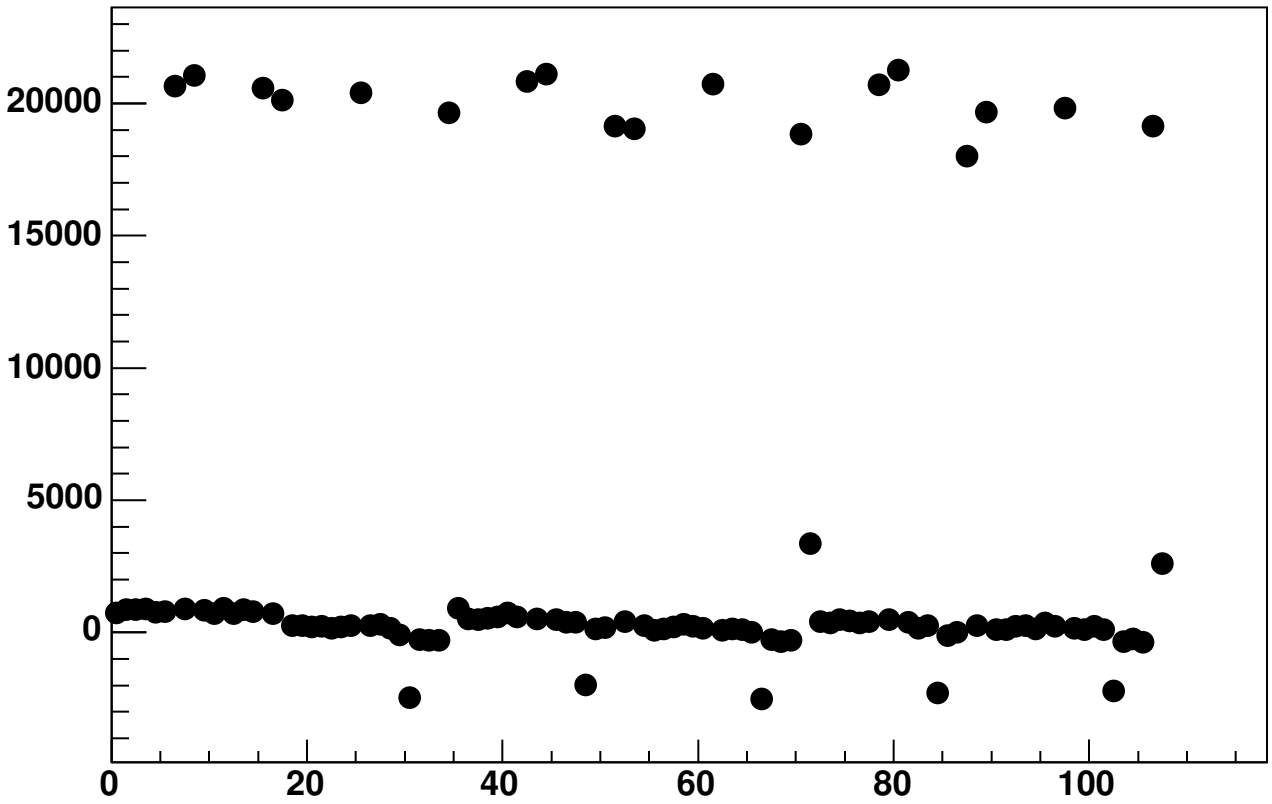
Enable 2, Hold=35, DAC=38000, ADC Mean vs 18*Chip+Chan



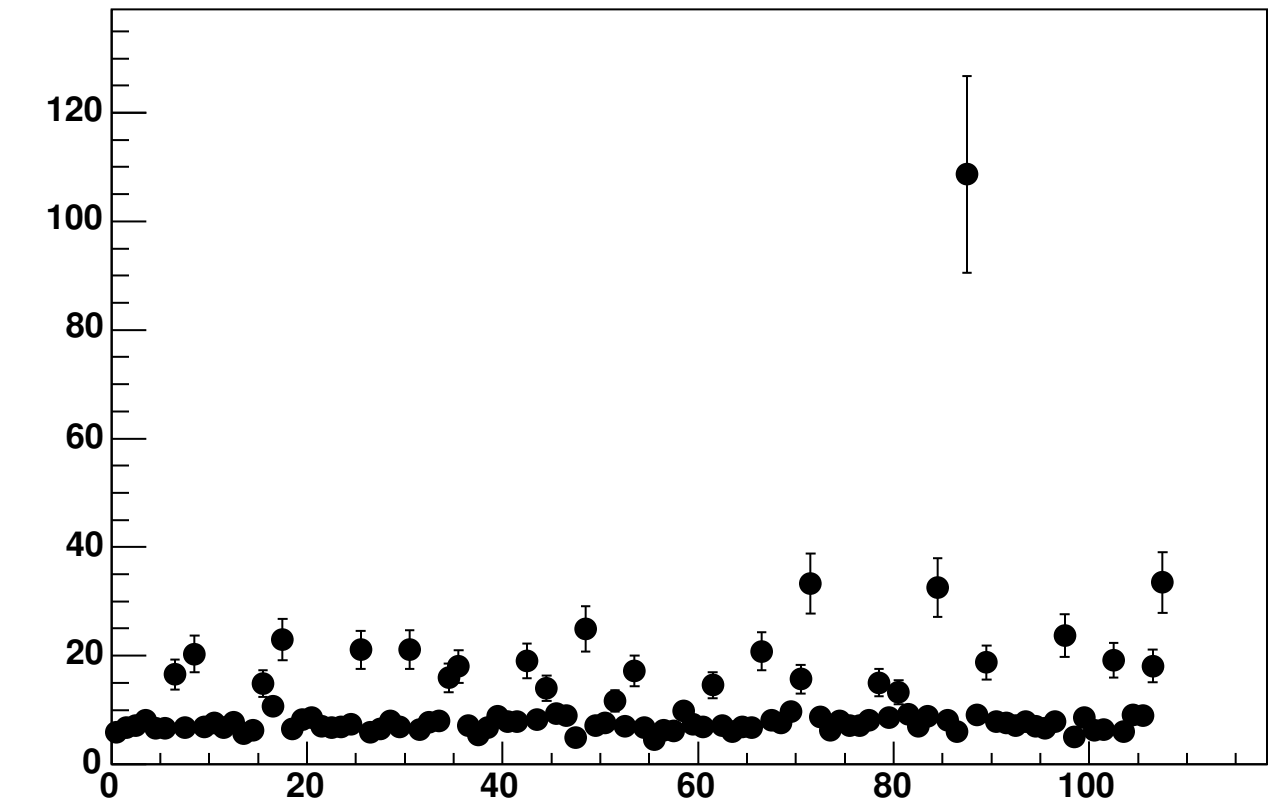
Enable 2, Hold=35, DAC=38000, ADC Noise vs 18*Chip+Chan



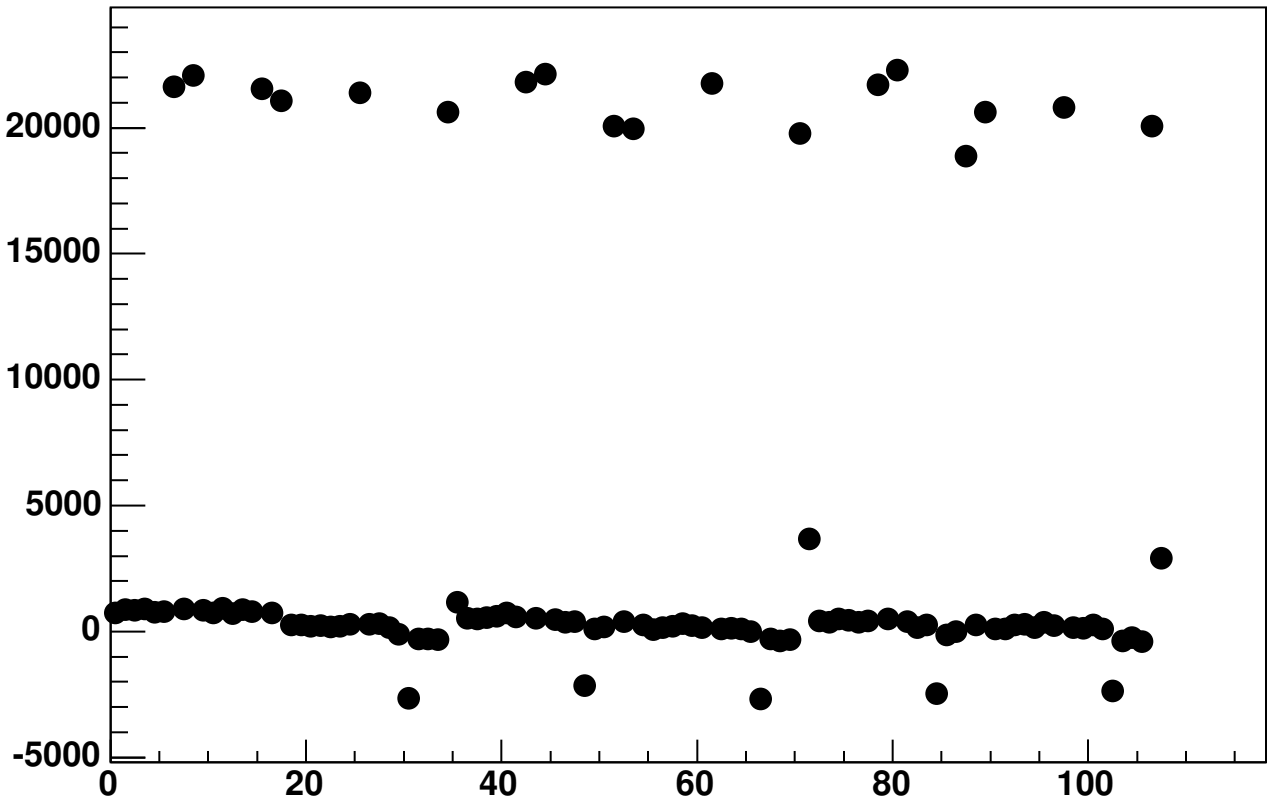
Enable 2, Hold=35, DAC=40000, ADC Mean vs 18*Chip+Chan



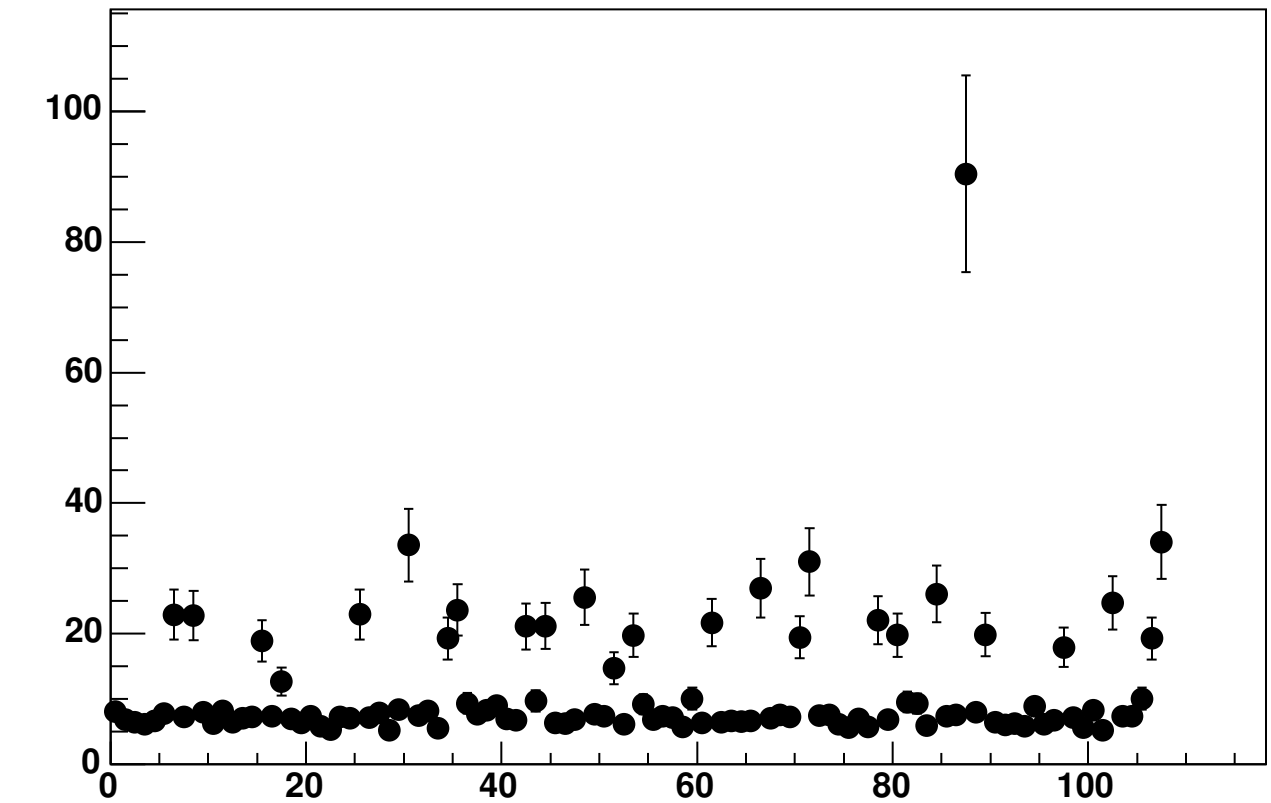
Enable 2, Hold=35, DAC=40000, ADC Noise vs 18*Chip+Chan



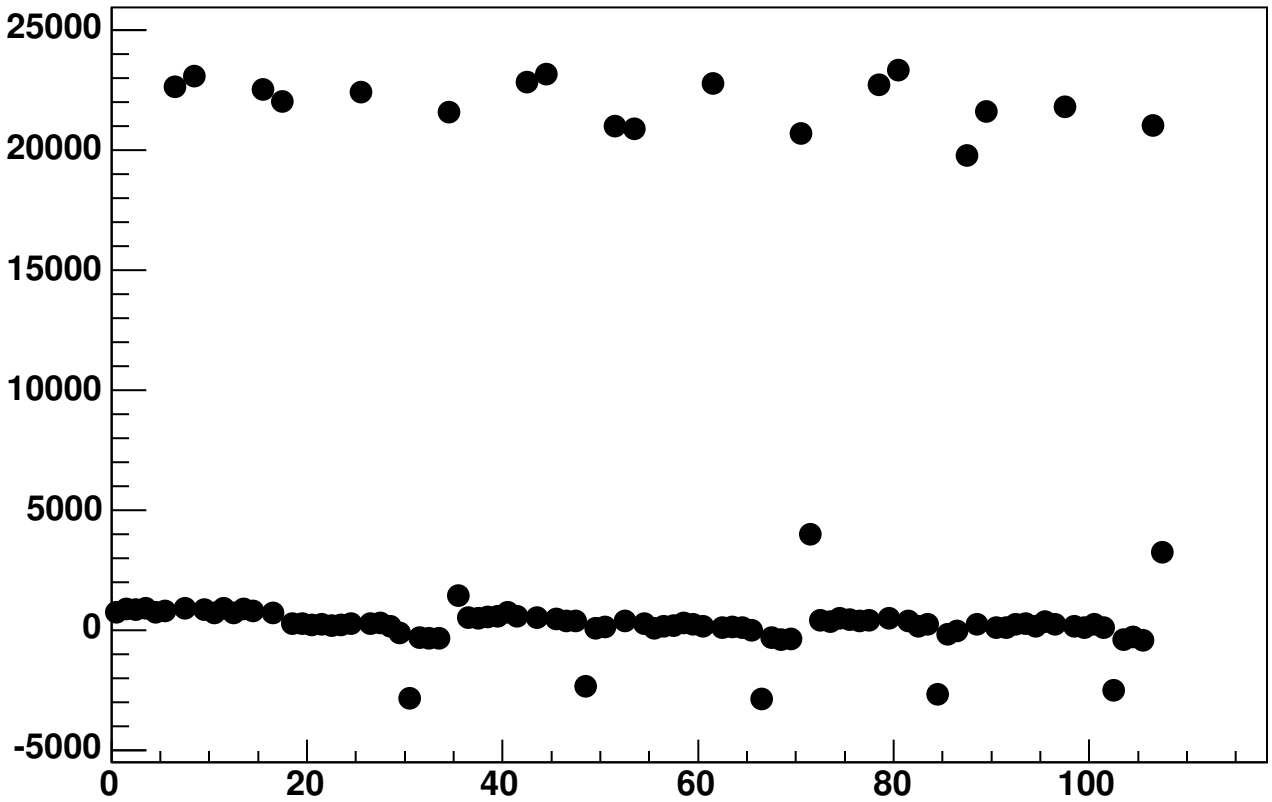
Enable 2, Hold=35, DAC=42000, ADC Mean vs 18*Chip+Chan



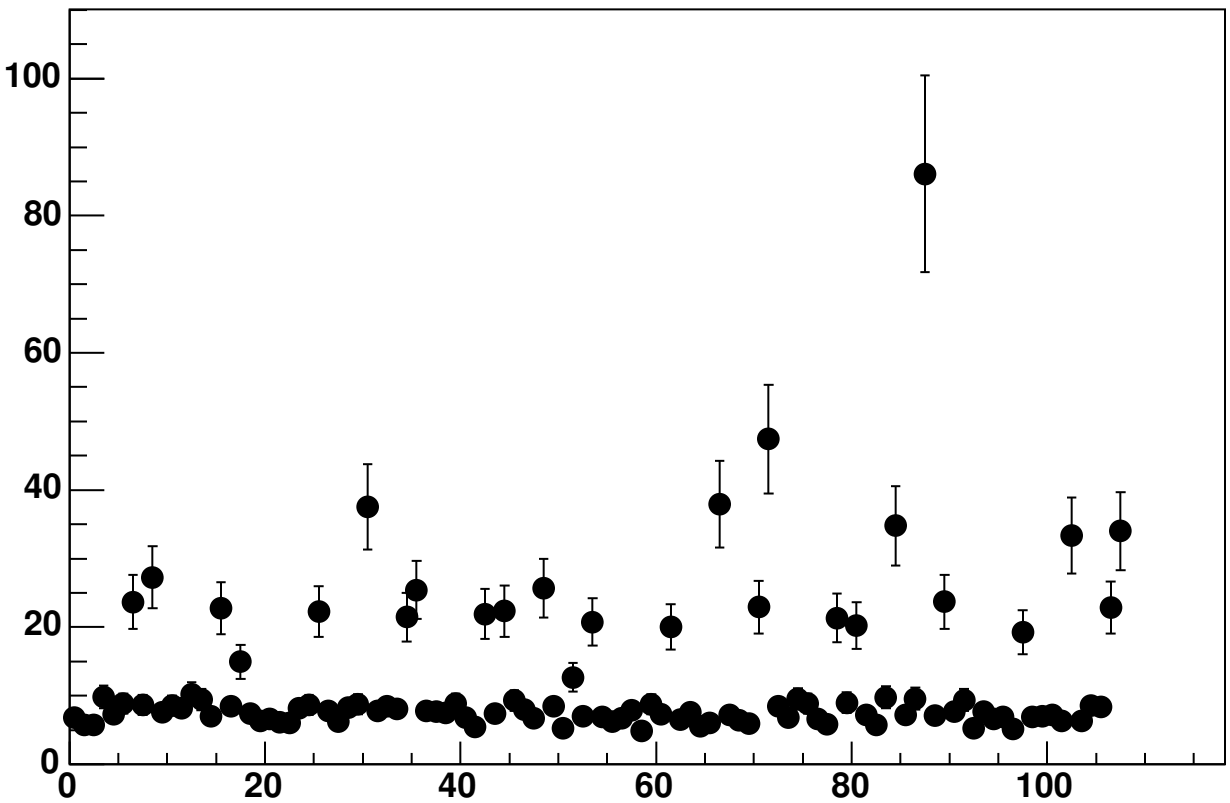
Enable 2, Hold=35, DAC=42000, ADC Noise vs 18*Chip+Chan



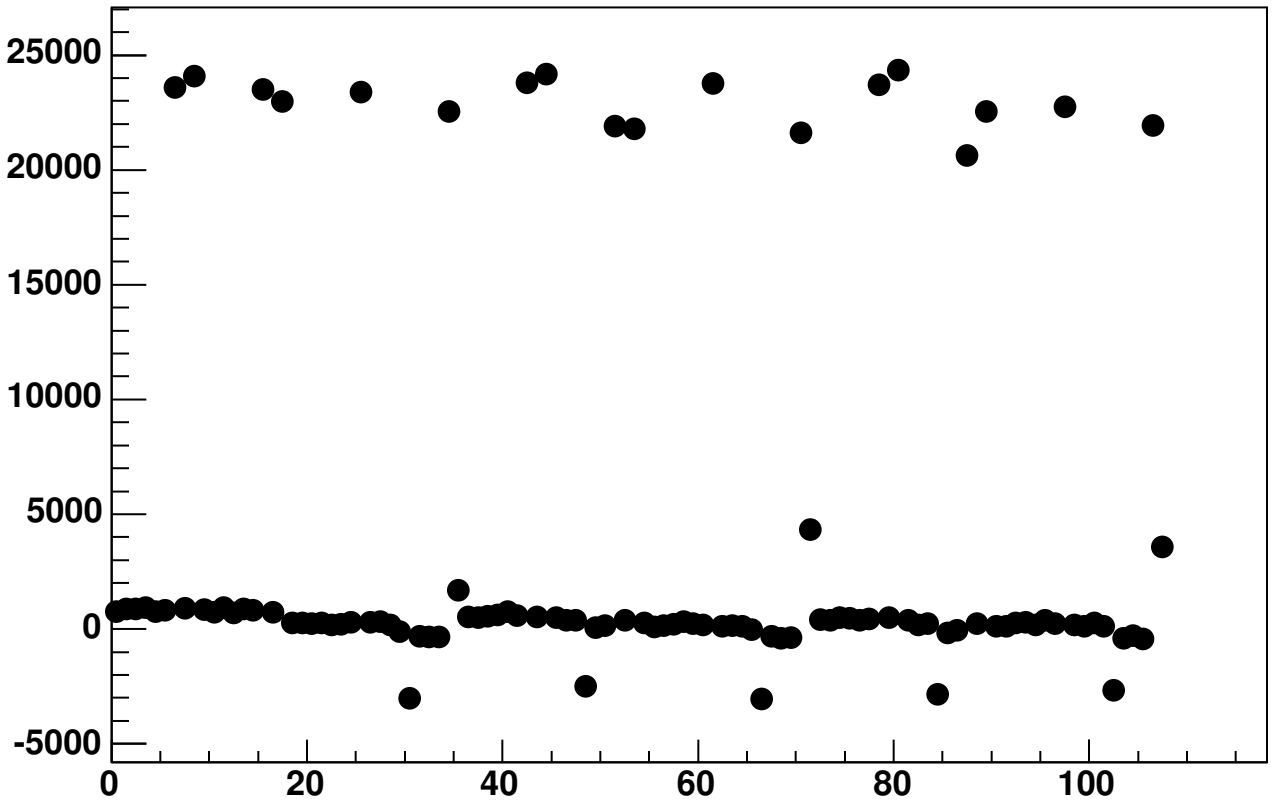
Enable 2, Hold=35, DAC=44000, ADC Mean vs 18*Chip+Chan



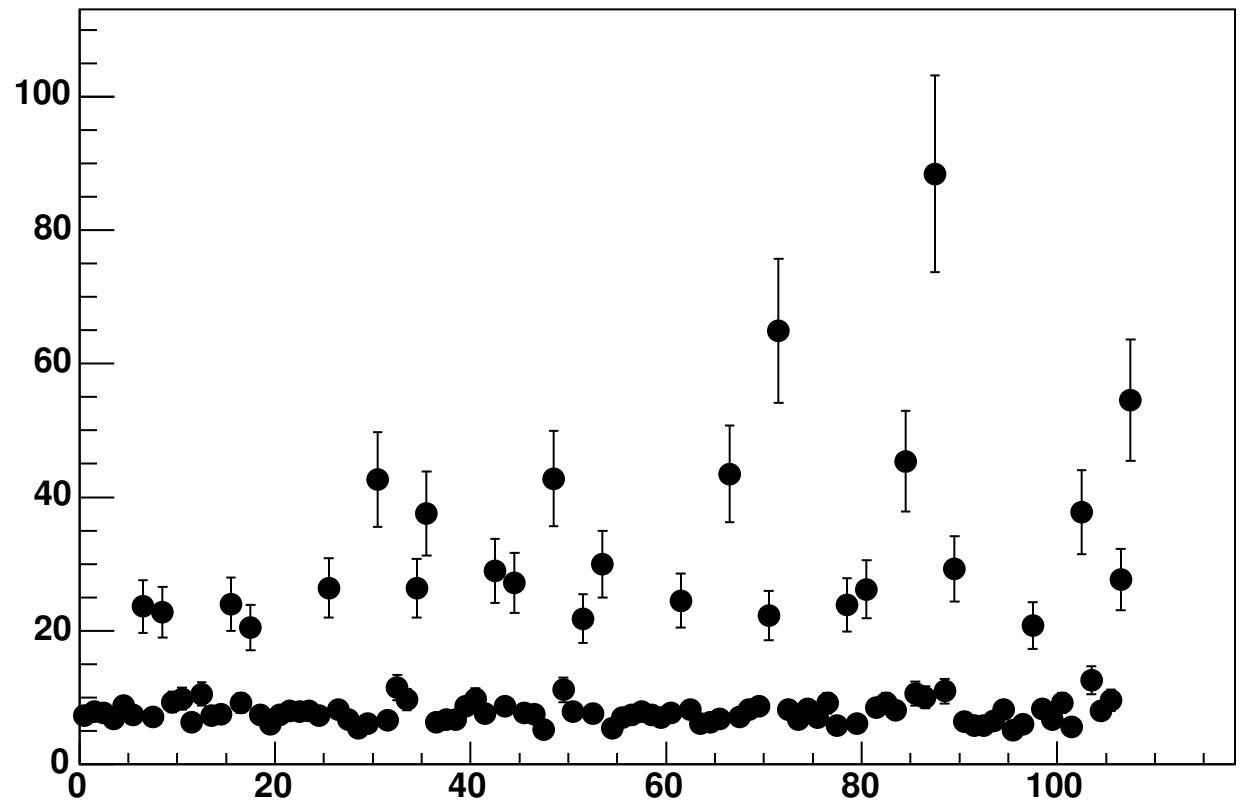
Enable 2, Hold=35, DAC=44000, ADC Noise vs 18*Chip+Chan



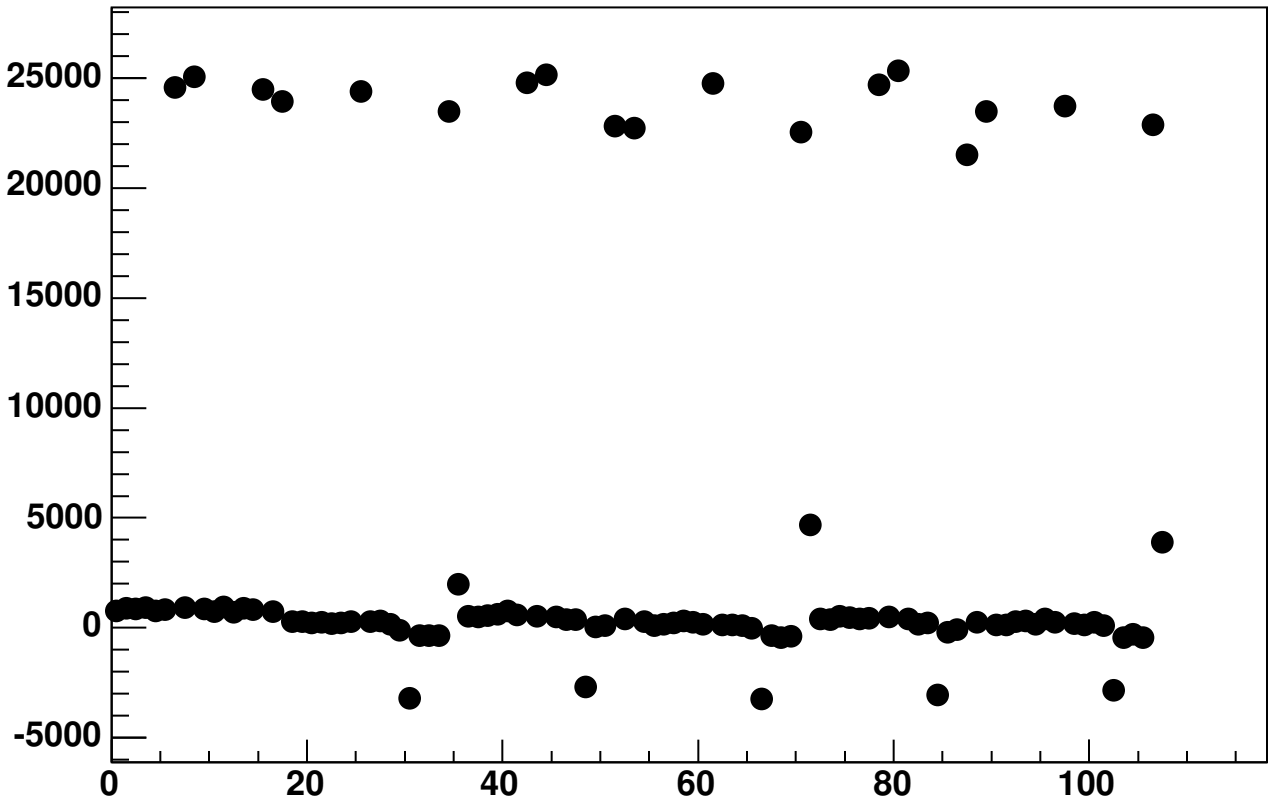
Enable 2, Hold=35, DAC=46000, ADC Mean vs 18*Chip+Chan



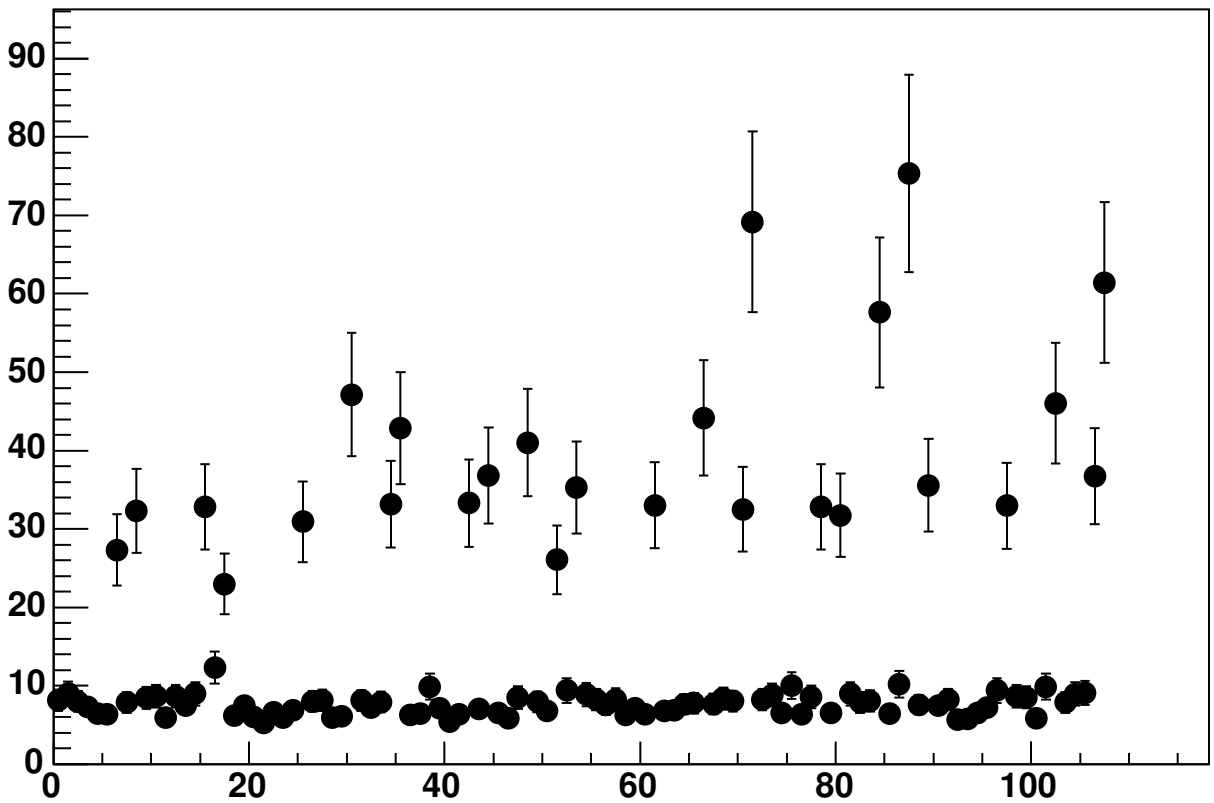
Enable 2, Hold=35, DAC=46000, ADC Noise vs 18*Chip+Chan



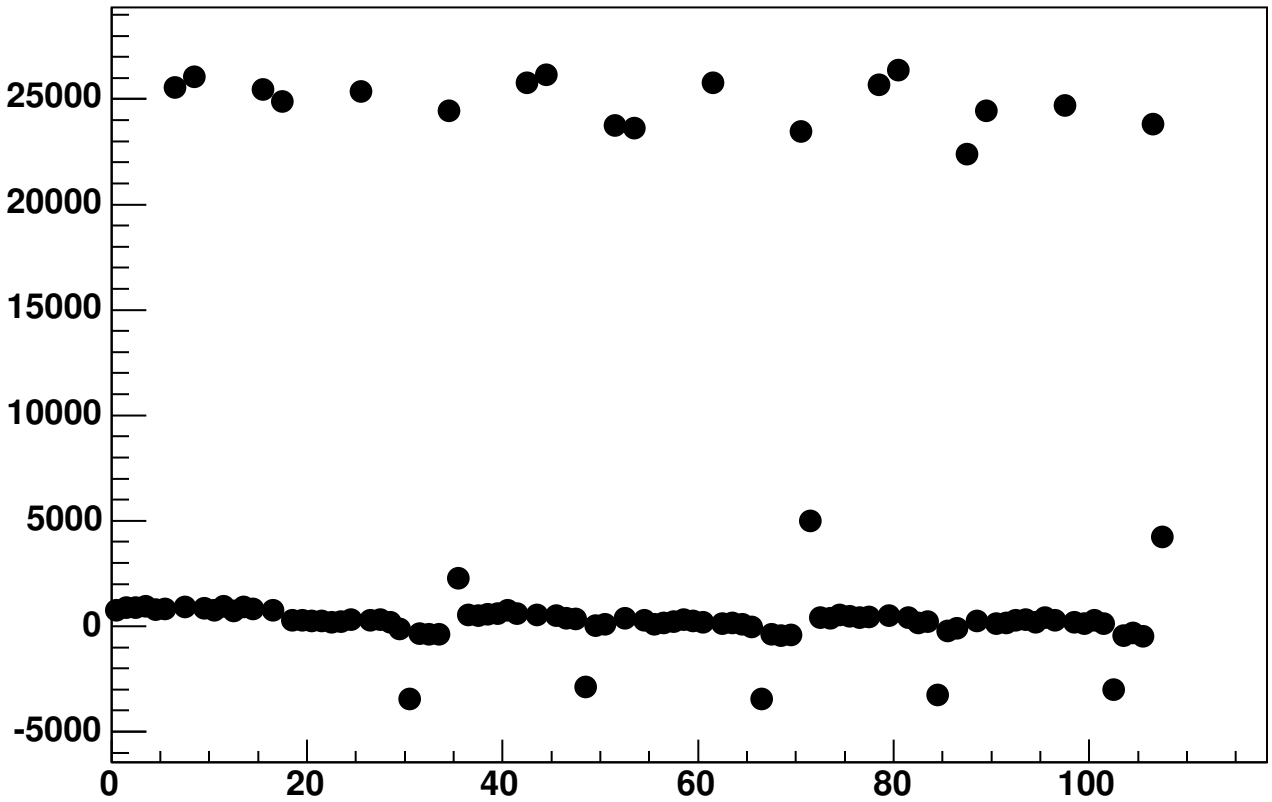
Enable 2, Hold=35, DAC=48000, ADC Mean vs 18*Chip+Chan



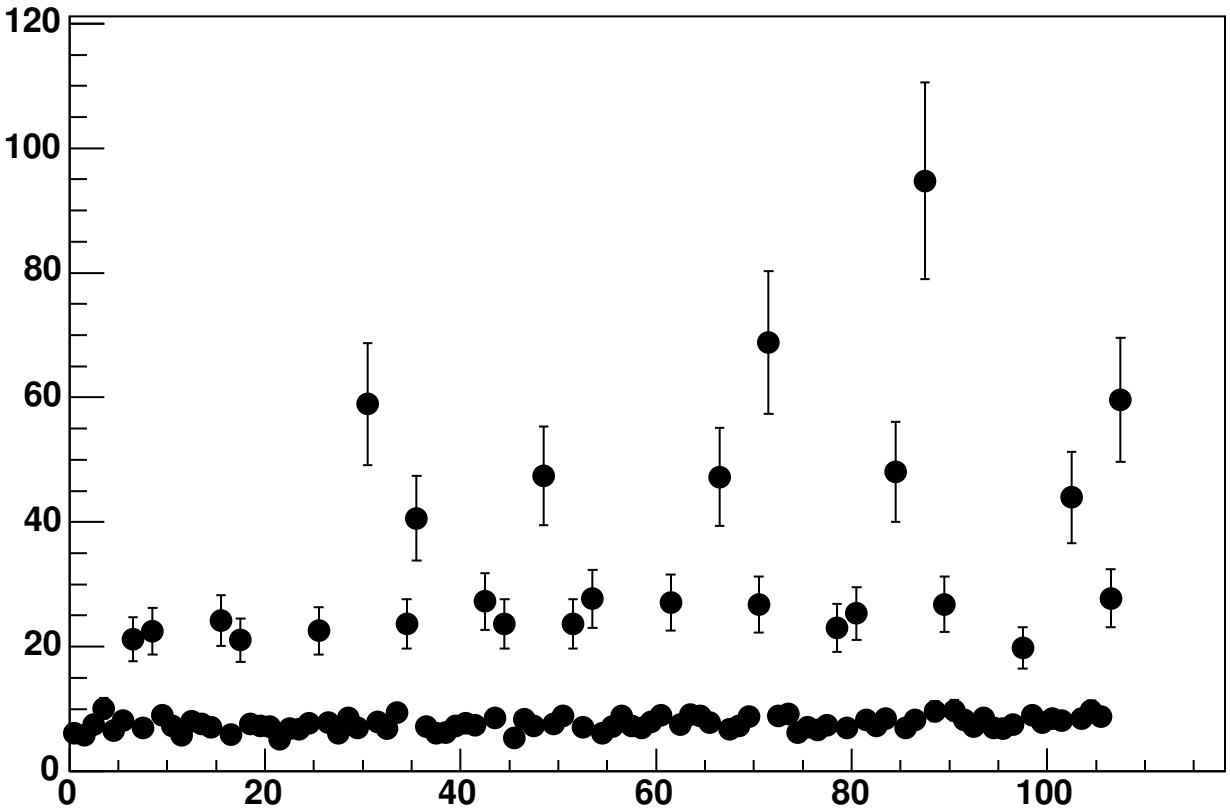
Enable 2, Hold=35, DAC=48000, ADC Noise vs 18*Chip+Chan



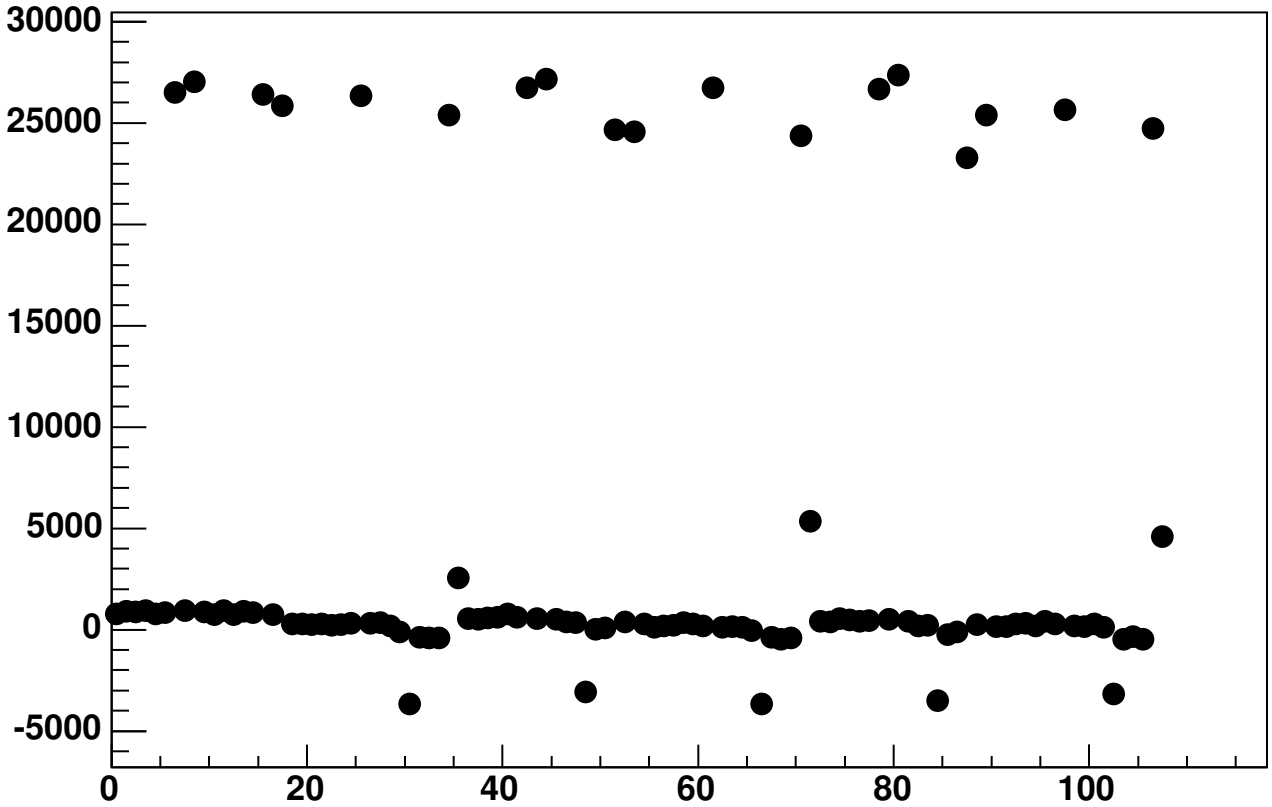
Enable 2, Hold=35, DAC=50000, ADC Mean vs 18*Chip+Chan



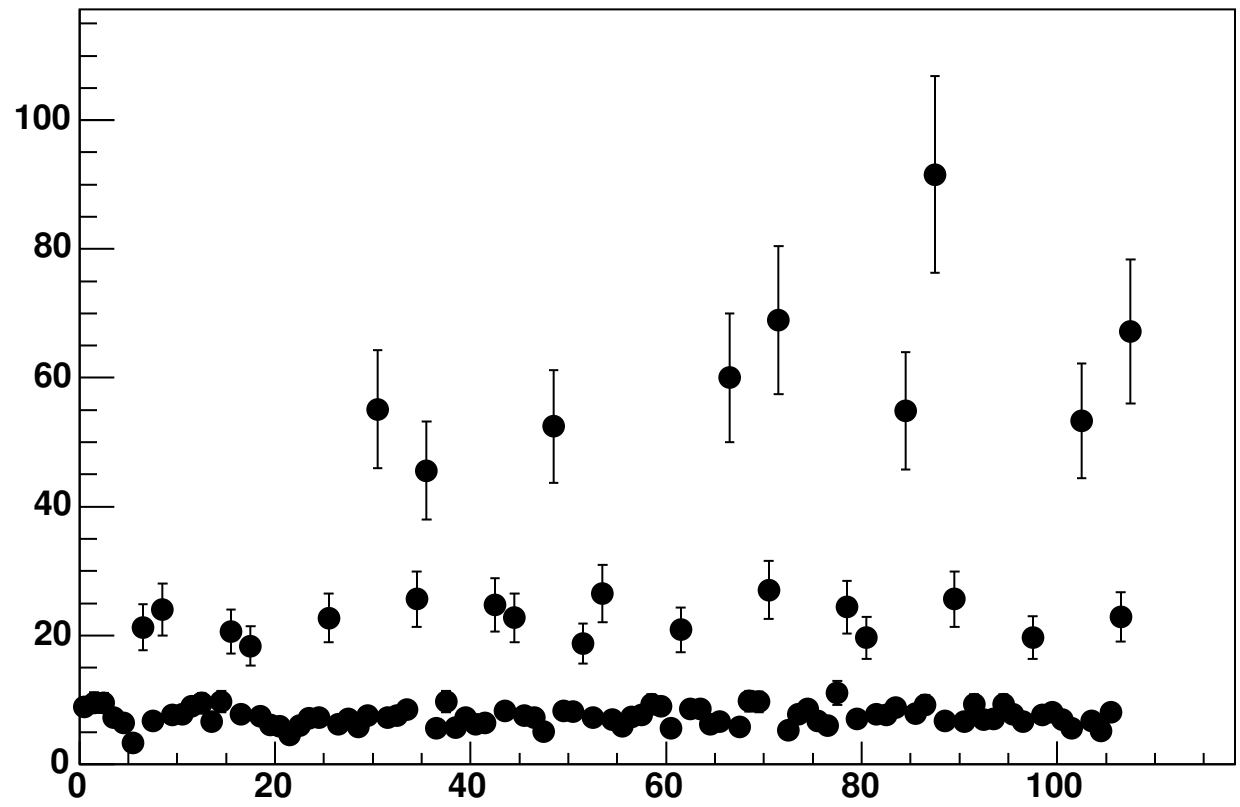
Enable 2, Hold=35, DAC=50000, ADC Noise vs 18*Chip+Chan



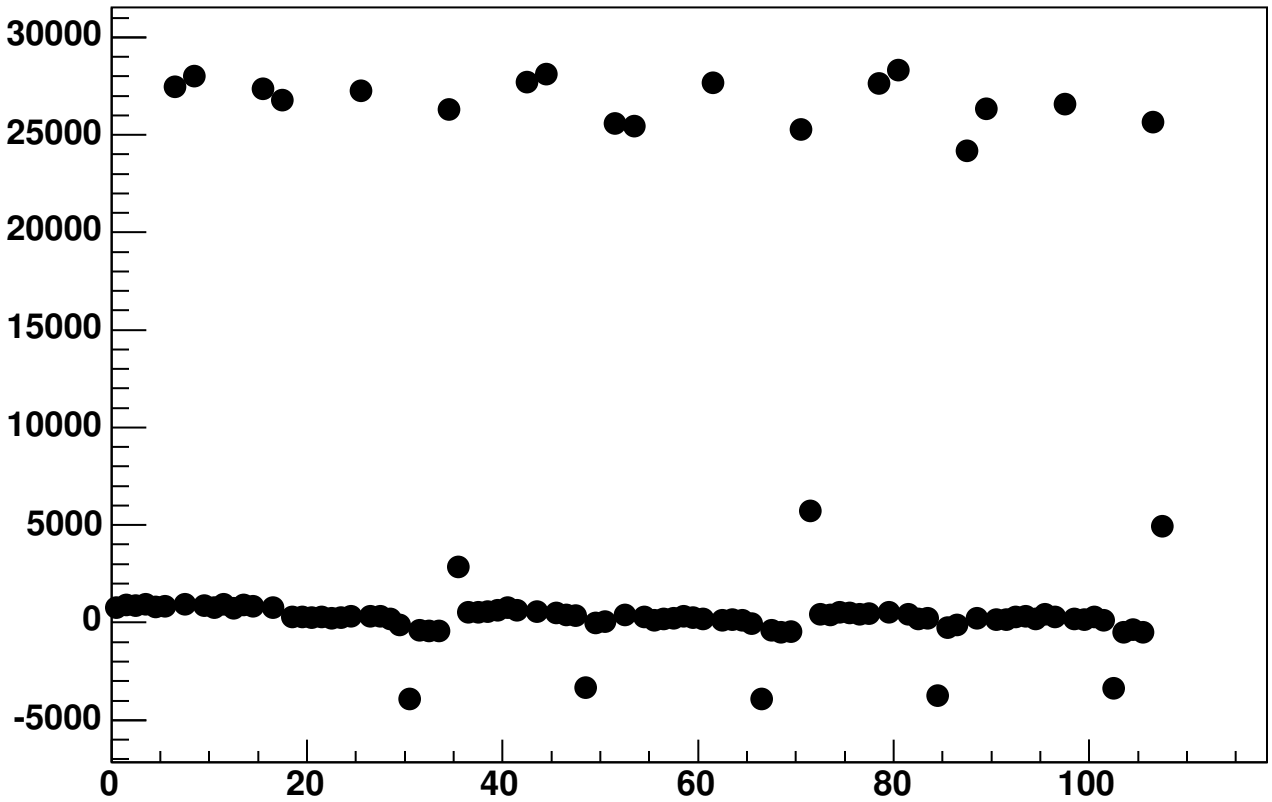
Enable 2, Hold=35, DAC=52000, ADC Mean vs 18*Chip+Chan



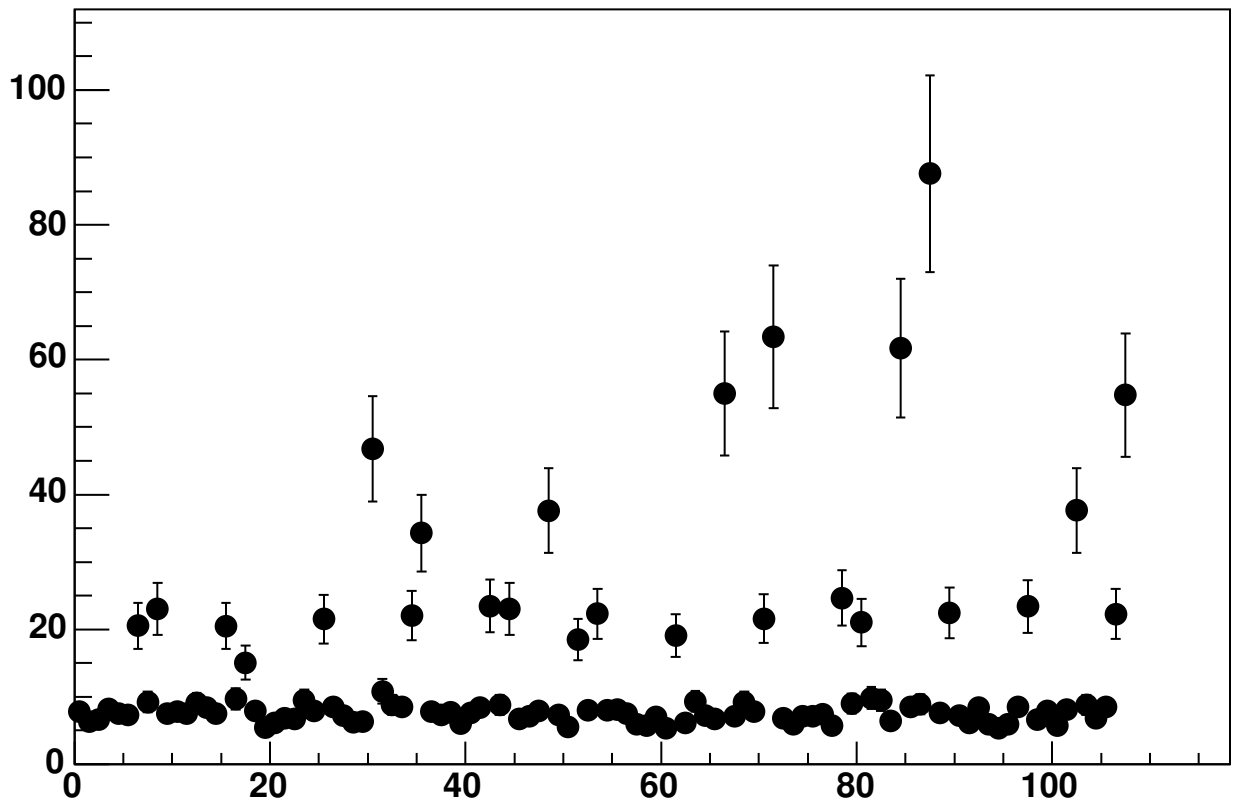
Enable 2, Hold=35, DAC=52000, ADC Noise vs 18*Chip+Chan



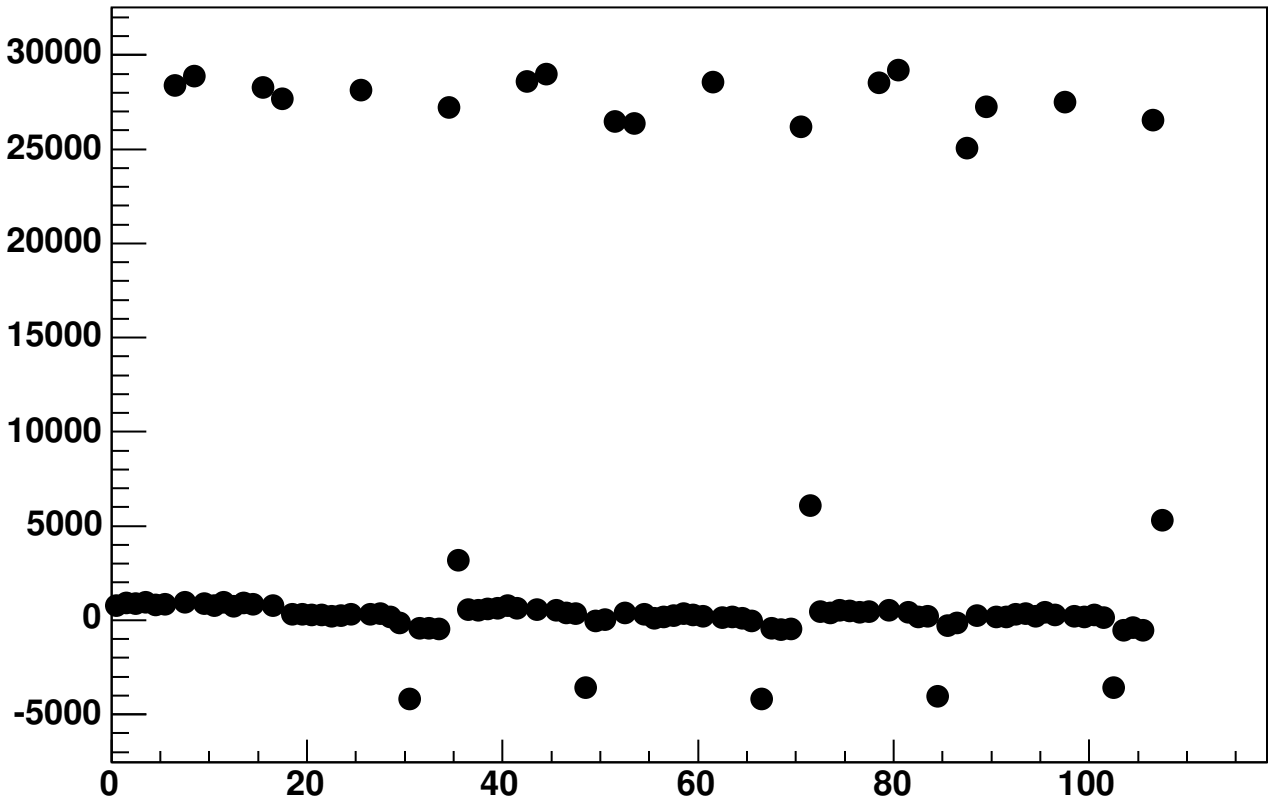
Enable 2, Hold=35, DAC=54000, ADC Mean vs 18*Chip+Chan



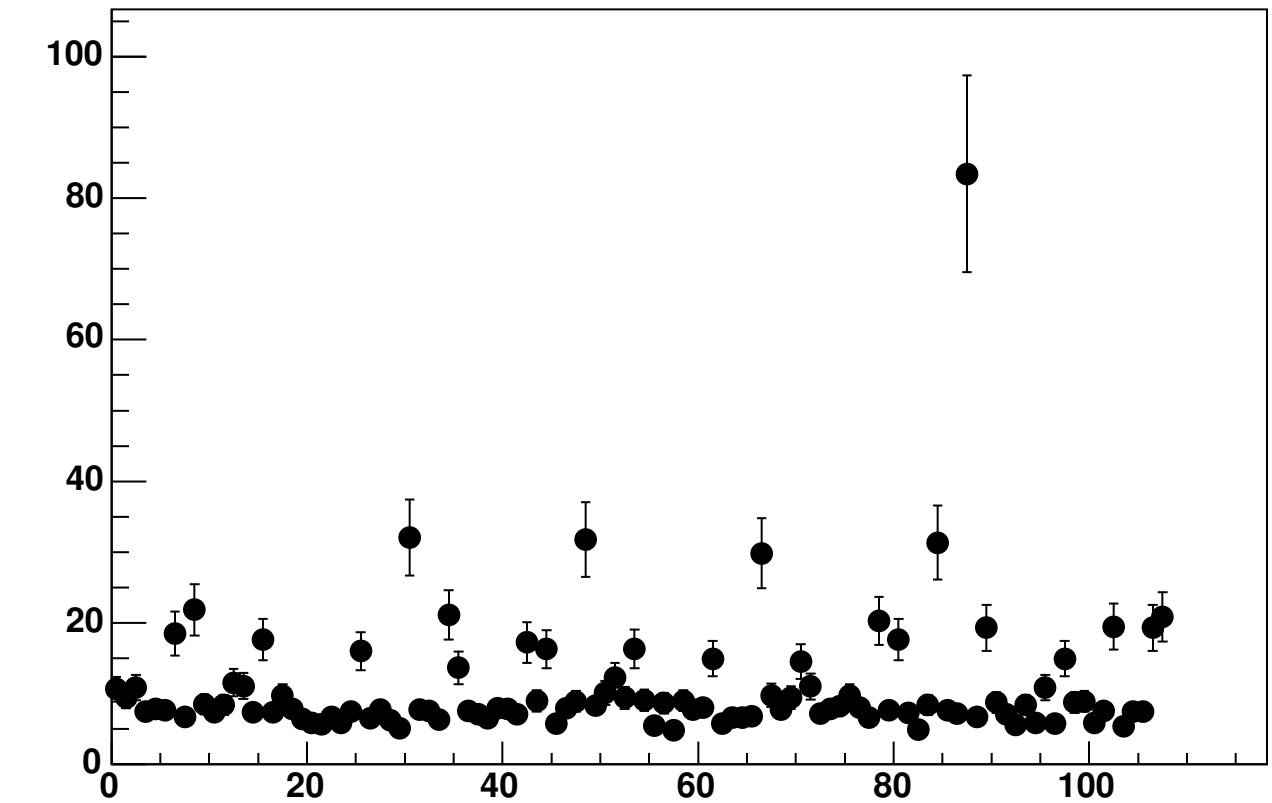
Enable 2, Hold=35, DAC=54000, ADC Noise vs 18*Chip+Chan



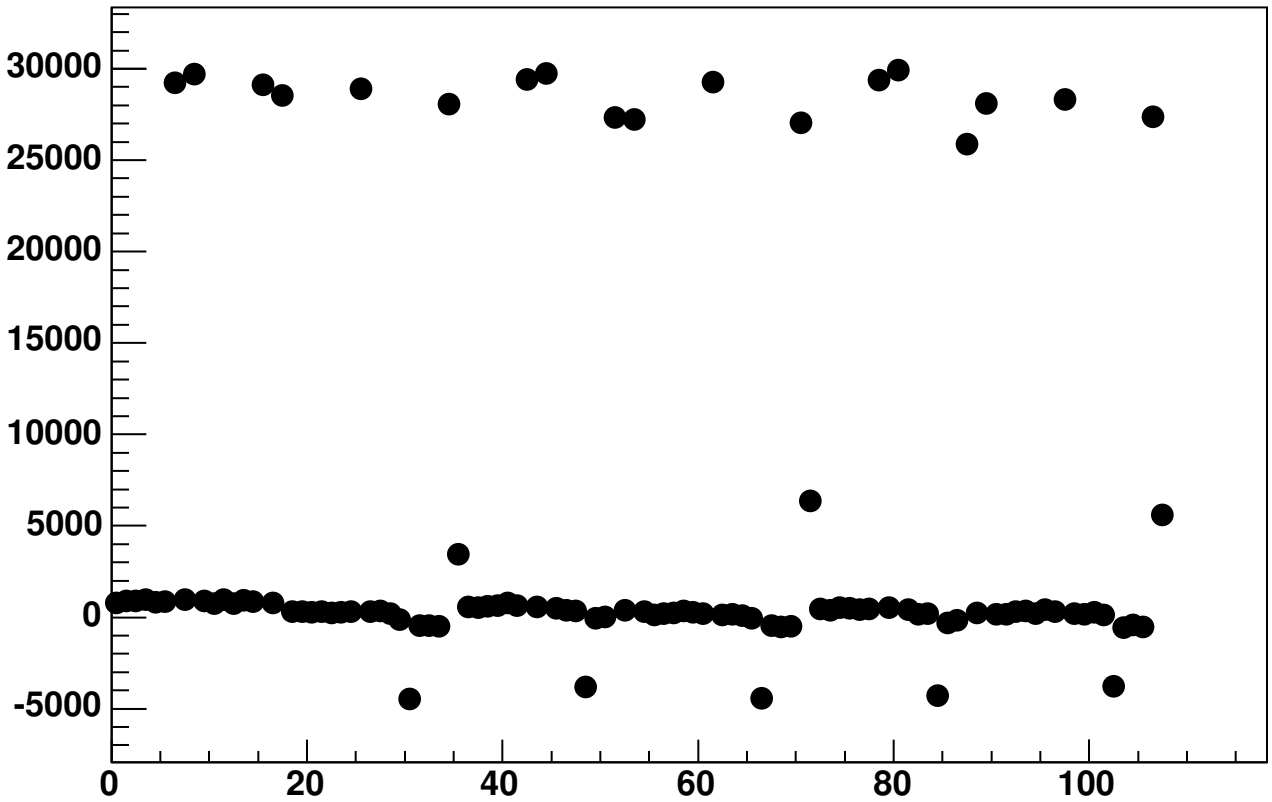
Enable 2, Hold=35, DAC=56000, ADC Mean vs 18*Chip+Chan



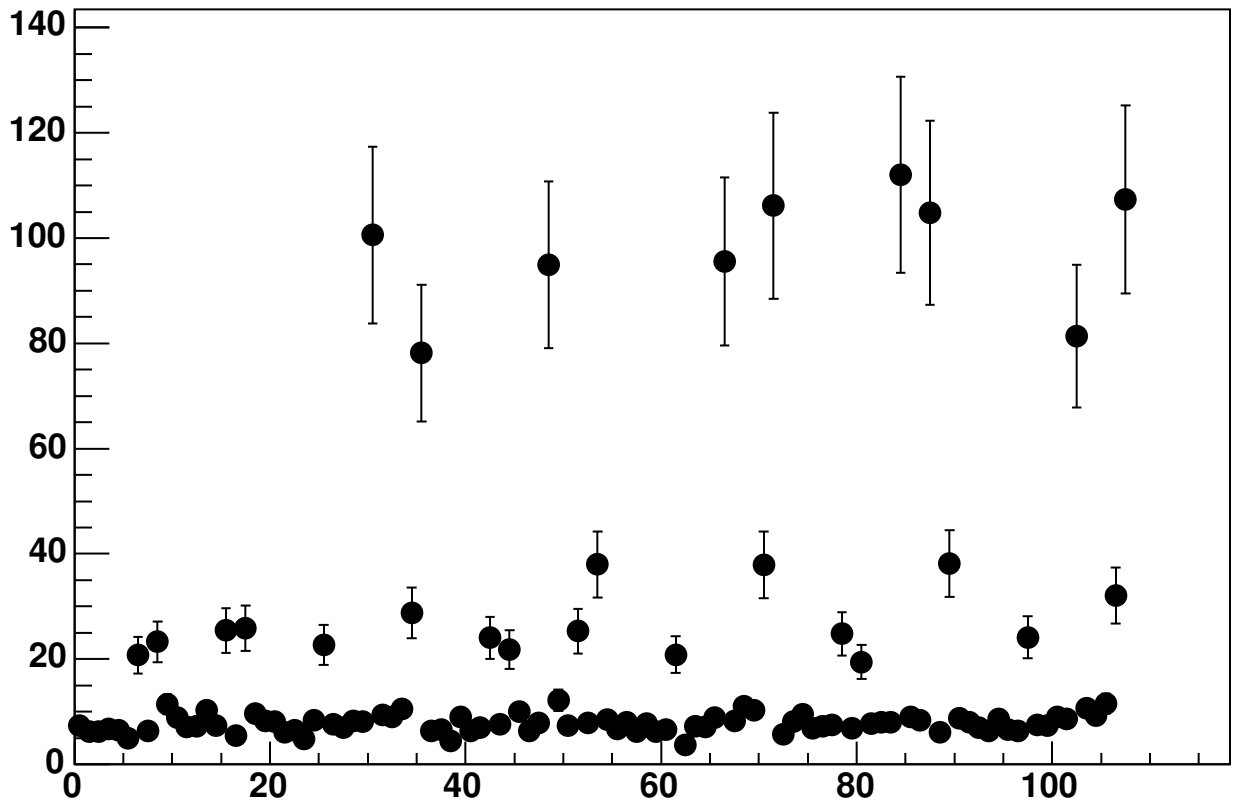
Enable 2, Hold=35, DAC=56000, ADC Noise vs 18*Chip+Chan



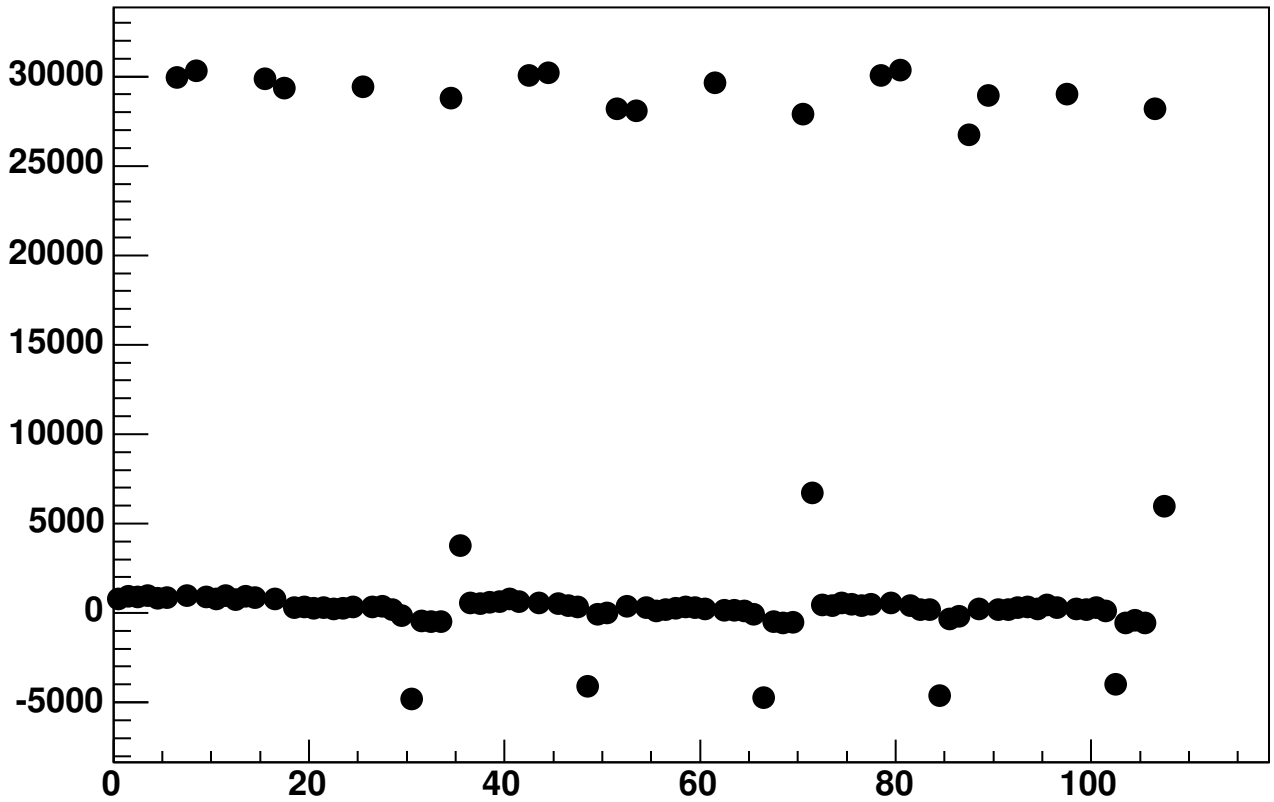
Enable 2, Hold=35, DAC=58000, ADC Mean vs 18*Chip+Chan



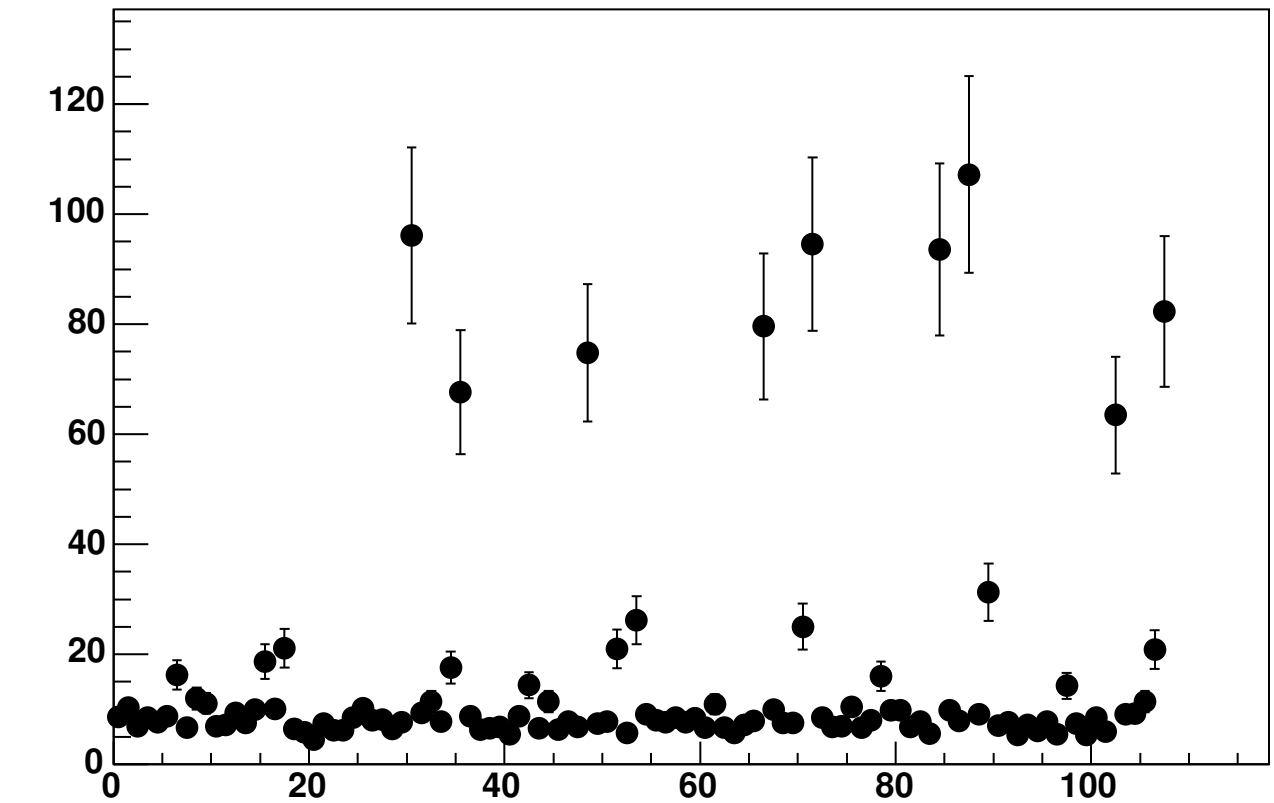
Enable 2, Hold=35, DAC=58000, ADC Noise vs 18*Chip+Chan



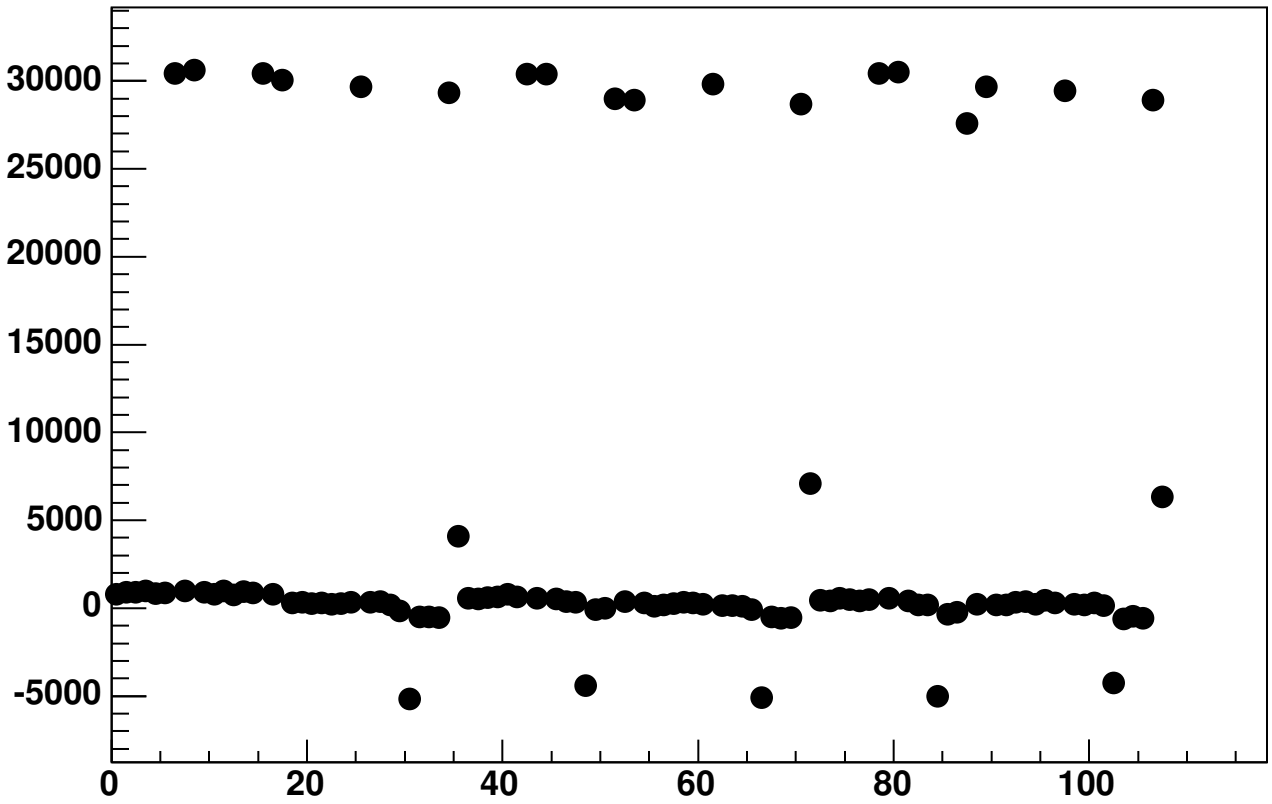
Enable 2, Hold=35, DAC=60000, ADC Mean vs 18*Chip+Chan



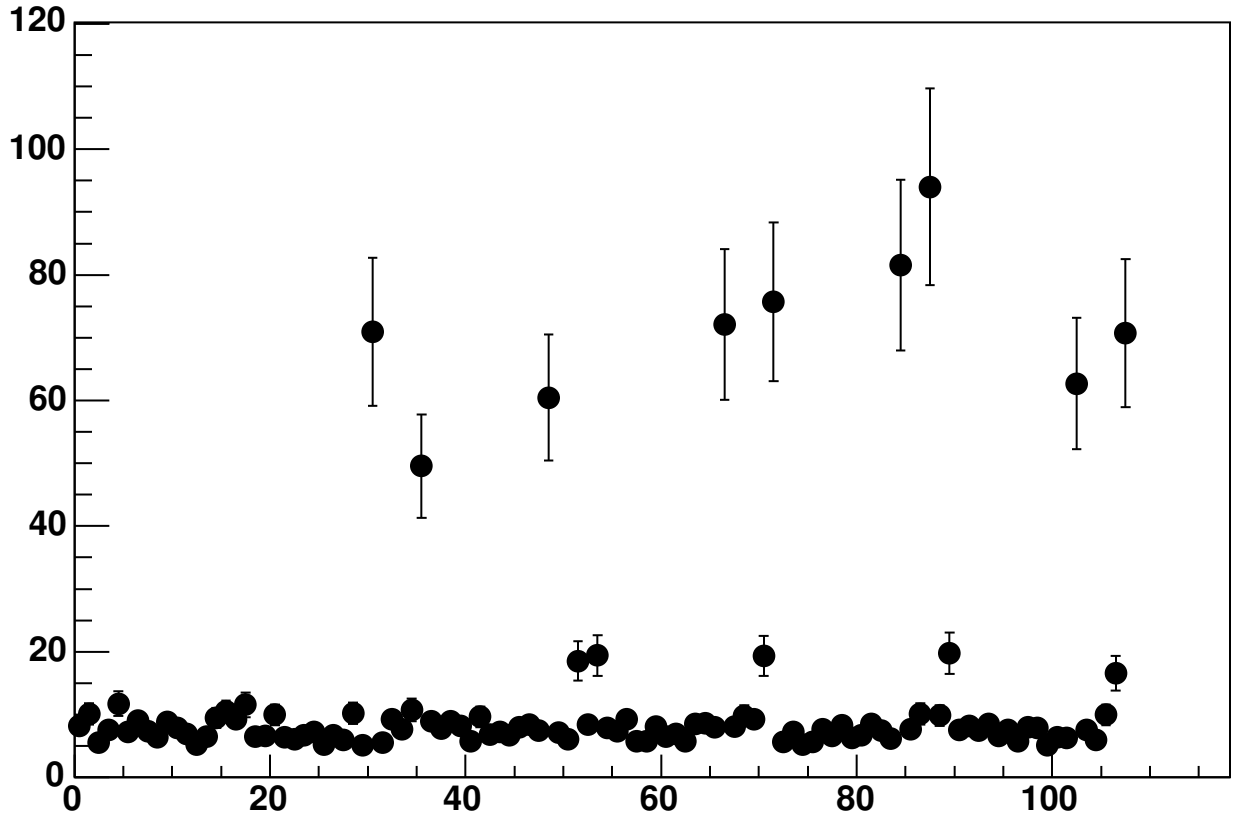
Enable 2, Hold=35, DAC=60000, ADC Noise vs 18*Chip+Chan



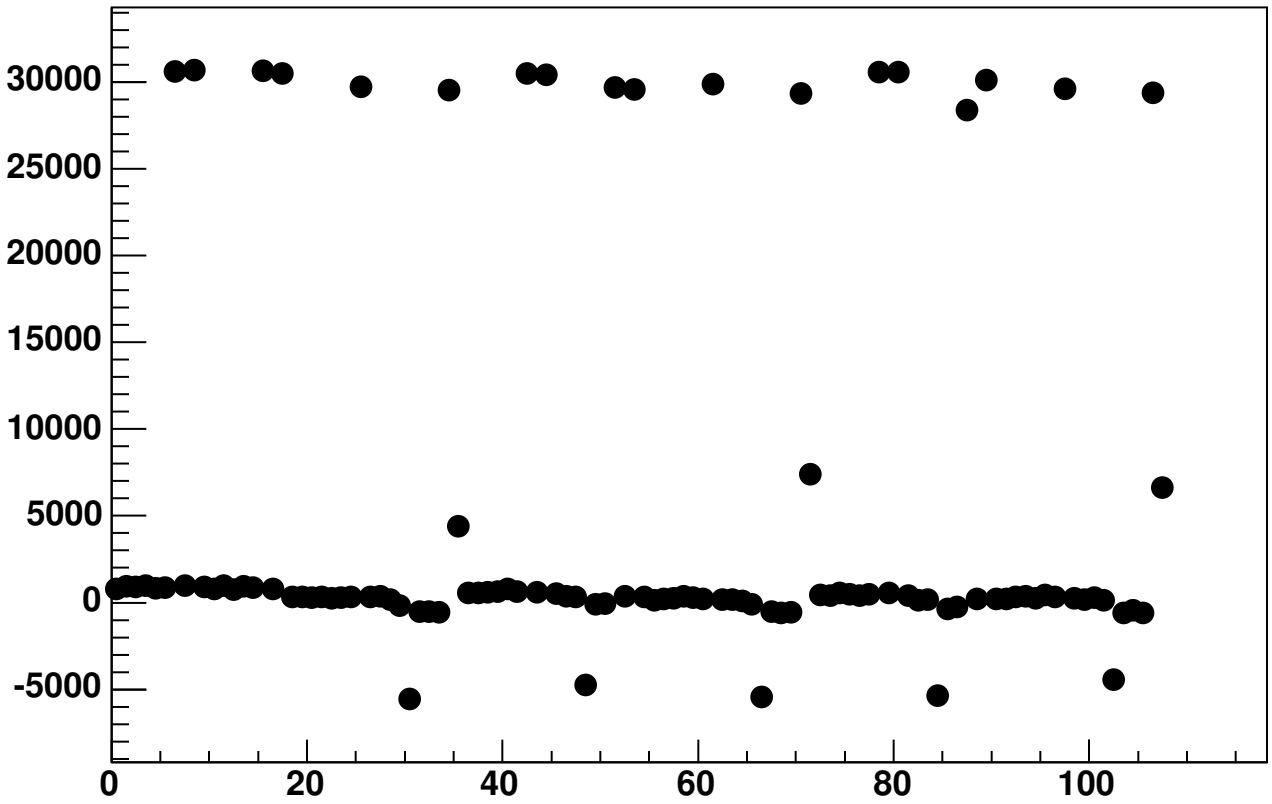
Enable 2, Hold=35, DAC=62000, ADC Mean vs 18*Chip+Chan



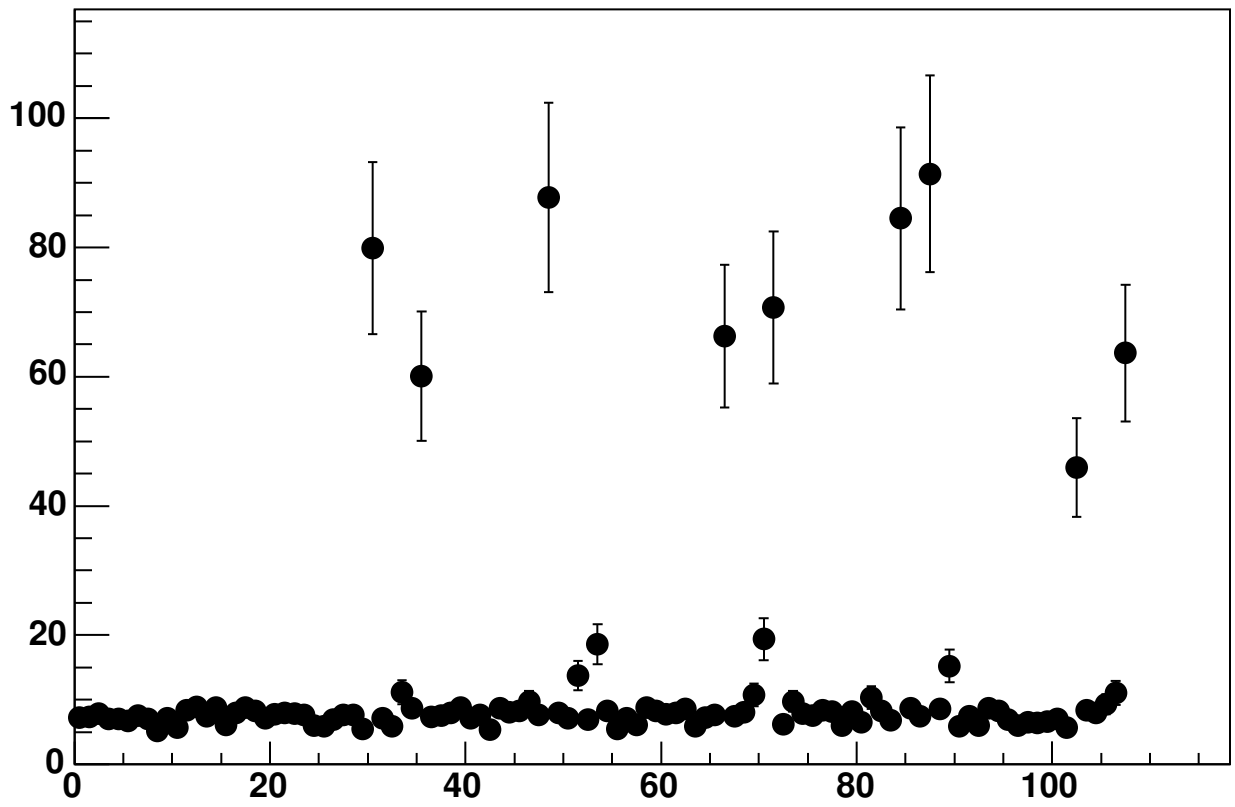
Enable 2, Hold=35, DAC=62000, ADC Noise vs 18*Chip+Chan



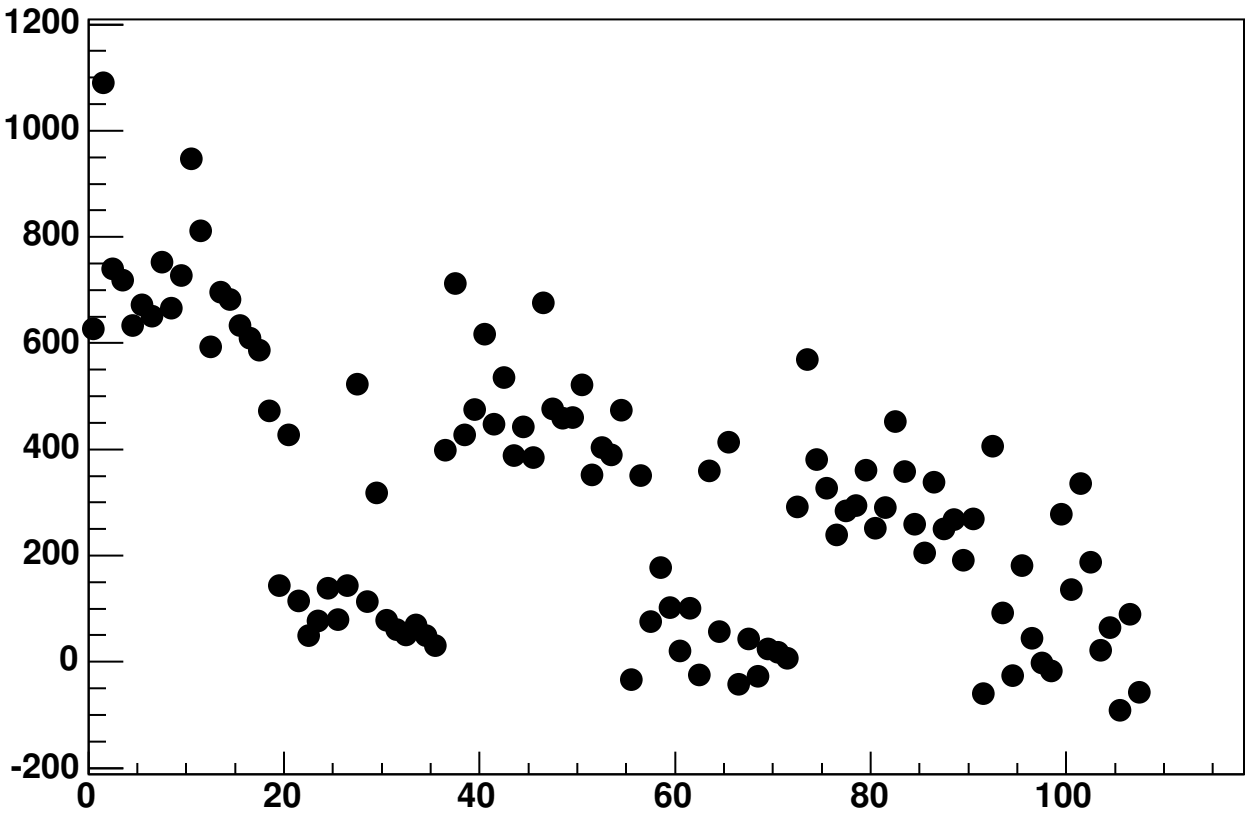
Enable 2, Hold=35, DAC=64000, ADC Mean vs 18*Chip+Chan



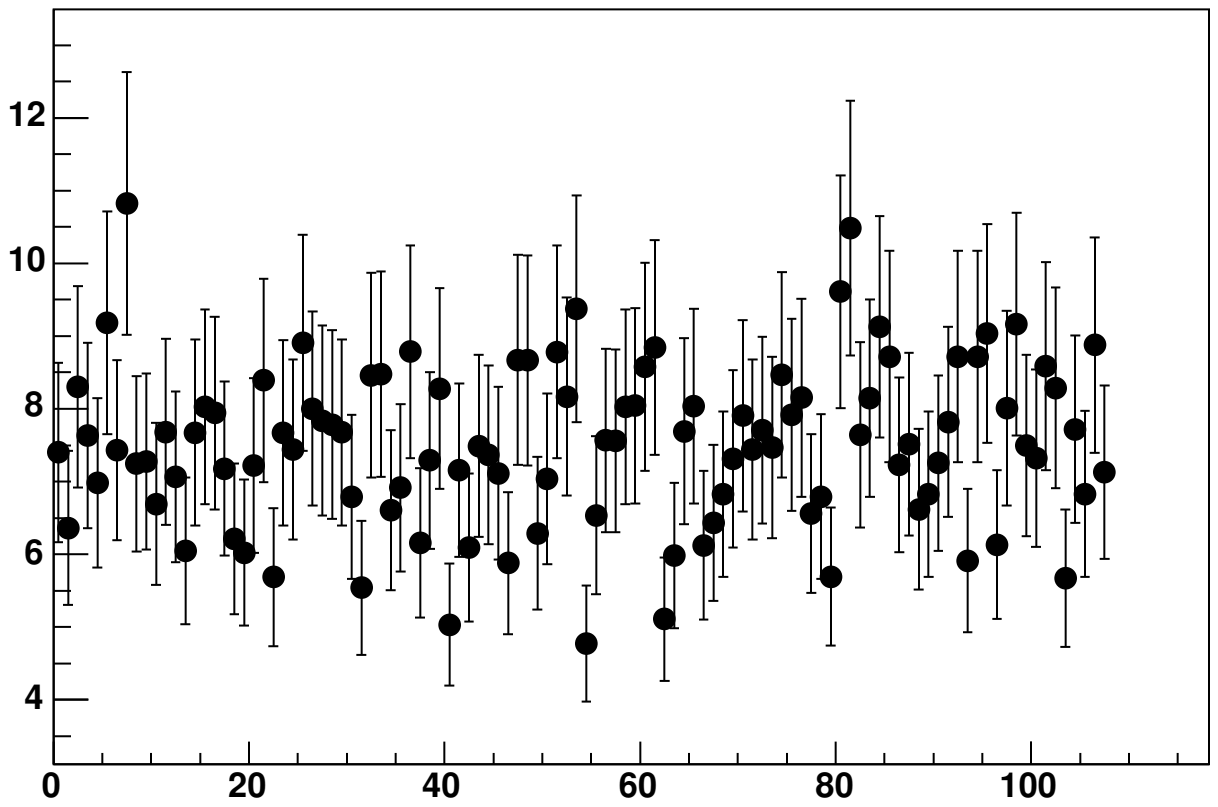
Enable 2, Hold=35, DAC=64000, ADC Noise vs 18*Chip+Chan



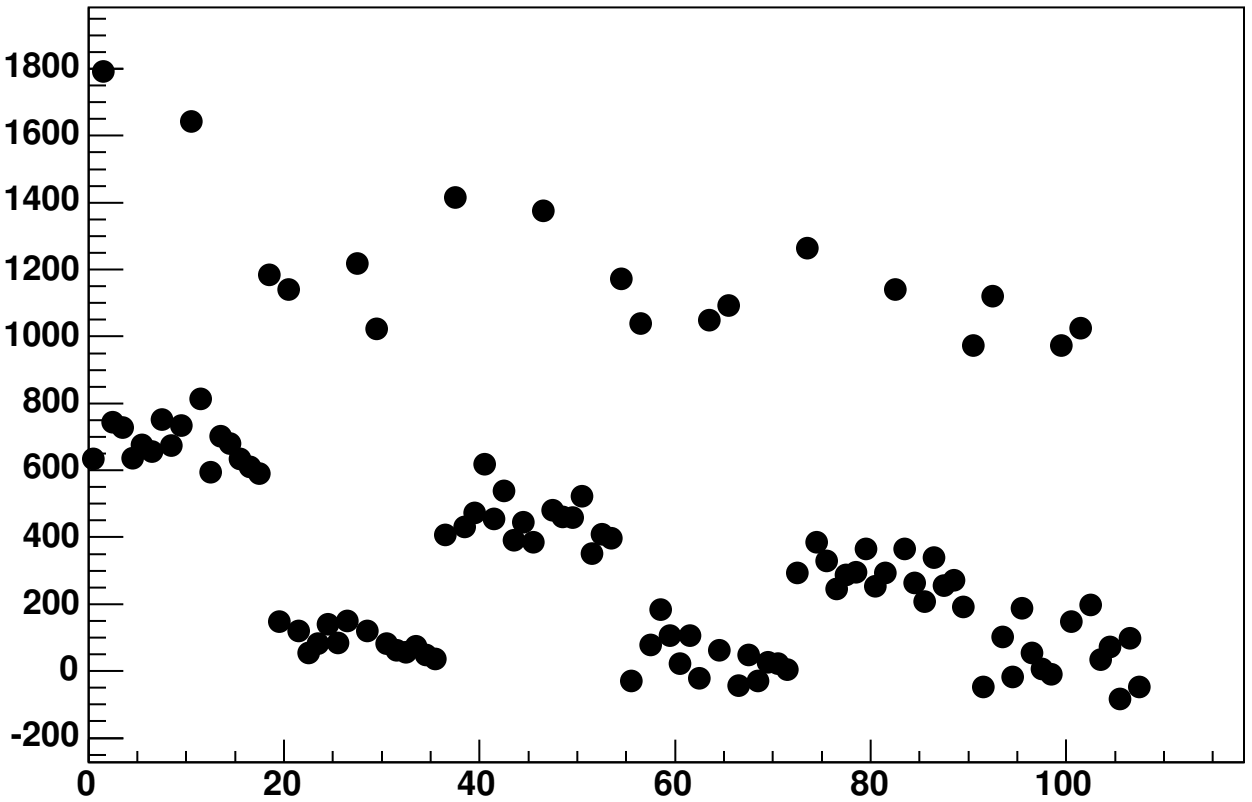
Enable 3, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



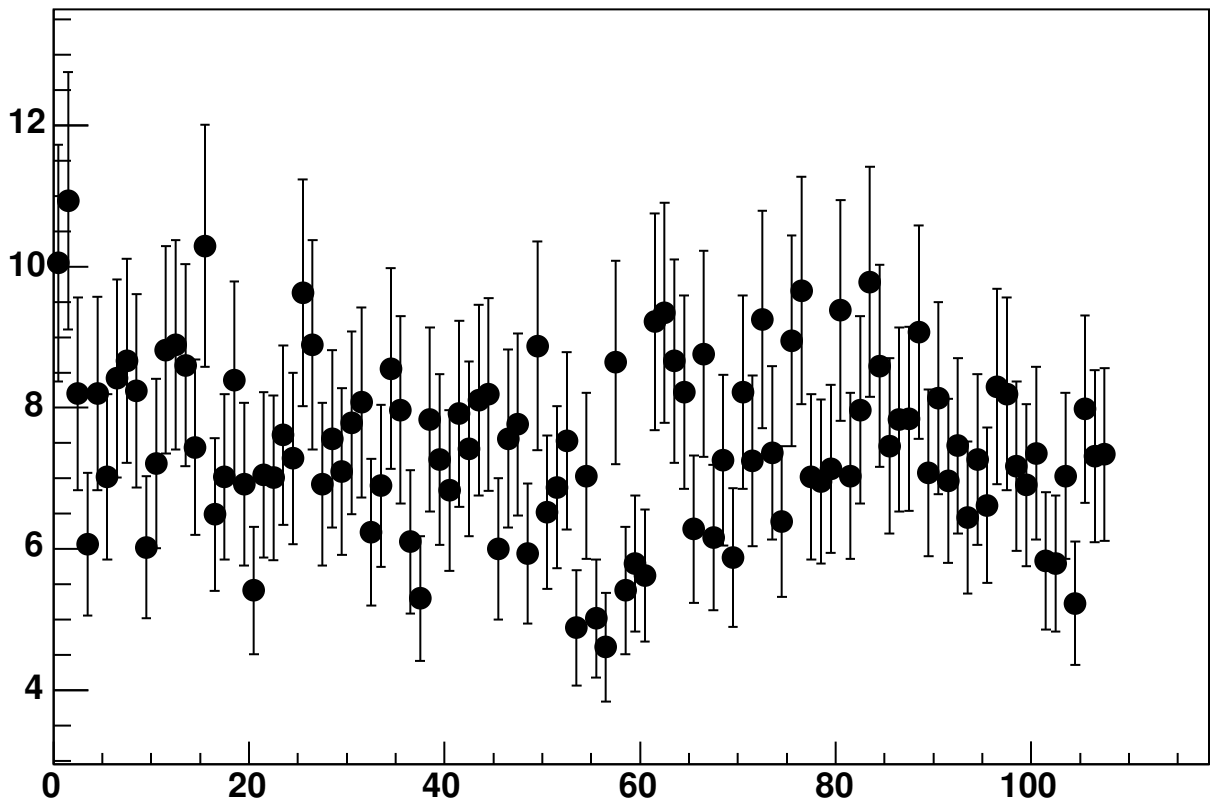
Enable 3, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



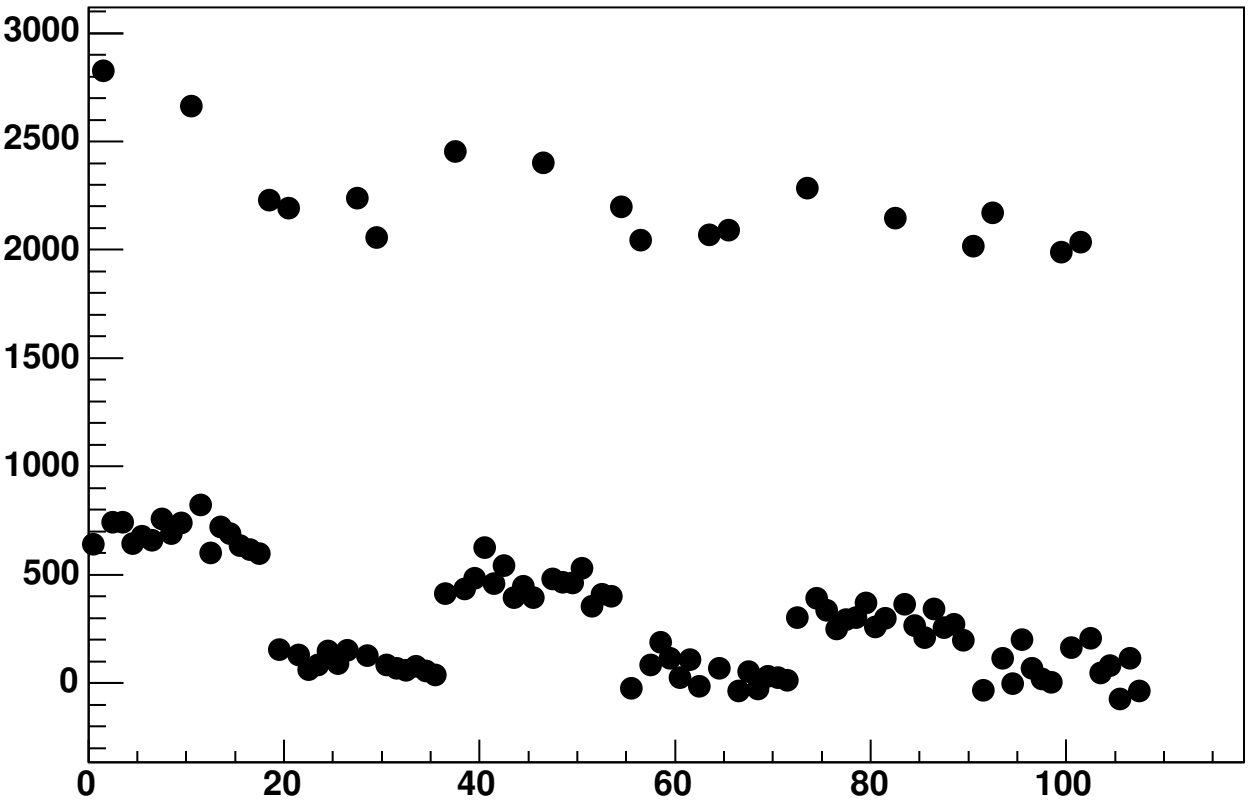
Enable 3, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



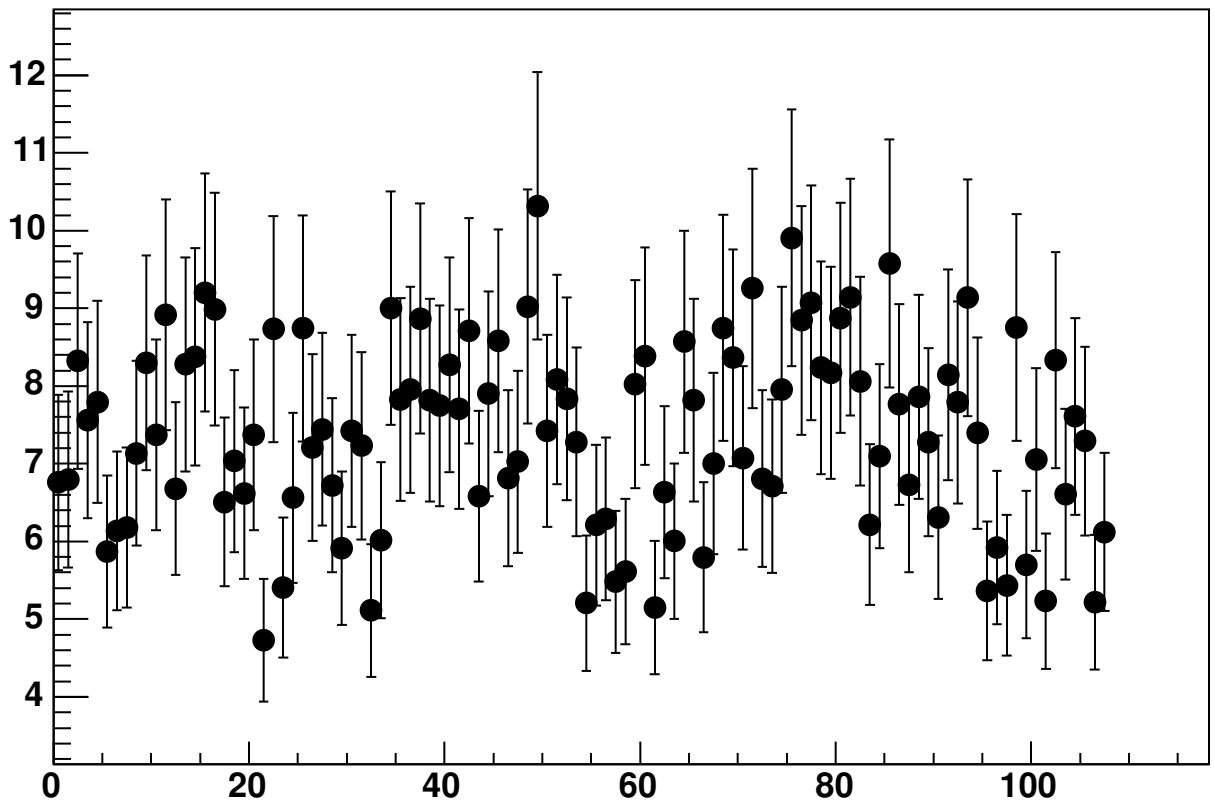
Enable 3, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



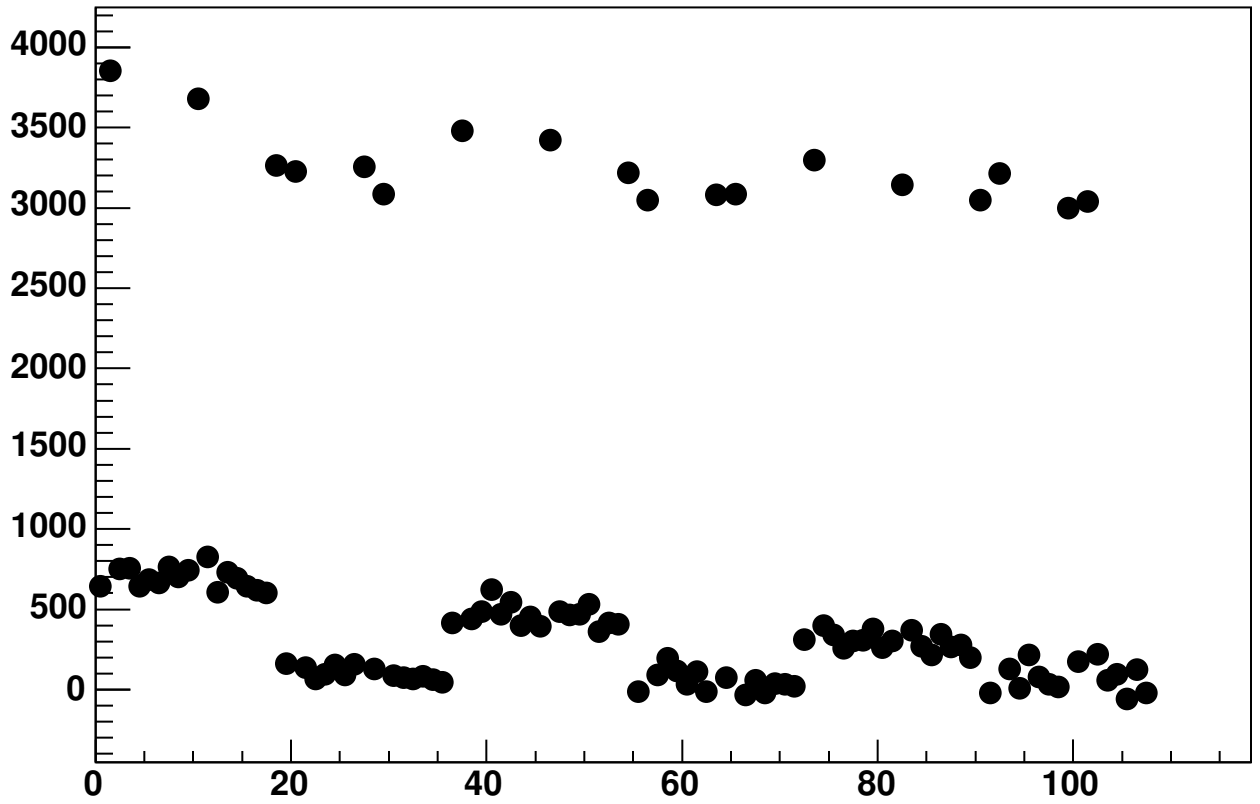
Enable 3, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



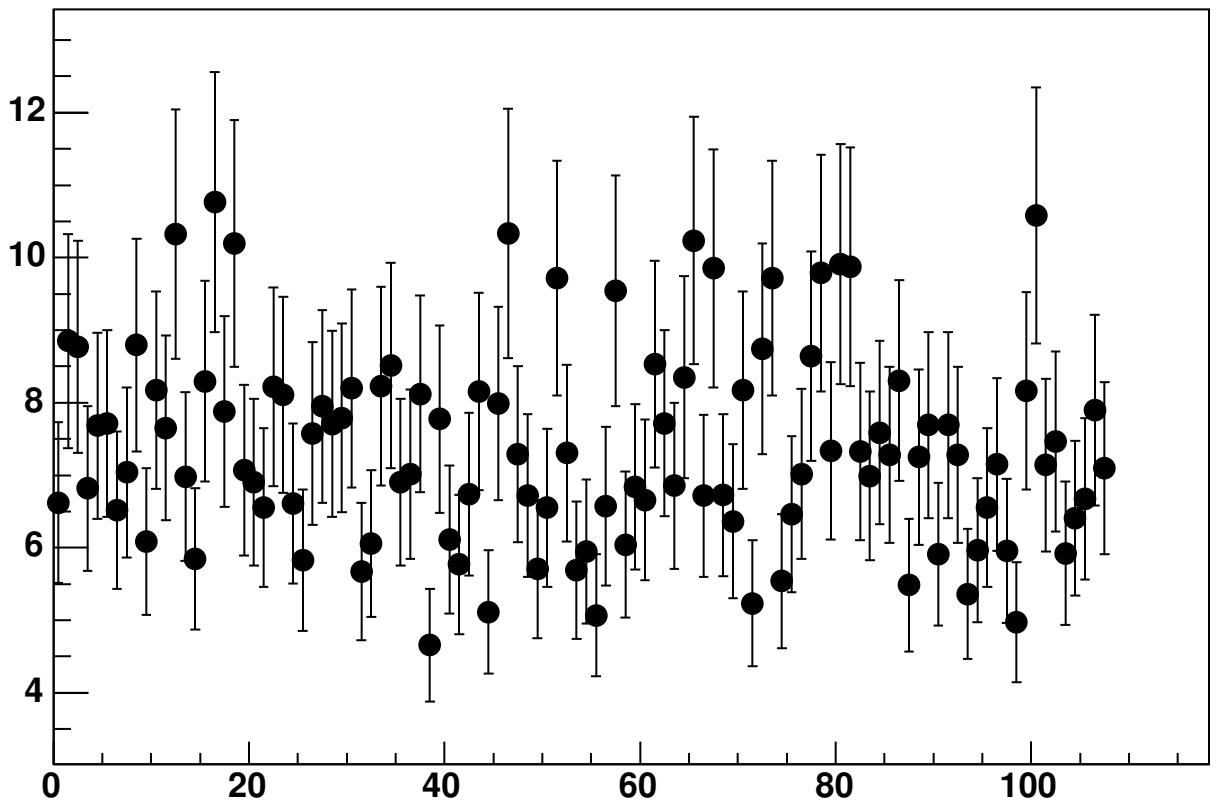
Enable 3, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



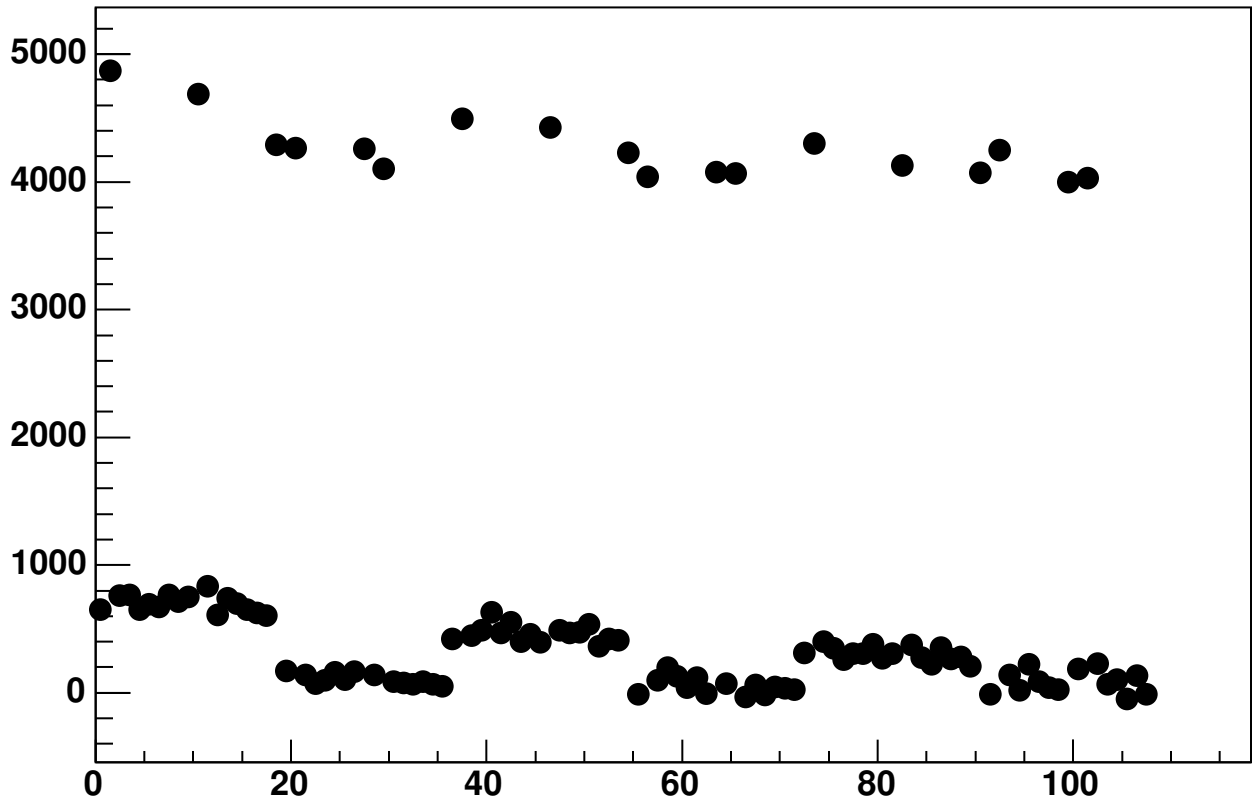
Enable 3, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



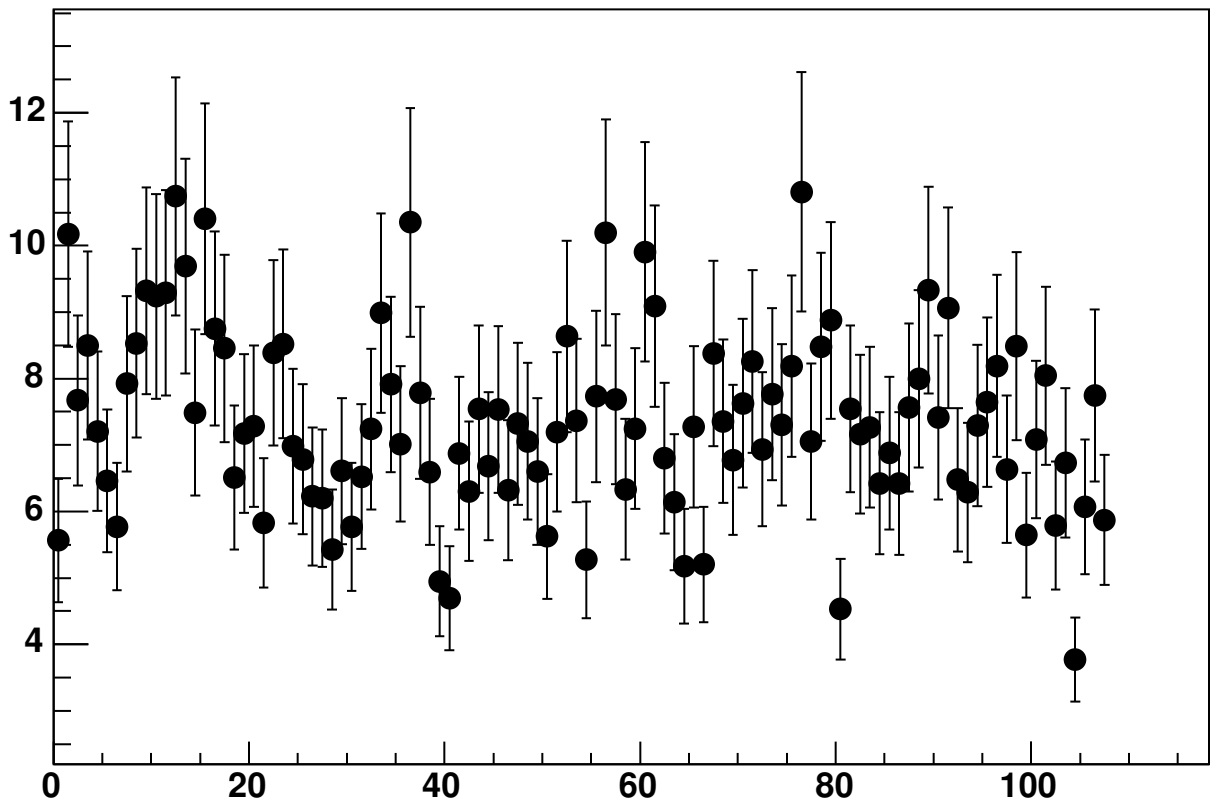
Enable 3, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



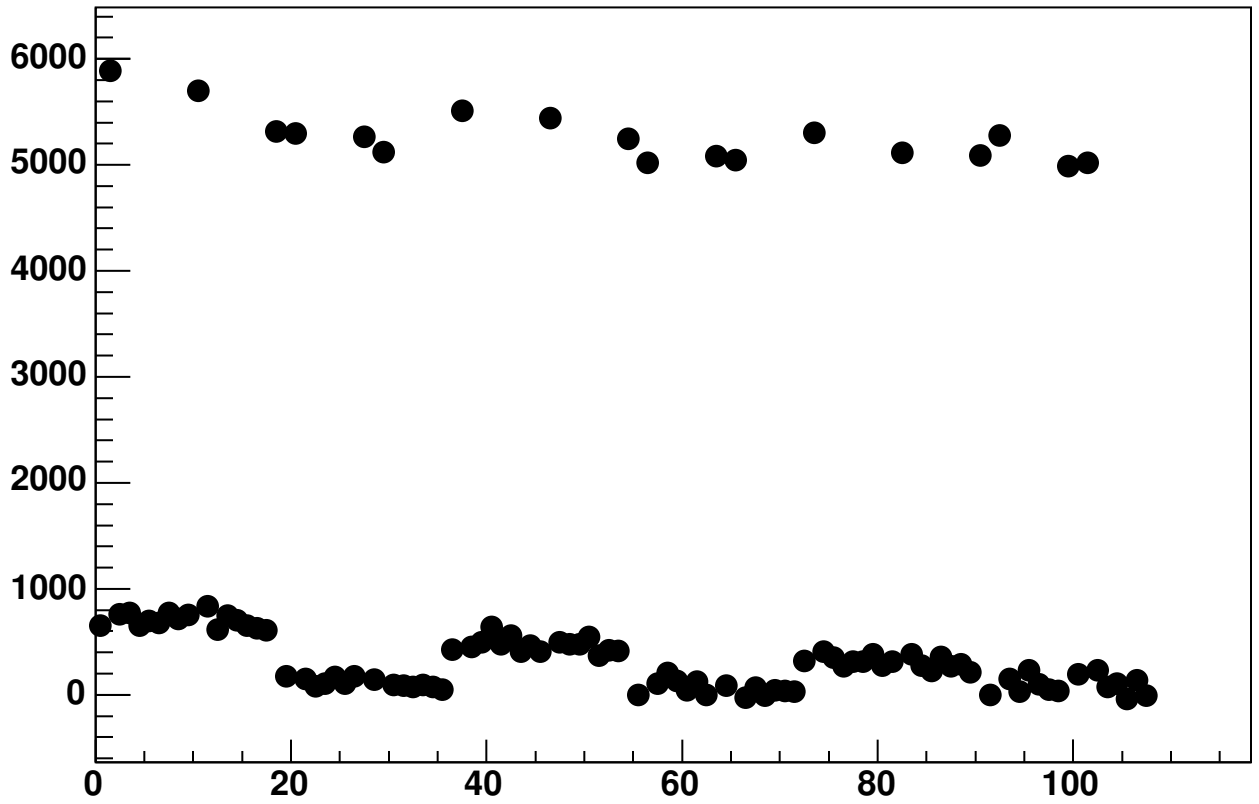
Enable 3, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



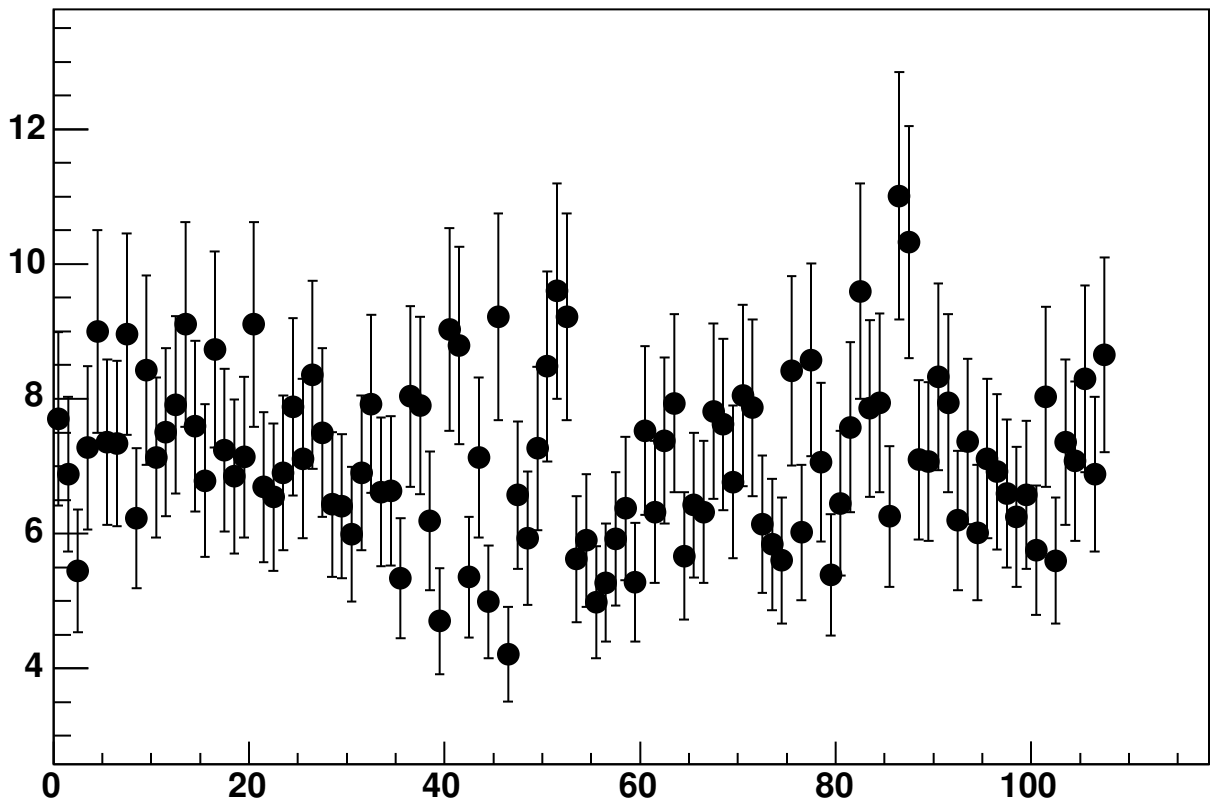
Enable 3, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



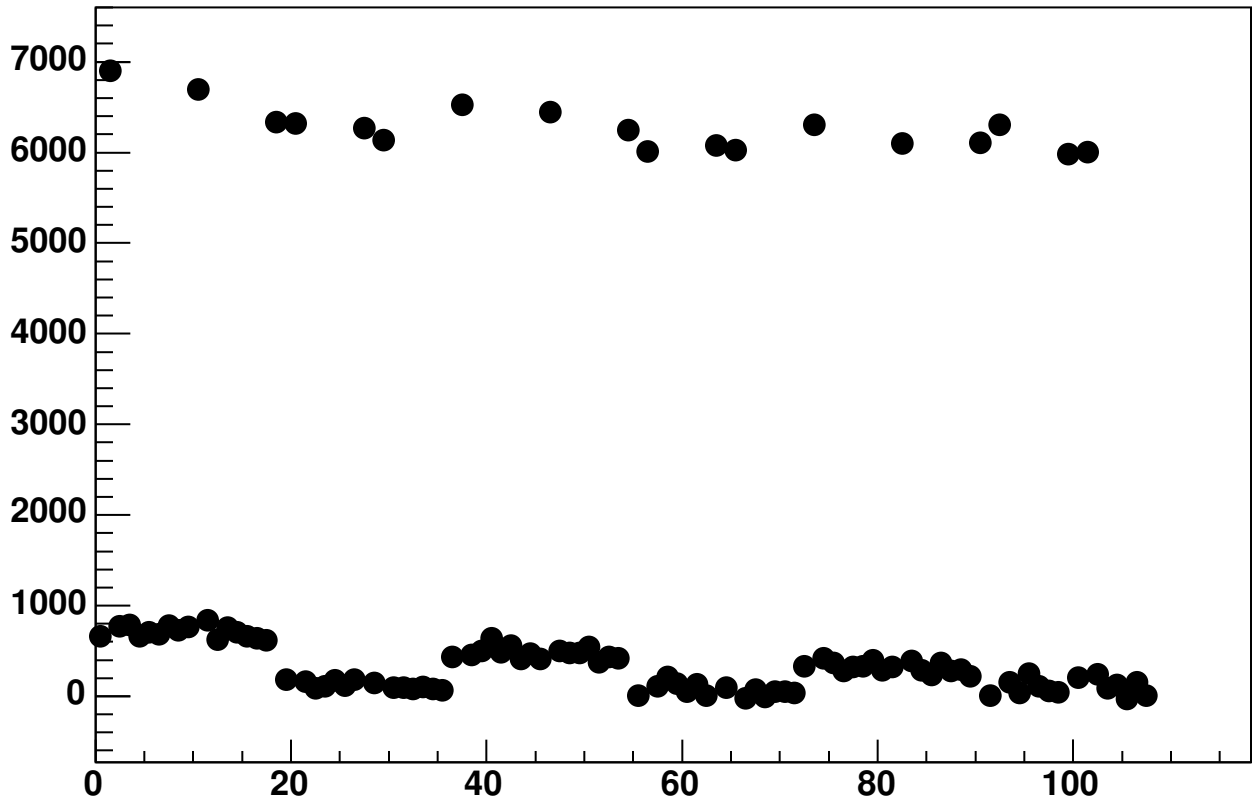
Enable 3, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



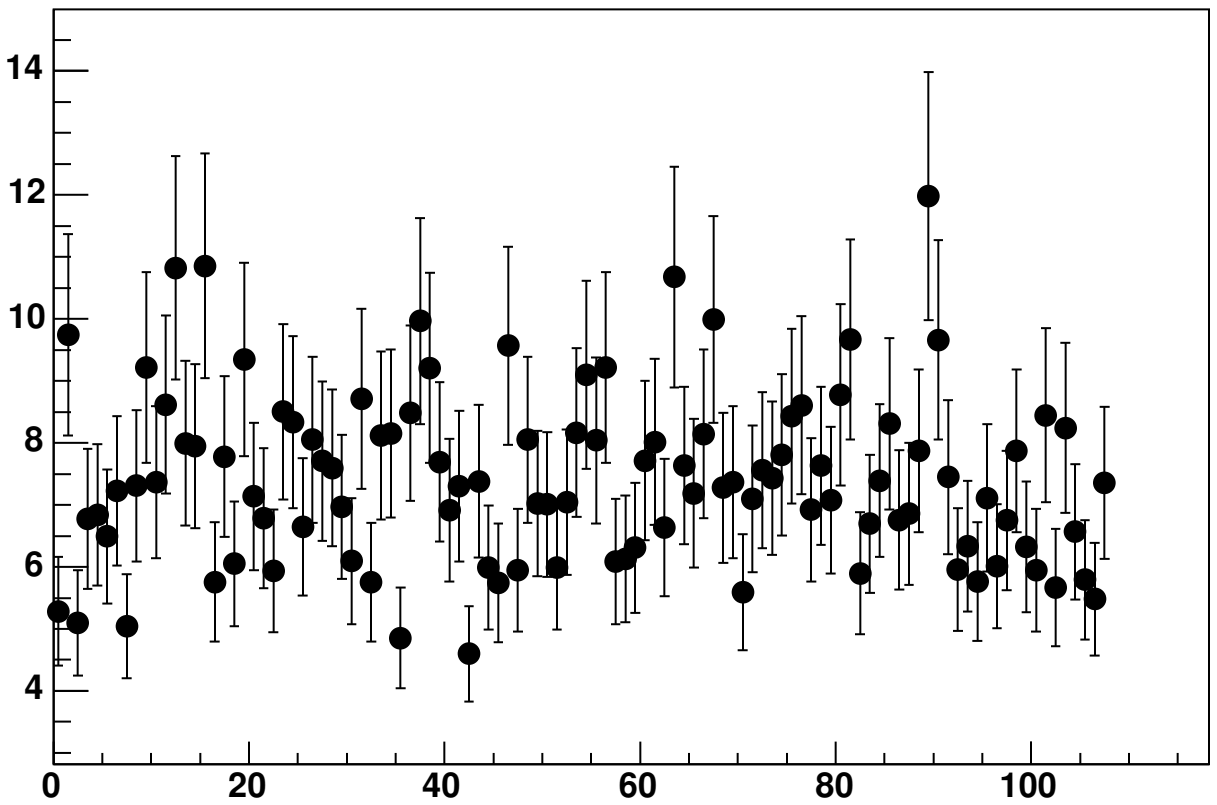
Enable 3, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



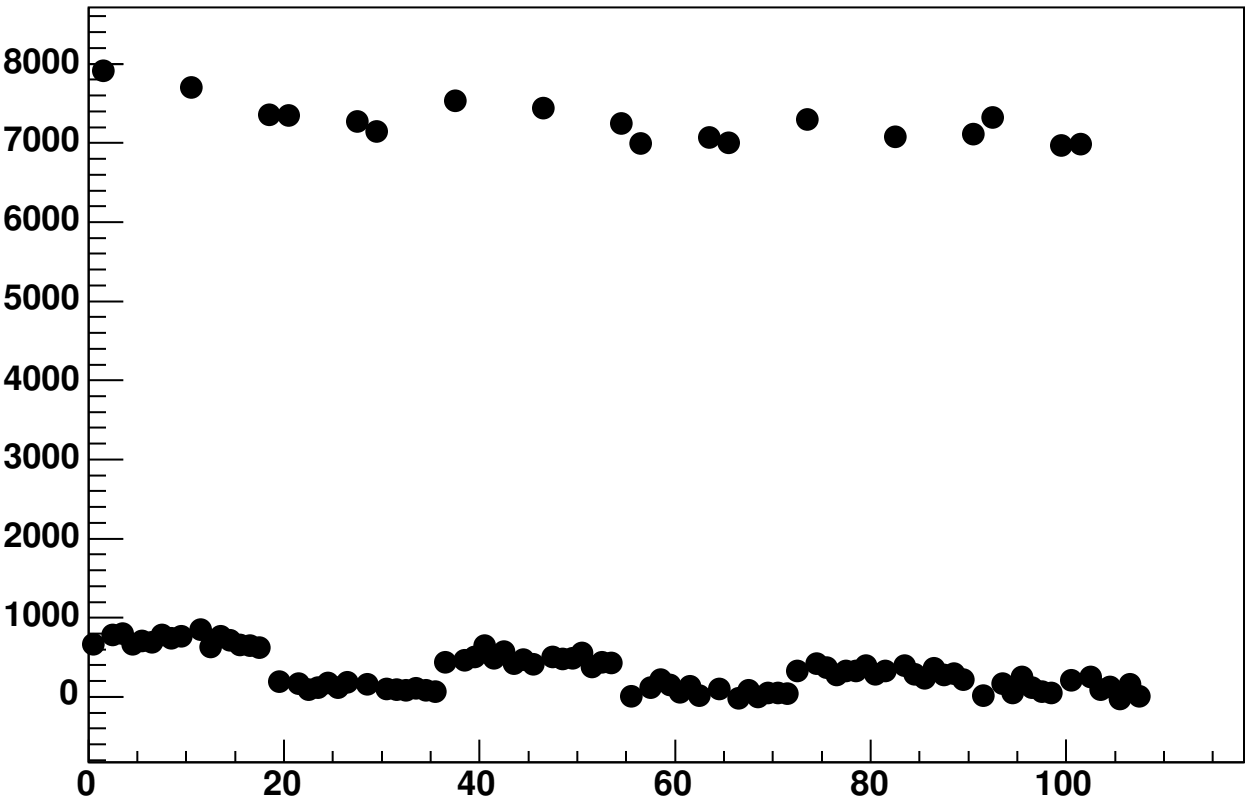
Enable 3, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



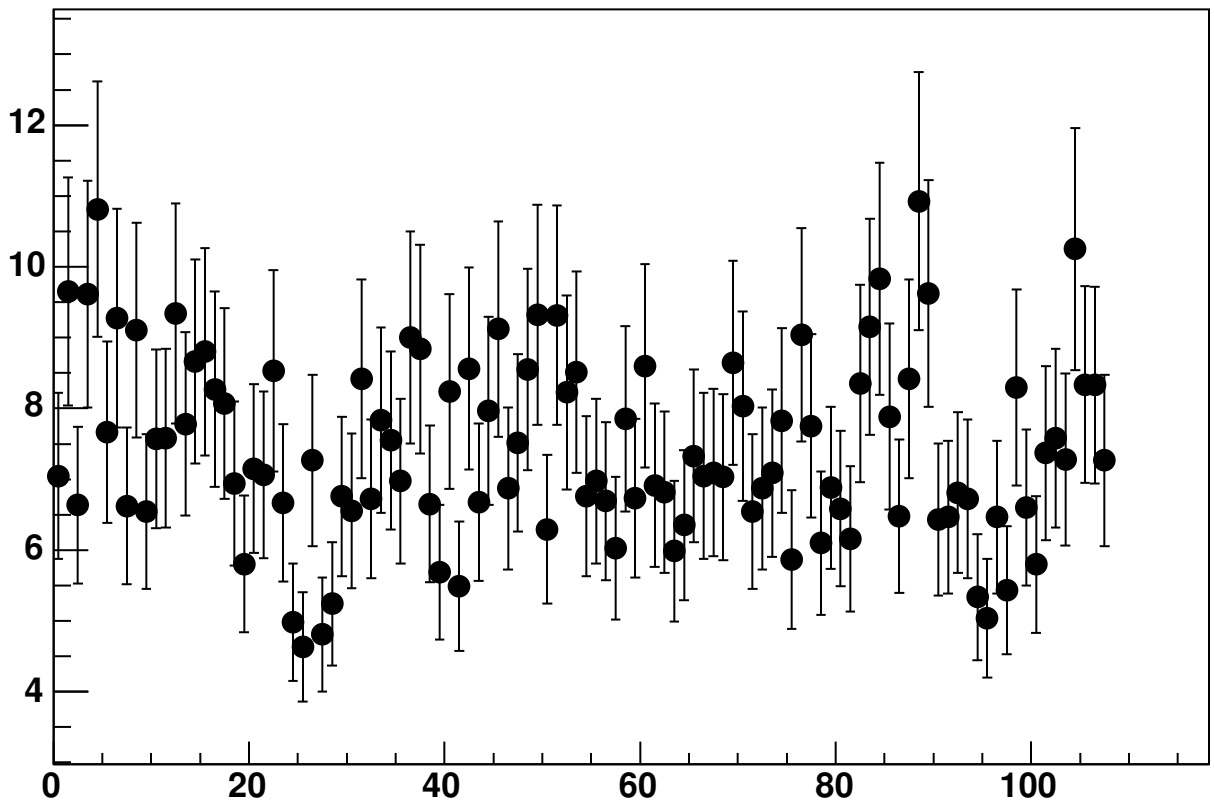
Enable 3, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



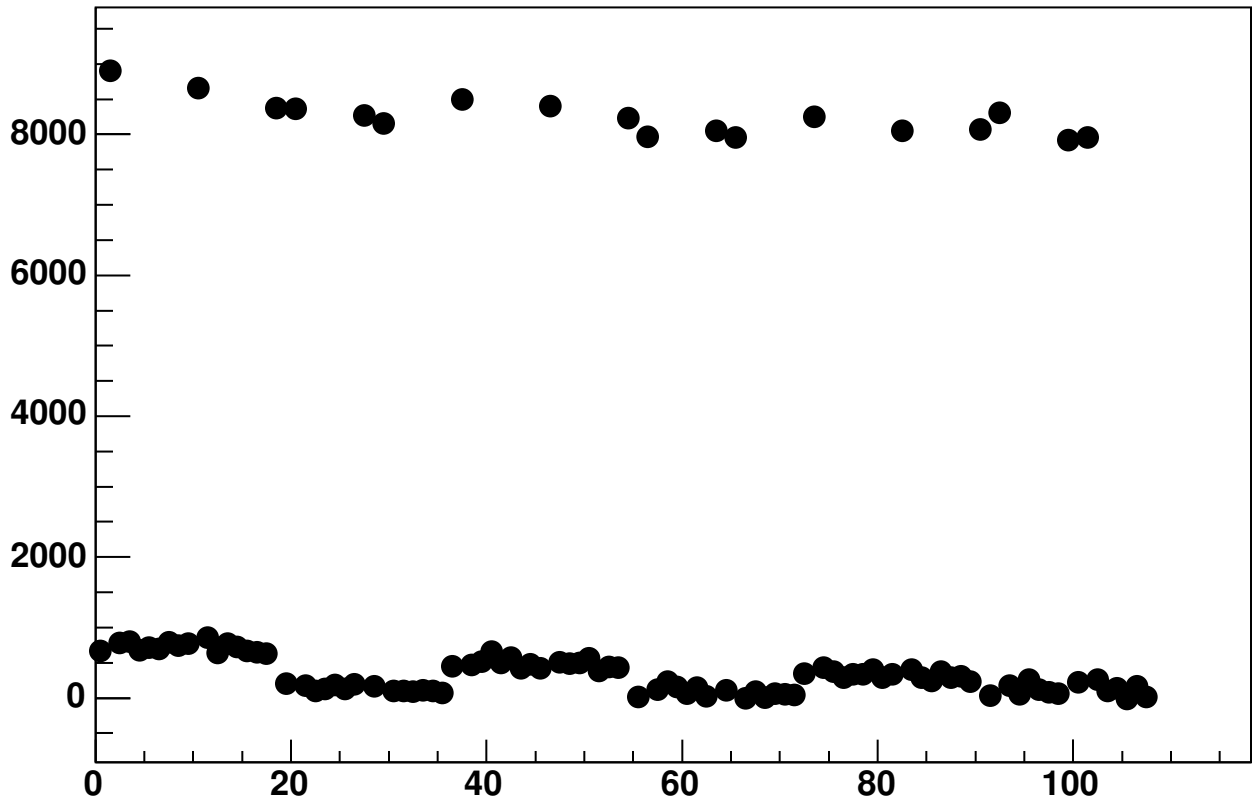
Enable 3, Hold=35, DAC=14000, ADC Mean vs 18*Chip+Chan



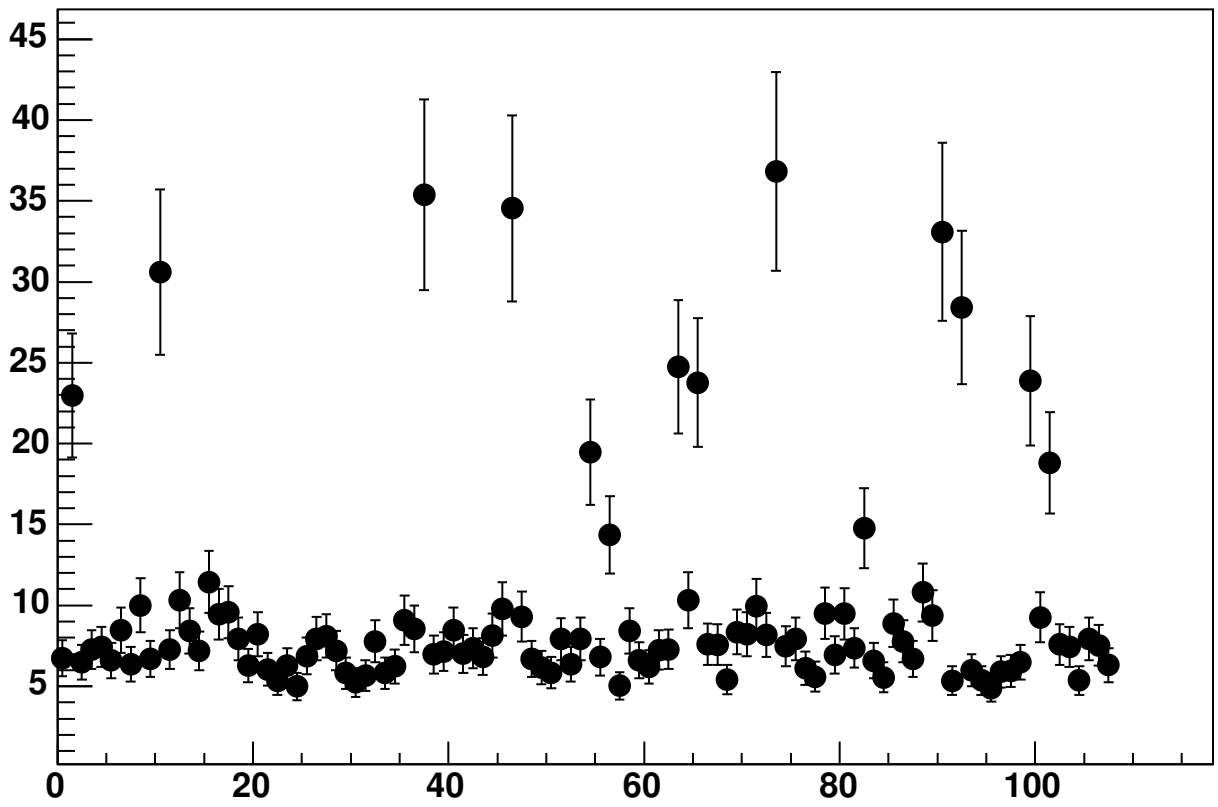
Enable 3, Hold=35, DAC=14000, ADC Noise vs 18*Chip+Chan



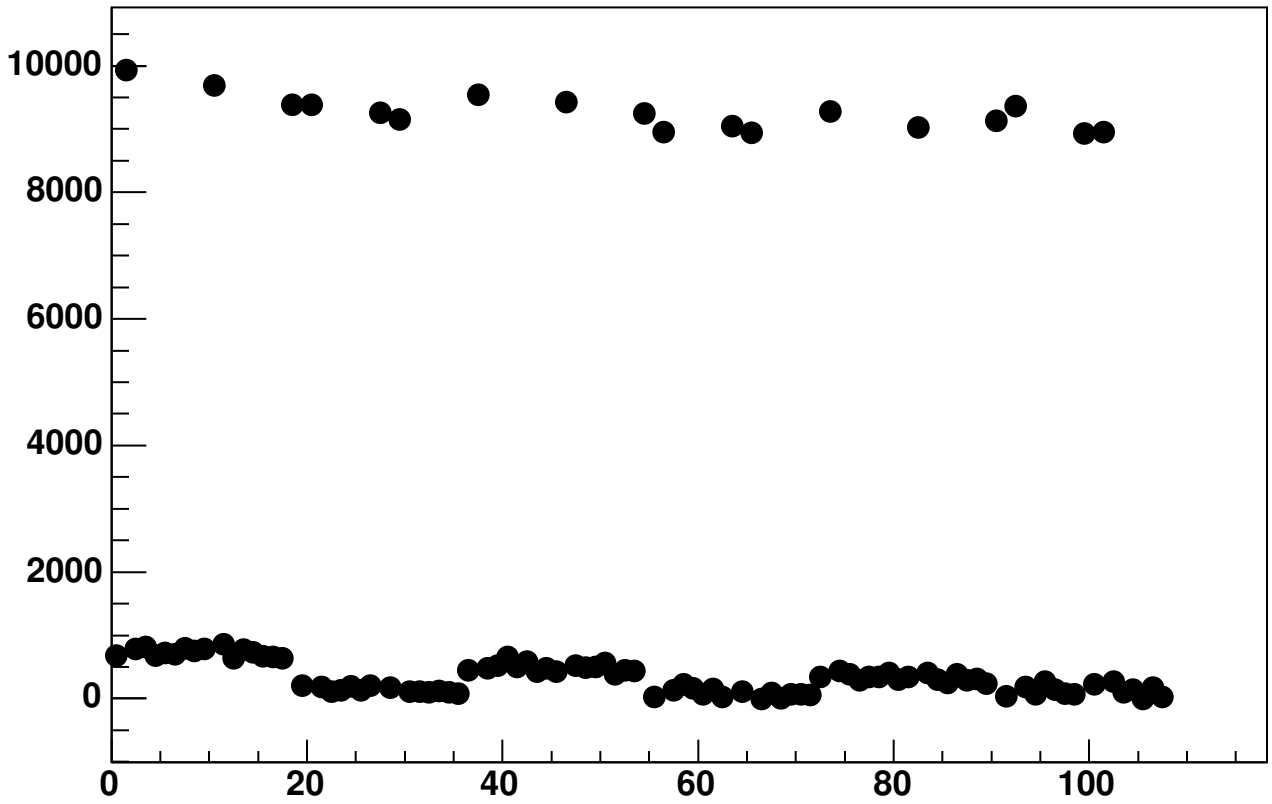
Enable 3, Hold=35, DAC=16000, ADC Mean vs 18*Chip+Chan



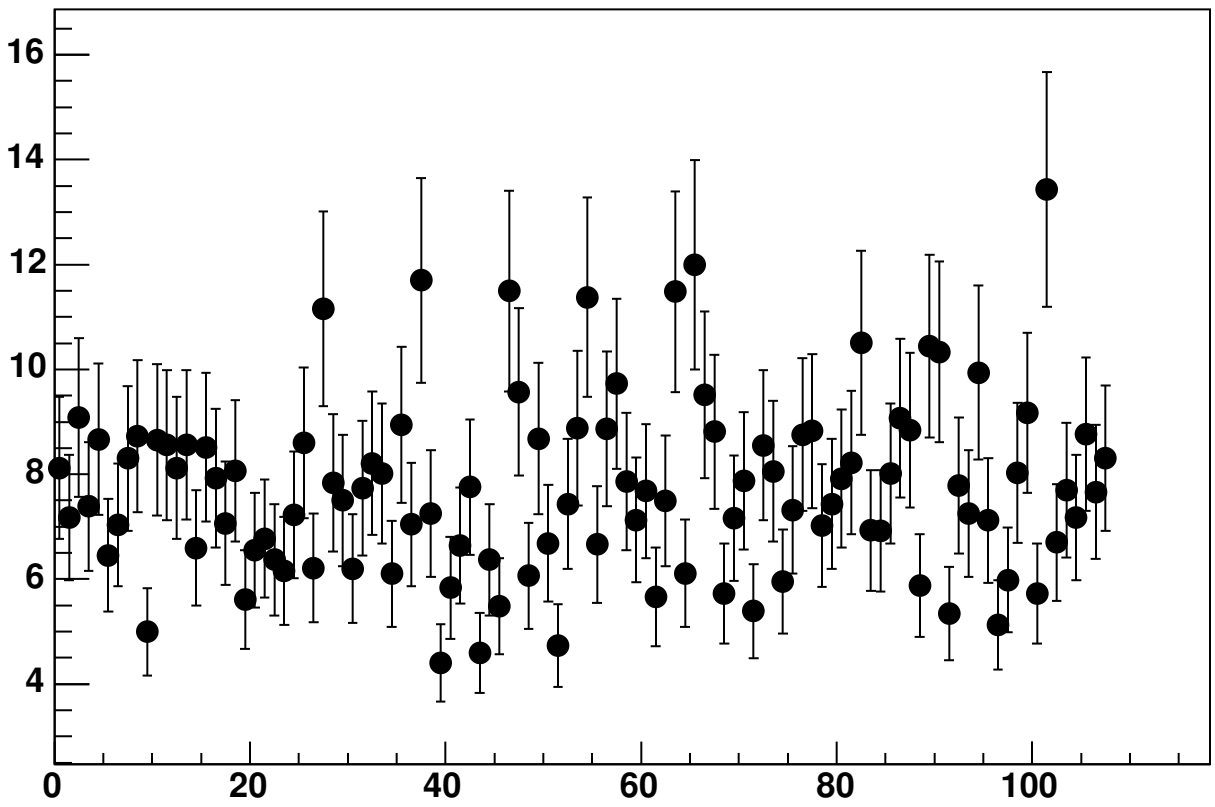
Enable 3, Hold=35, DAC=16000, ADC Noise vs 18*Chip+Chan



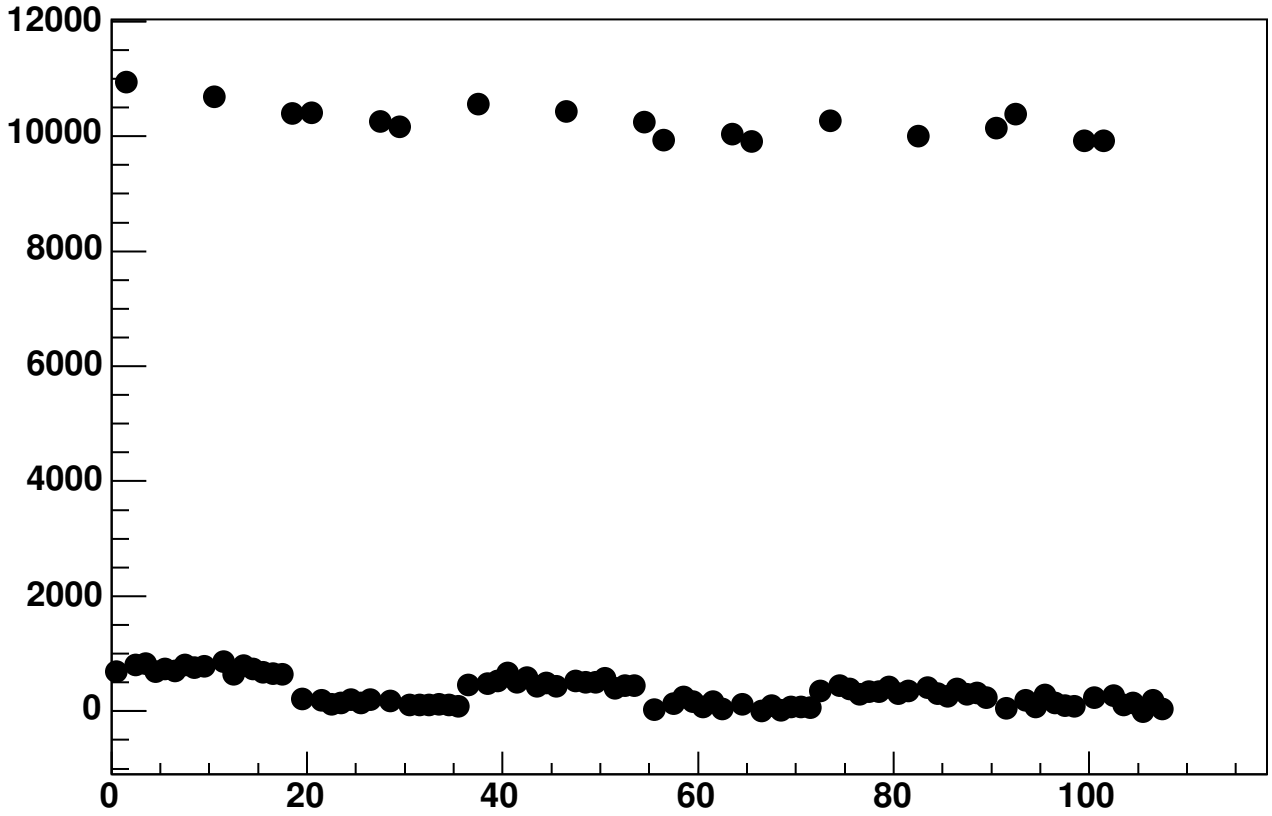
Enable 3, Hold=35, DAC=18000, ADC Mean vs 18*Chip+Chan



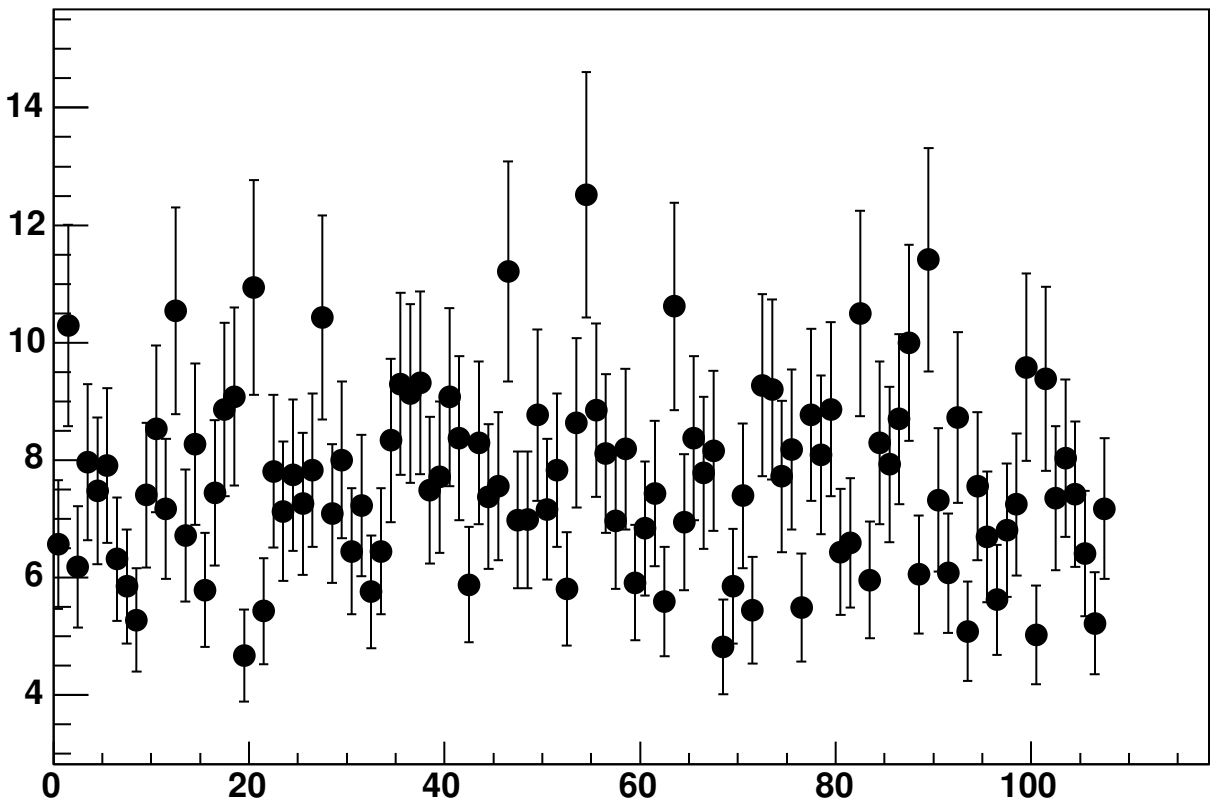
Enable 3, Hold=35, DAC=18000, ADC Noise vs 18*Chip+Chan



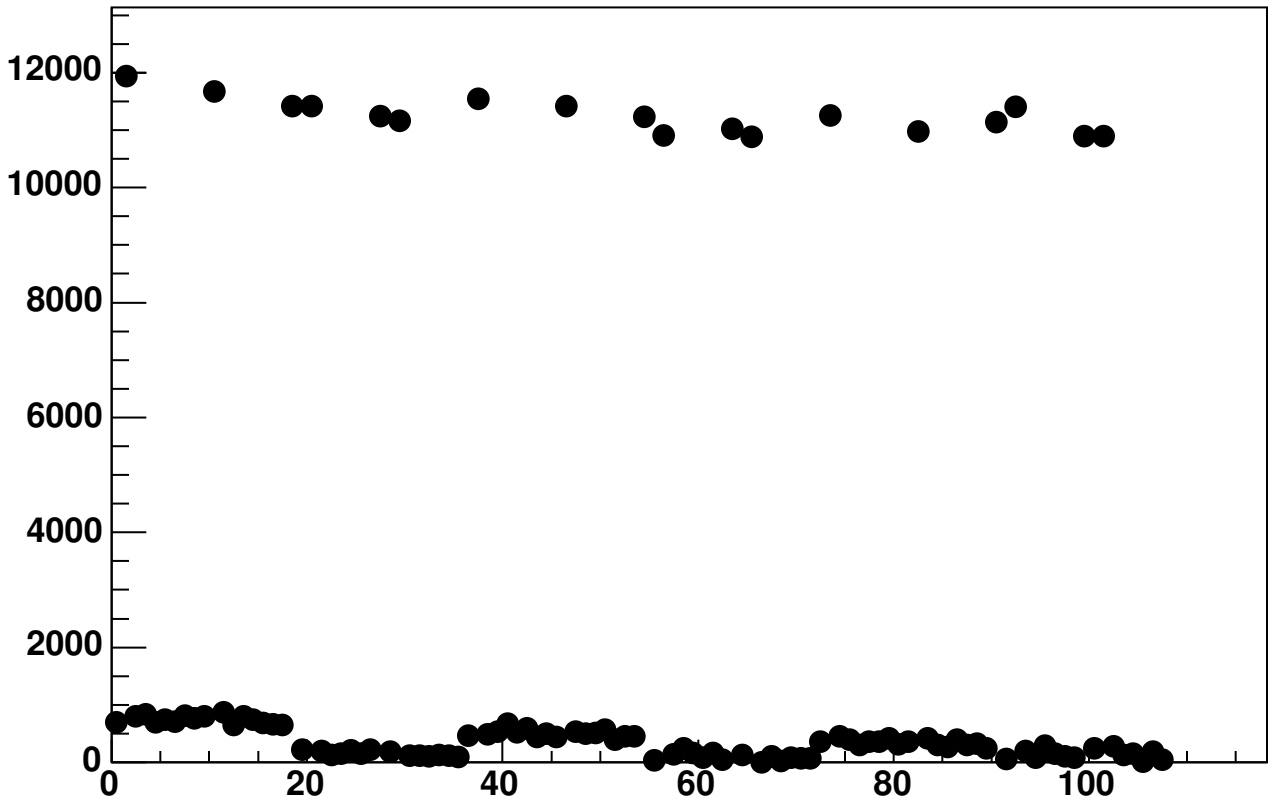
Enable 3, Hold=35, DAC=20000, ADC Mean vs 18*Chip+Chan



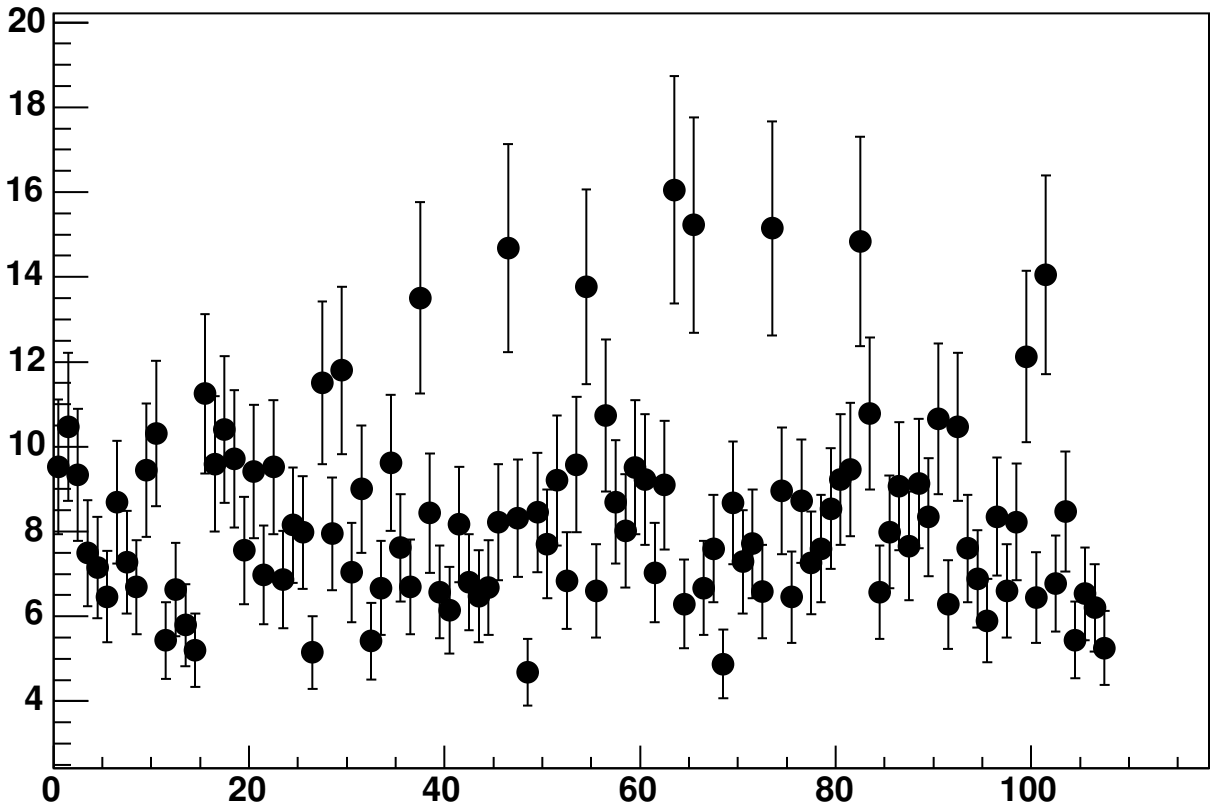
Enable 3, Hold=35, DAC=20000, ADC Noise vs 18*Chip+Chan



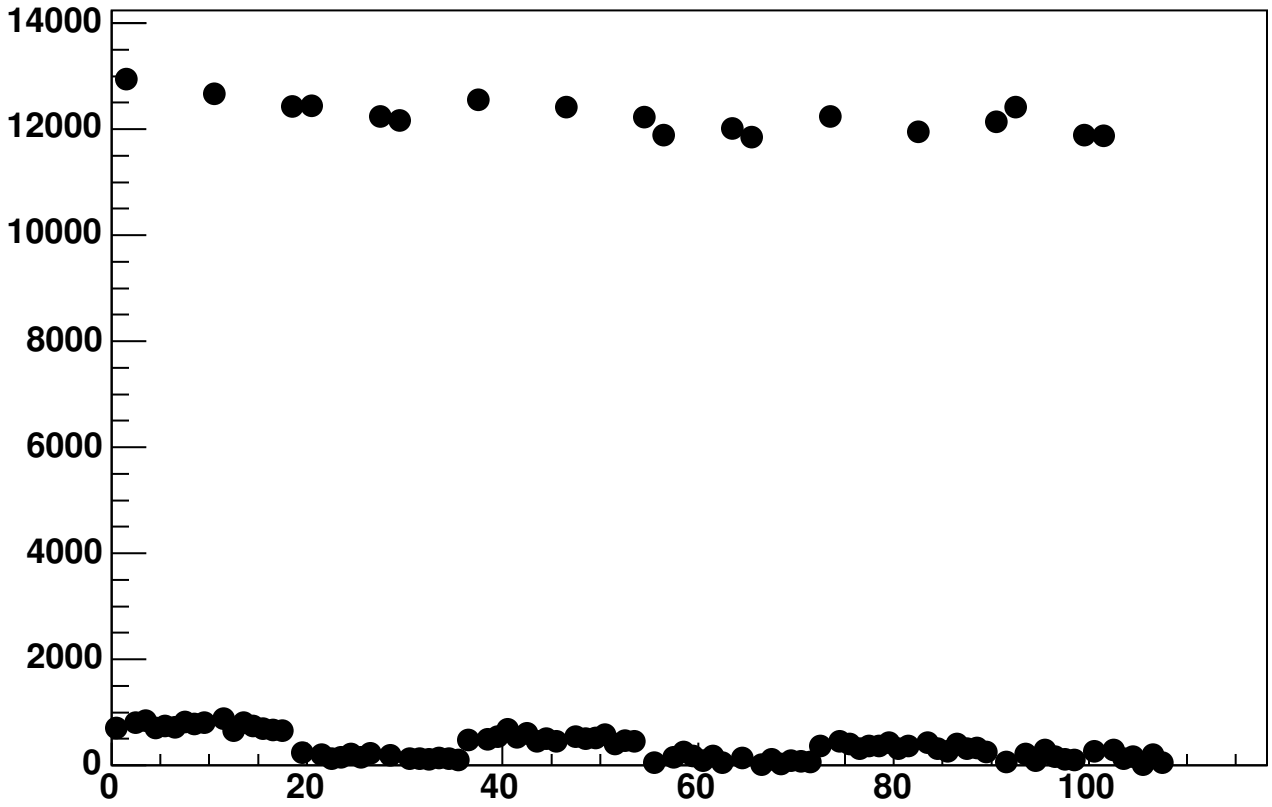
Enable 3, Hold=35, DAC=22000, ADC Mean vs 18*Chip+Chan



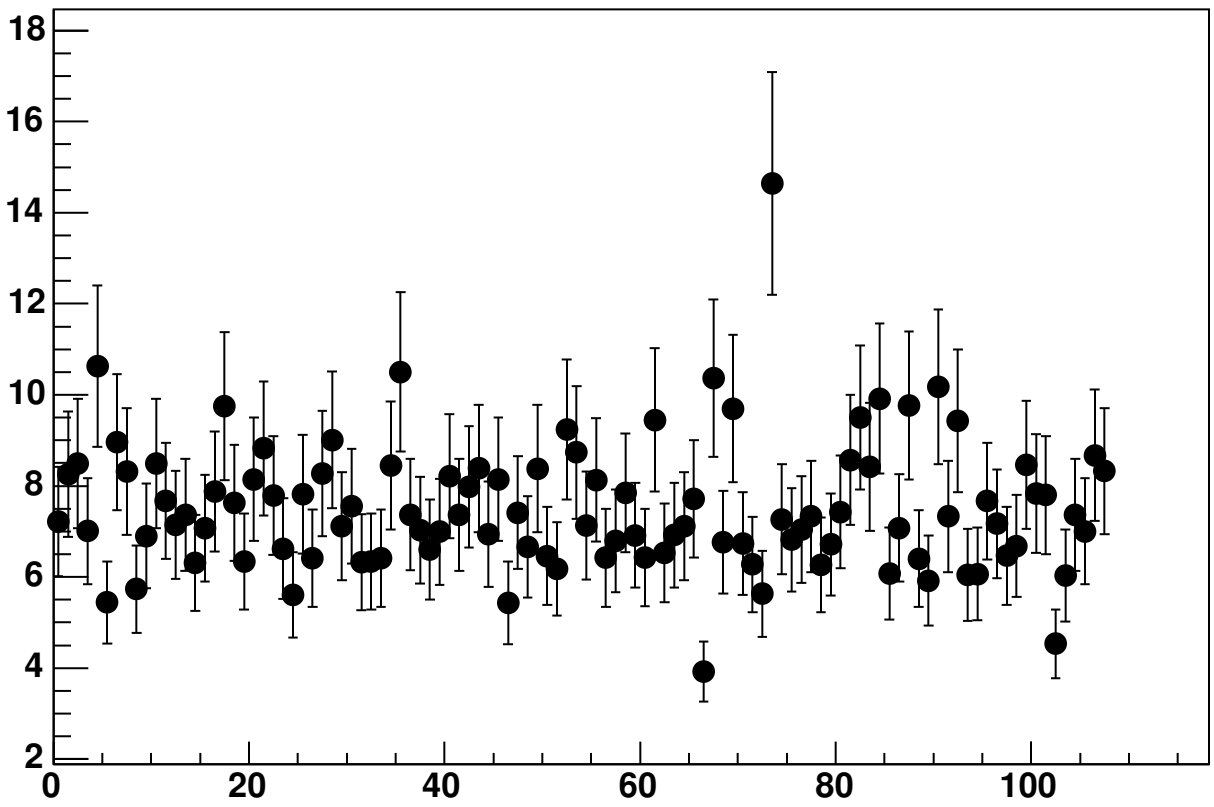
Enable 3, Hold=35, DAC=22000, ADC Noise vs 18*Chip+Chan



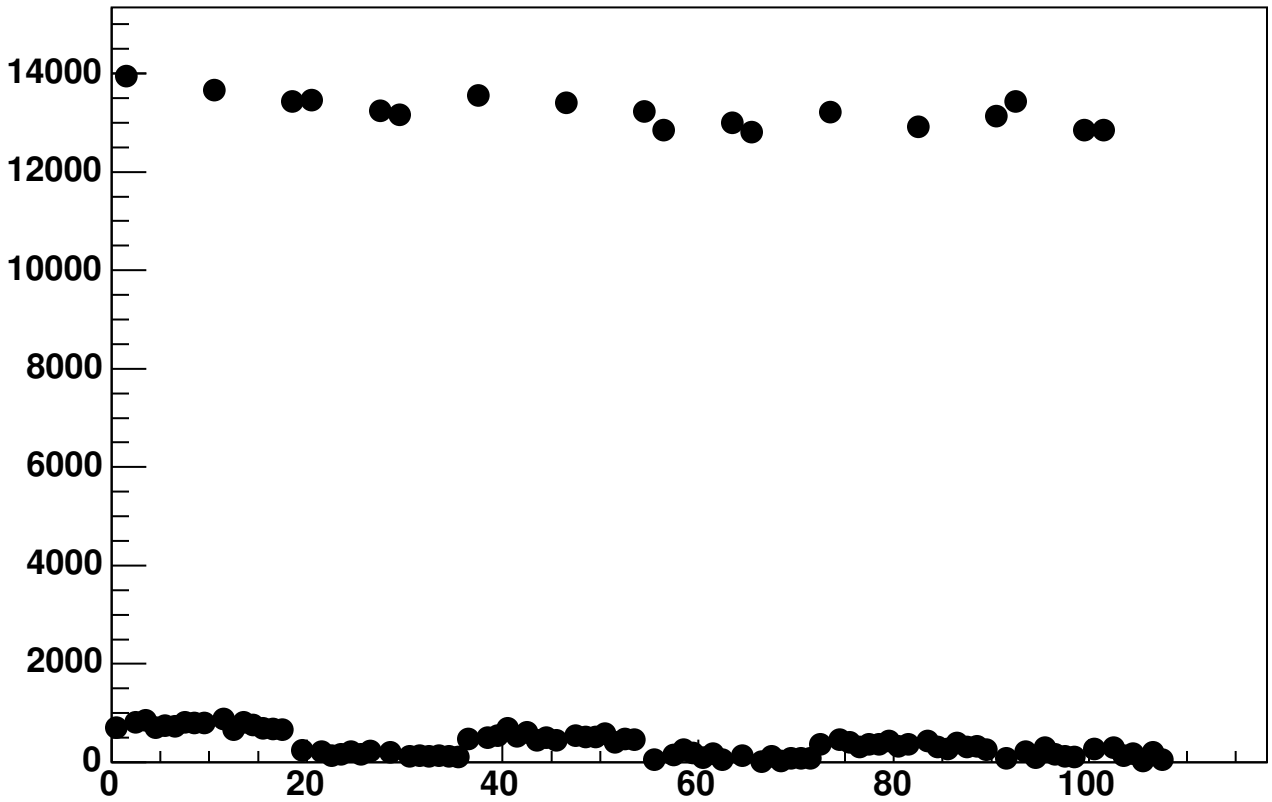
Enable 3, Hold=35, DAC=24000, ADC Mean vs 18*Chip+Chan



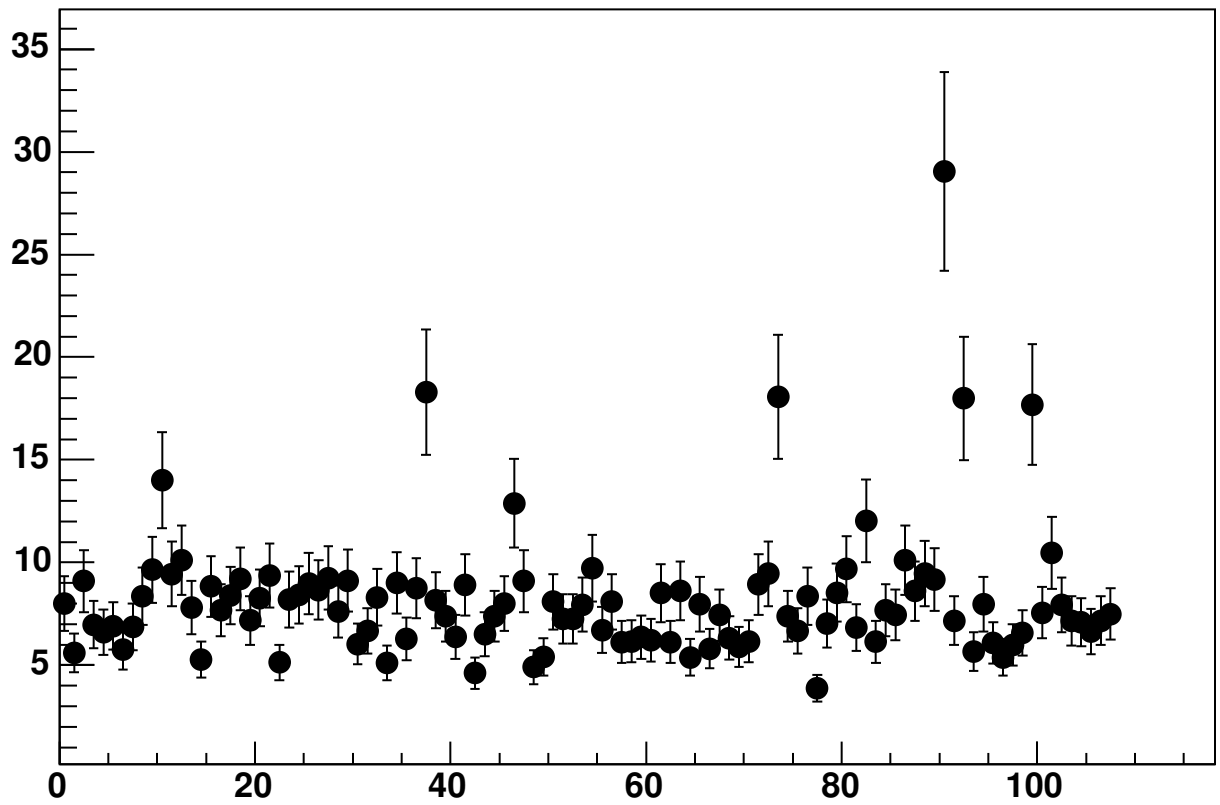
Enable 3, Hold=35, DAC=24000, ADC Noise vs 18*Chip+Chan



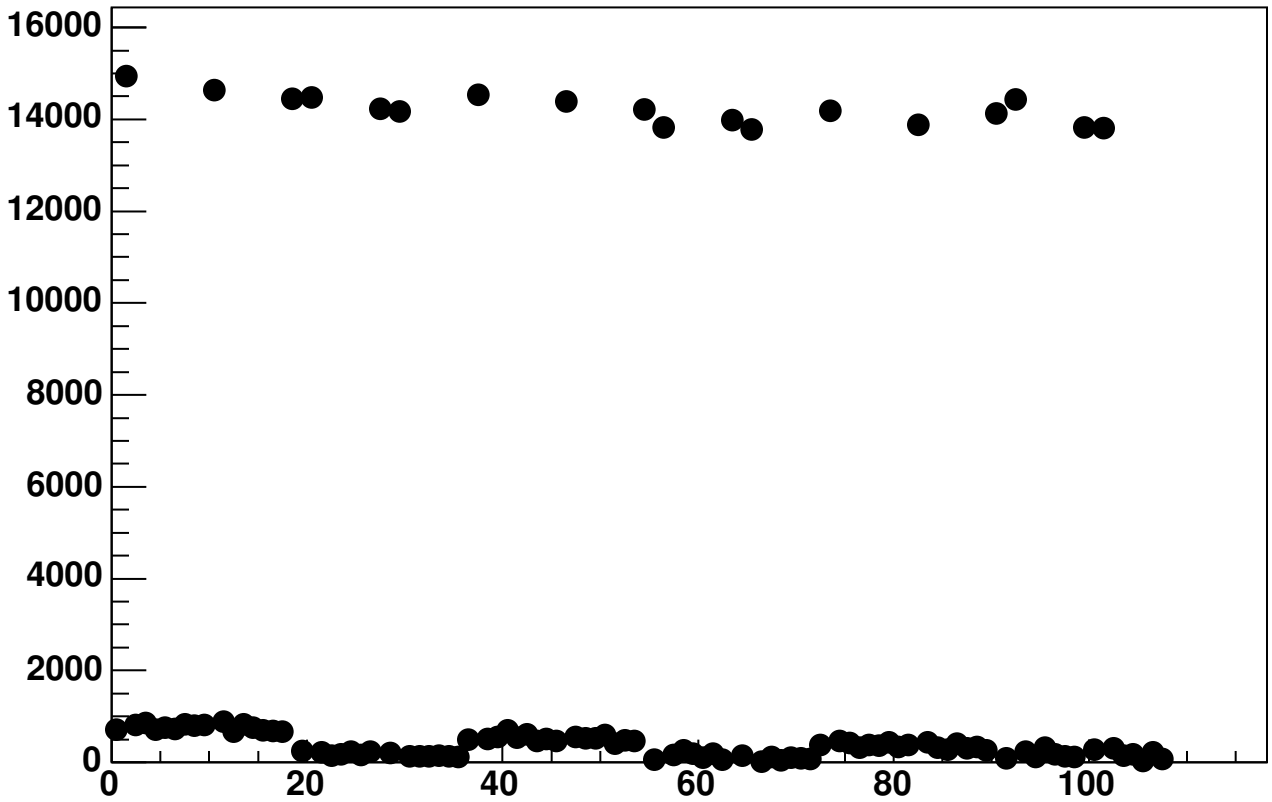
Enable 3, Hold=35, DAC=26000, ADC Mean vs 18*Chip+Chan



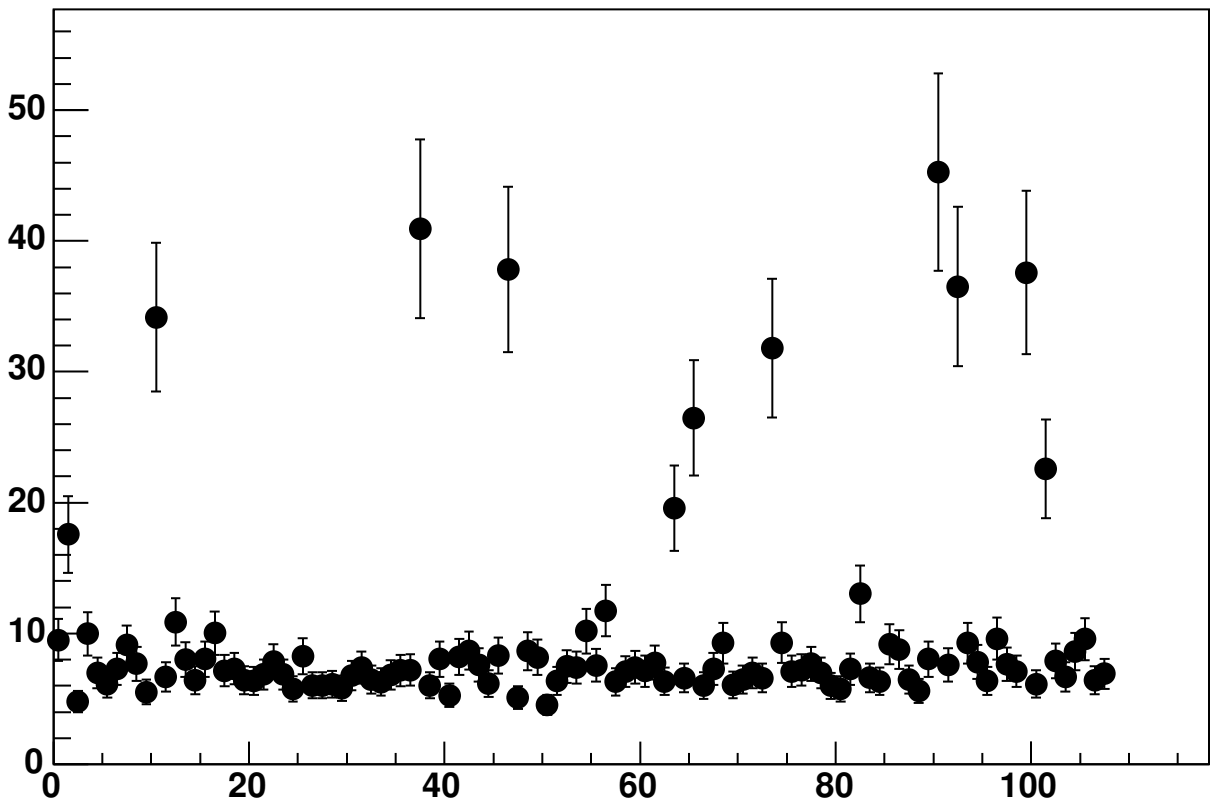
Enable 3, Hold=35, DAC=26000, ADC Noise vs 18*Chip+Chan



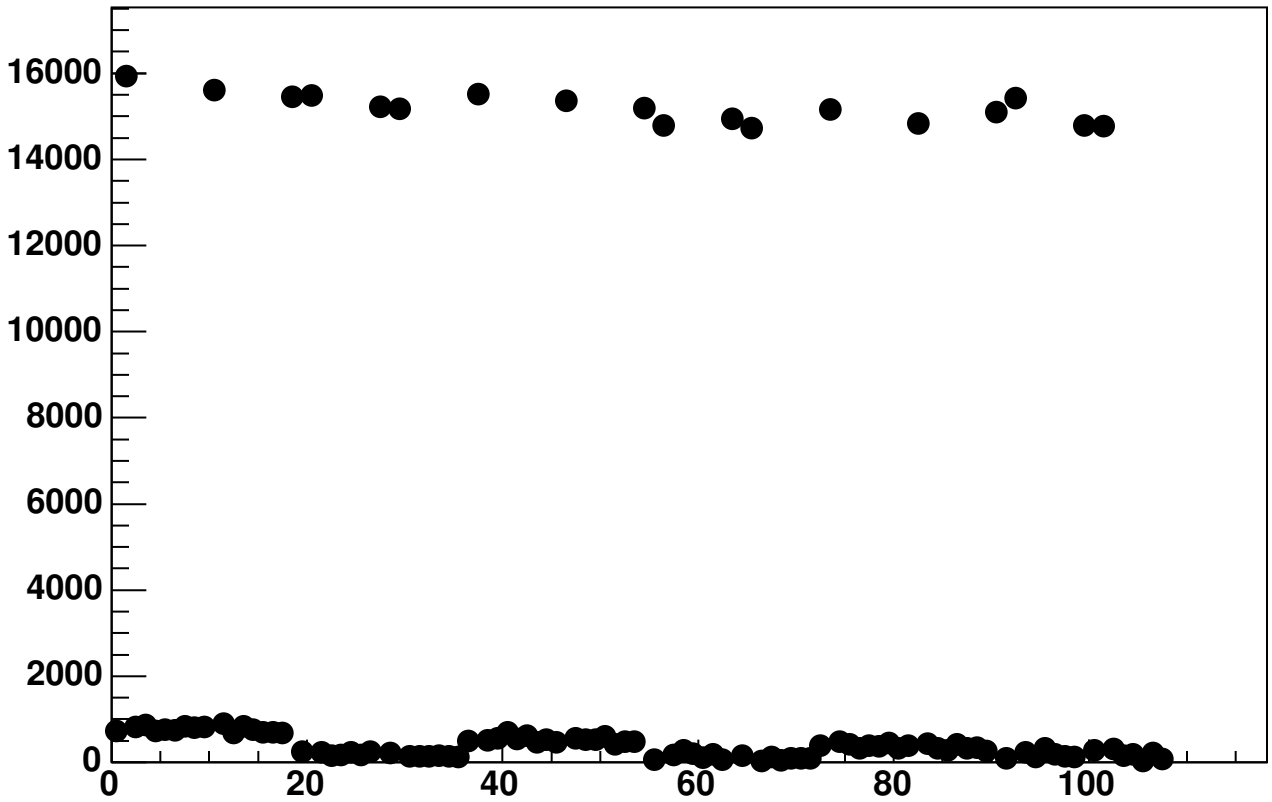
Enable 3, Hold=35, DAC=28000, ADC Mean vs 18*Chip+Chan



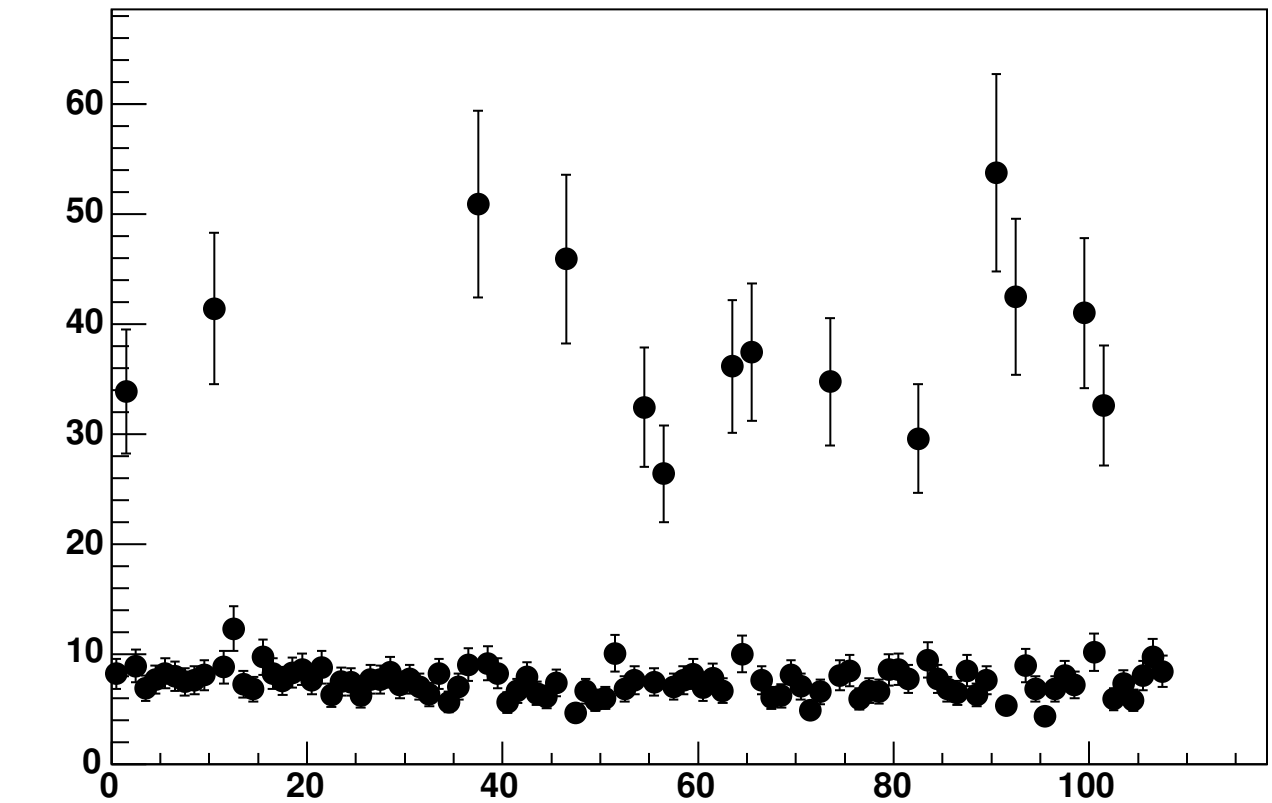
Enable 3, Hold=35, DAC=28000, ADC Noise vs 18*Chip+Chan



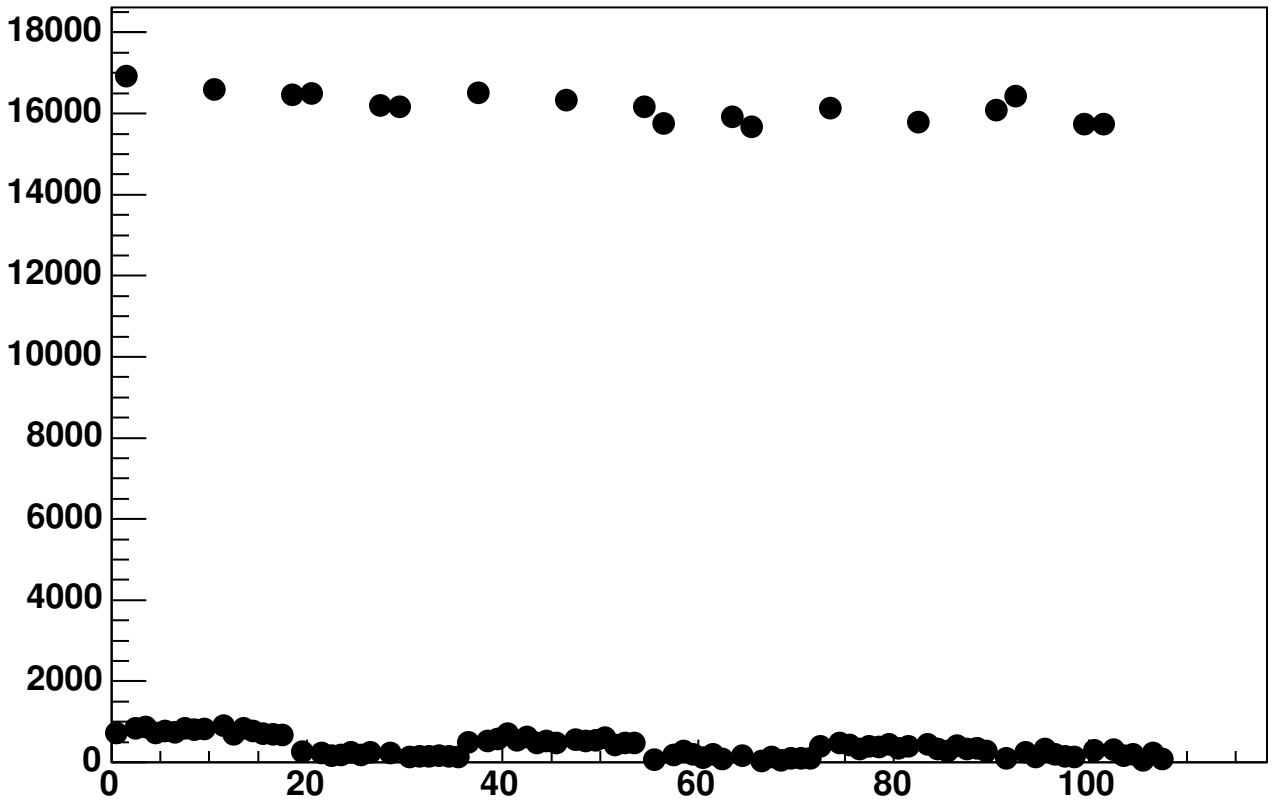
Enable 3, Hold=35, DAC=30000, ADC Mean vs 18*Chip+Chan



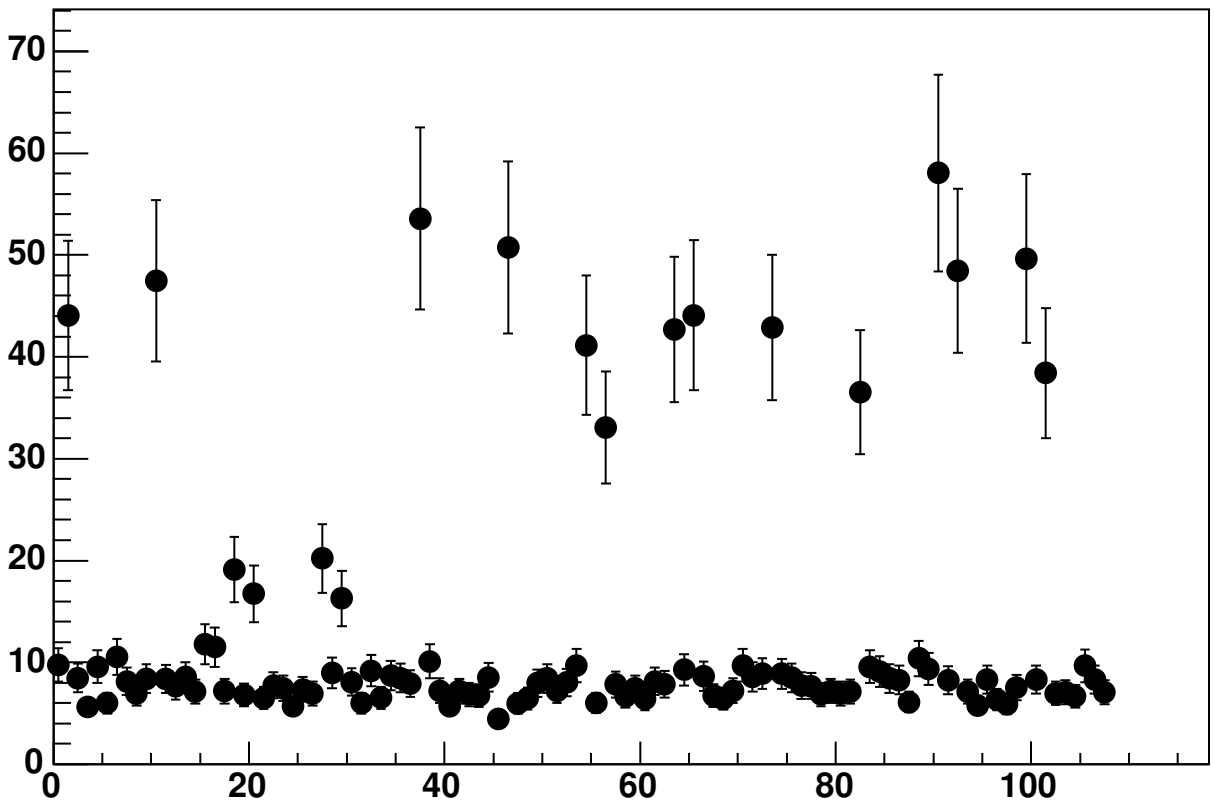
Enable 3, Hold=35, DAC=30000, ADC Noise vs 18*Chip+Chan



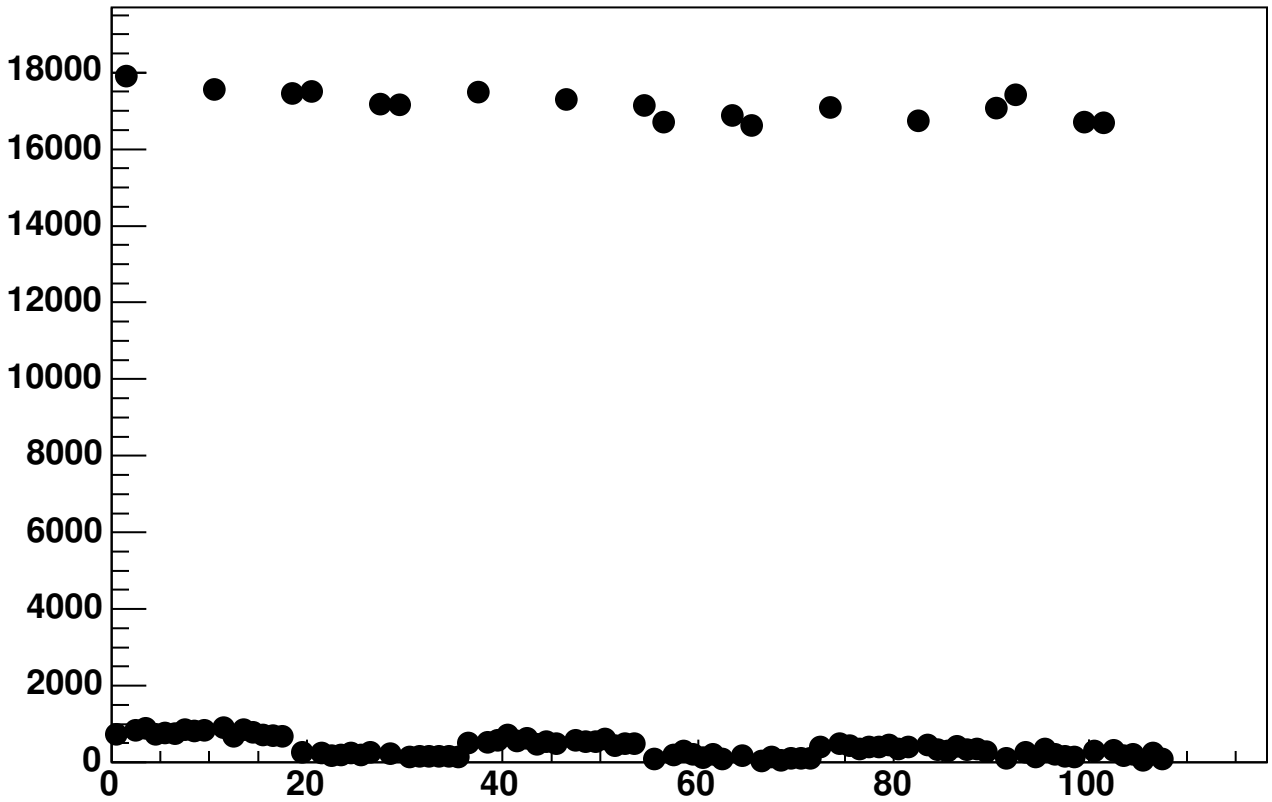
Enable 3, Hold=35, DAC=32000, ADC Mean vs 18*Chip+Chan



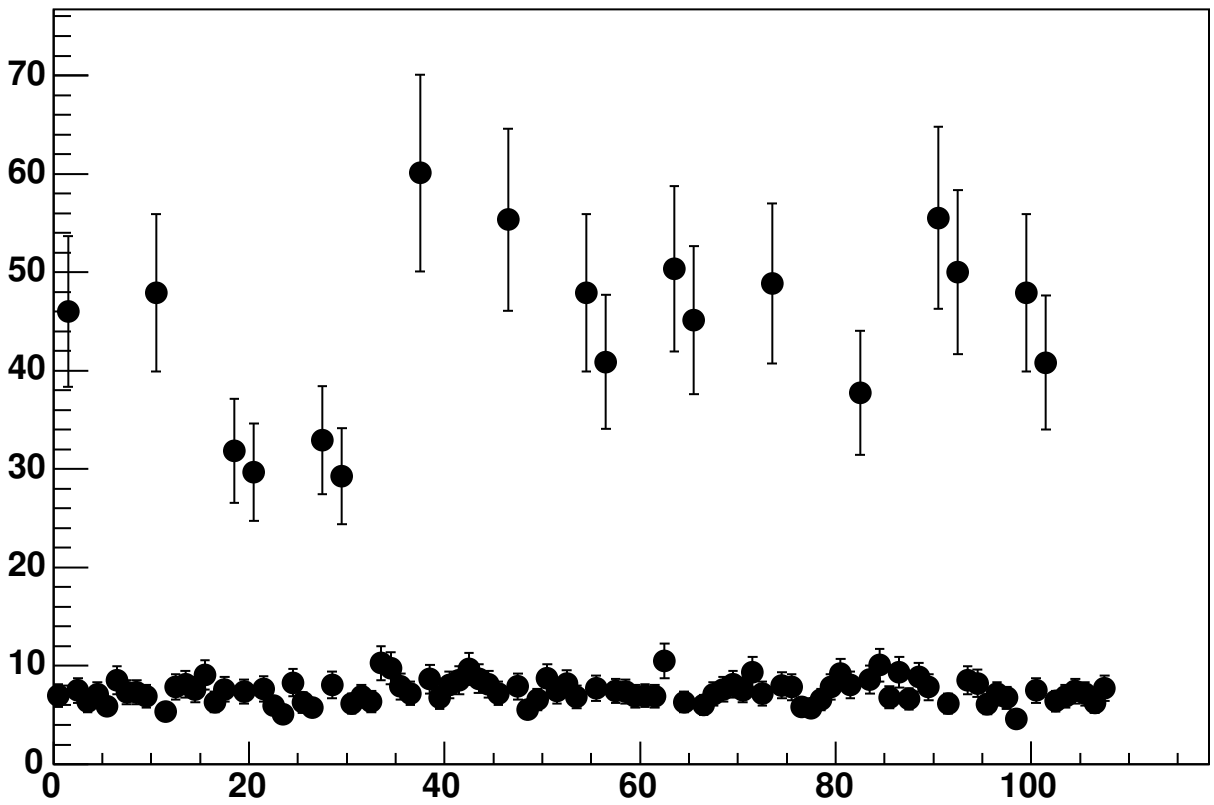
Enable 3, Hold=35, DAC=32000, ADC Noise vs 18*Chip+Chan



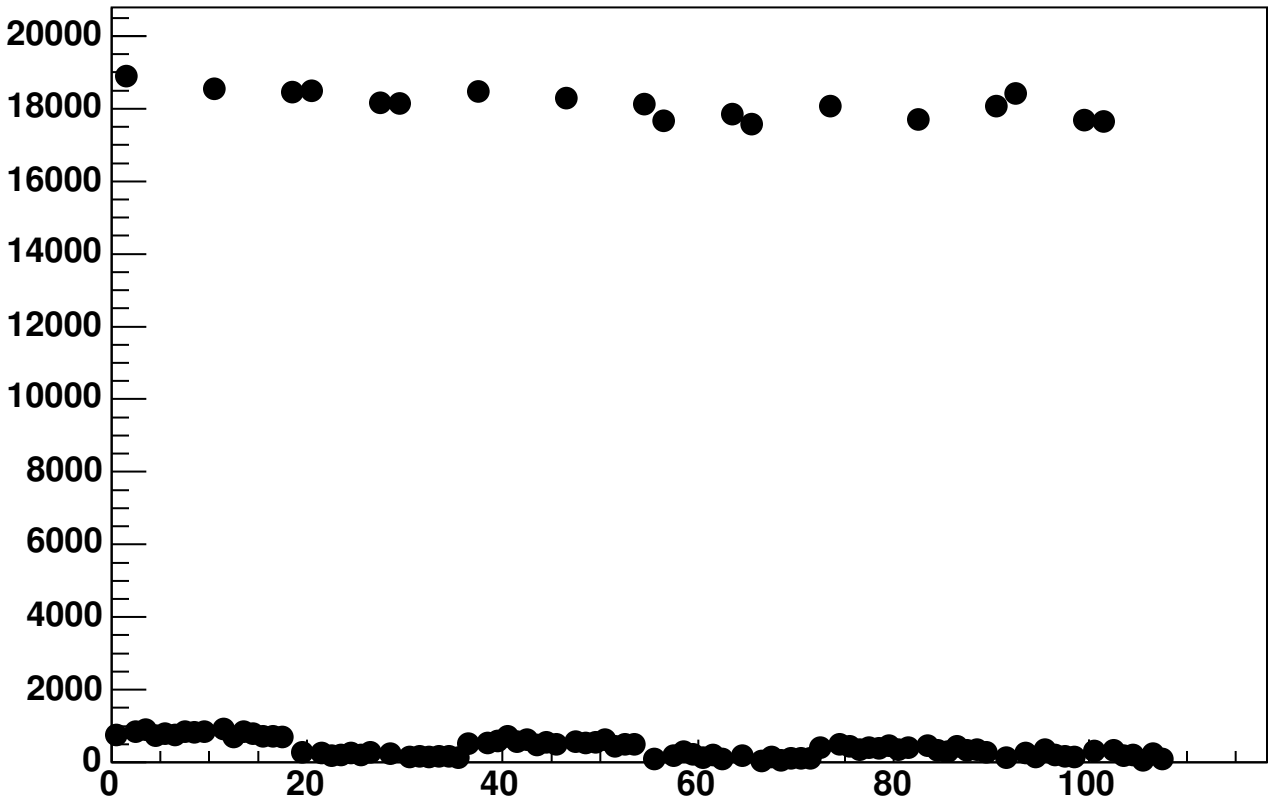
Enable 3, Hold=35, DAC=34000, ADC Mean vs 18*Chip+Chan



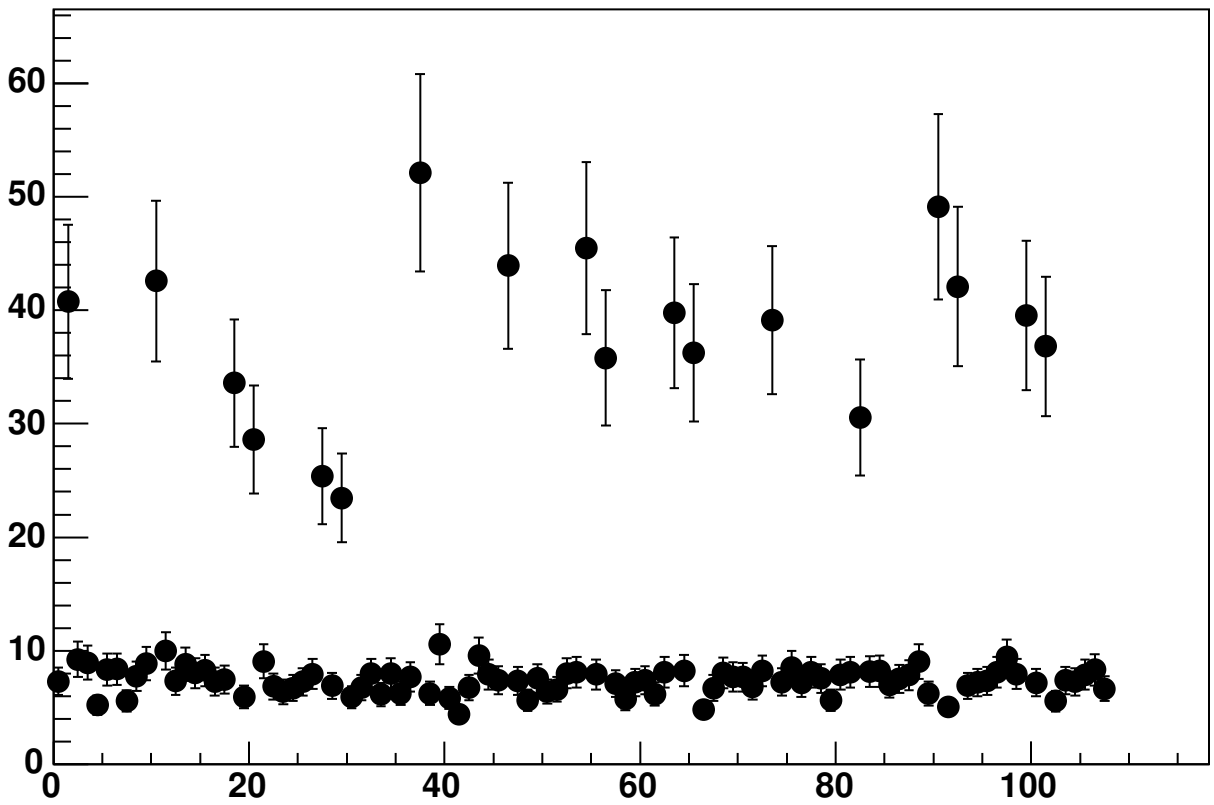
Enable 3, Hold=35, DAC=34000, ADC Noise vs 18*Chip+Chan



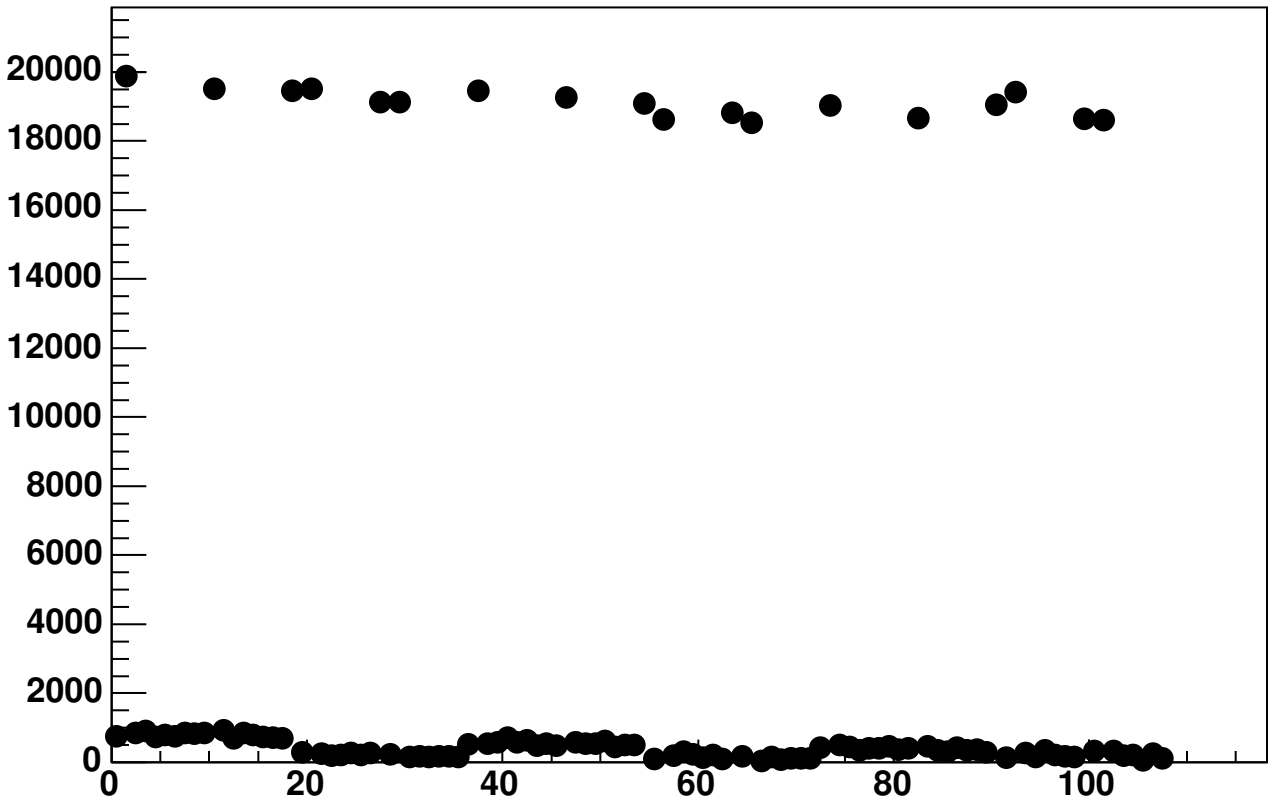
Enable 3, Hold=35, DAC=36000, ADC Mean vs 18*Chip+Chan



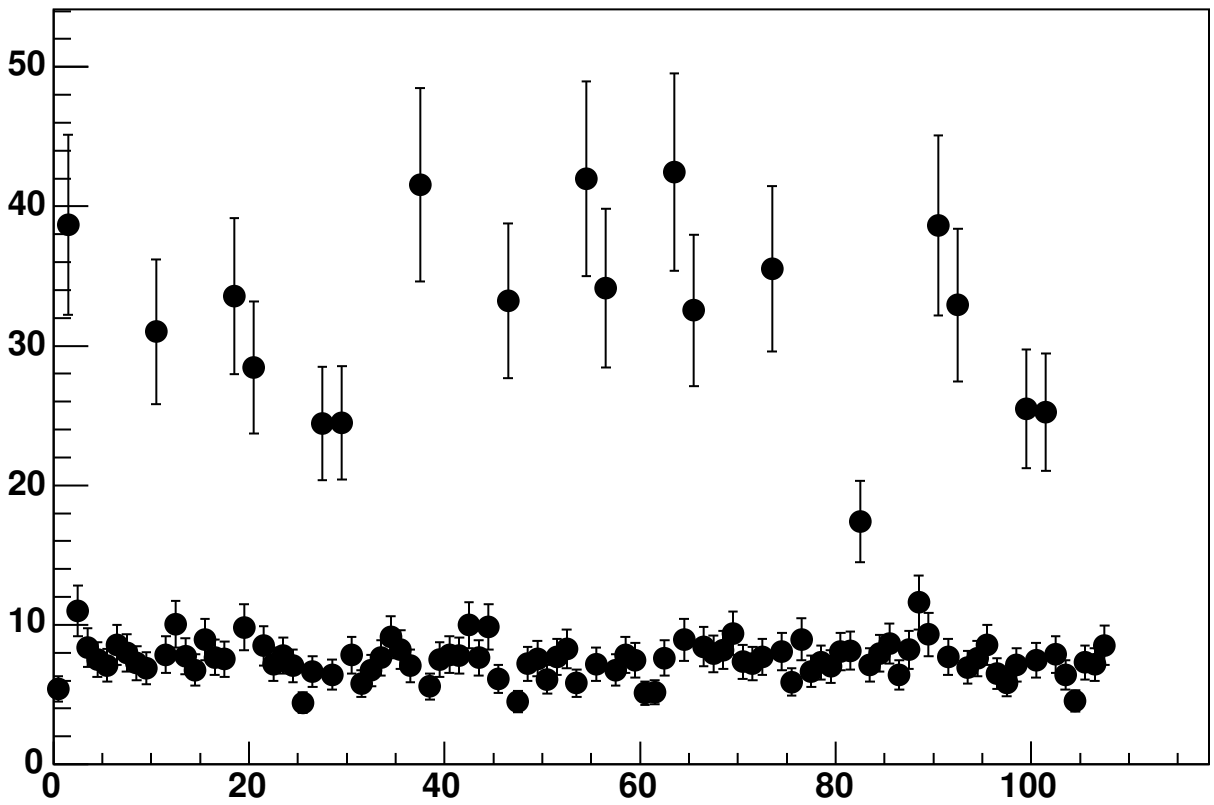
Enable 3, Hold=35, DAC=36000, ADC Noise vs 18*Chip+Chan



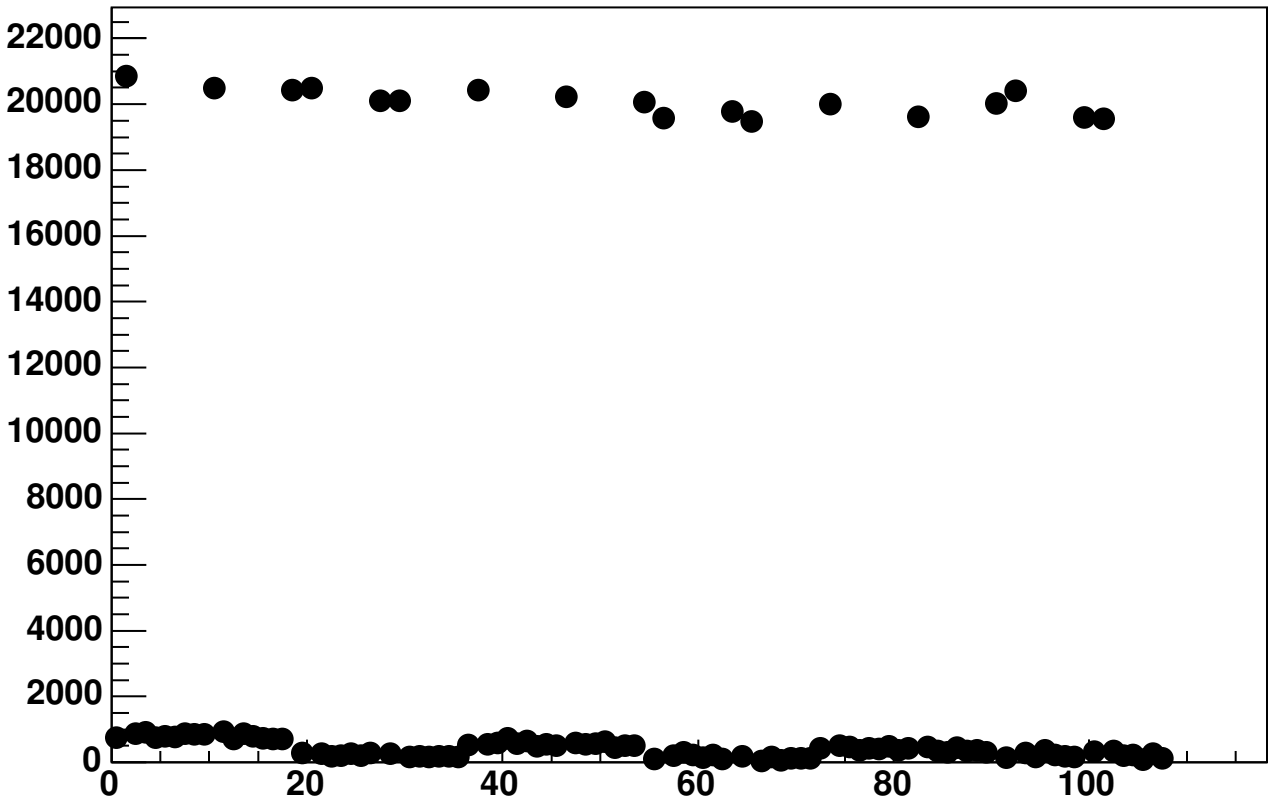
Enable 3, Hold=35, DAC=38000, ADC Mean vs 18*Chip+Chan



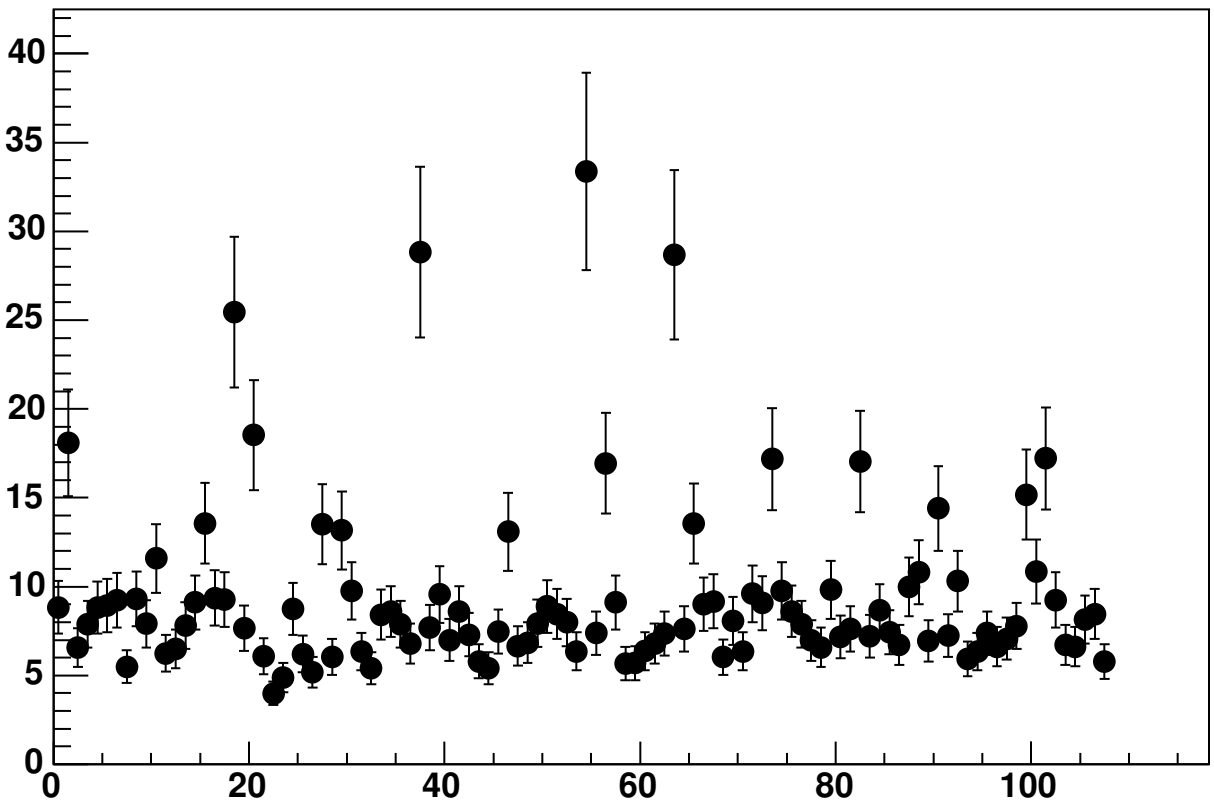
Enable 3, Hold=35, DAC=38000, ADC Noise vs 18*Chip+Chan



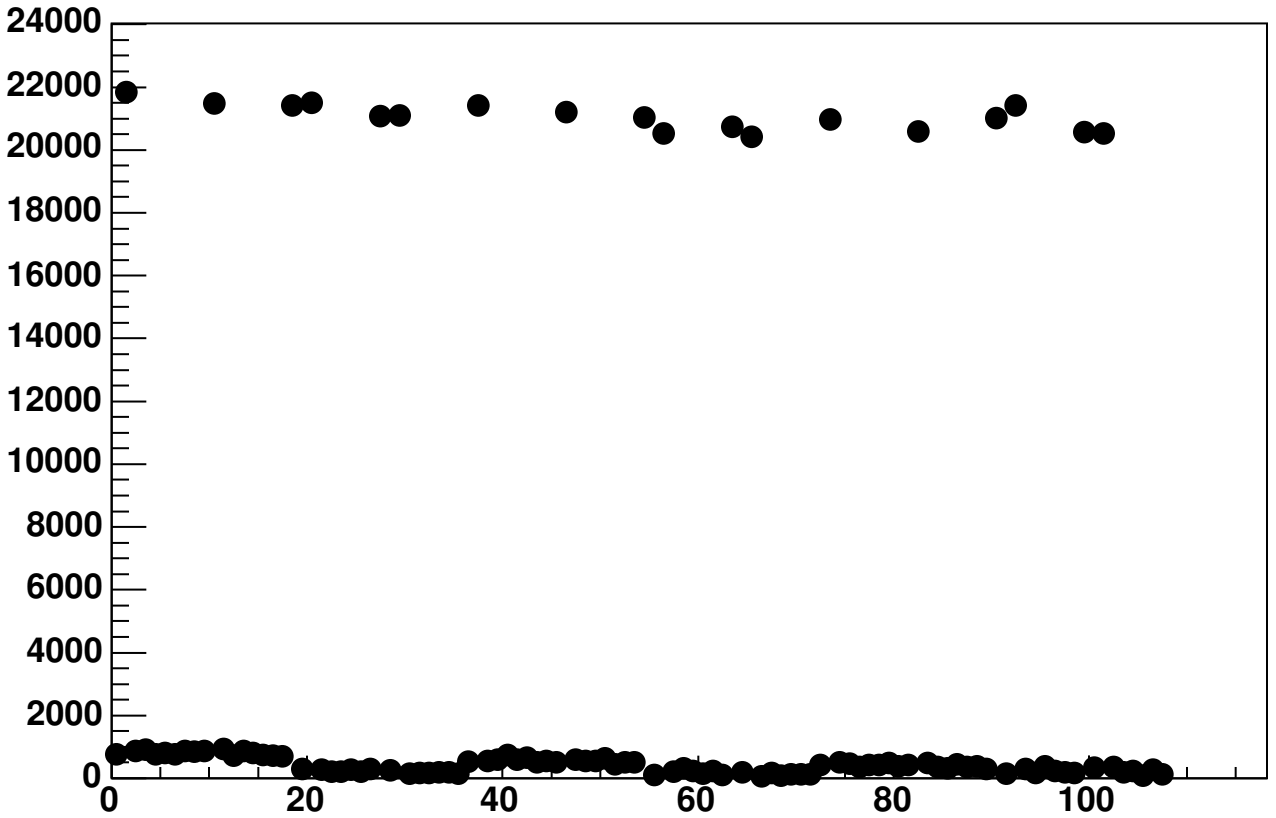
Enable 3, Hold=35, DAC=40000, ADC Mean vs 18*Chip+Chan



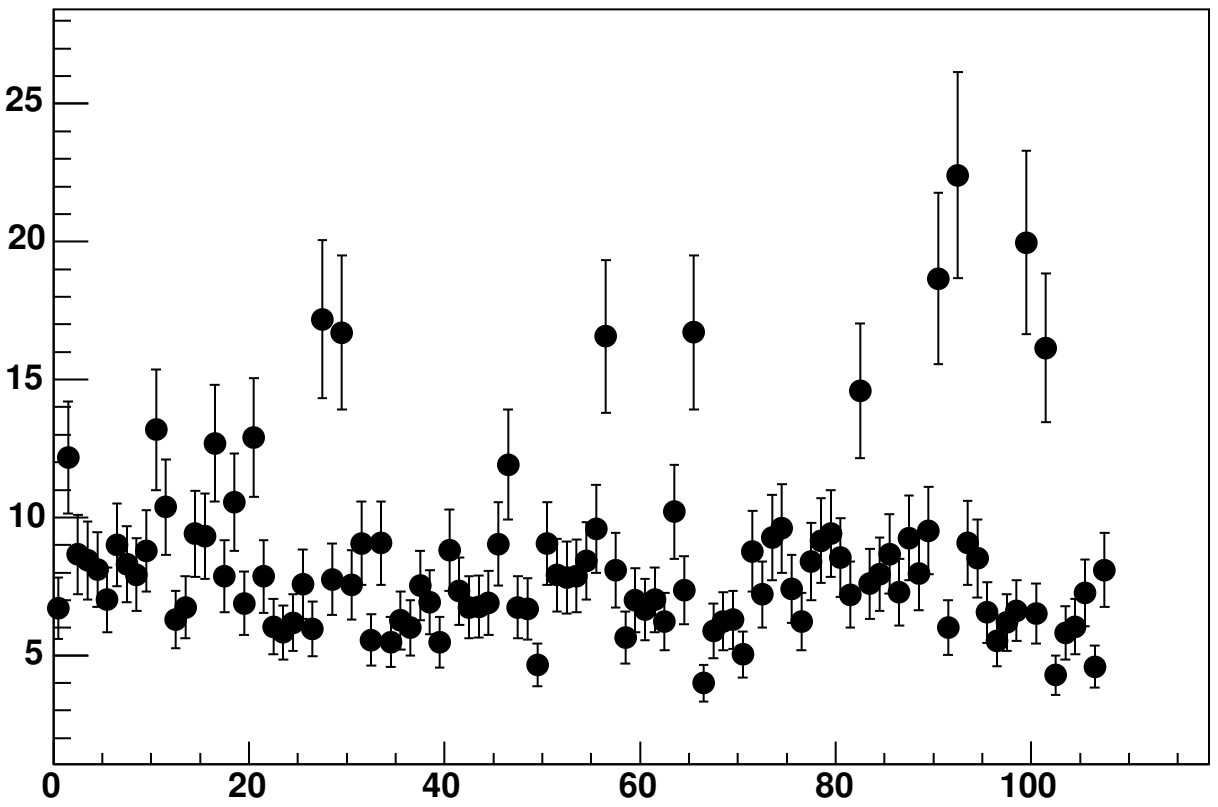
Enable 3, Hold=35, DAC=40000, ADC Noise vs 18*Chip+Chan



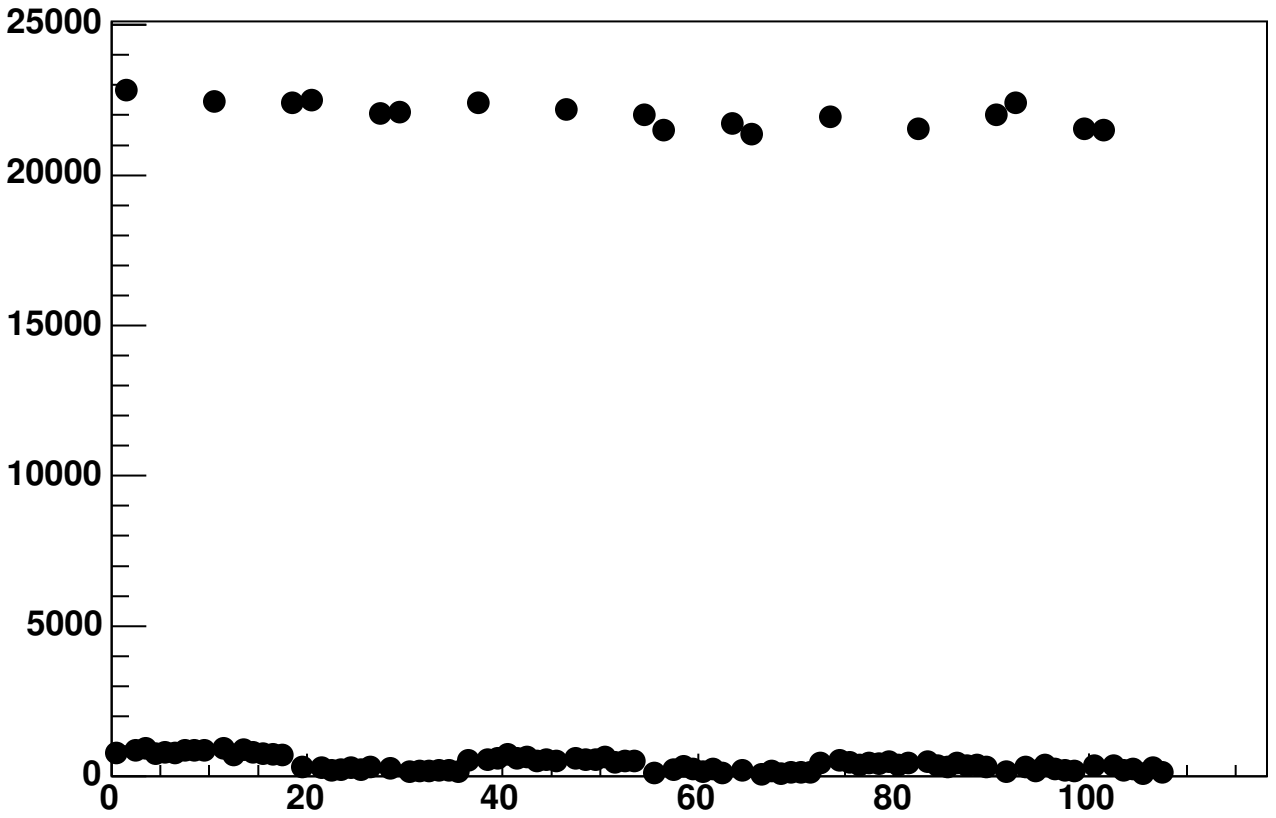
Enable 3, Hold=35, DAC=42000, ADC Mean vs 18*Chip+Chan



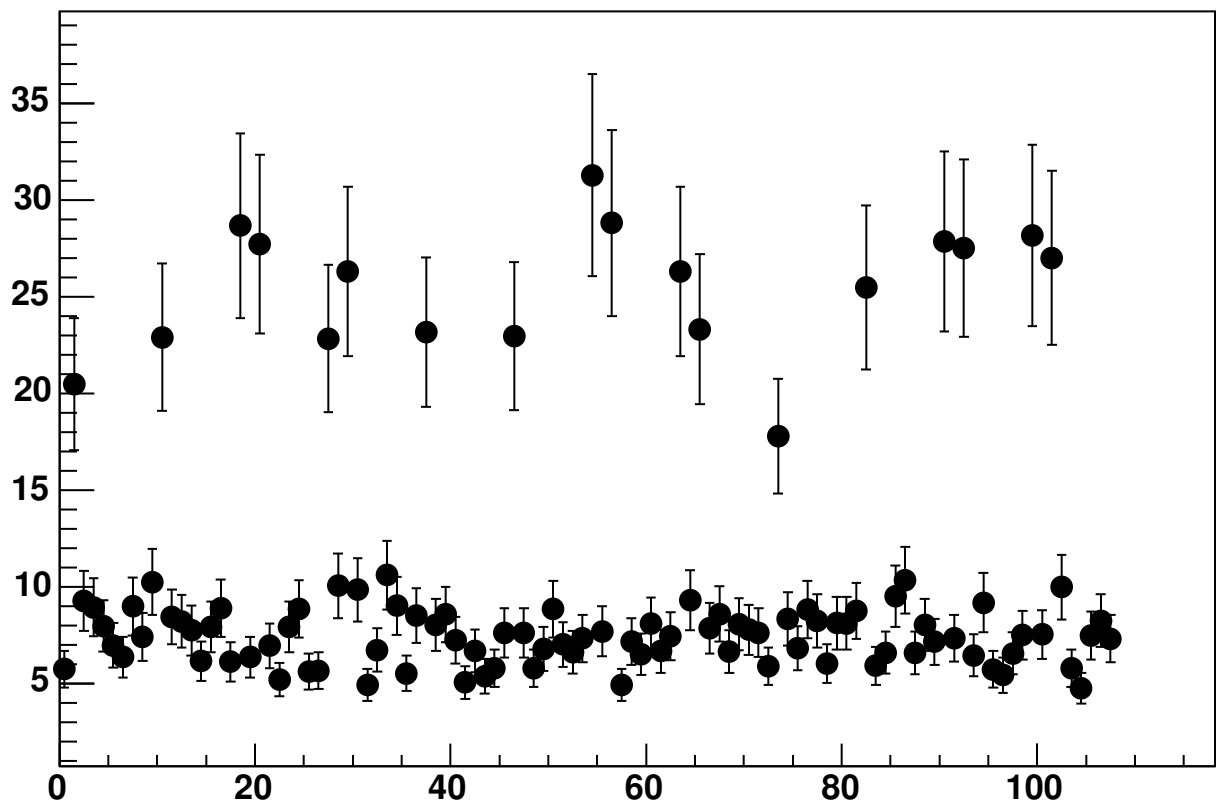
Enable 3, Hold=35, DAC=42000, ADC Noise vs 18*Chip+Chan



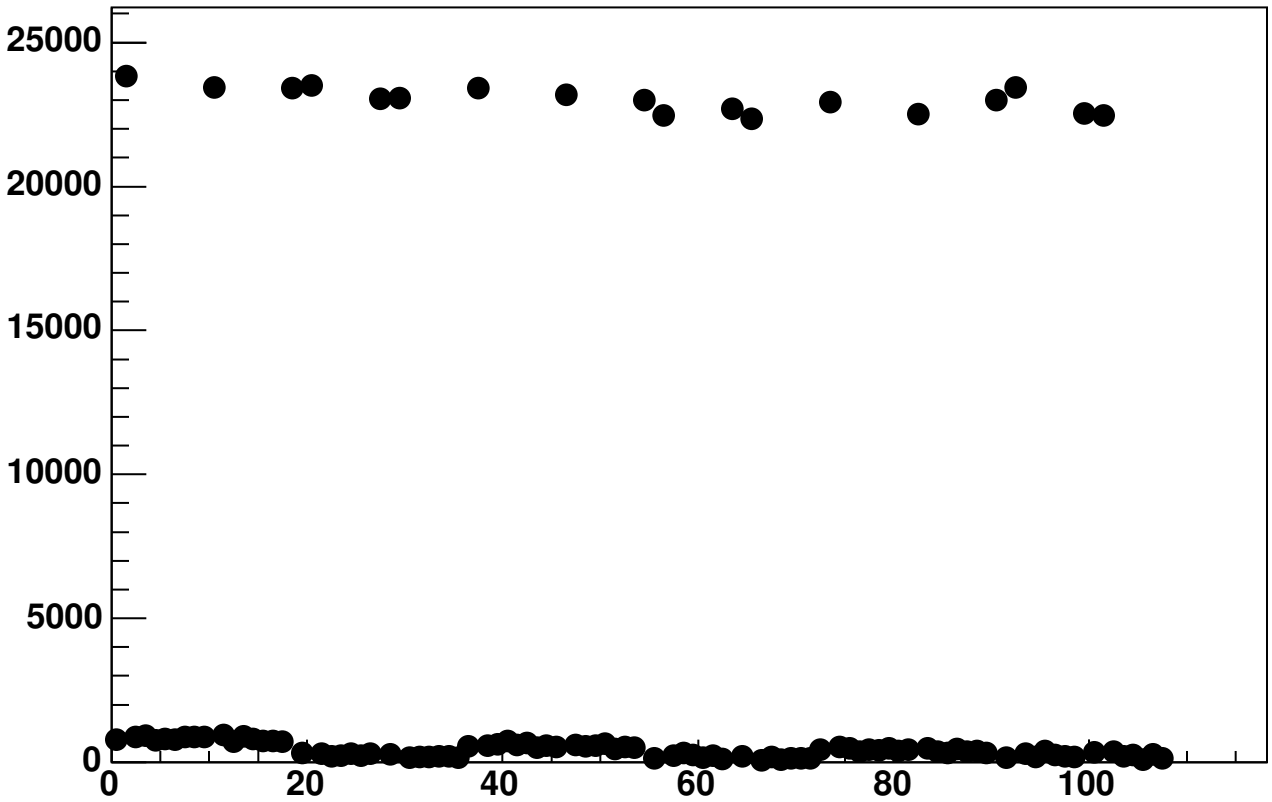
Enable 3, Hold=35, DAC=44000, ADC Mean vs 18*Chip+Chan



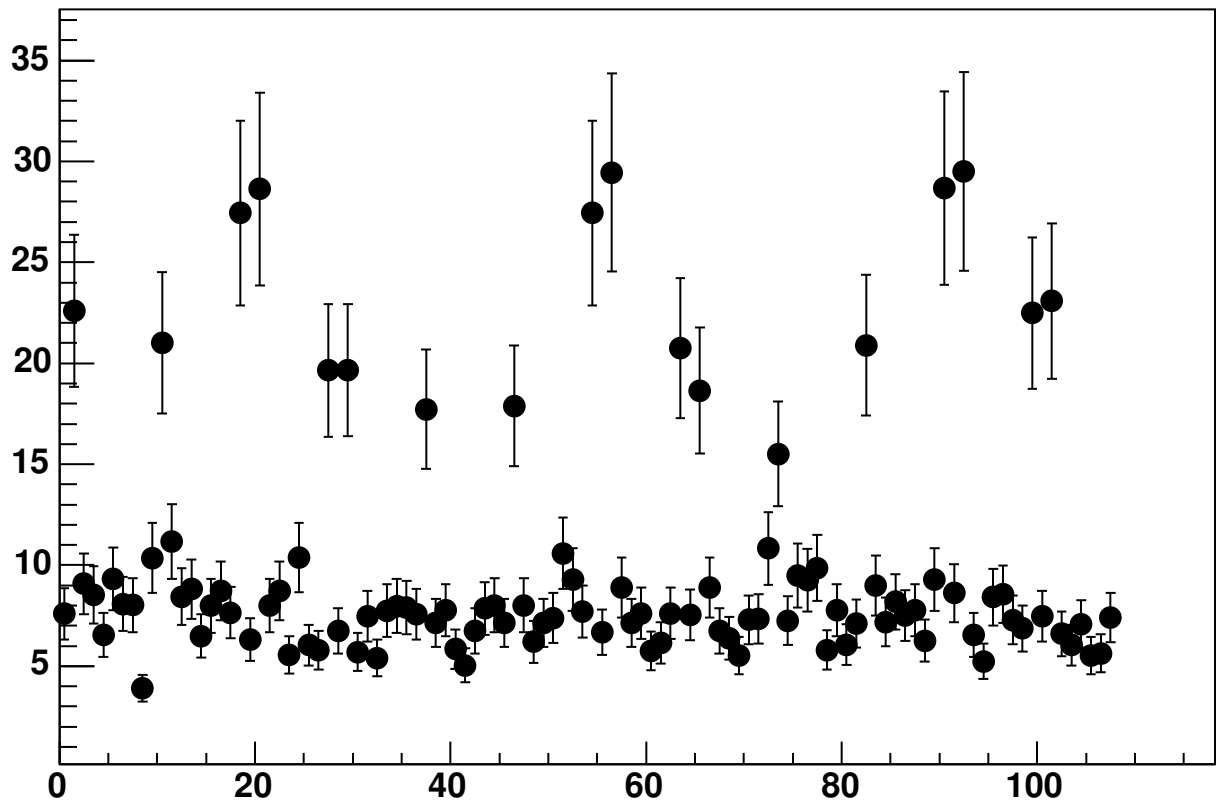
Enable 3, Hold=35, DAC=44000, ADC Noise vs 18*Chip+Chan



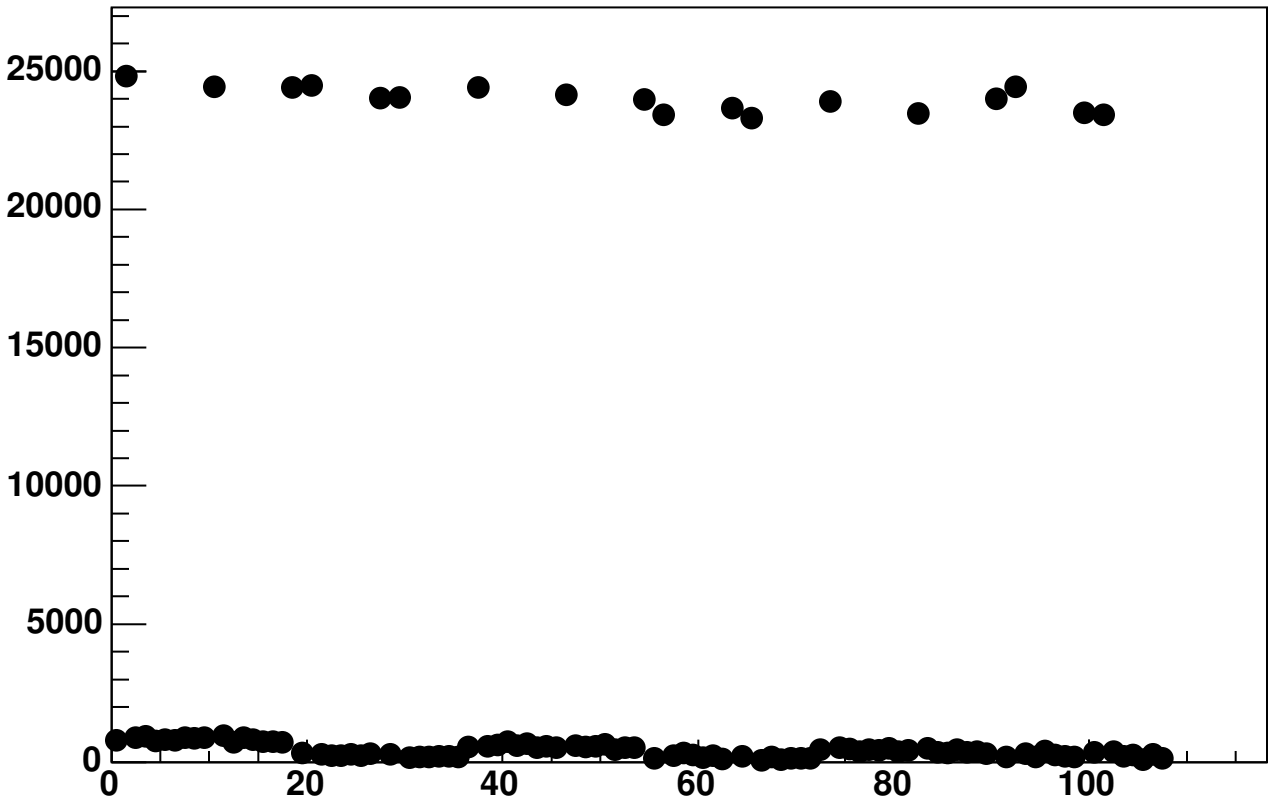
Enable 3, Hold=35, DAC=46000, ADC Mean vs 18*Chip+Chan



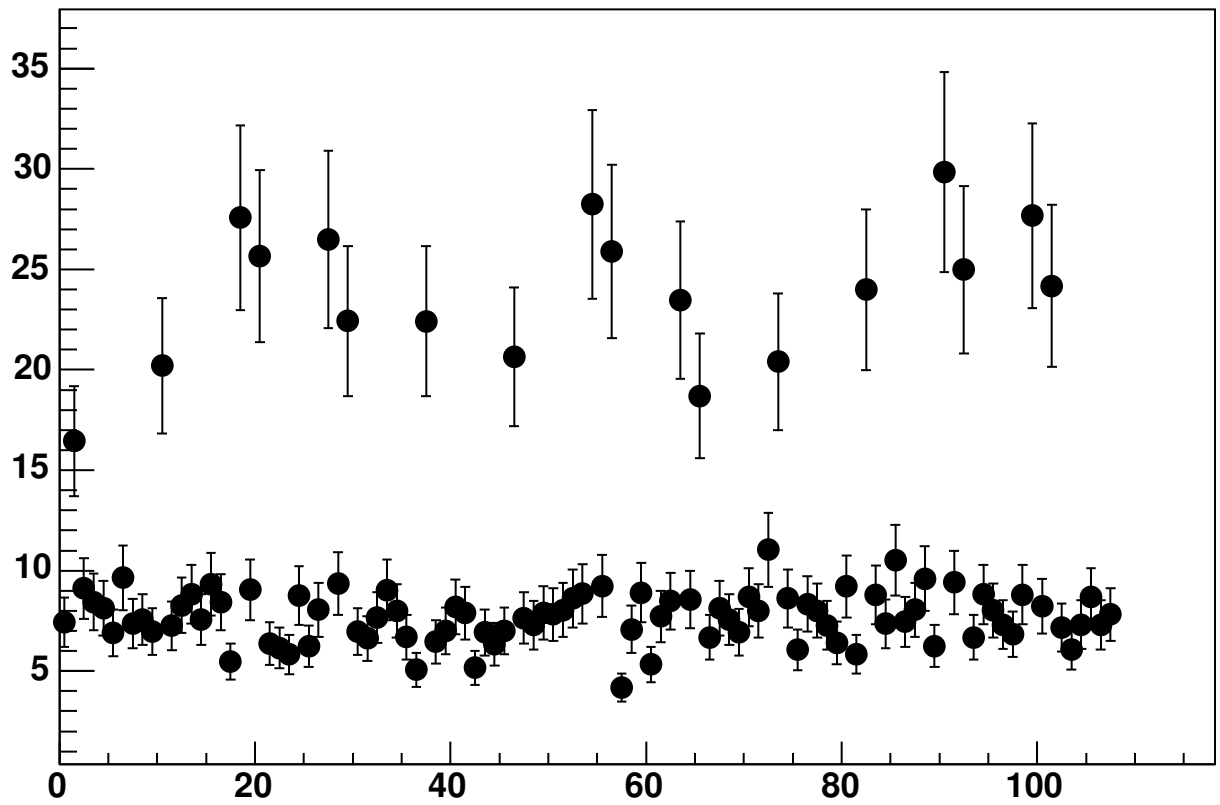
Enable 3, Hold=35, DAC=46000, ADC Noise vs 18*Chip+Chan



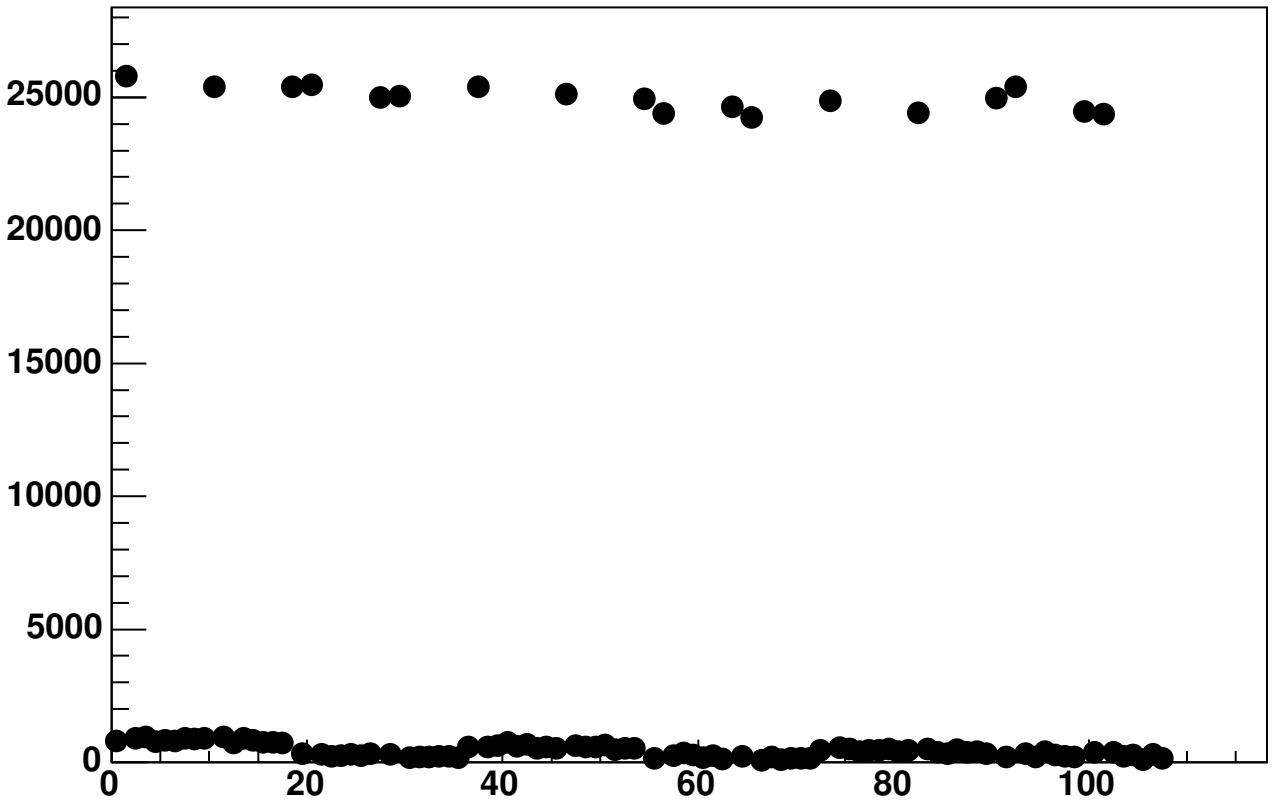
Enable 3, Hold=35, DAC=48000, ADC Mean vs 18*Chip+Chan



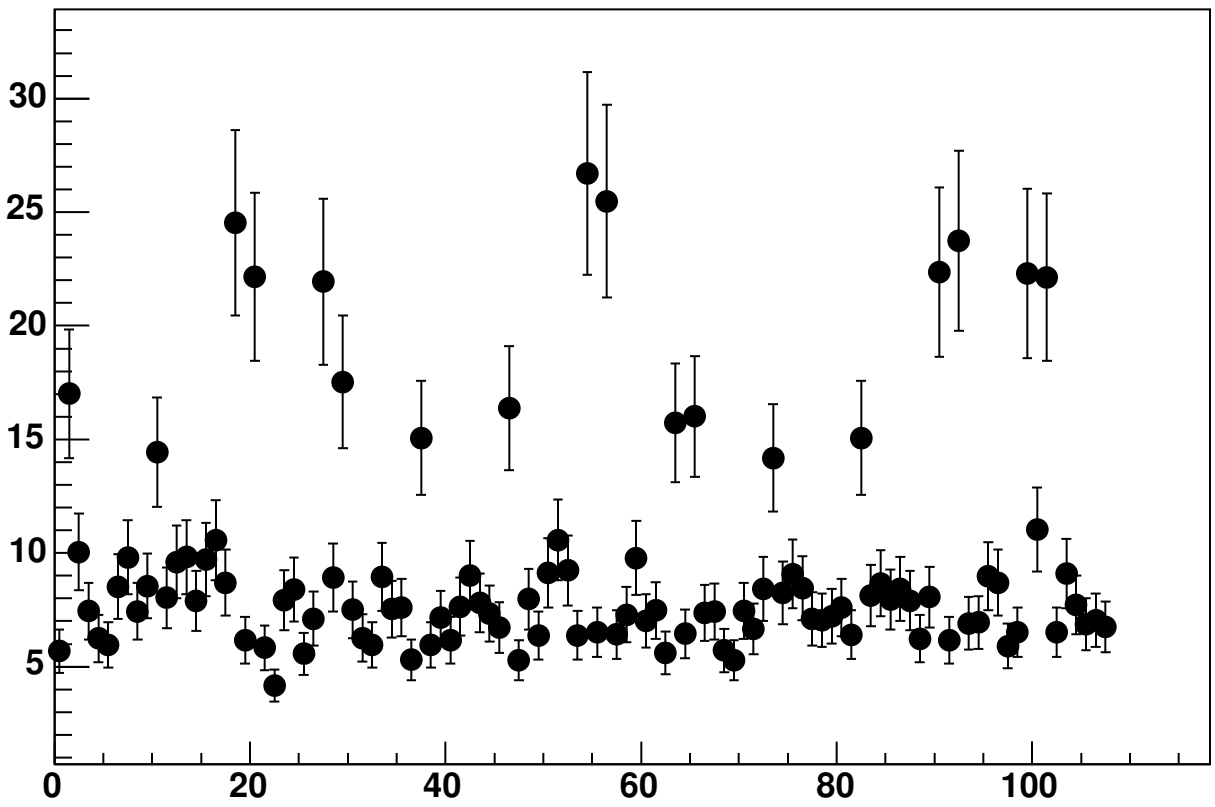
Enable 3, Hold=35, DAC=48000, ADC Noise vs 18*Chip+Chan



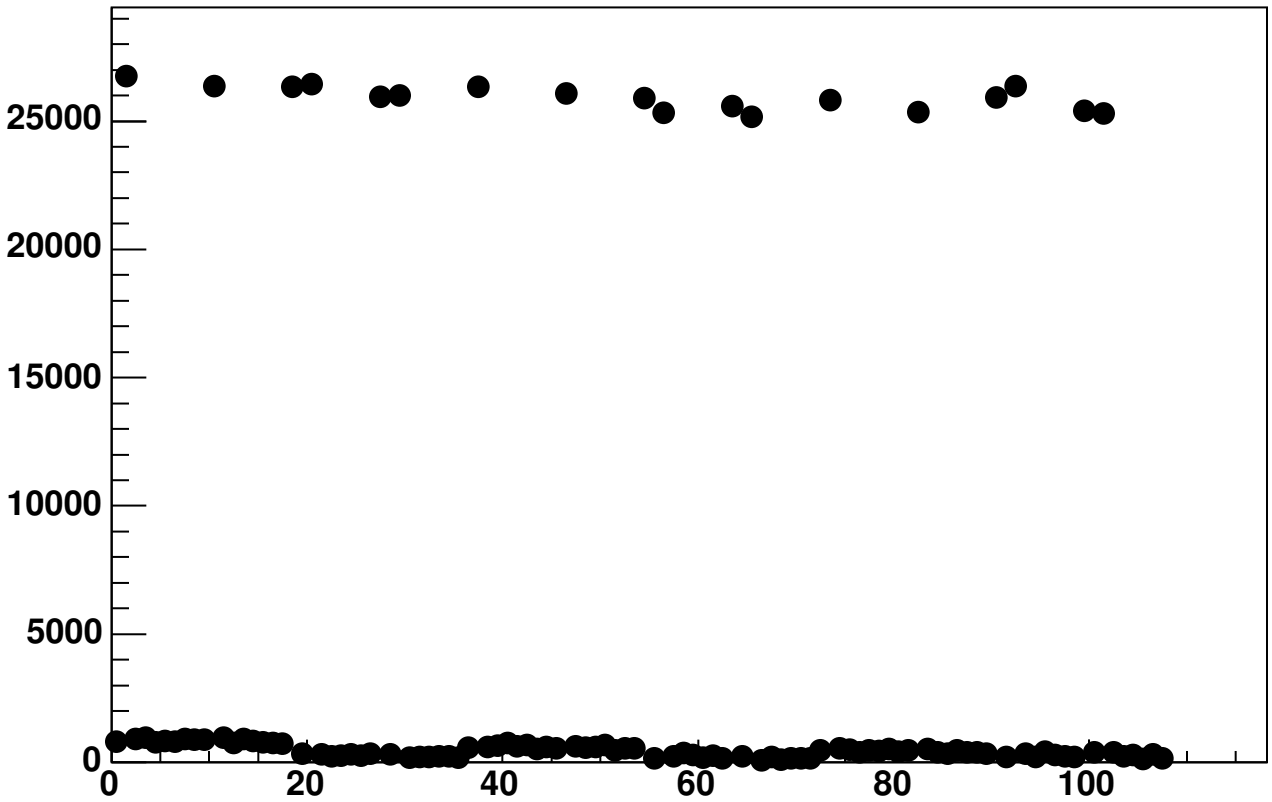
Enable 3, Hold=35, DAC=50000, ADC Mean vs 18*Chip+Chan



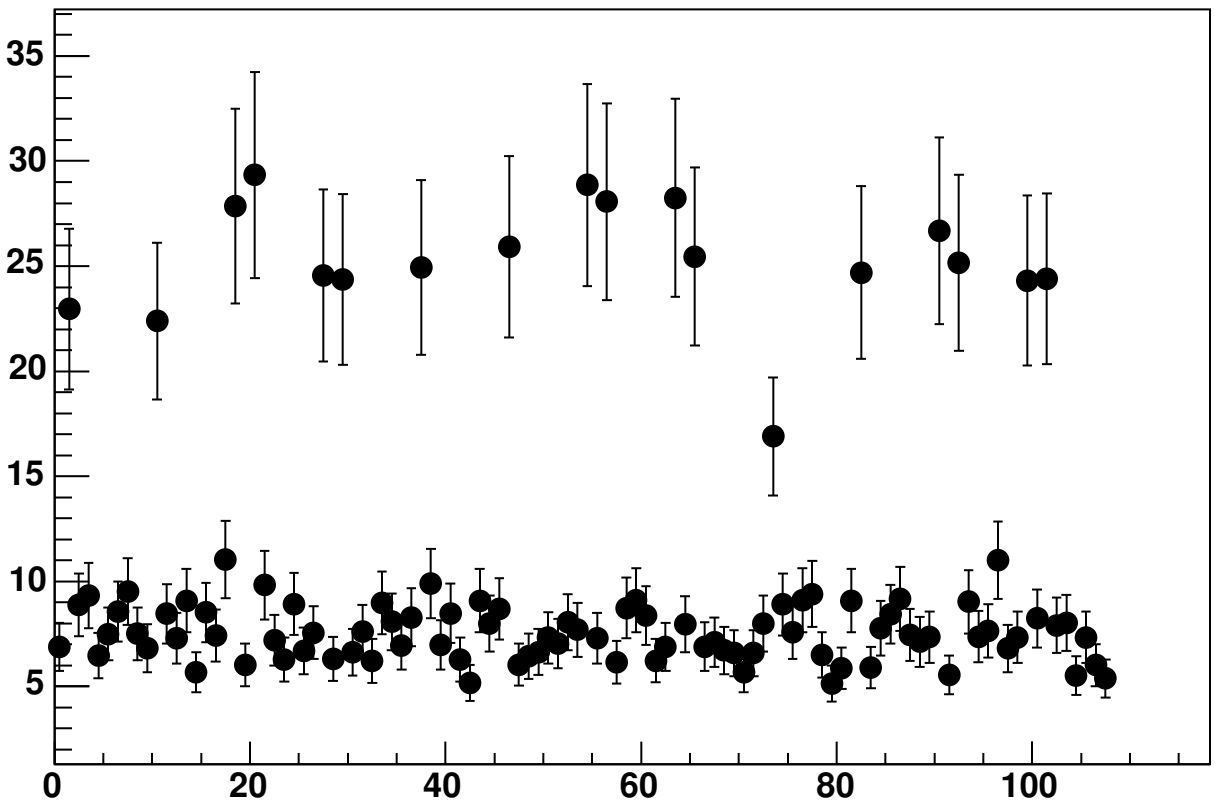
Enable 3, Hold=35, DAC=50000, ADC Noise vs 18*Chip+Chan



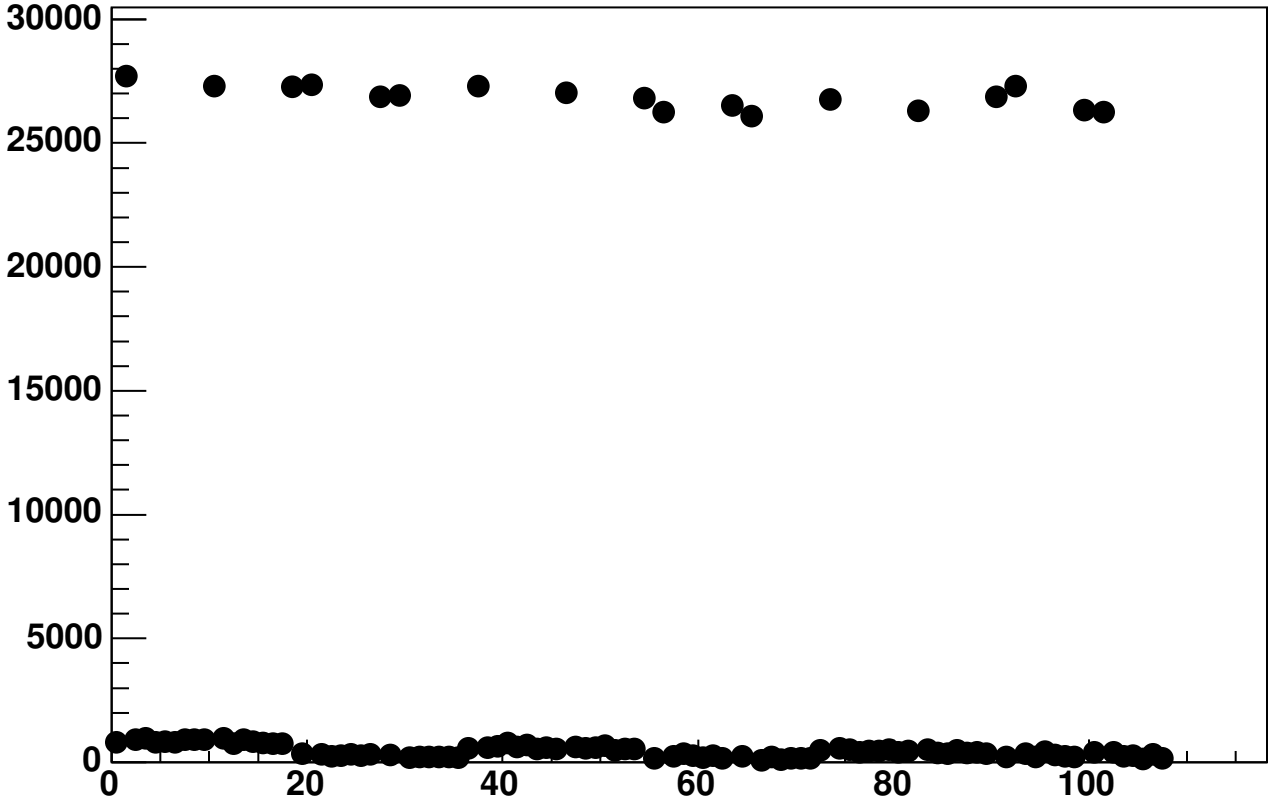
Enable 3, Hold=35, DAC=52000, ADC Mean vs 18*Chip+Chan



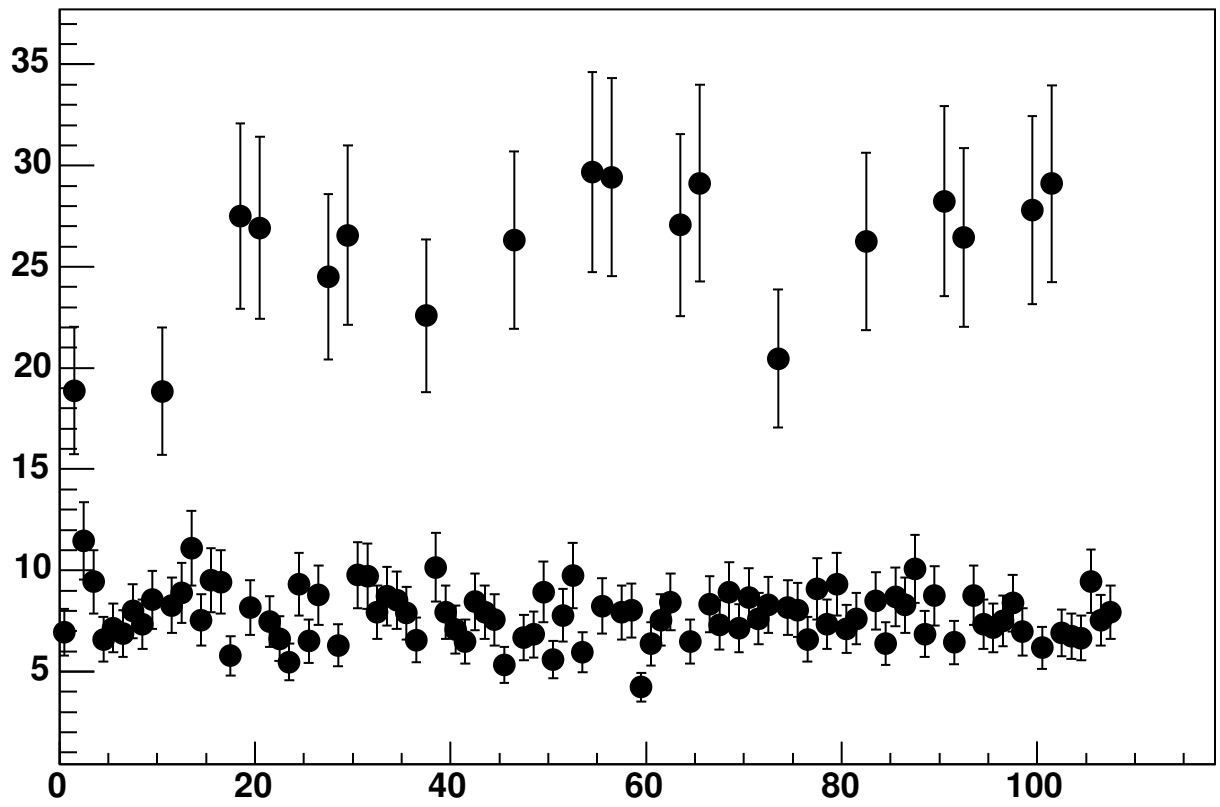
Enable 3, Hold=35, DAC=52000, ADC Noise vs 18*Chip+Chan



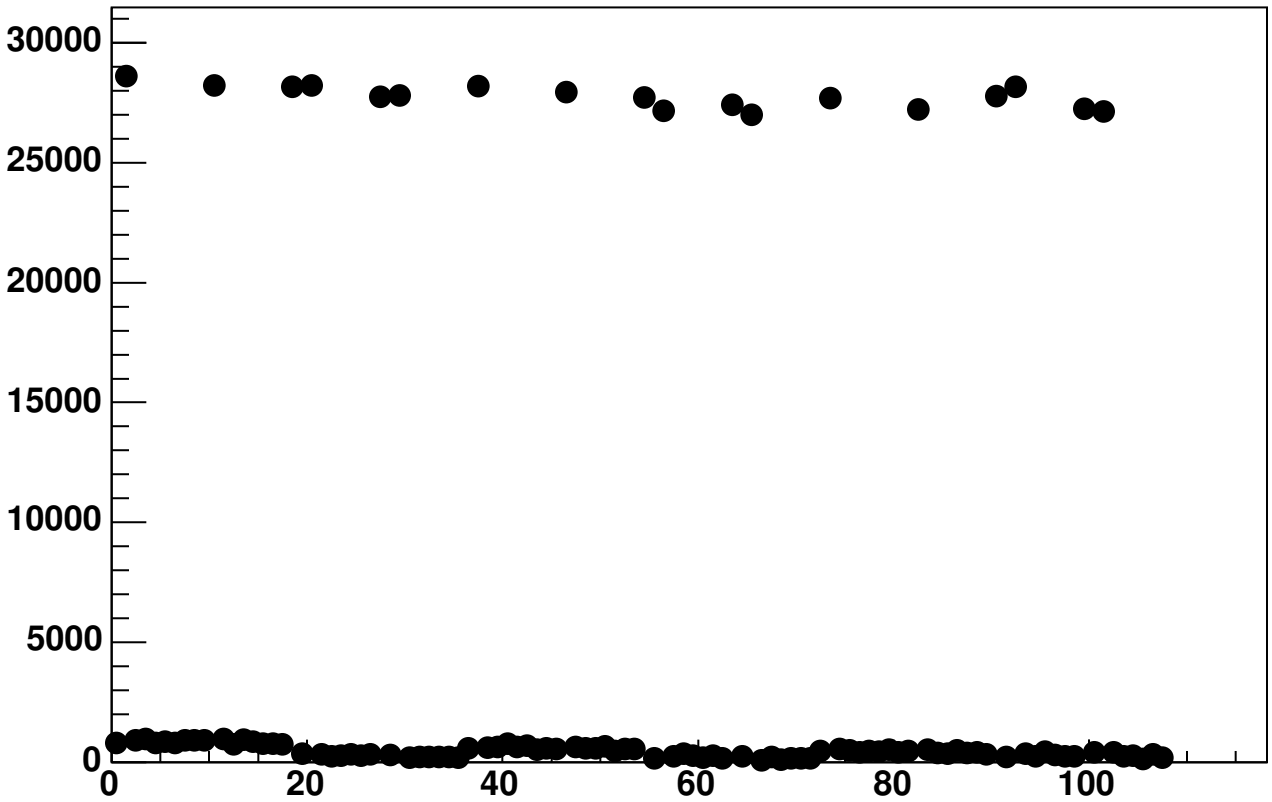
Enable 3, Hold=35, DAC=54000, ADC Mean vs 18*Chip+Chan



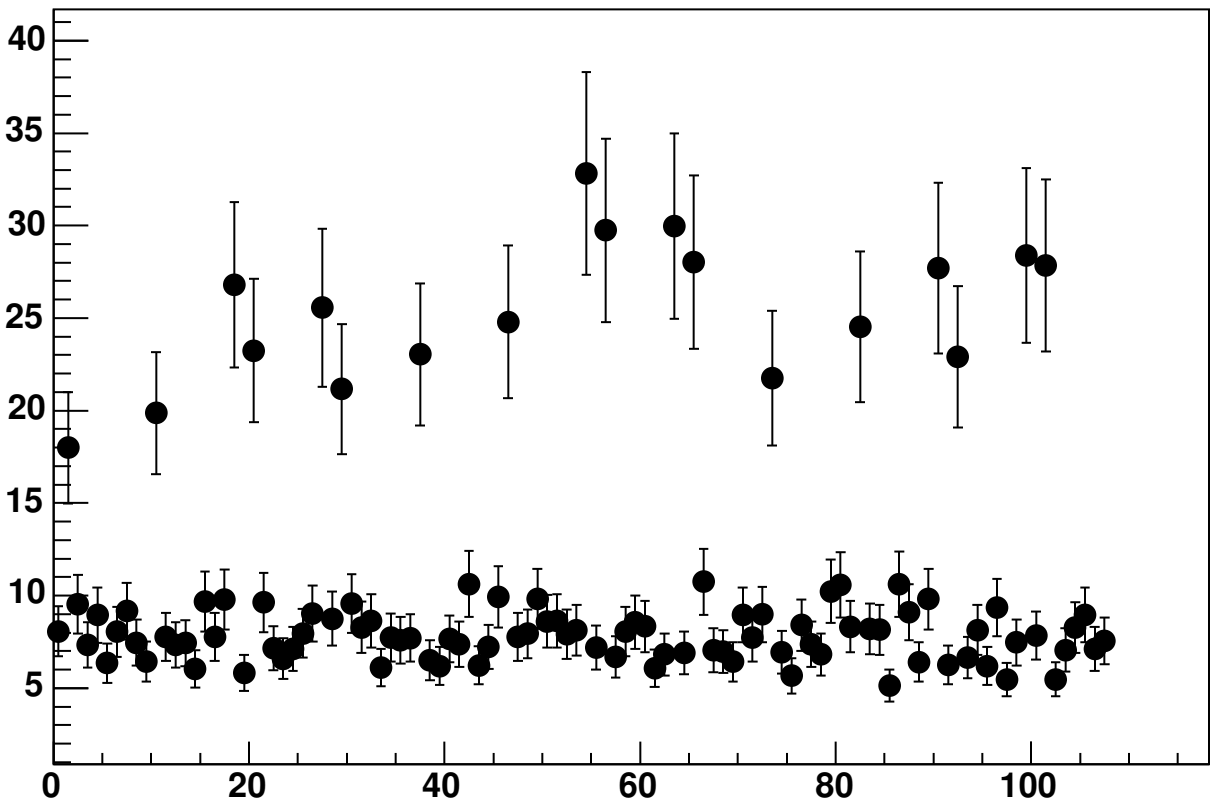
Enable 3, Hold=35, DAC=54000, ADC Noise vs 18*Chip+Chan



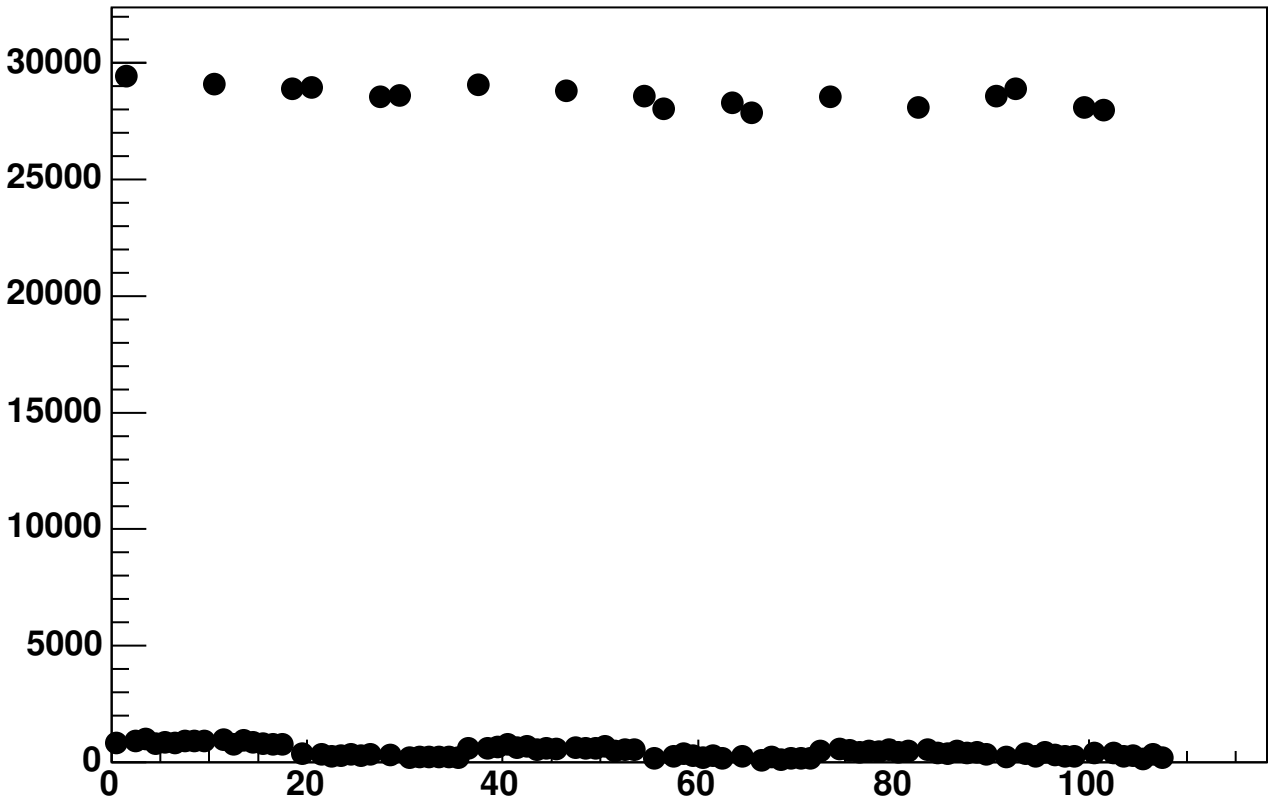
Enable 3, Hold=35, DAC=56000, ADC Mean vs 18*Chip+Chan



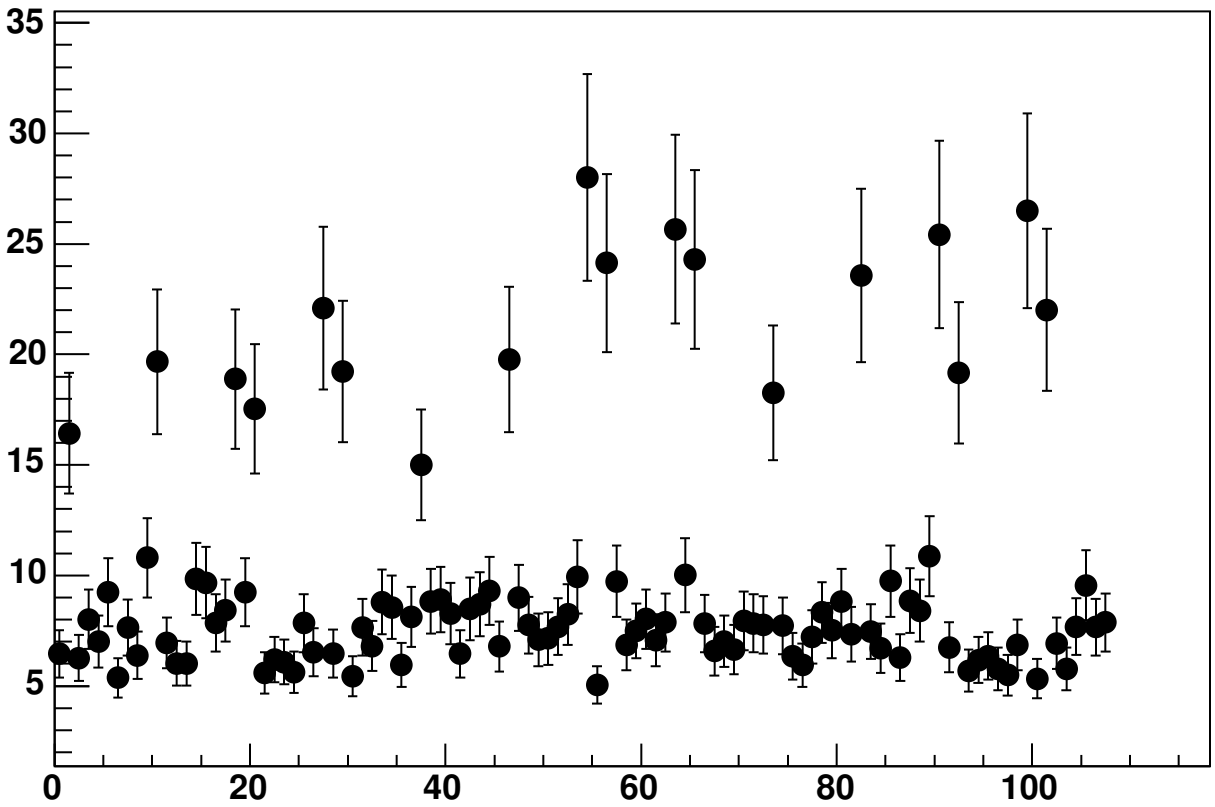
Enable 3, Hold=35, DAC=56000, ADC Noise vs 18*Chip+Chan



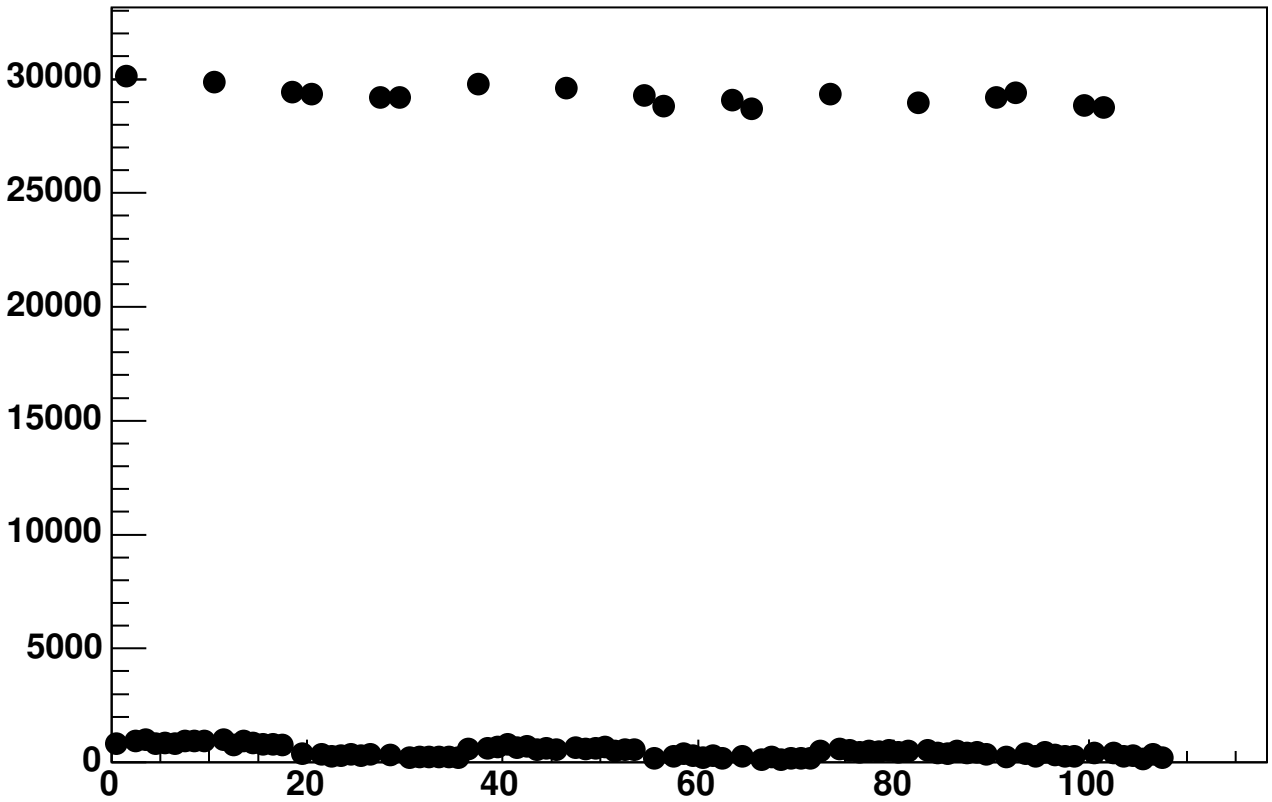
Enable 3, Hold=35, DAC=58000, ADC Mean vs 18*Chip+Chan



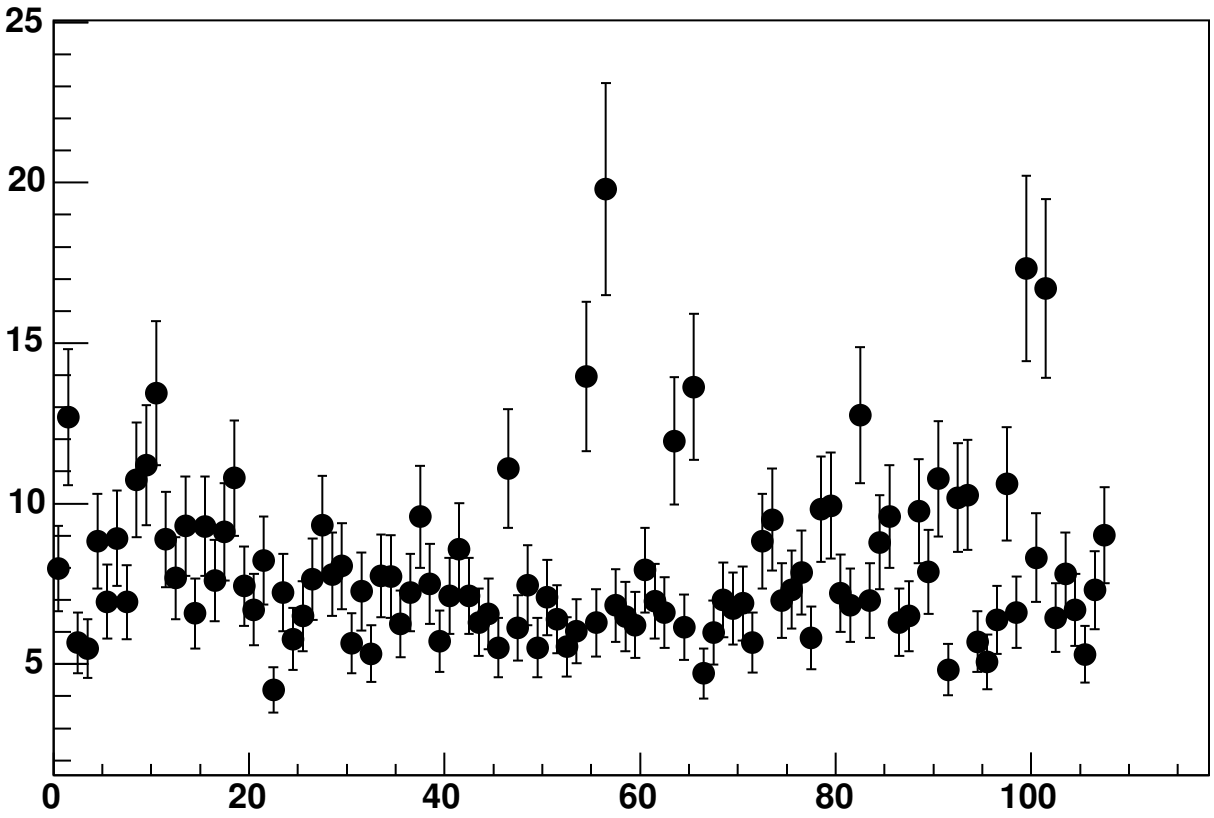
Enable 3, Hold=35, DAC=58000, ADC Noise vs 18*Chip+Chan



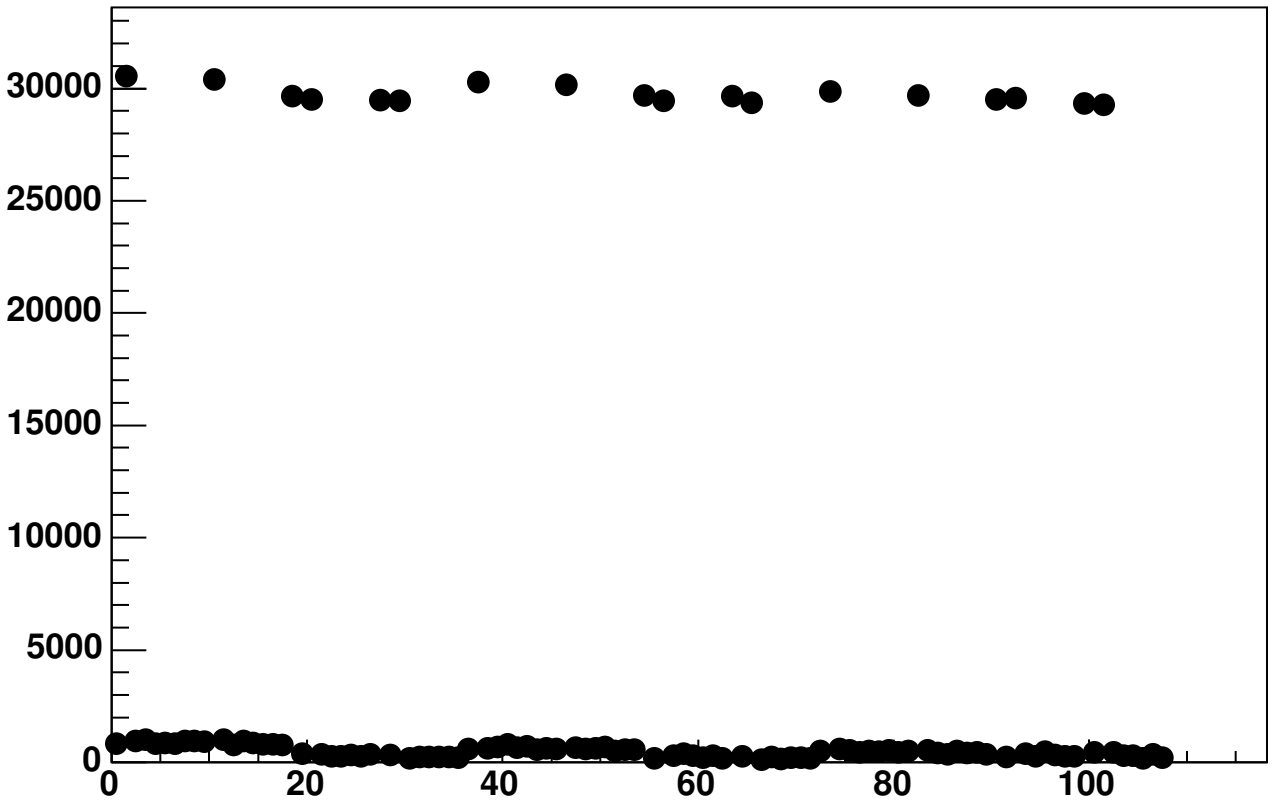
Enable 3, Hold=35, DAC=60000, ADC Mean vs 18*Chip+Chan



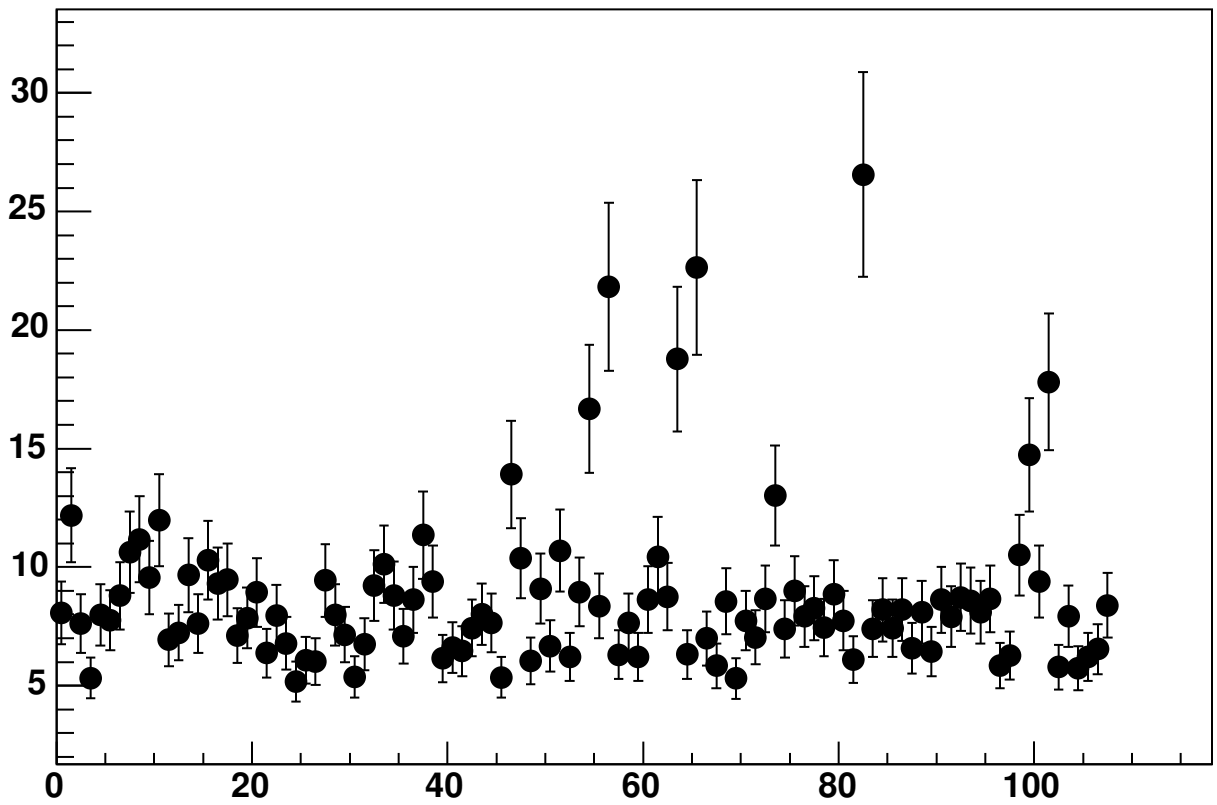
Enable 3, Hold=35, DAC=60000, ADC Noise vs 18*Chip+Chan



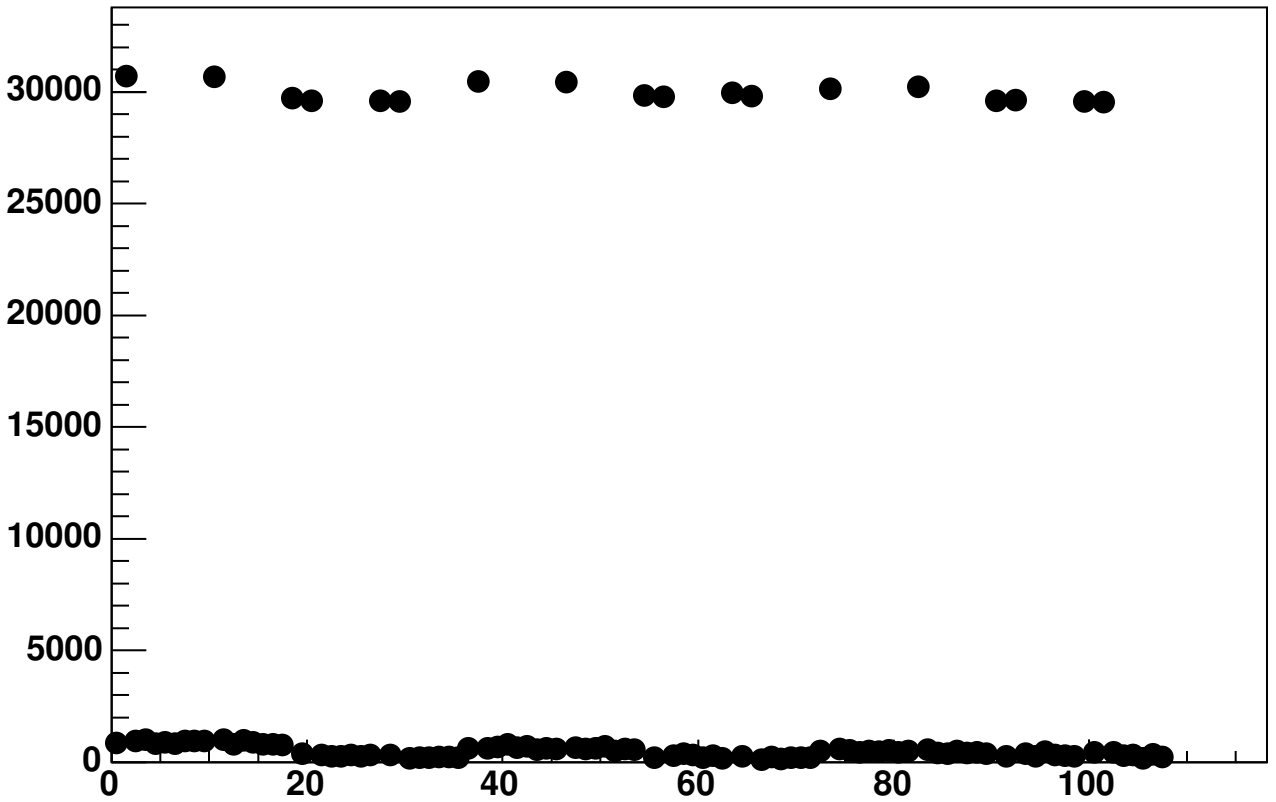
Enable 3, Hold=35, DAC=62000, ADC Mean vs 18*Chip+Chan



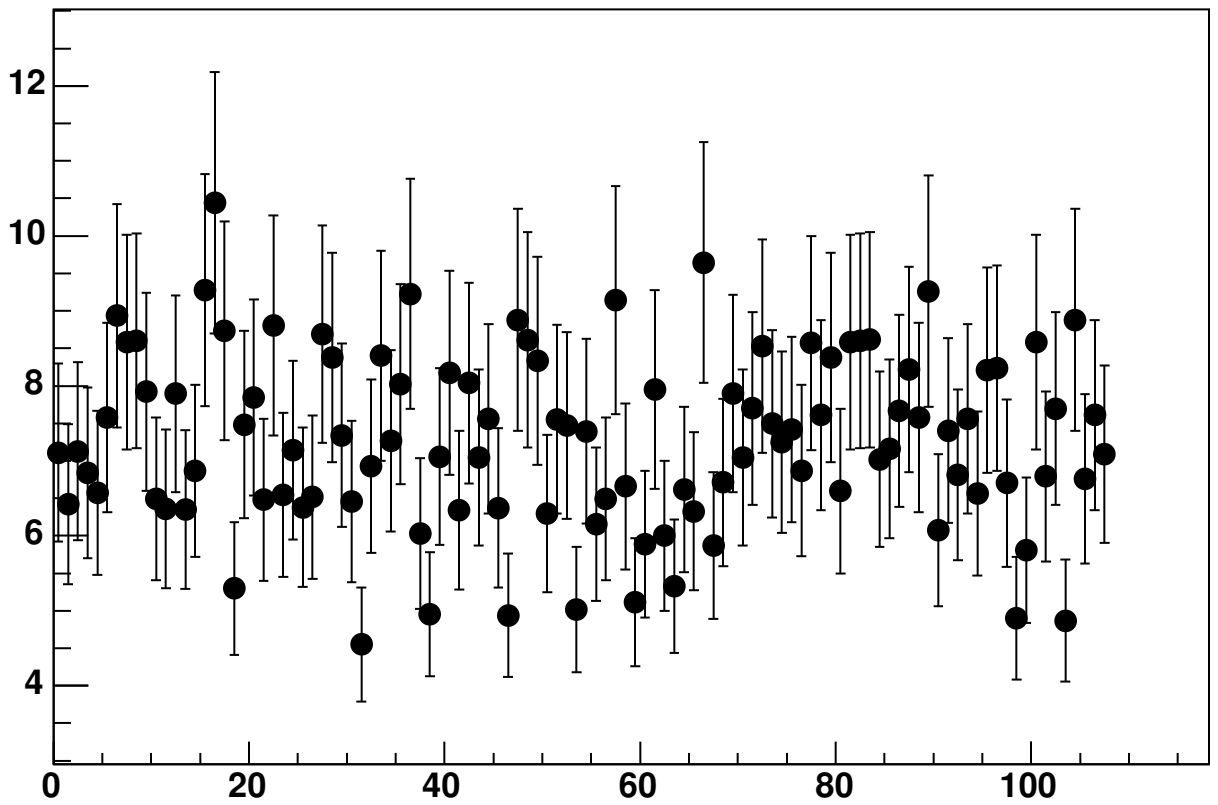
Enable 3, Hold=35, DAC=62000, ADC Noise vs 18*Chip+Chan



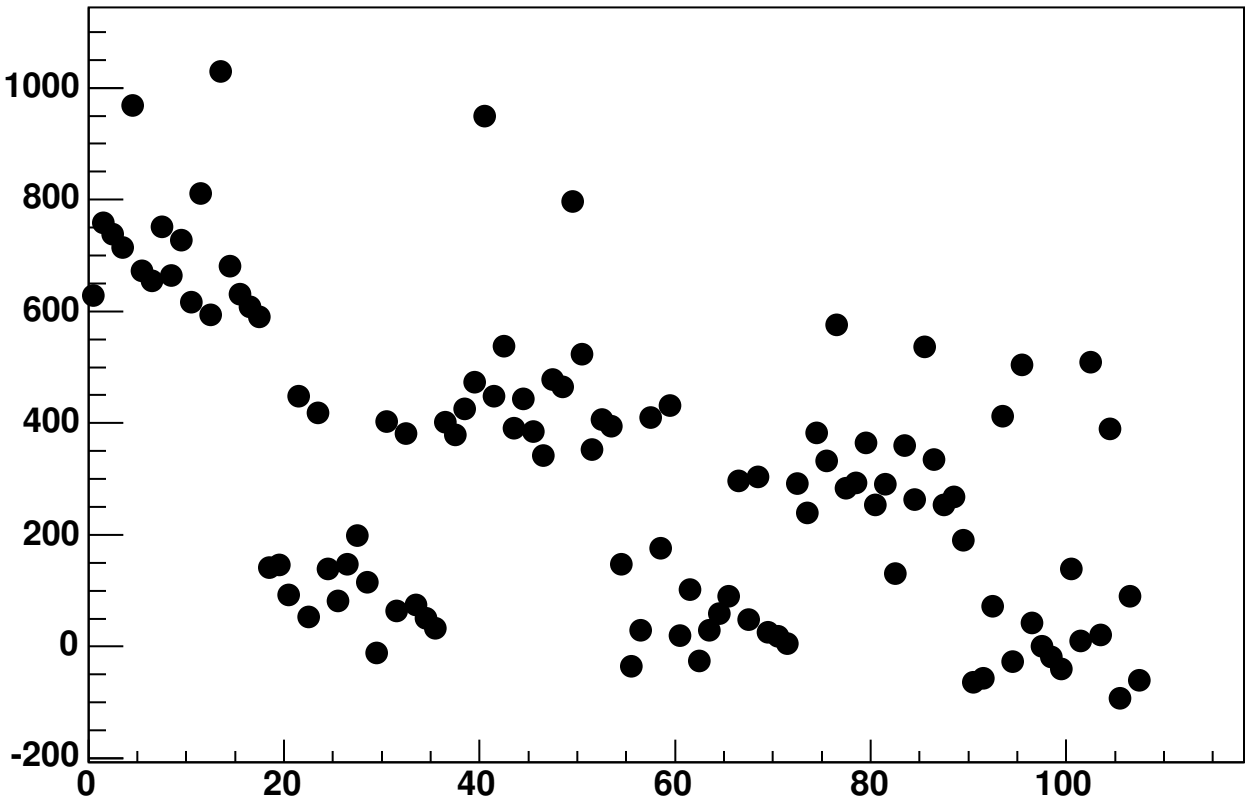
Enable 3, Hold=35, DAC=64000, ADC Mean vs 18*Chip+Chan



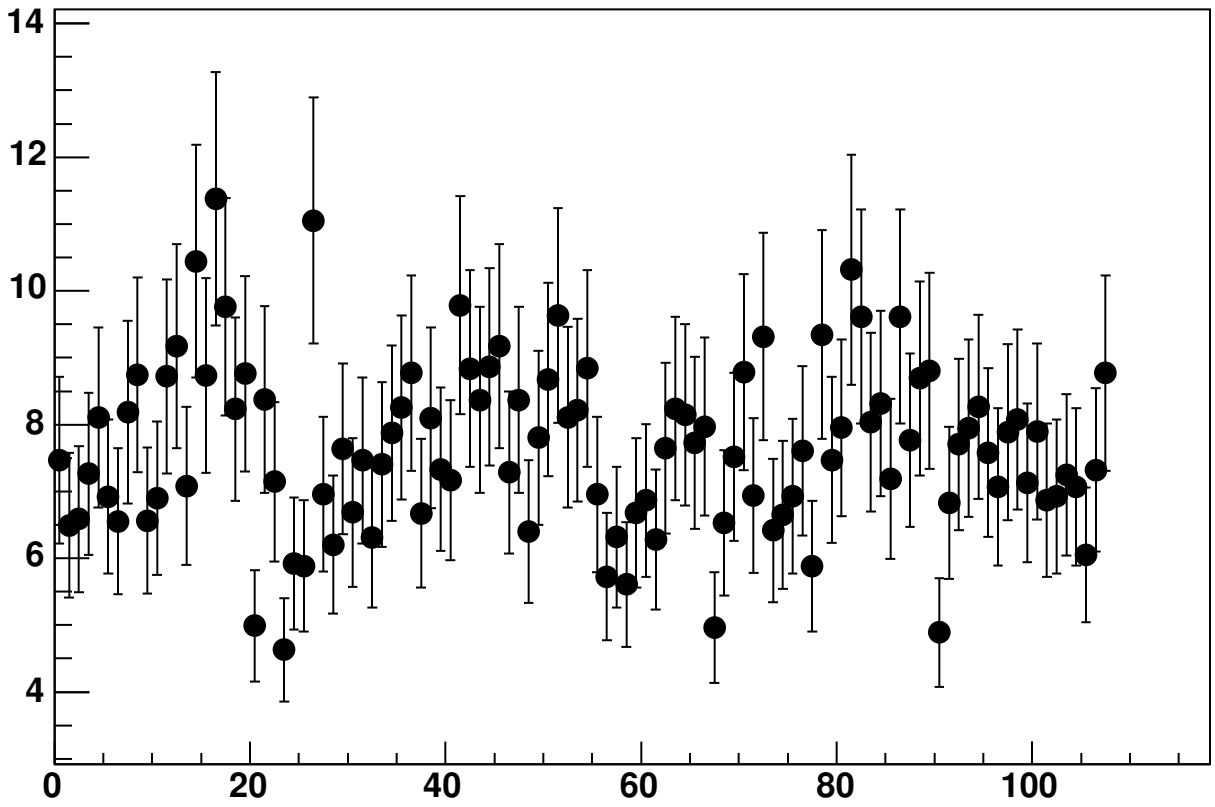
Enable 3, Hold=35, DAC=64000, ADC Noise vs 18*Chip+Chan



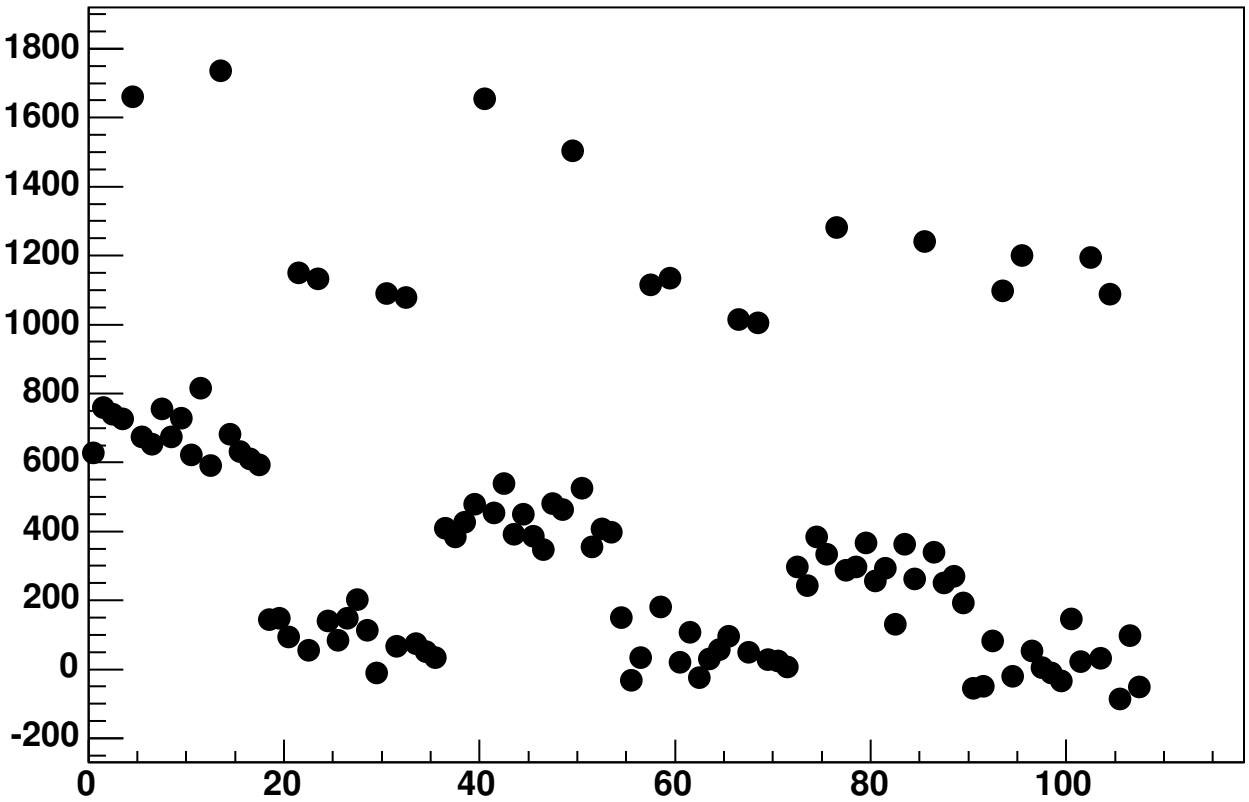
Enable 4, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



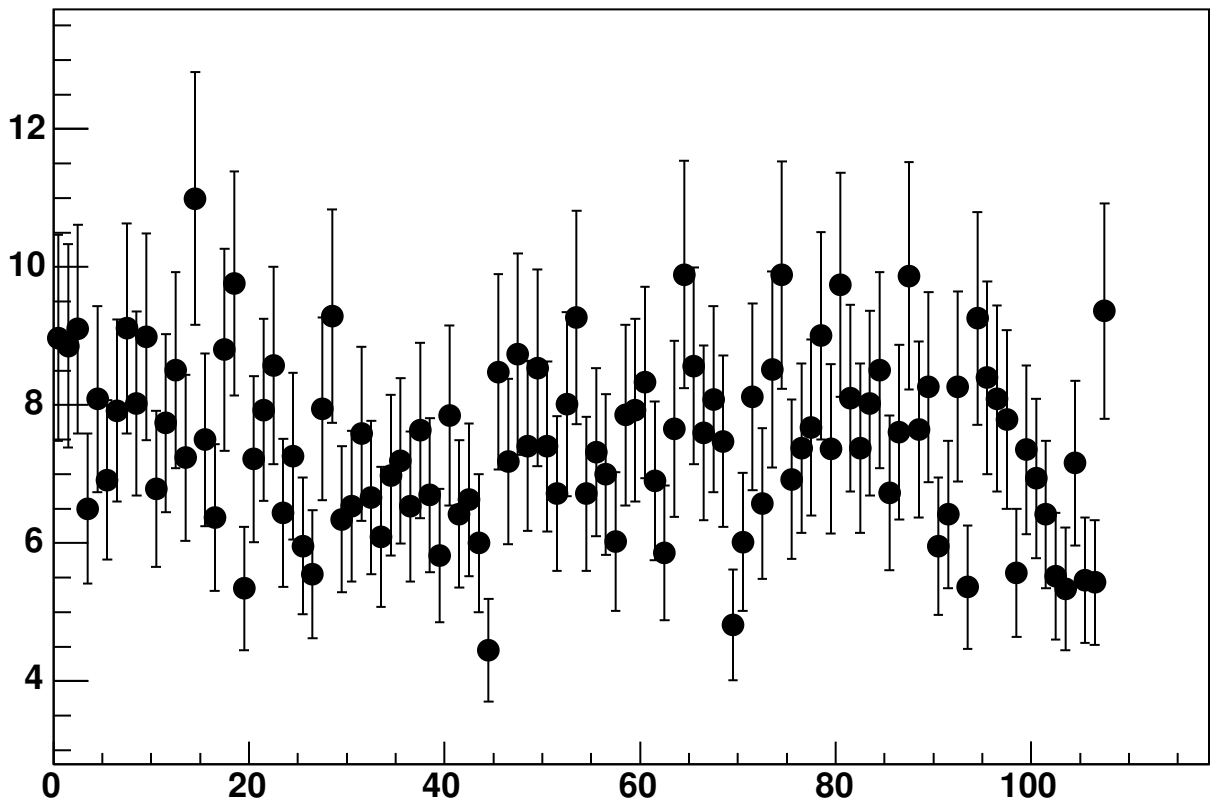
Enable 4, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



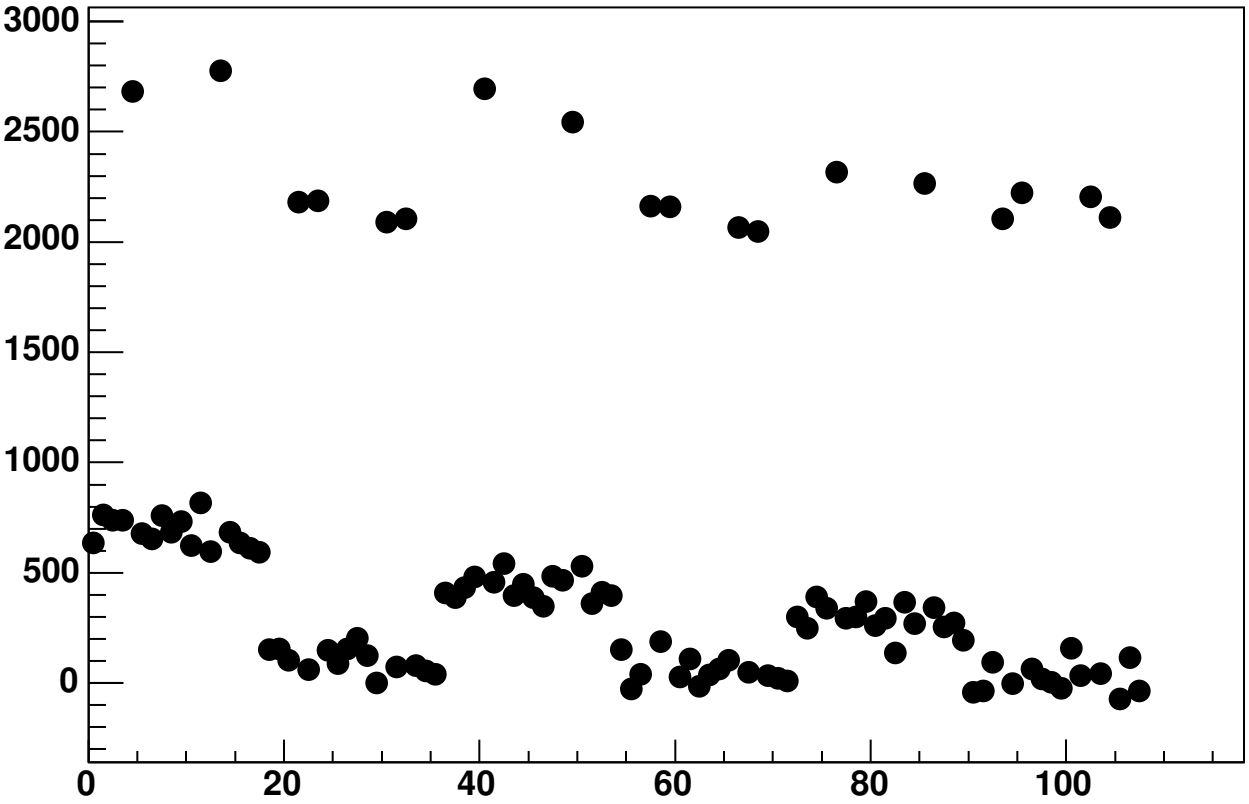
Enable 4, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



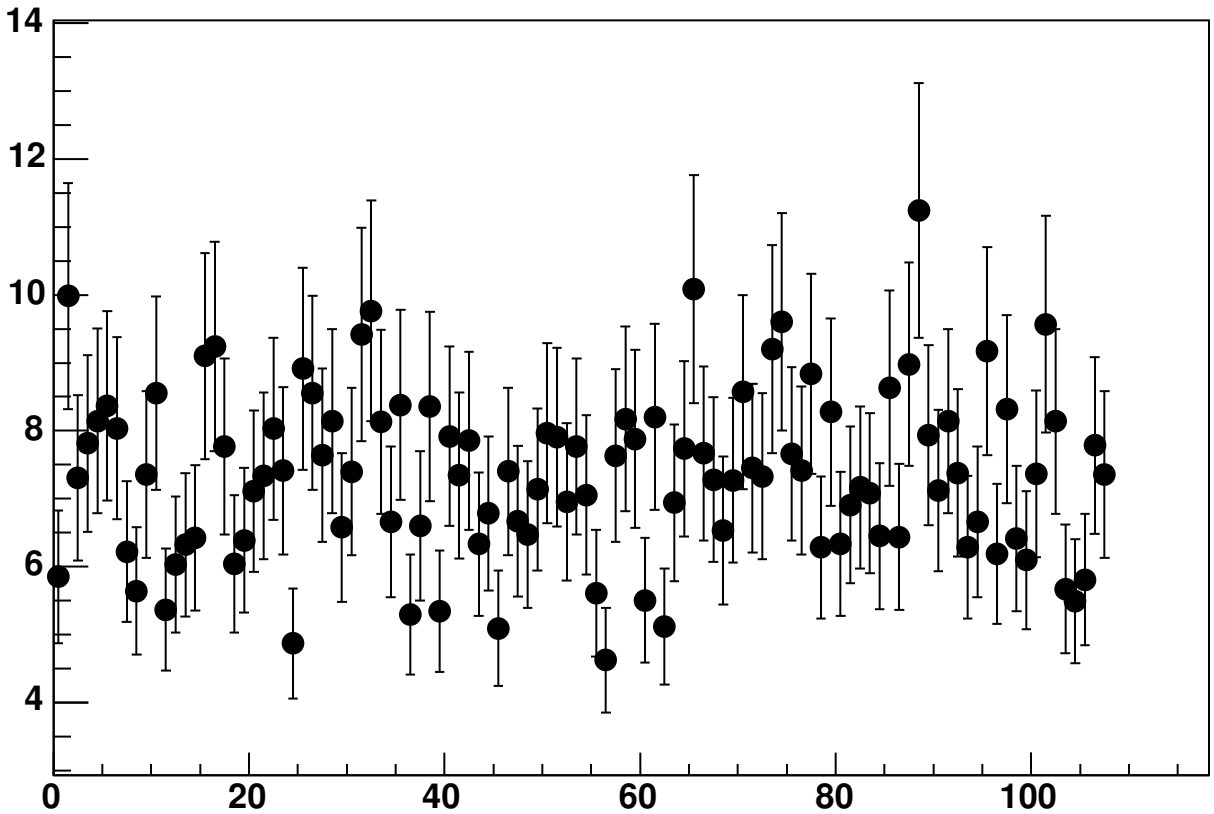
Enable 4, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



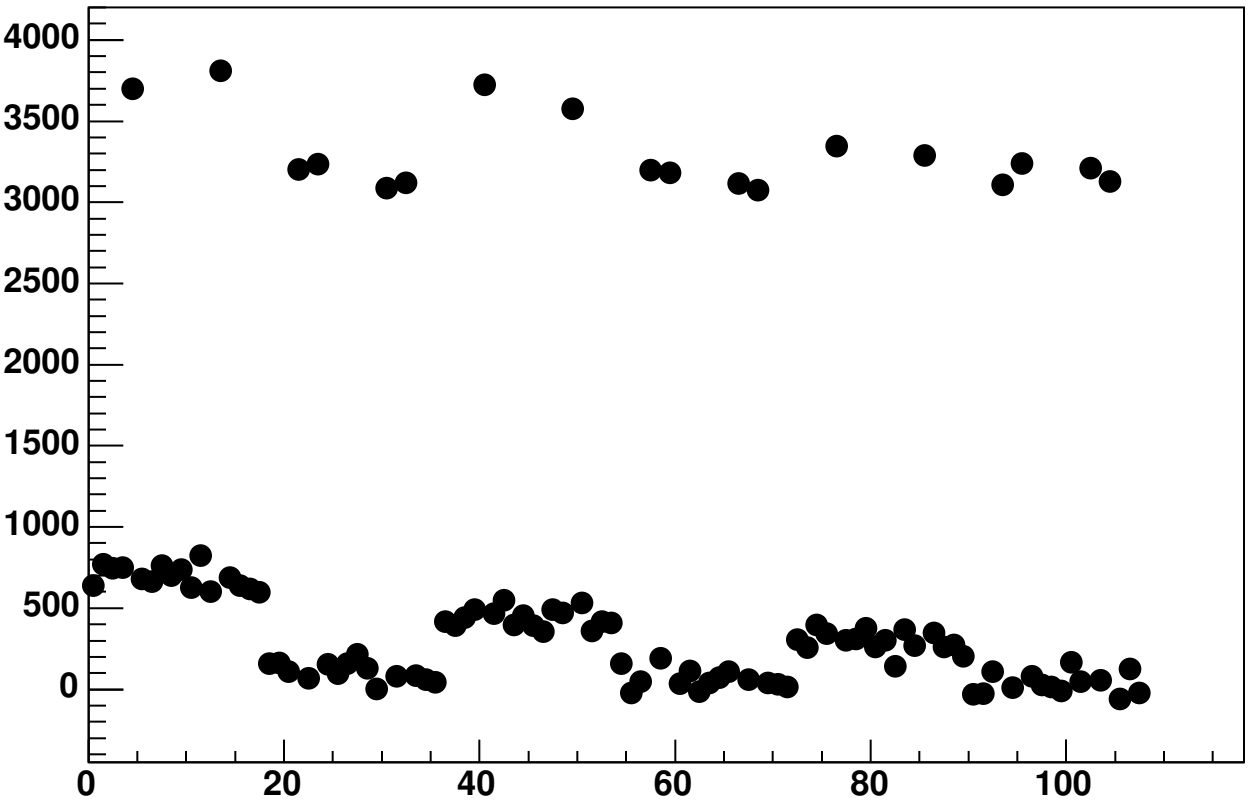
Enable 4, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



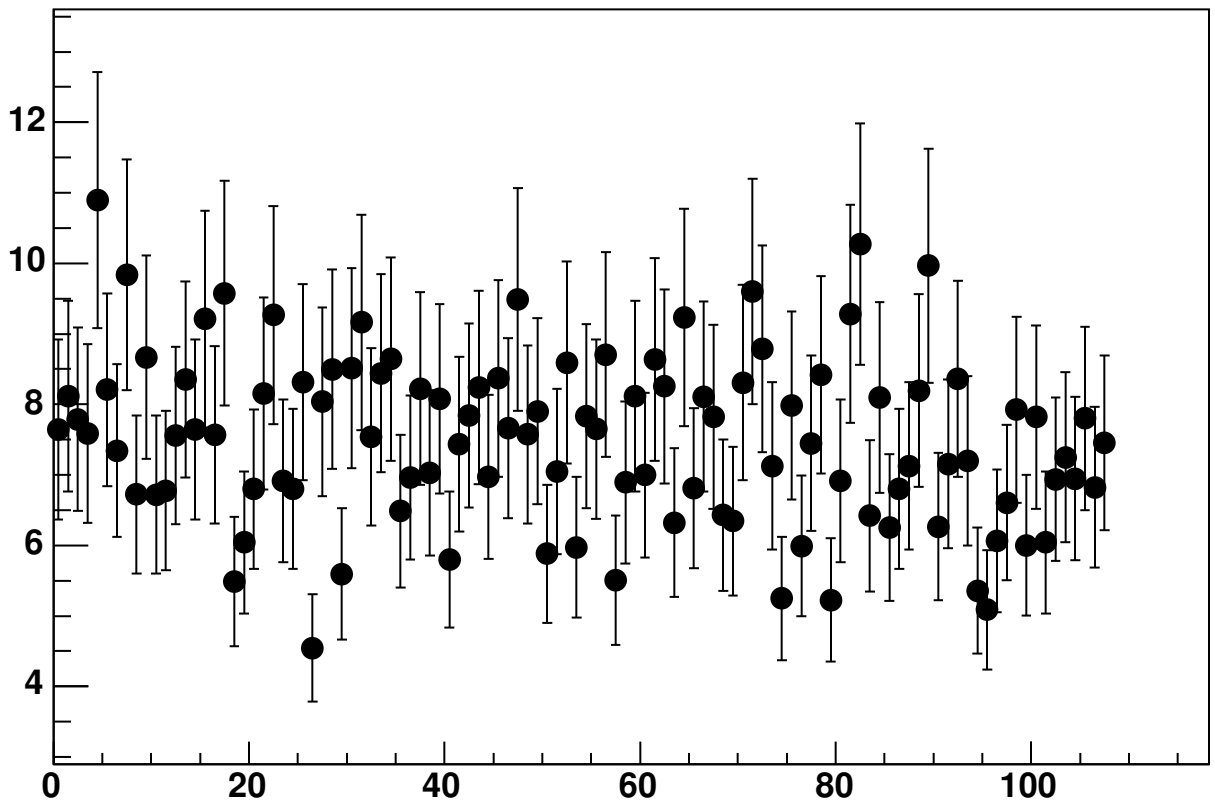
Enable 4, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



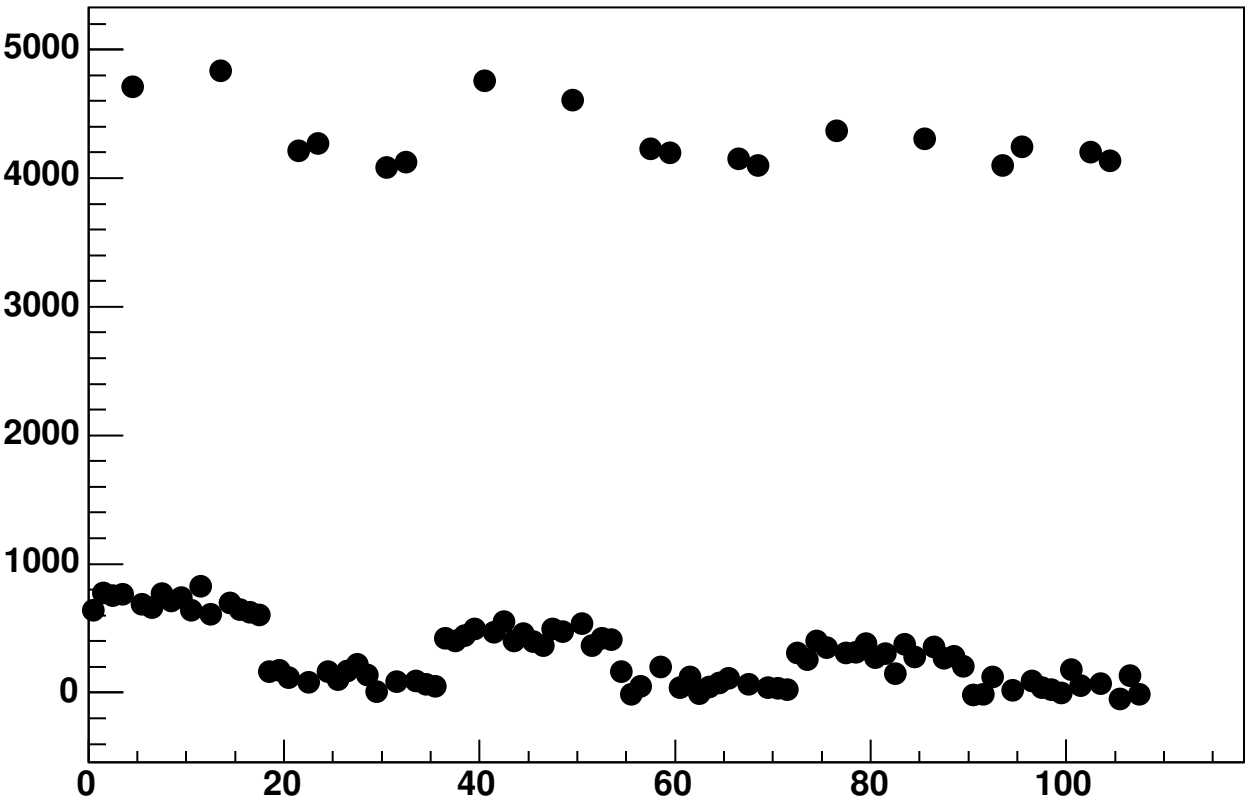
Enable 4, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



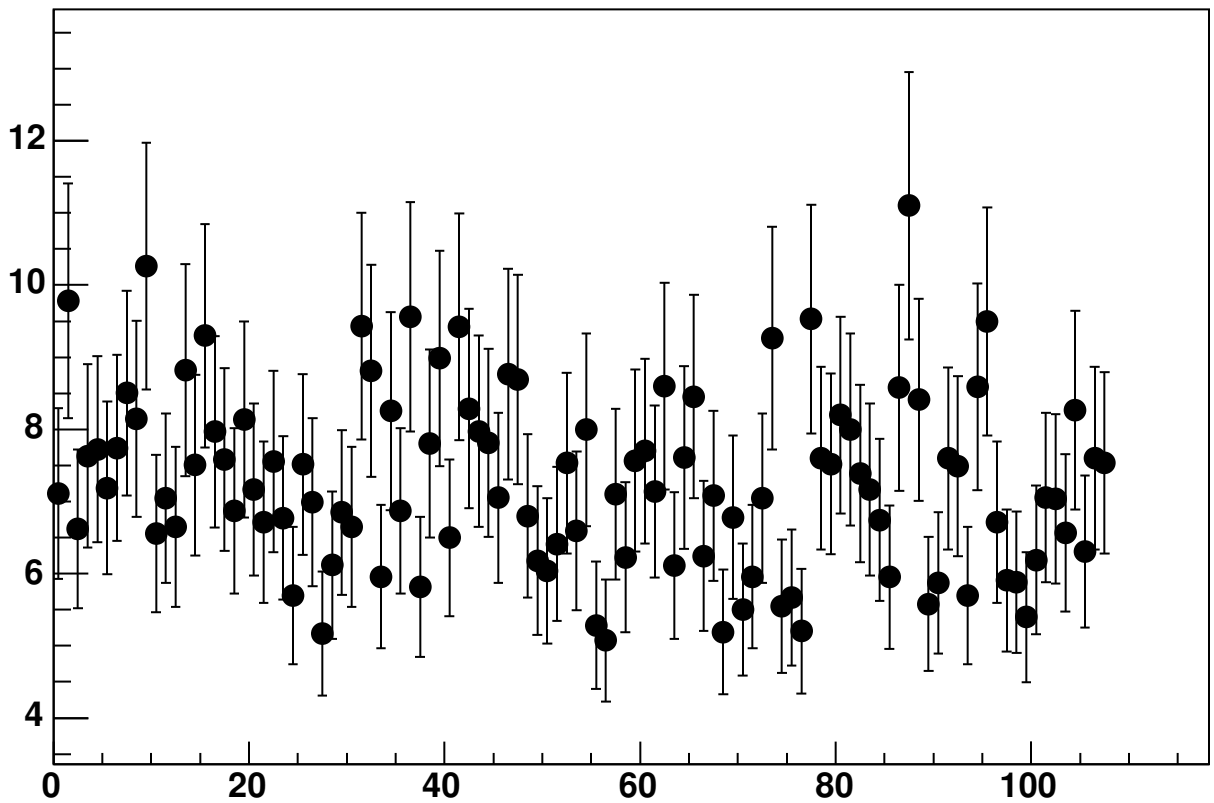
Enable 4, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



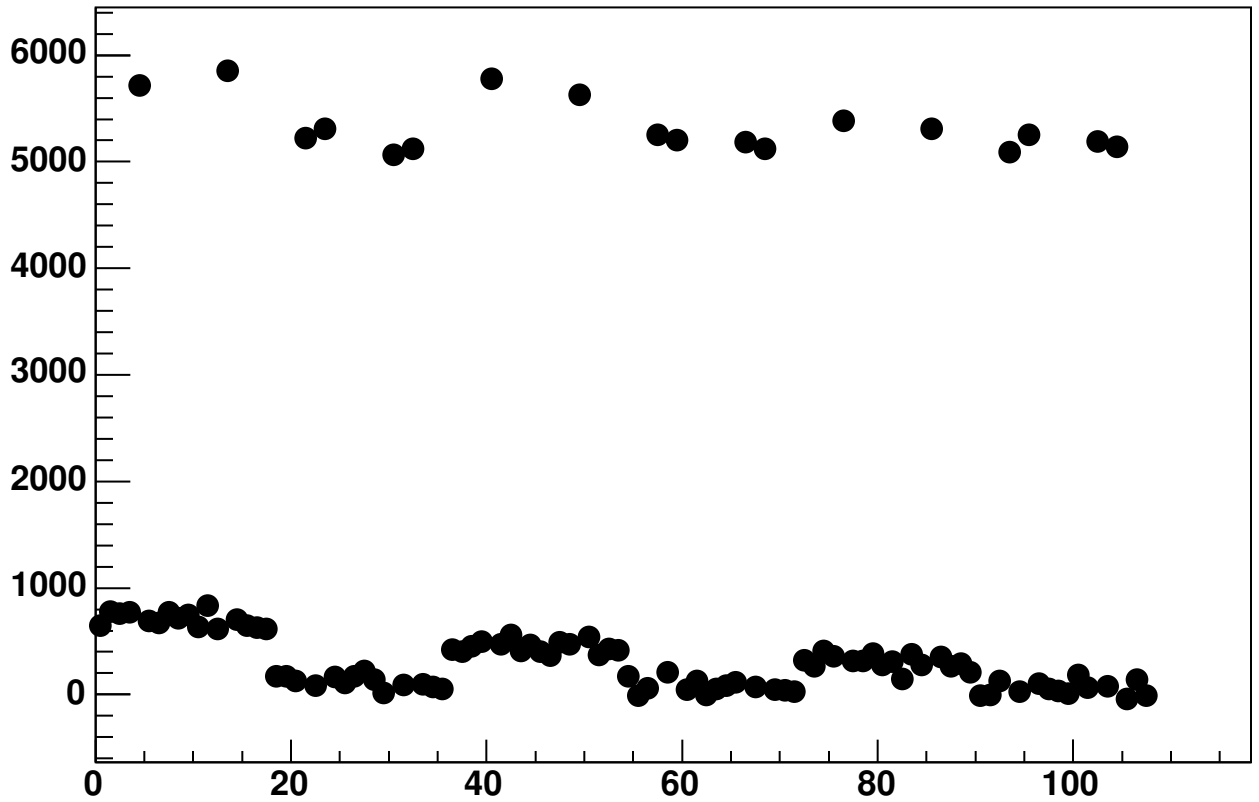
Enable 4, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



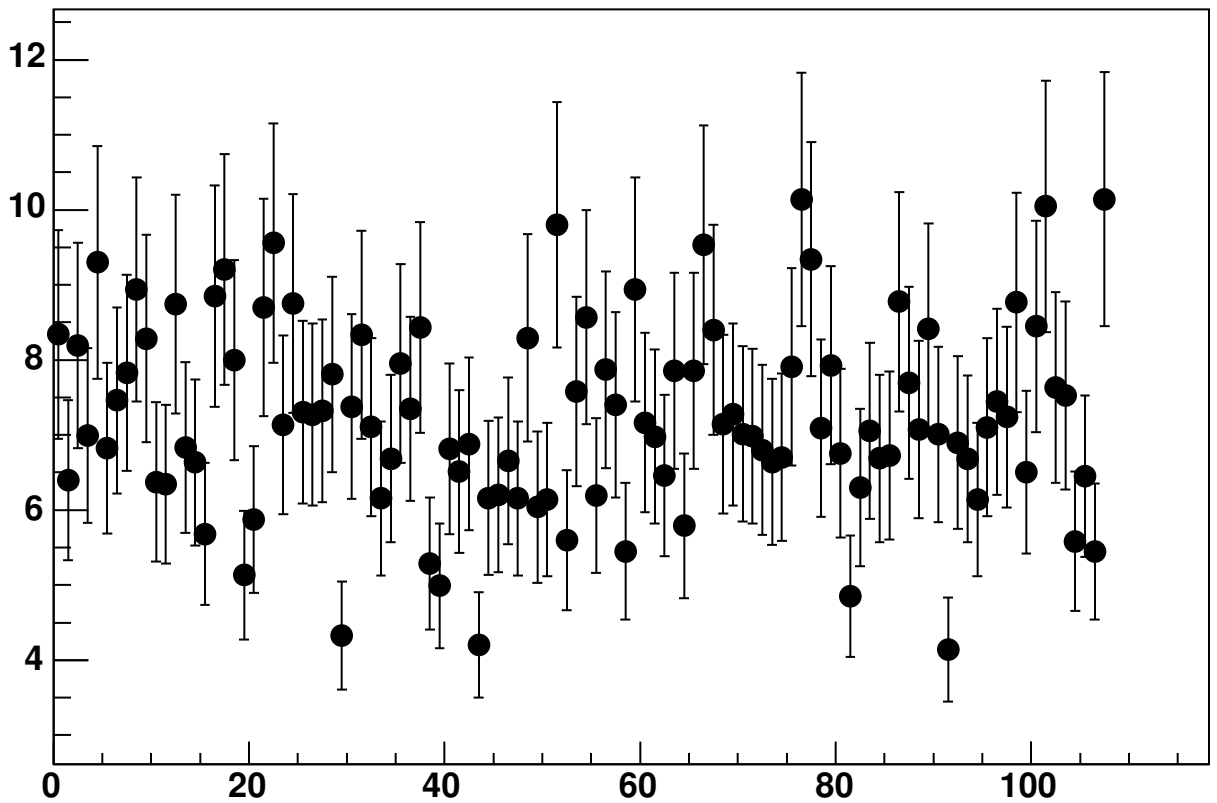
Enable 4, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



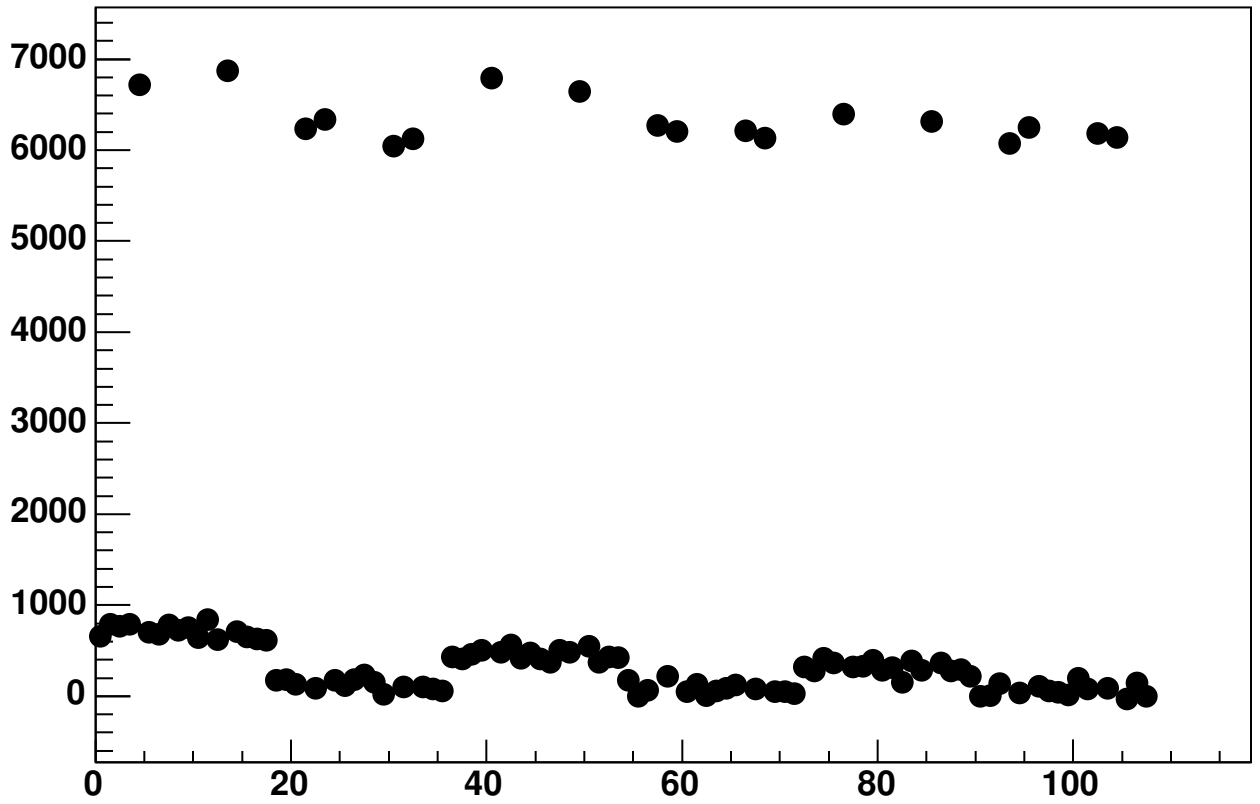
Enable 4, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



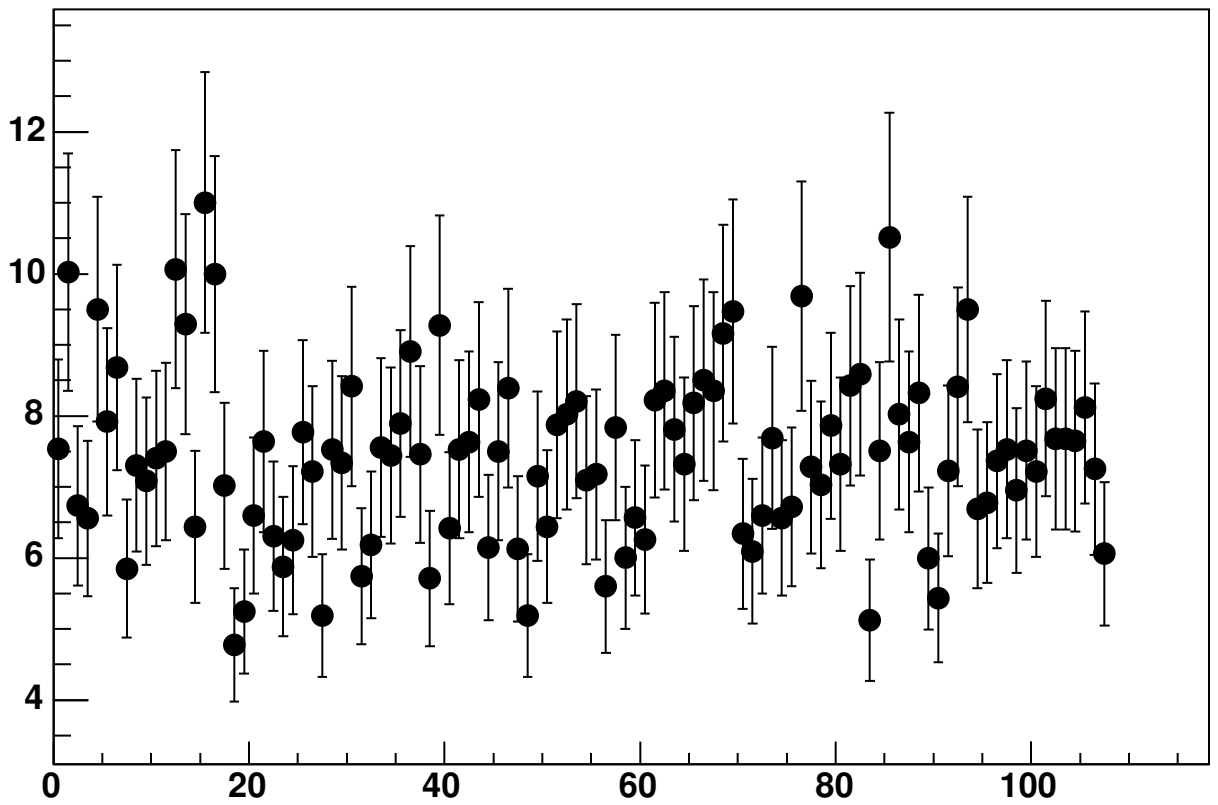
Enable 4, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



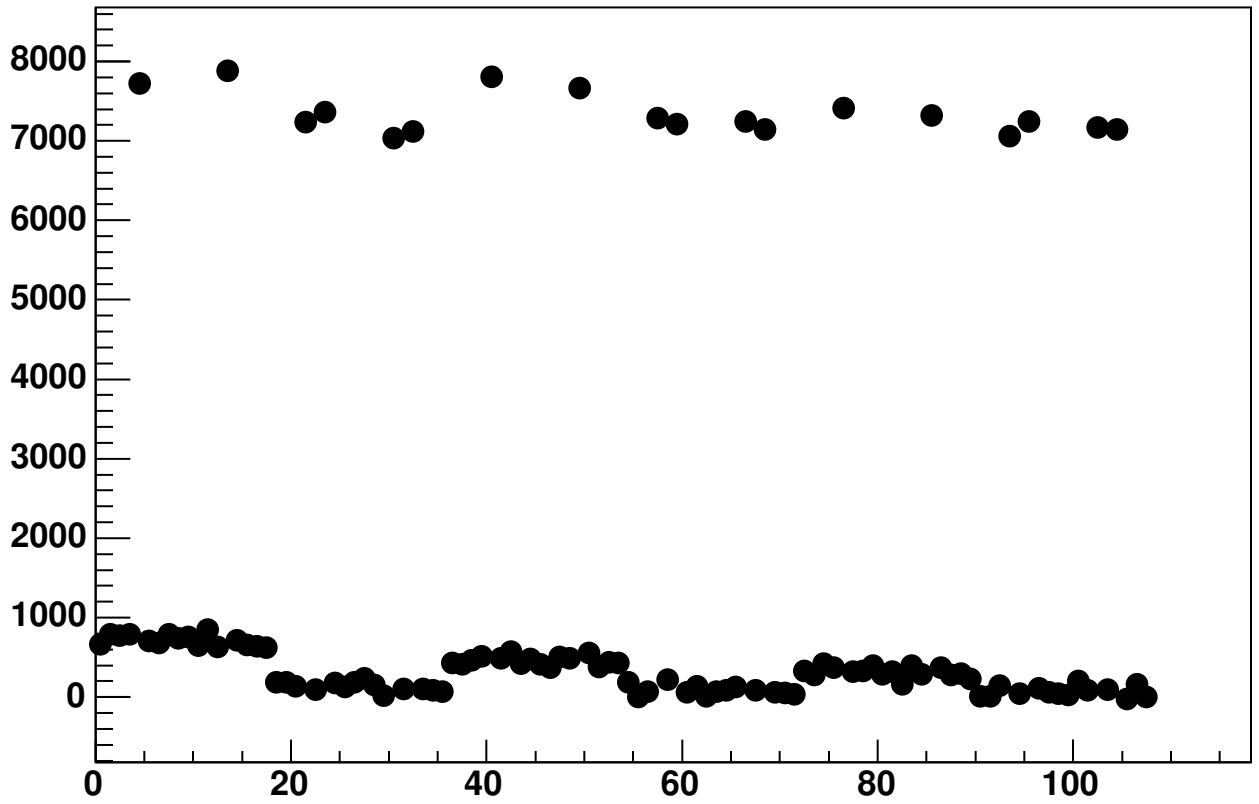
Enable 4, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



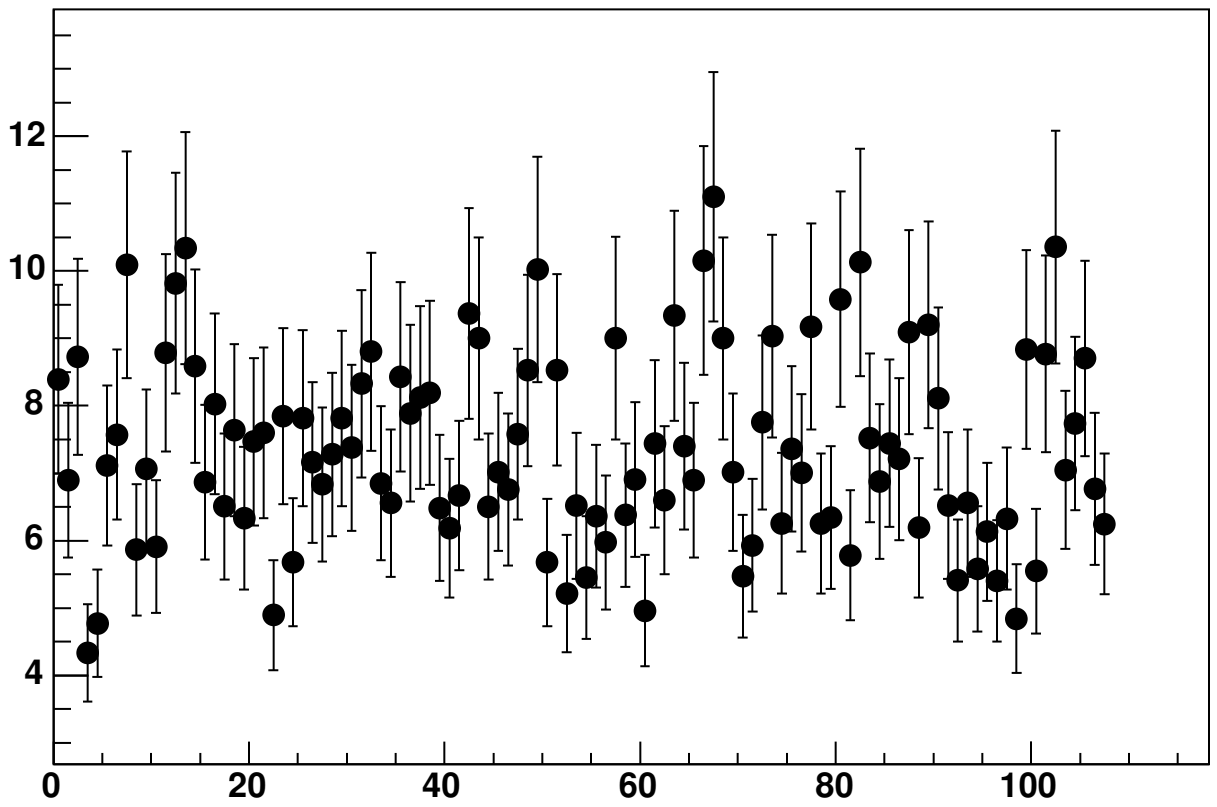
Enable 4, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



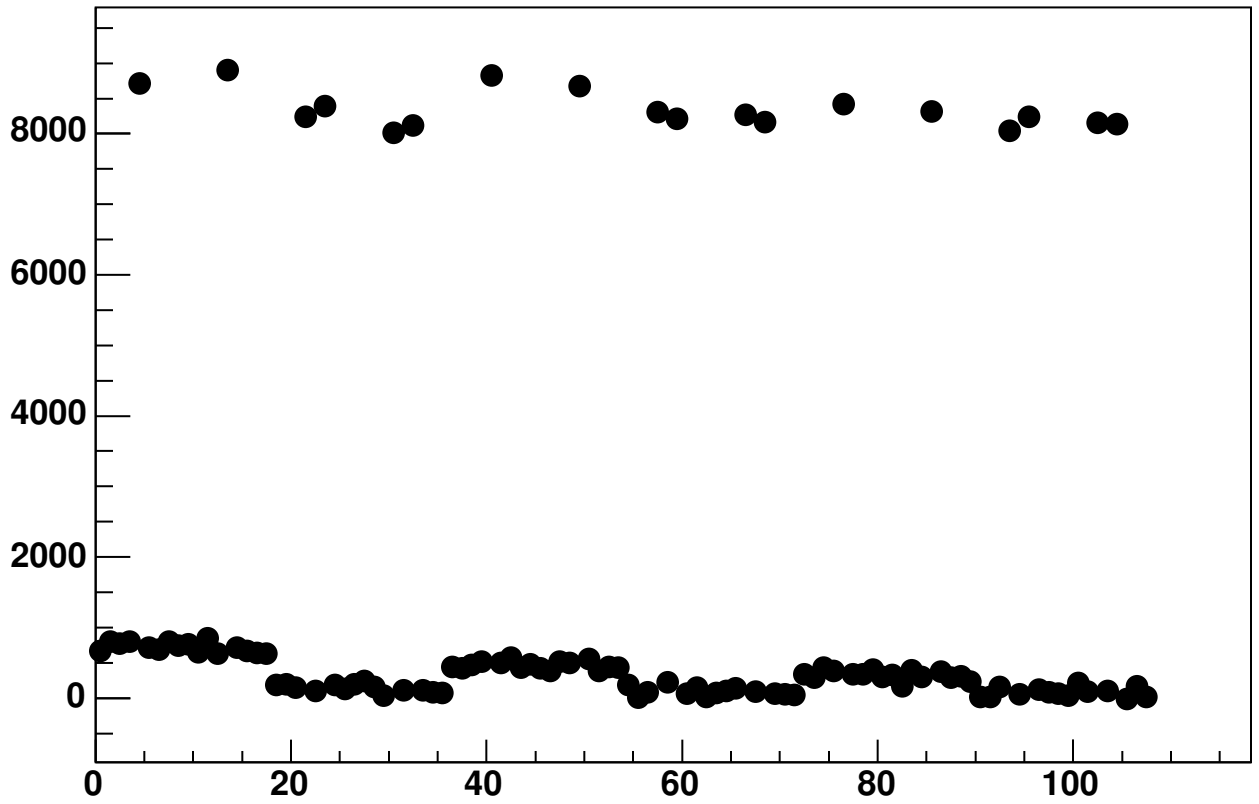
Enable 4, Hold=35, DAC=14000, ADC Mean vs 18*Chip+Chan



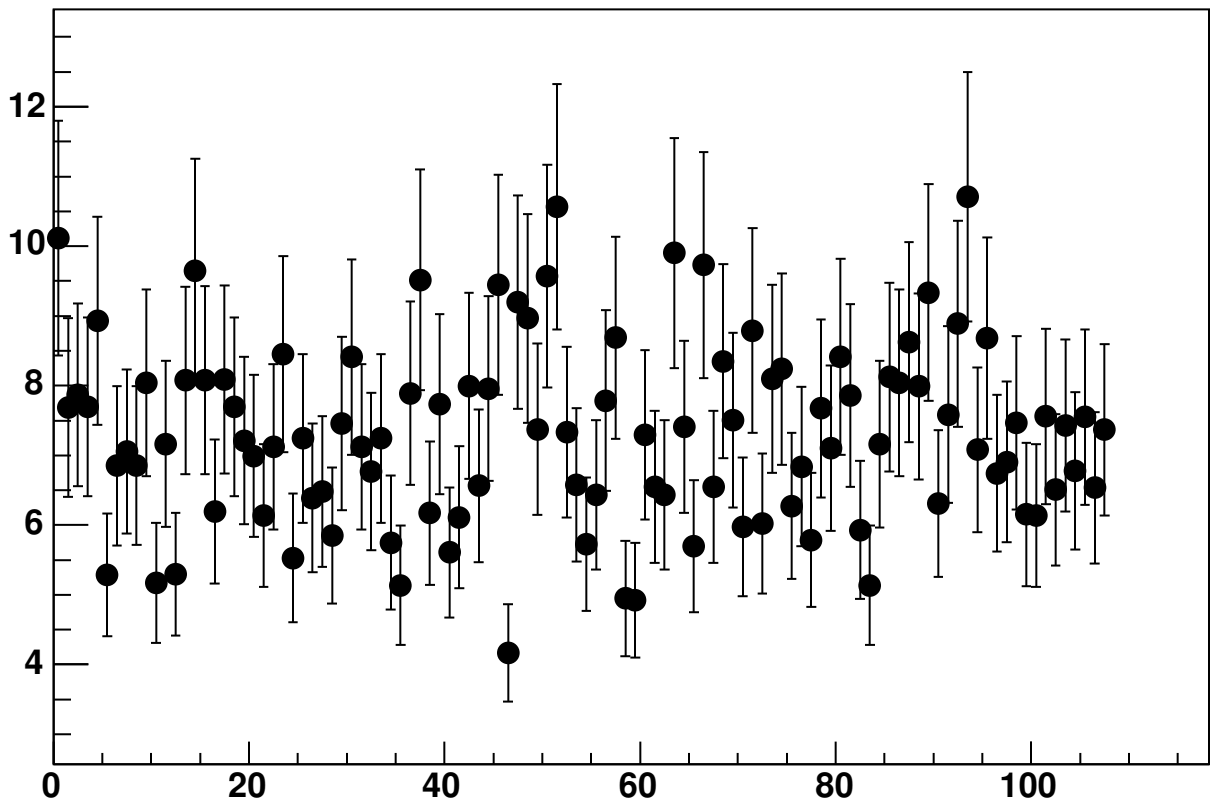
Enable 4, Hold=35, DAC=14000, ADC Noise vs 18*Chip+Chan



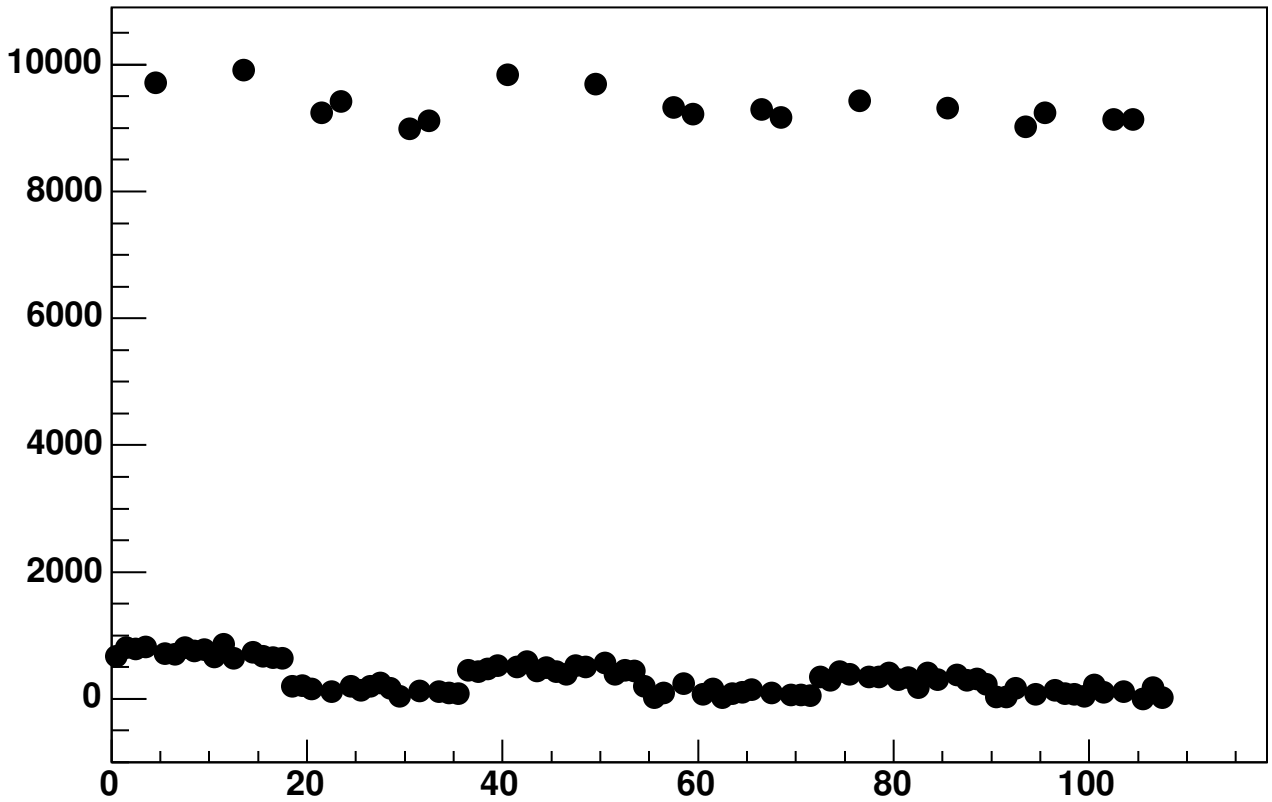
Enable 4, Hold=35, DAC=16000, ADC Mean vs 18*Chip+Chan



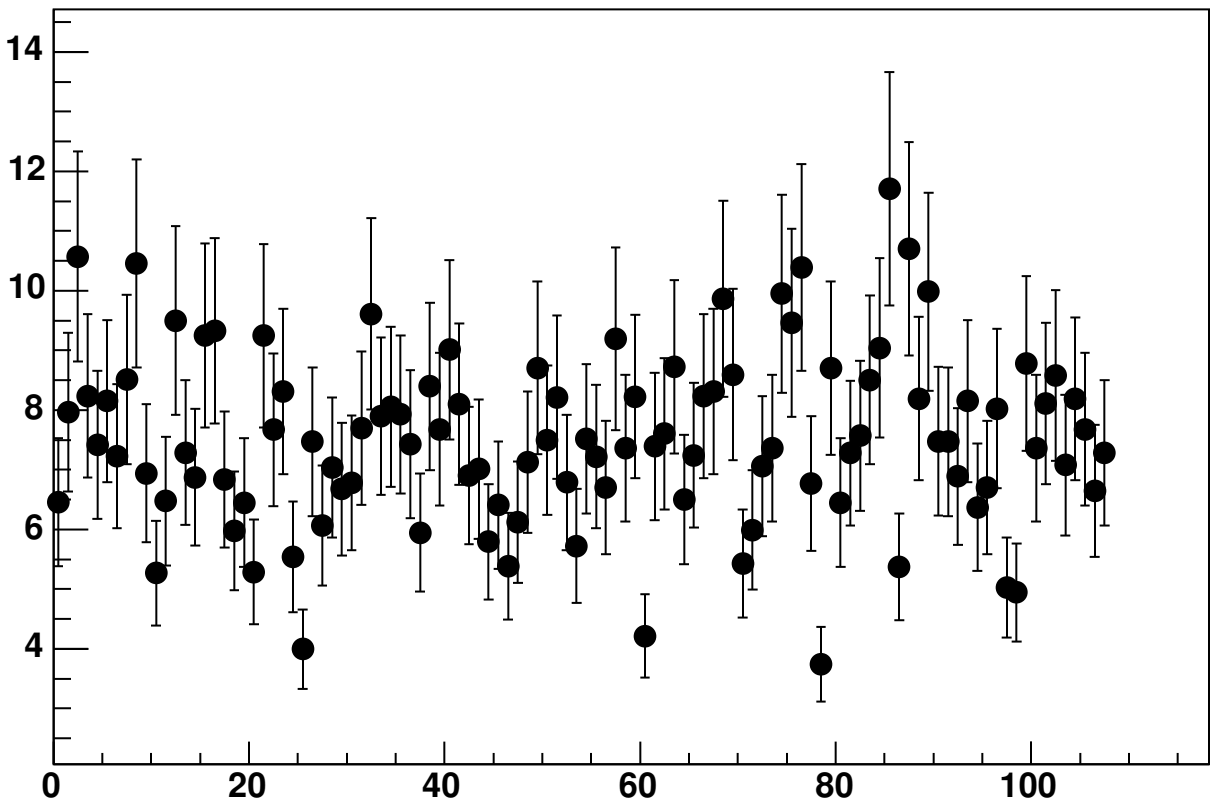
Enable 4, Hold=35, DAC=16000, ADC Noise vs 18*Chip+Chan



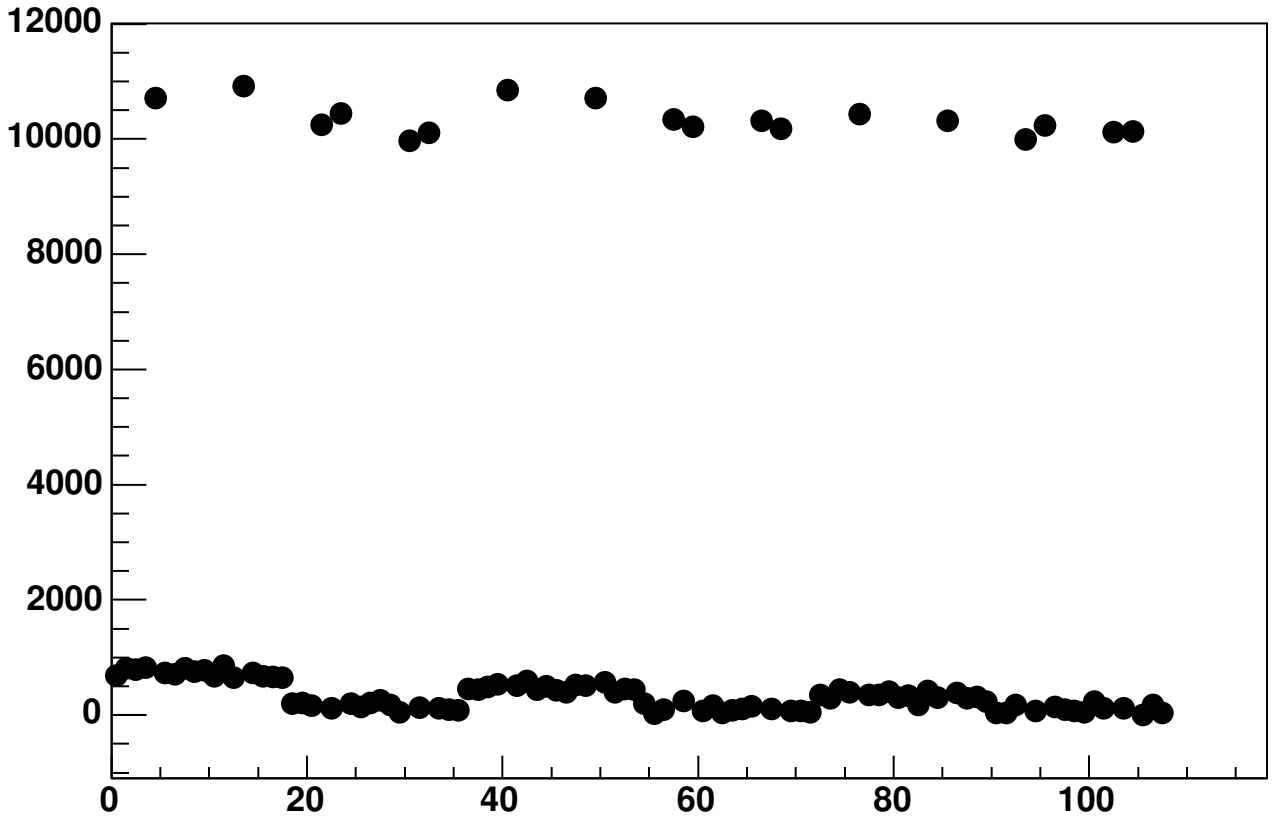
Enable 4, Hold=35, DAC=18000, ADC Mean vs 18*Chip+Chan



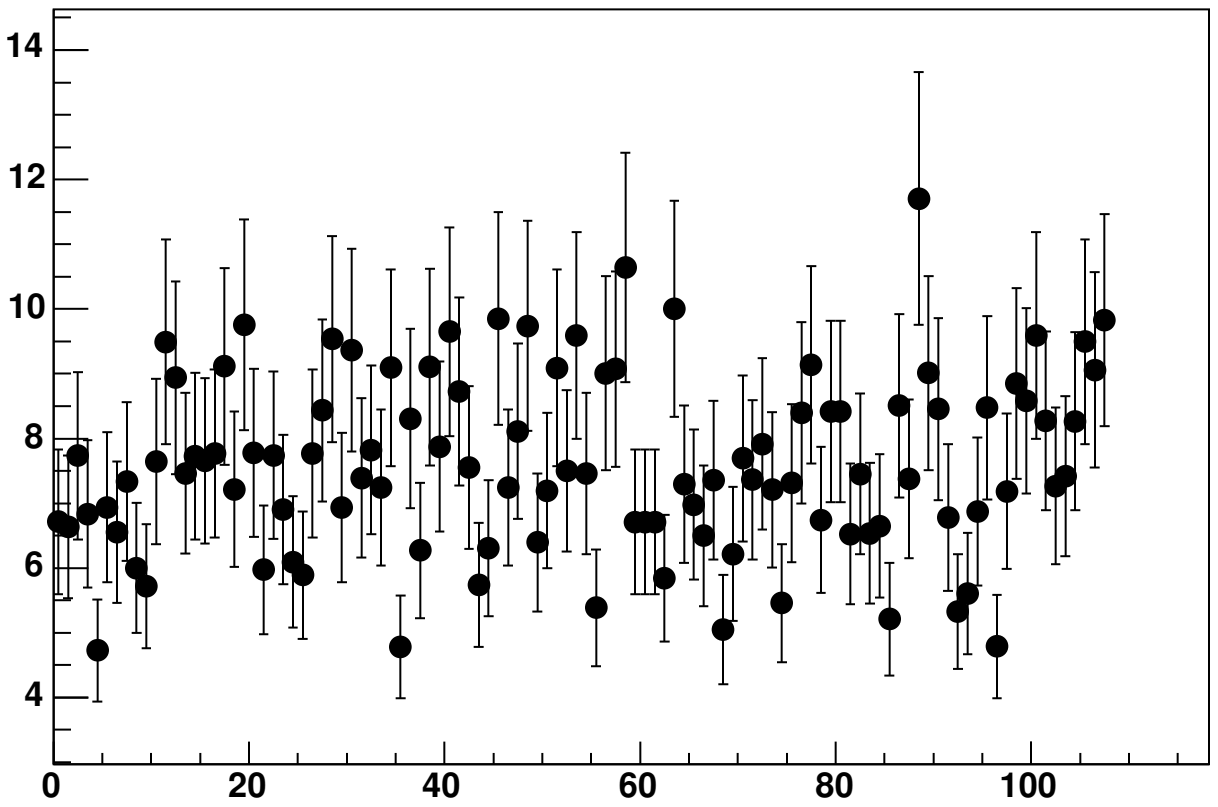
Enable 4, Hold=35, DAC=18000, ADC Noise vs 18*Chip+Chan



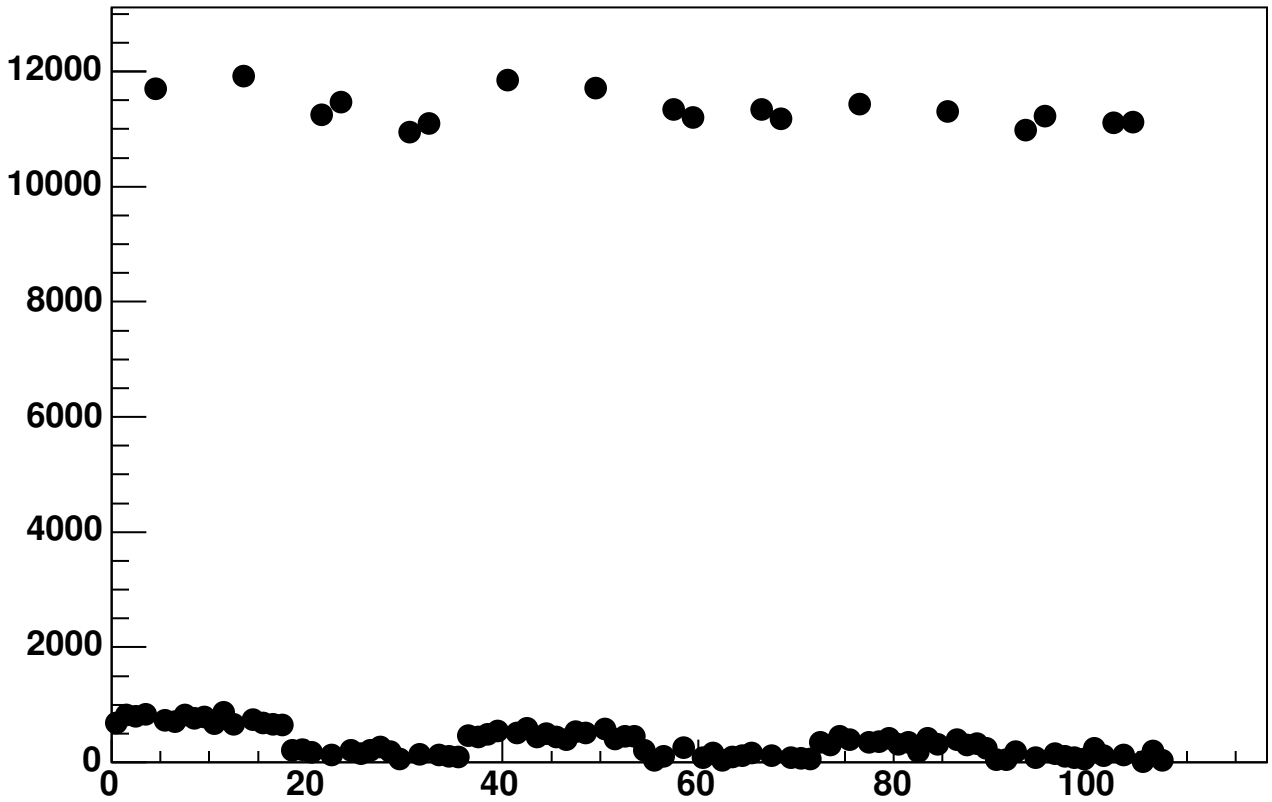
Enable 4, Hold=35, DAC=20000, ADC Mean vs 18*Chip+Chan



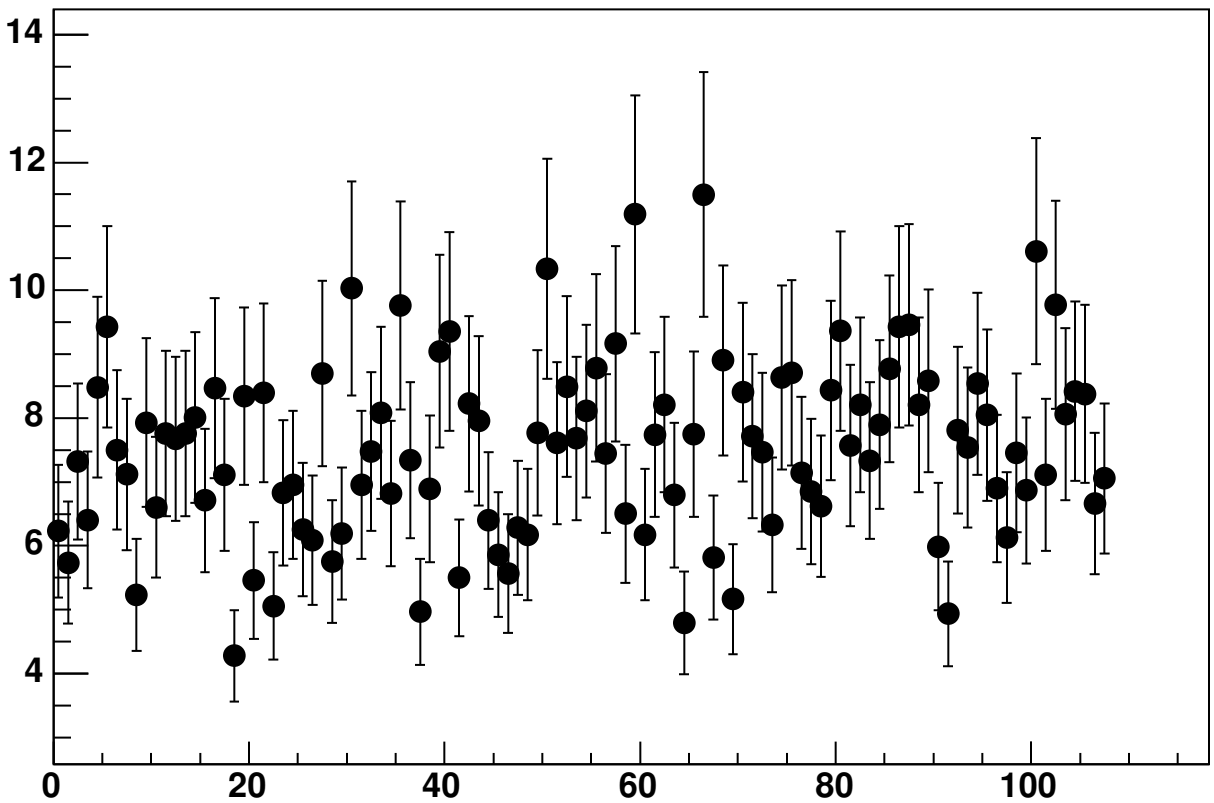
Enable 4, Hold=35, DAC=20000, ADC Noise vs 18*Chip+Chan



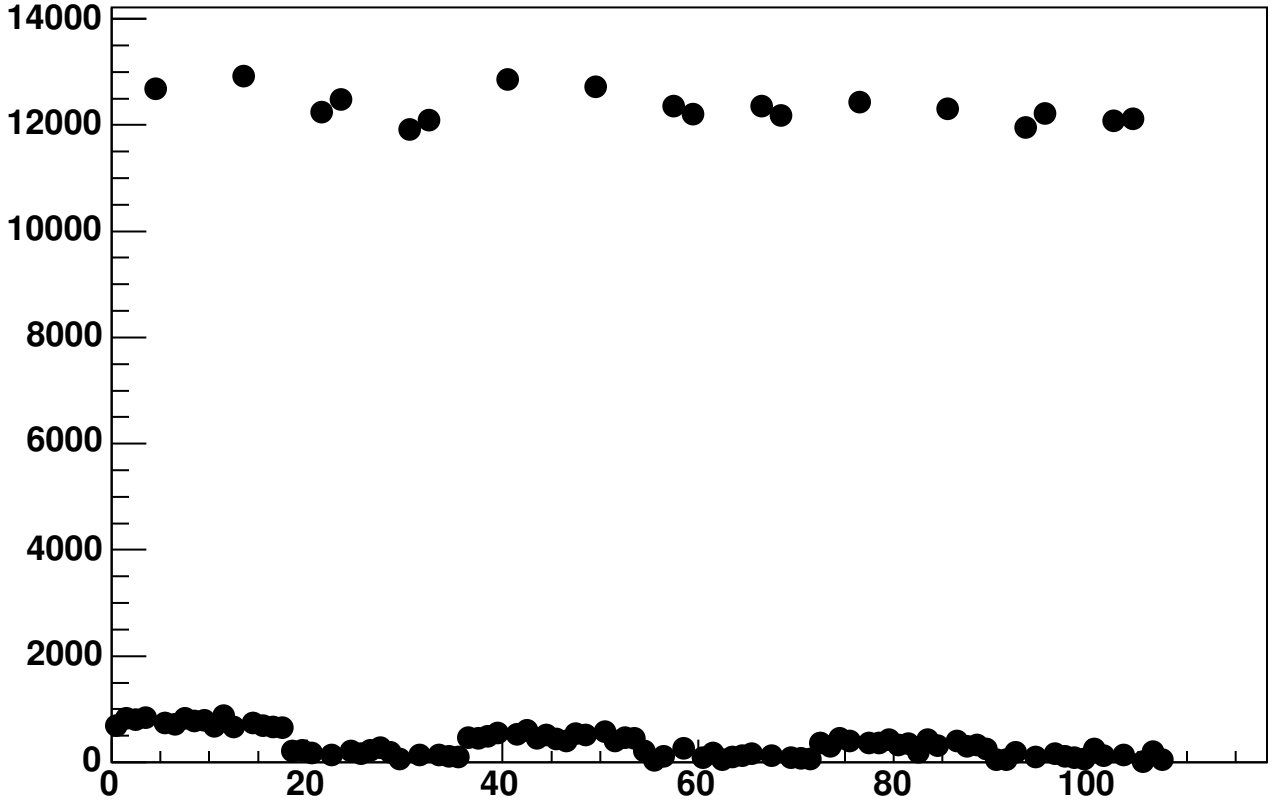
Enable 4, Hold=35, DAC=22000, ADC Mean vs 18*Chip+Chan



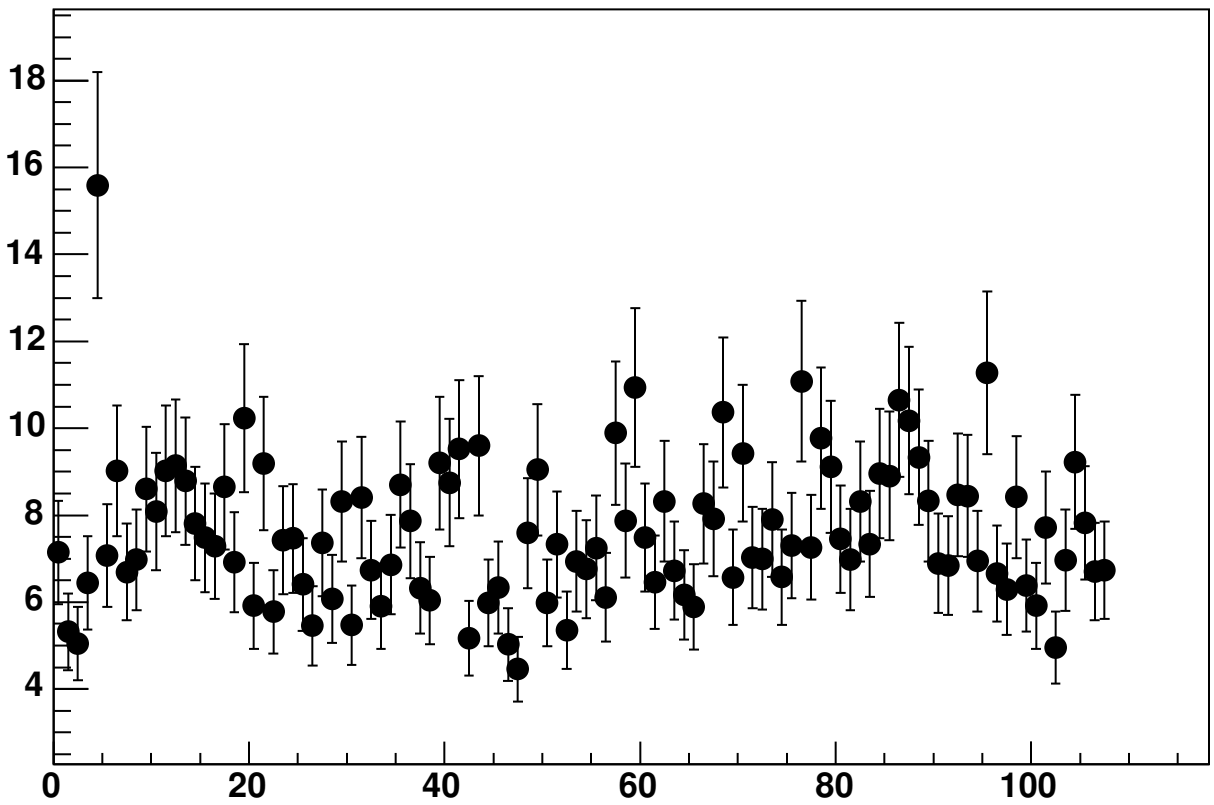
Enable 4, Hold=35, DAC=22000, ADC Noise vs 18*Chip+Chan



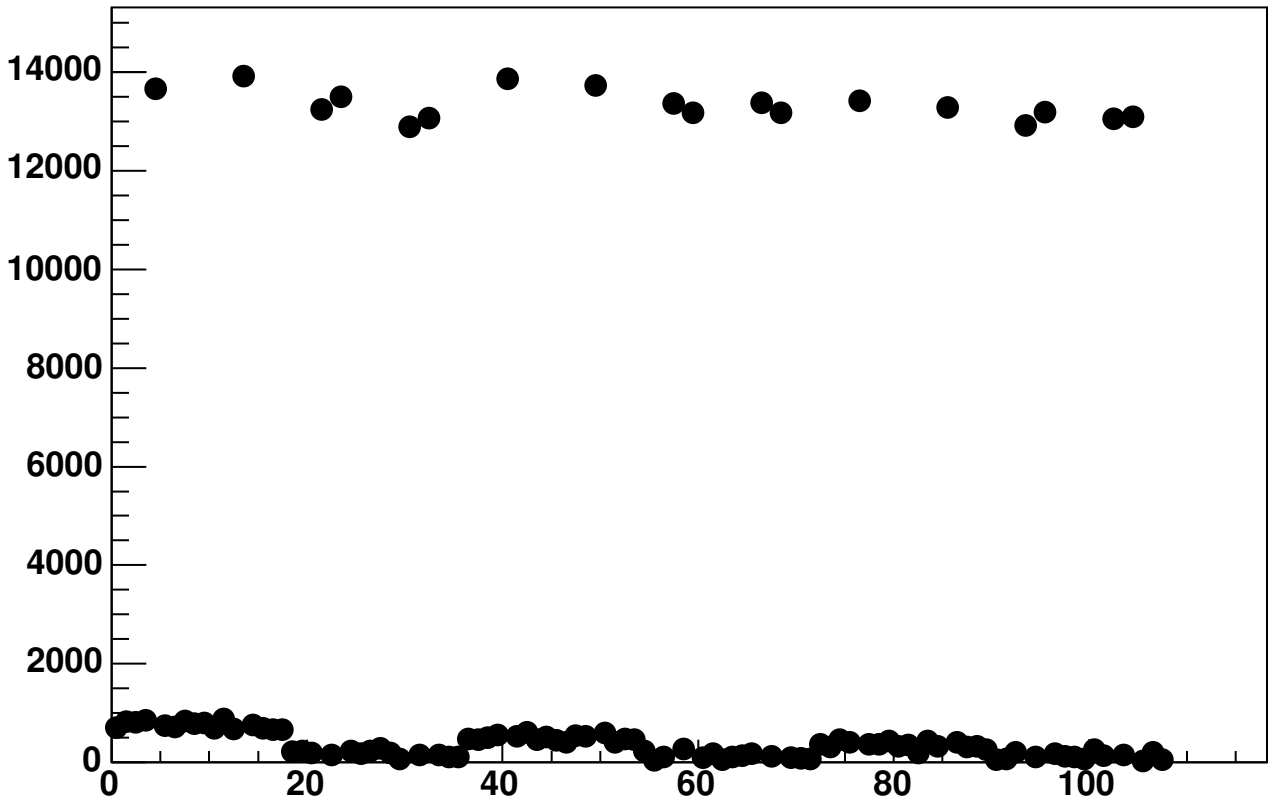
Enable 4, Hold=35, DAC=24000, ADC Mean vs 18*Chip+Chan



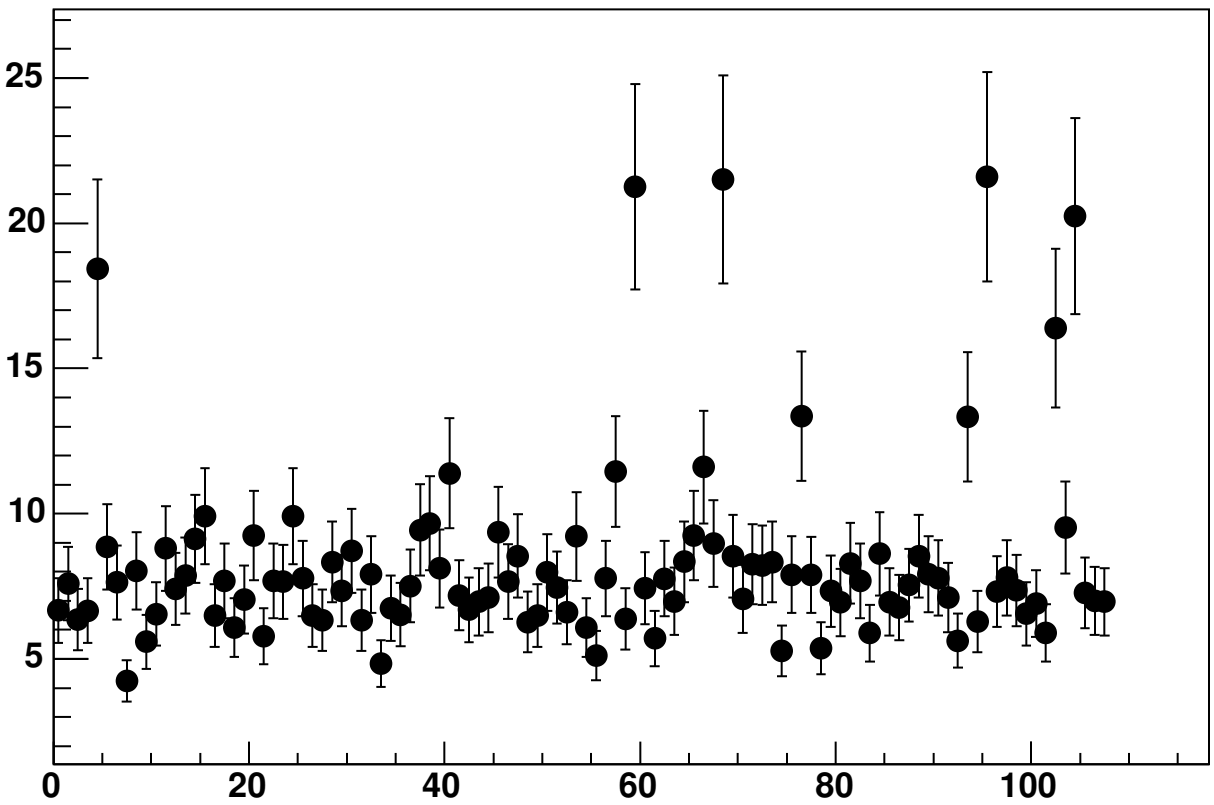
Enable 4, Hold=35, DAC=24000, ADC Noise vs 18*Chip+Chan



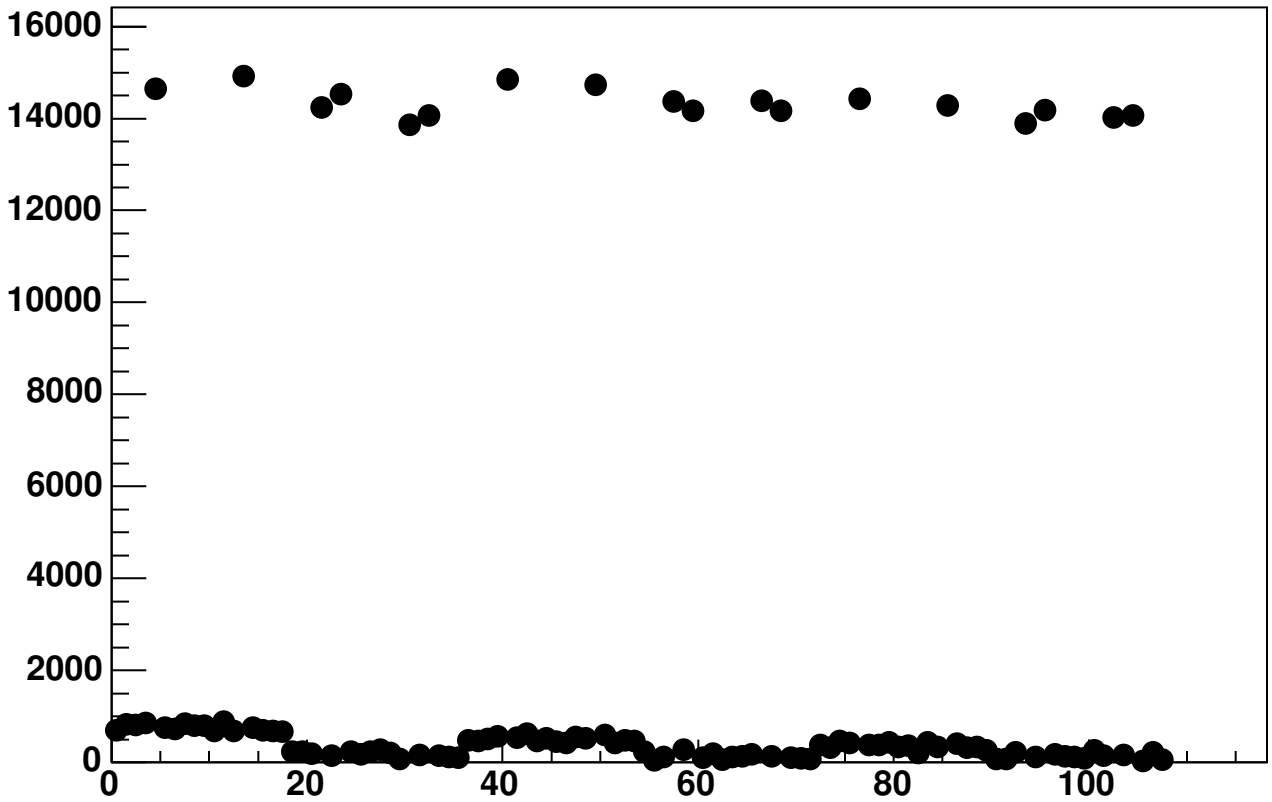
Enable 4, Hold=35, DAC=26000, ADC Mean vs 18*Chip+Chan



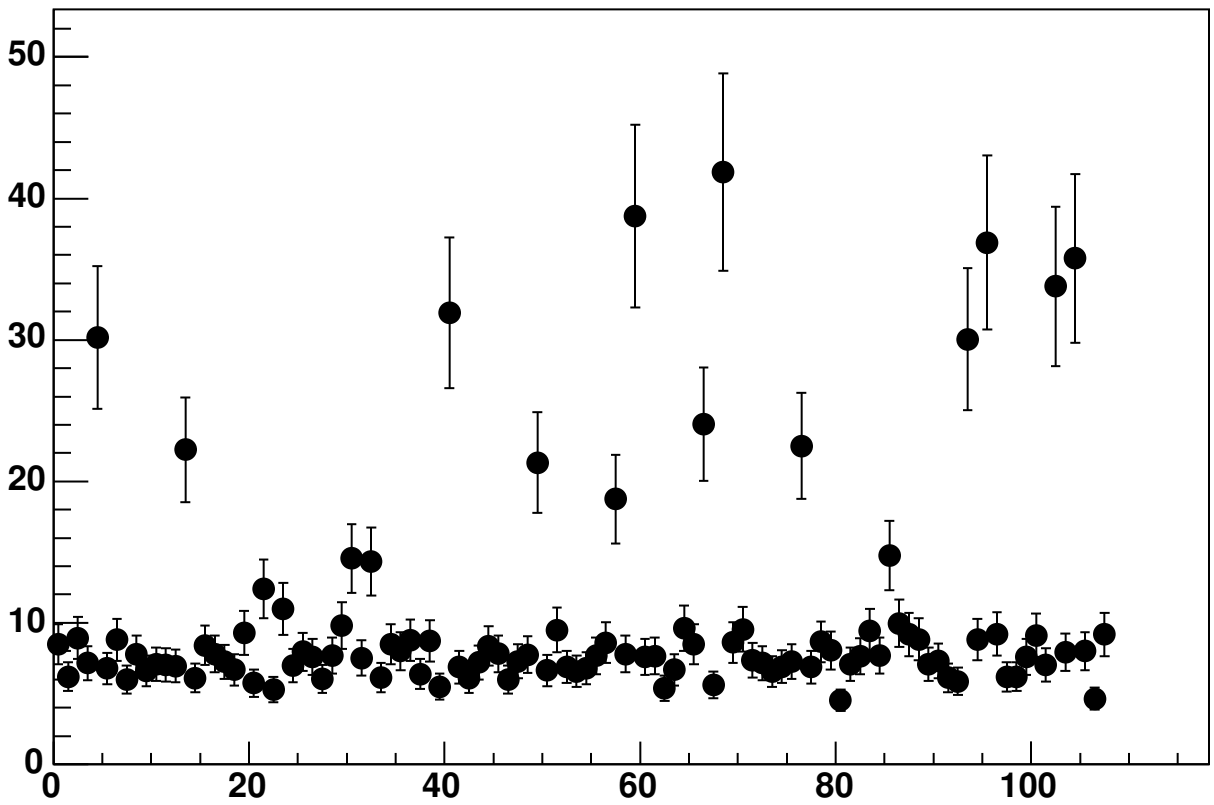
Enable 4, Hold=35, DAC=26000, ADC Noise vs 18*Chip+Chan



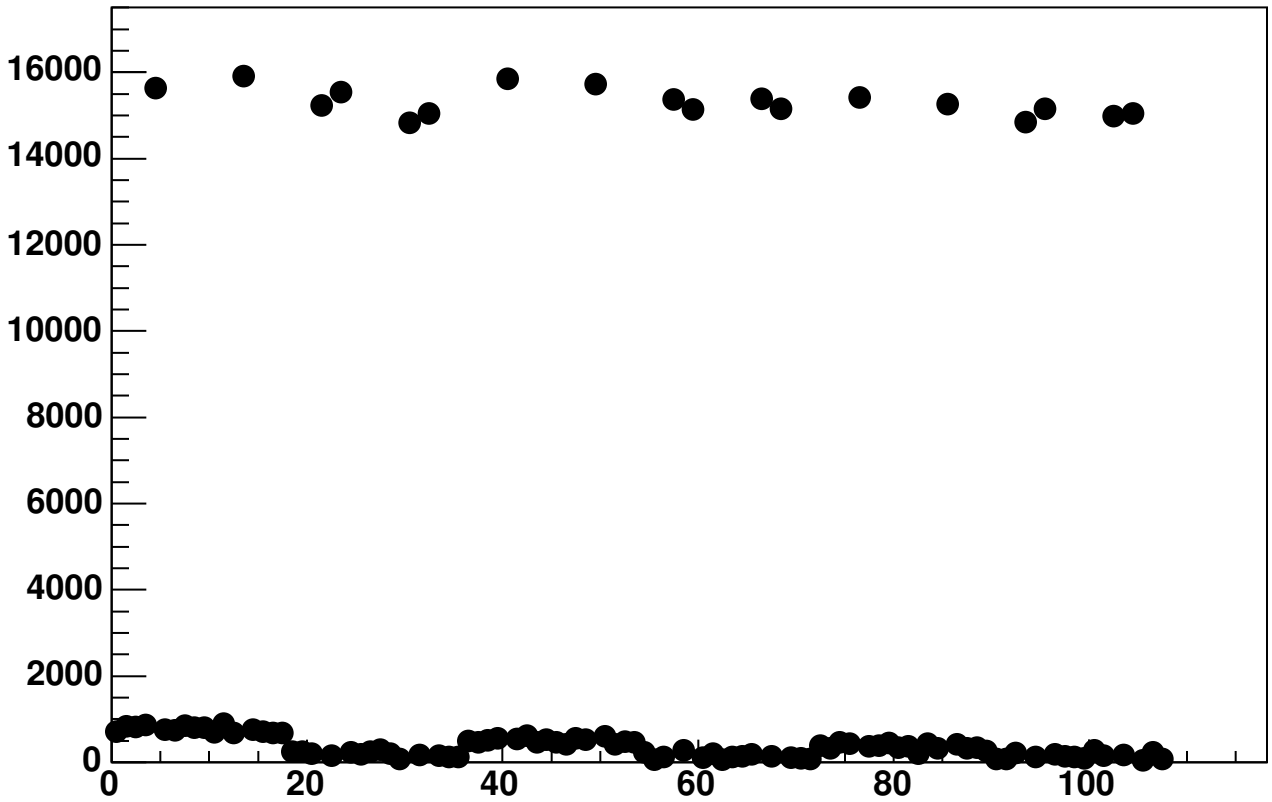
Enable 4, Hold=35, DAC=28000, ADC Mean vs 18*Chip+Chan



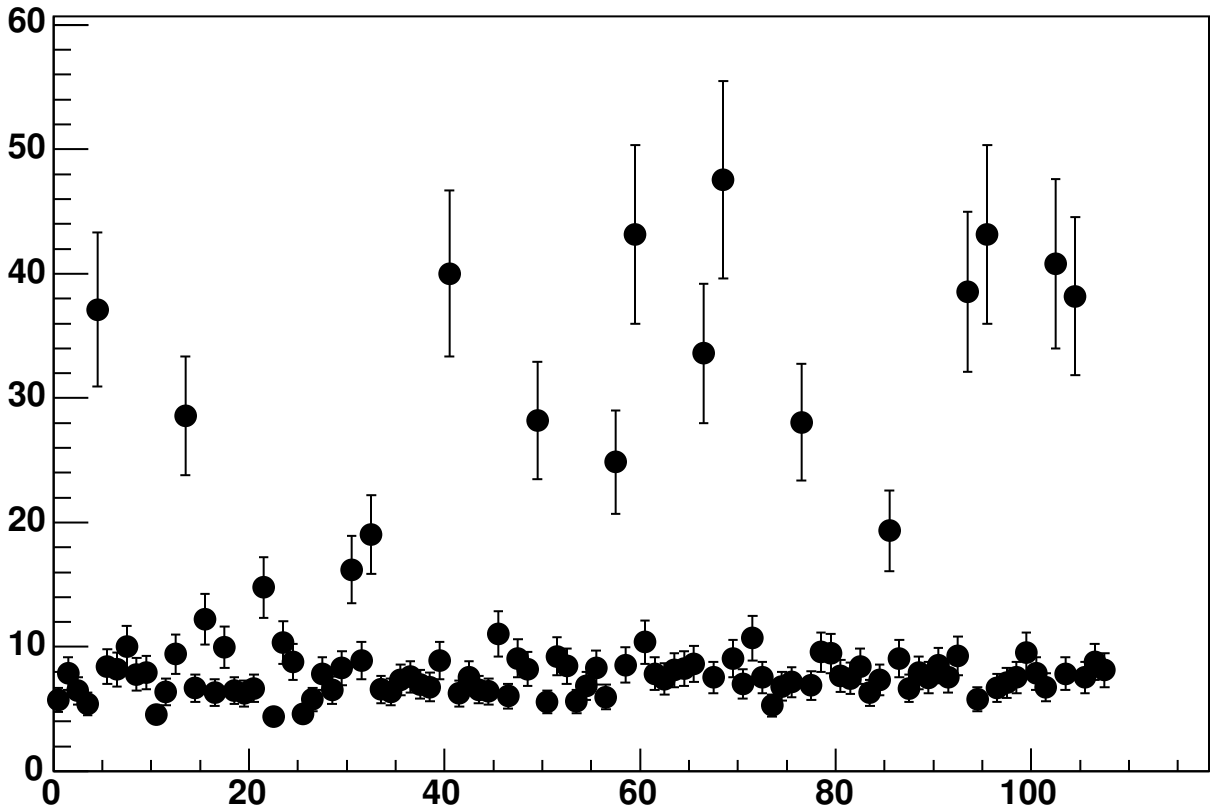
Enable 4, Hold=35, DAC=28000, ADC Noise vs 18*Chip+Chan



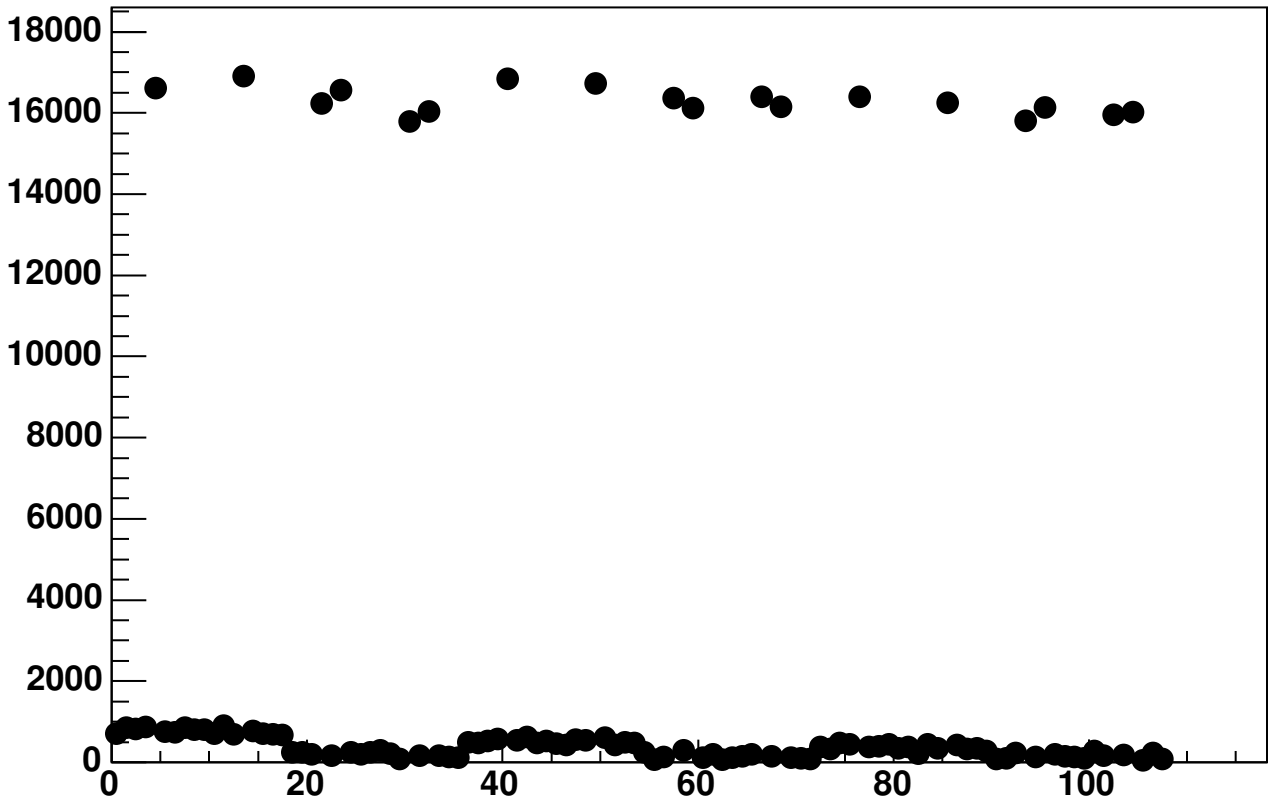
Enable 4, Hold=35, DAC=30000, ADC Mean vs 18*Chip+Chan



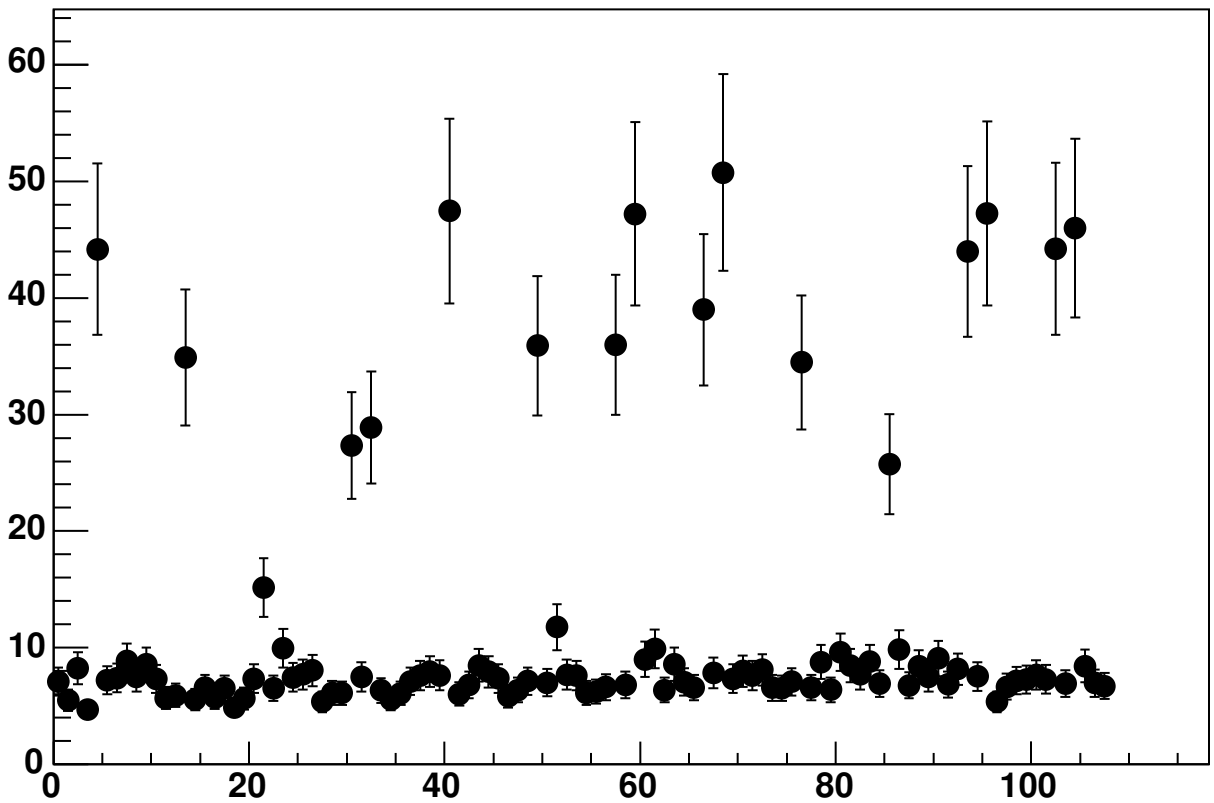
Enable 4, Hold=35, DAC=30000, ADC Noise vs 18*Chip+Chan



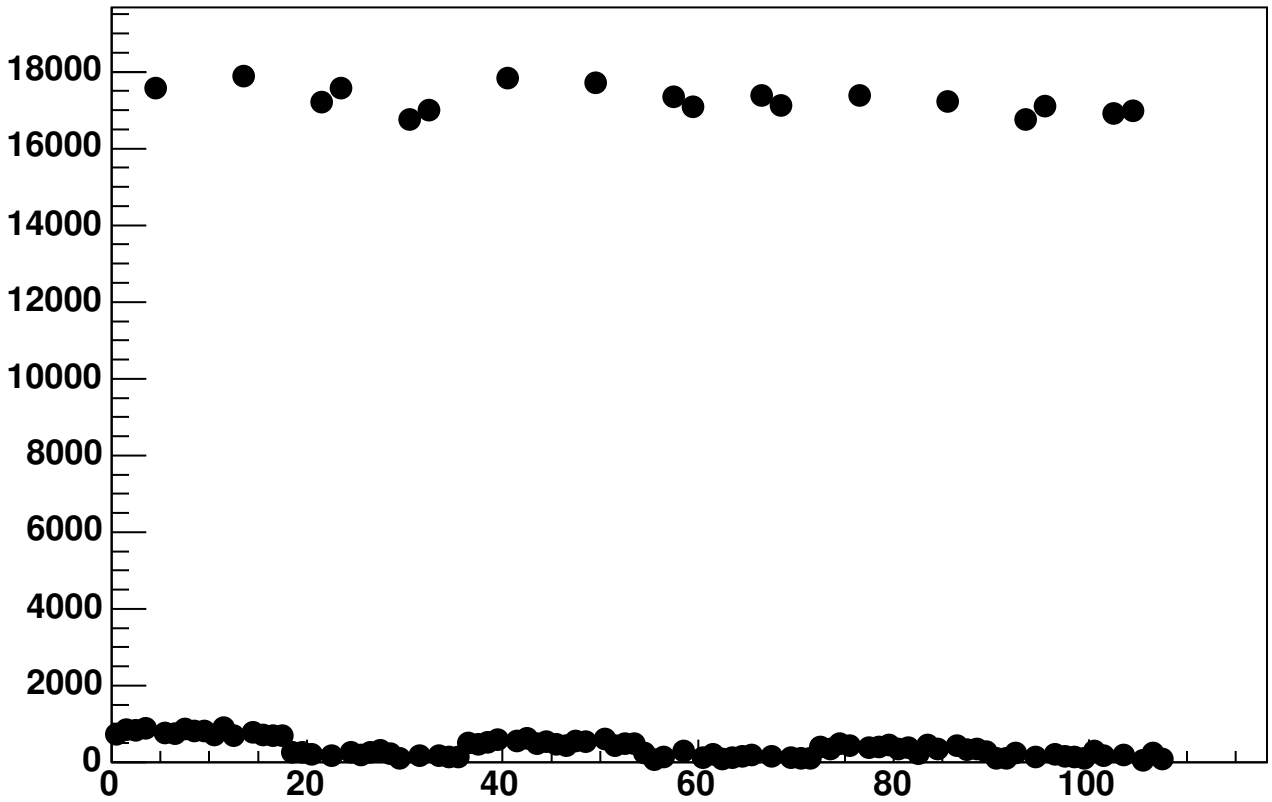
Enable 4, Hold=35, DAC=32000, ADC Mean vs 18*Chip+Chan



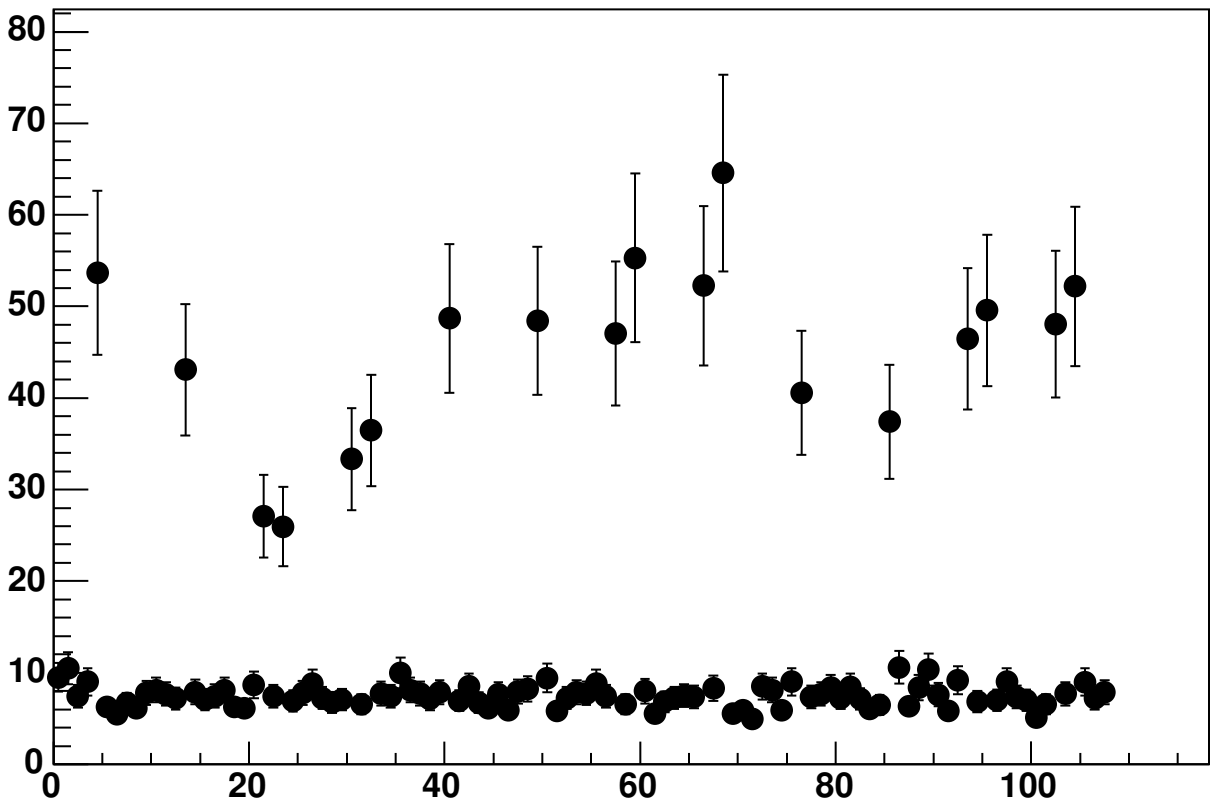
Enable 4, Hold=35, DAC=32000, ADC Noise vs 18*Chip+Chan



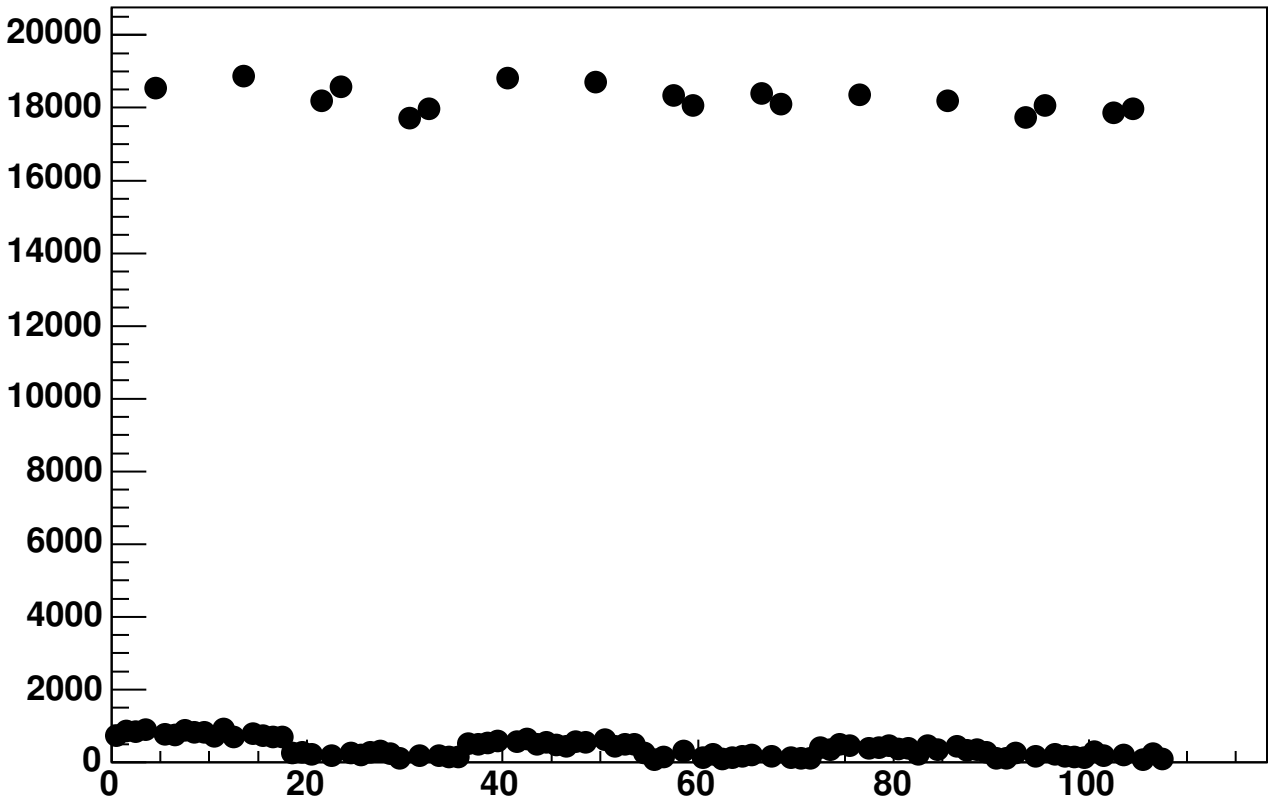
Enable 4, Hold=35, DAC=34000, ADC Mean vs 18*Chip+Chan



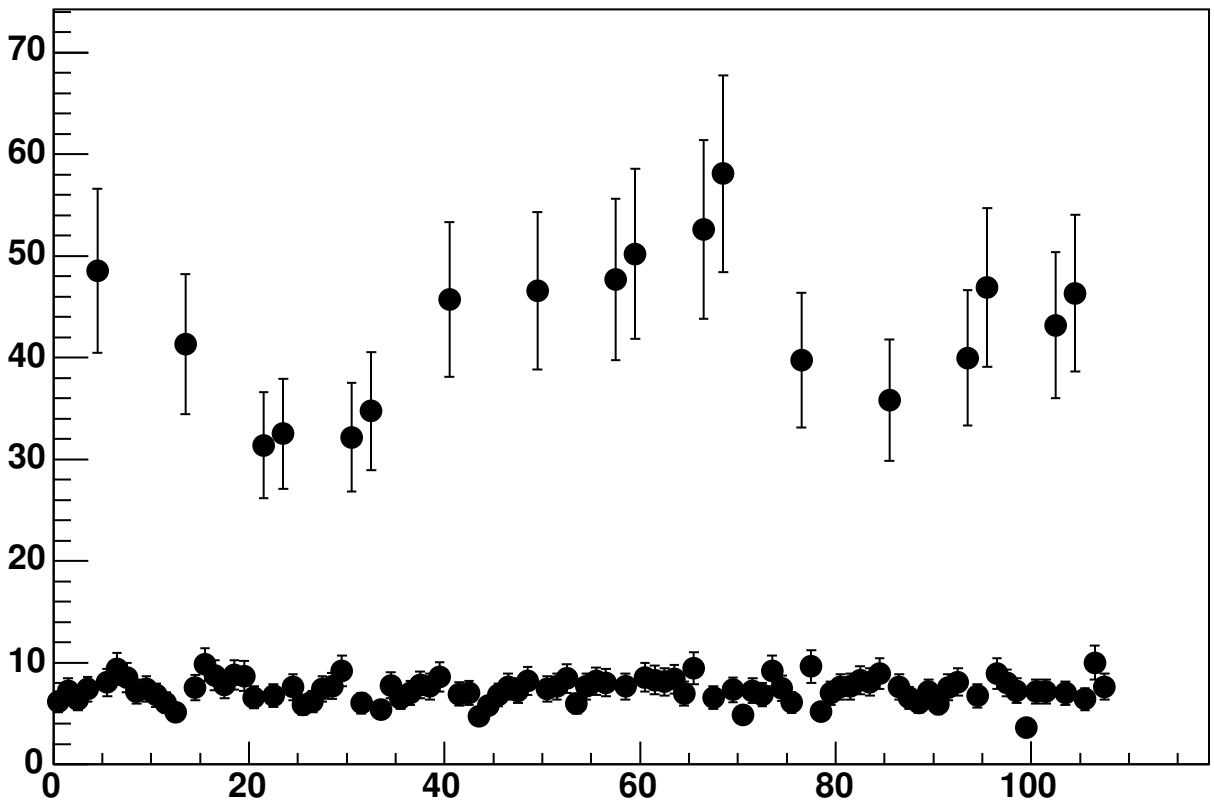
Enable 4, Hold=35, DAC=34000, ADC Noise vs 18*Chip+Chan



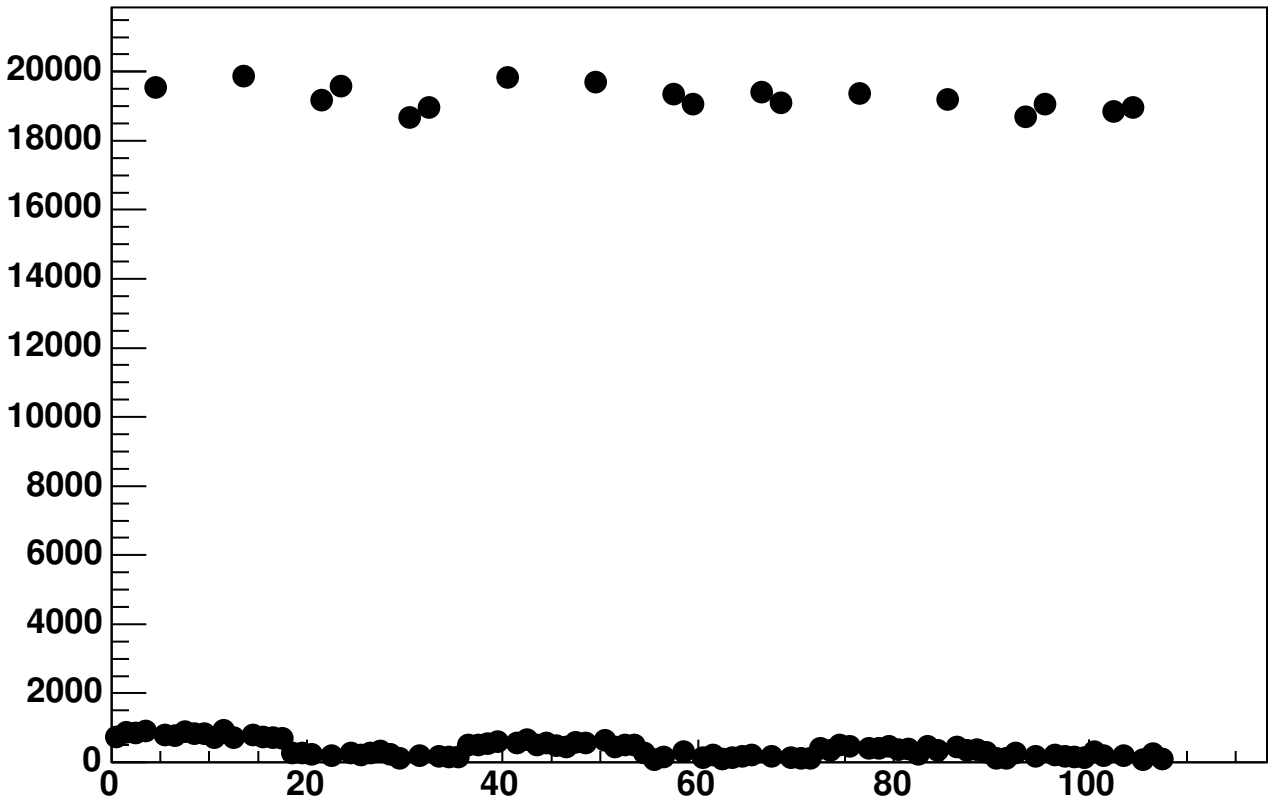
Enable 4, Hold=35, DAC=36000, ADC Mean vs 18*Chip+Chan



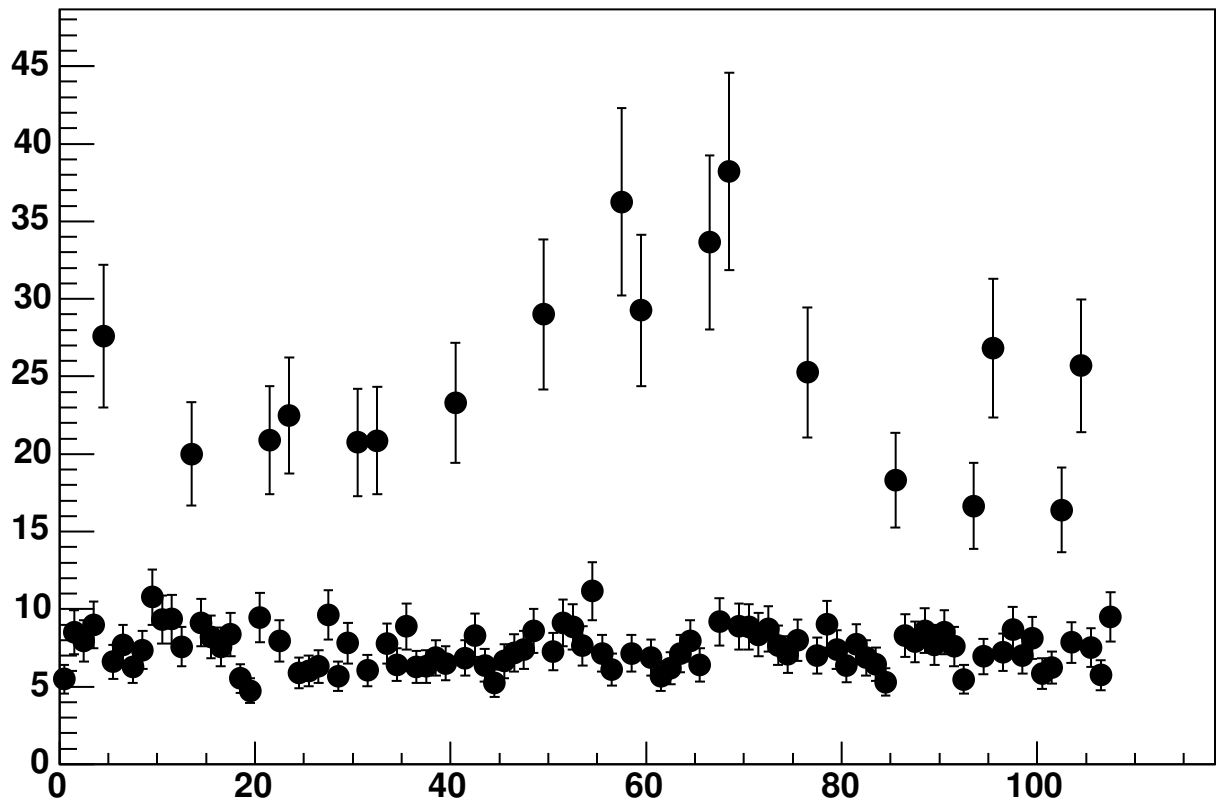
Enable 4, Hold=35, DAC=36000, ADC Noise vs 18*Chip+Chan



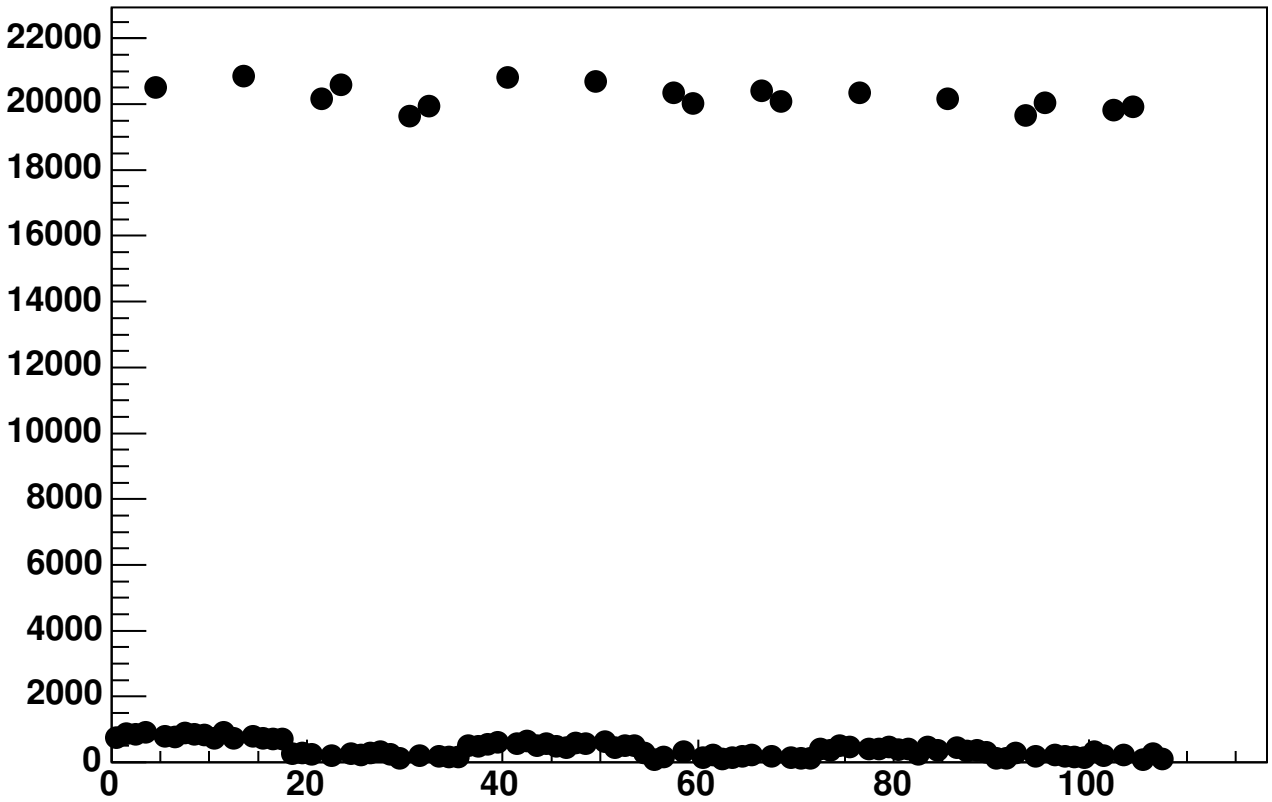
Enable 4, Hold=35, DAC=38000, ADC Mean vs 18*Chip+Chan



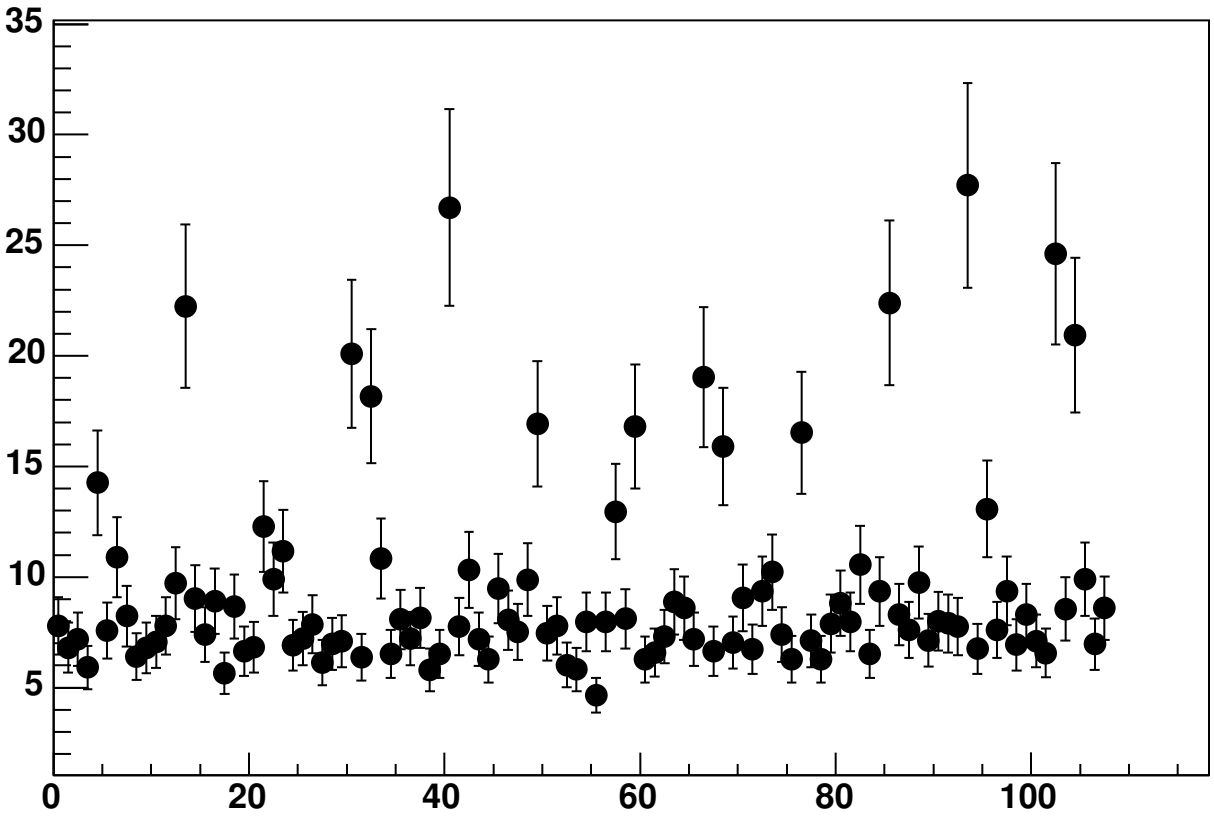
Enable 4, Hold=35, DAC=38000, ADC Noise vs 18*Chip+Chan



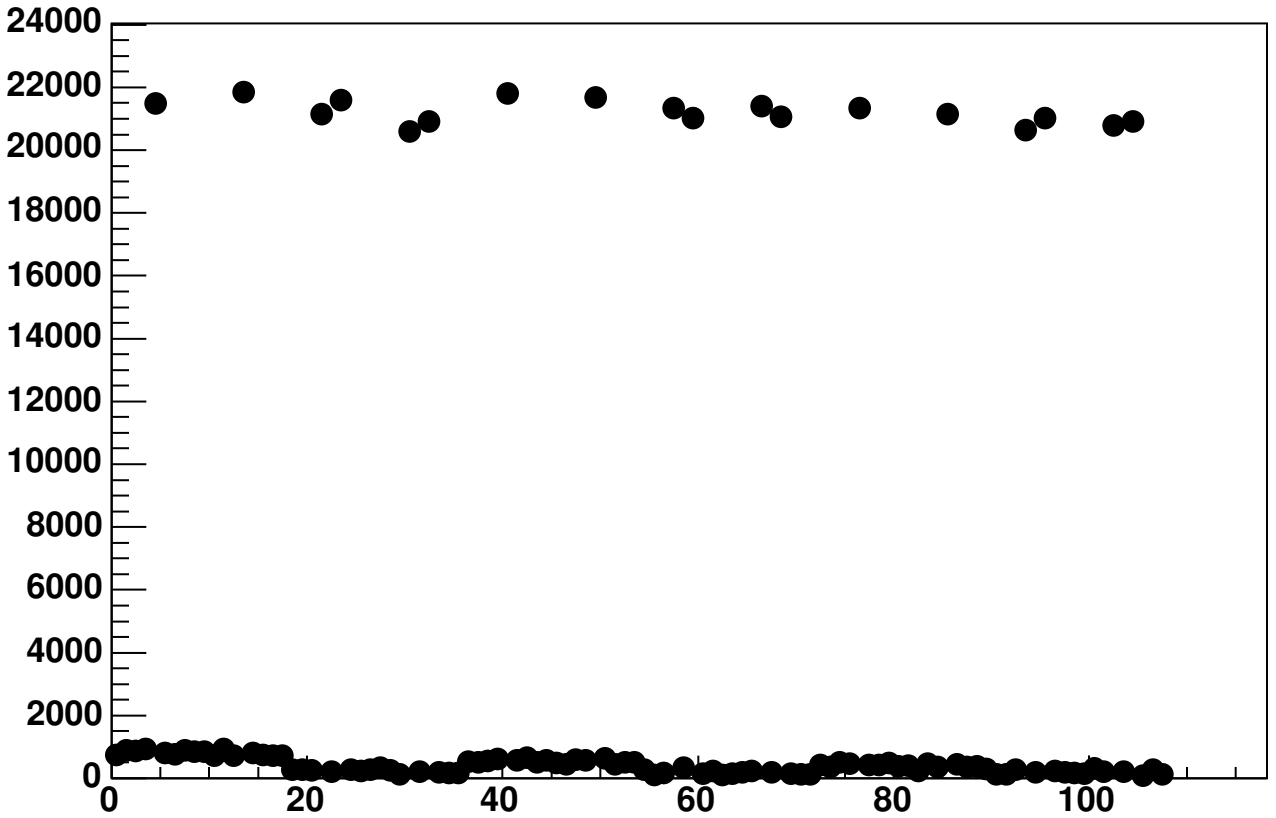
Enable 4, Hold=35, DAC=40000, ADC Mean vs 18*Chip+Chan



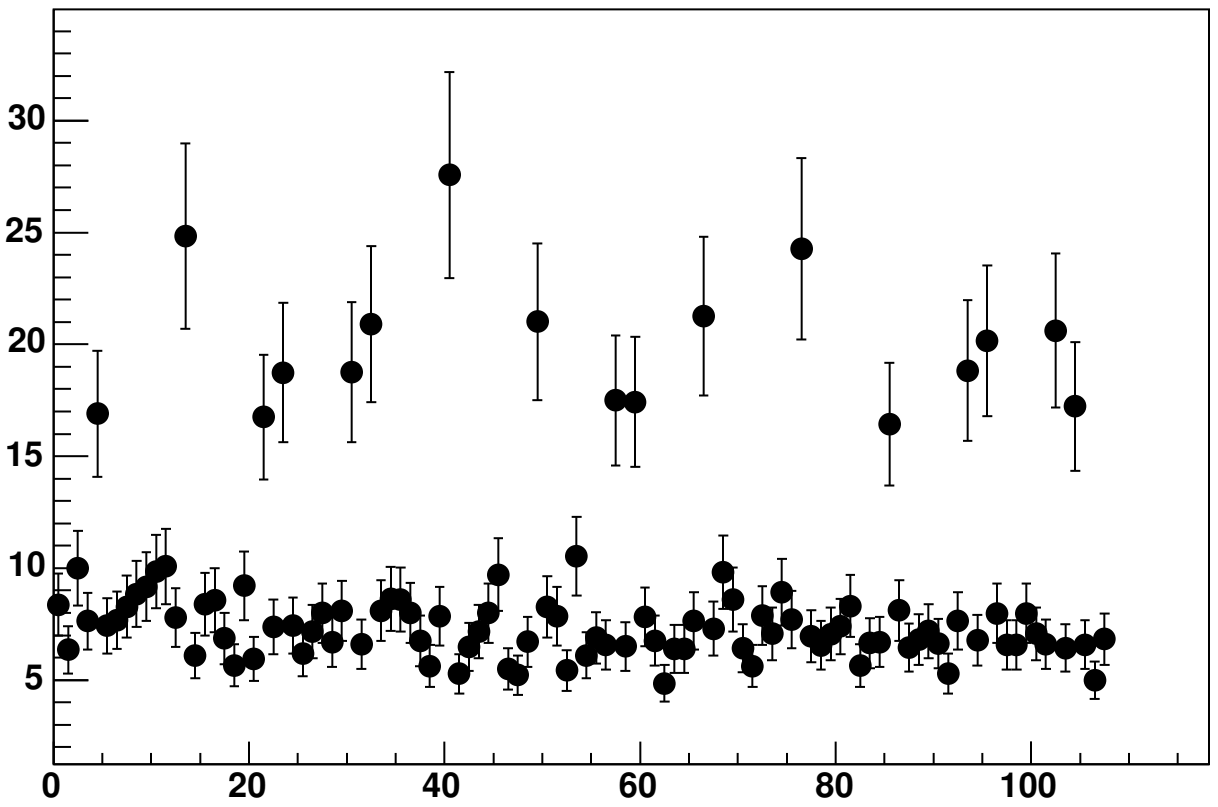
Enable 4, Hold=35, DAC=40000, ADC Noise vs 18*Chip+Chan



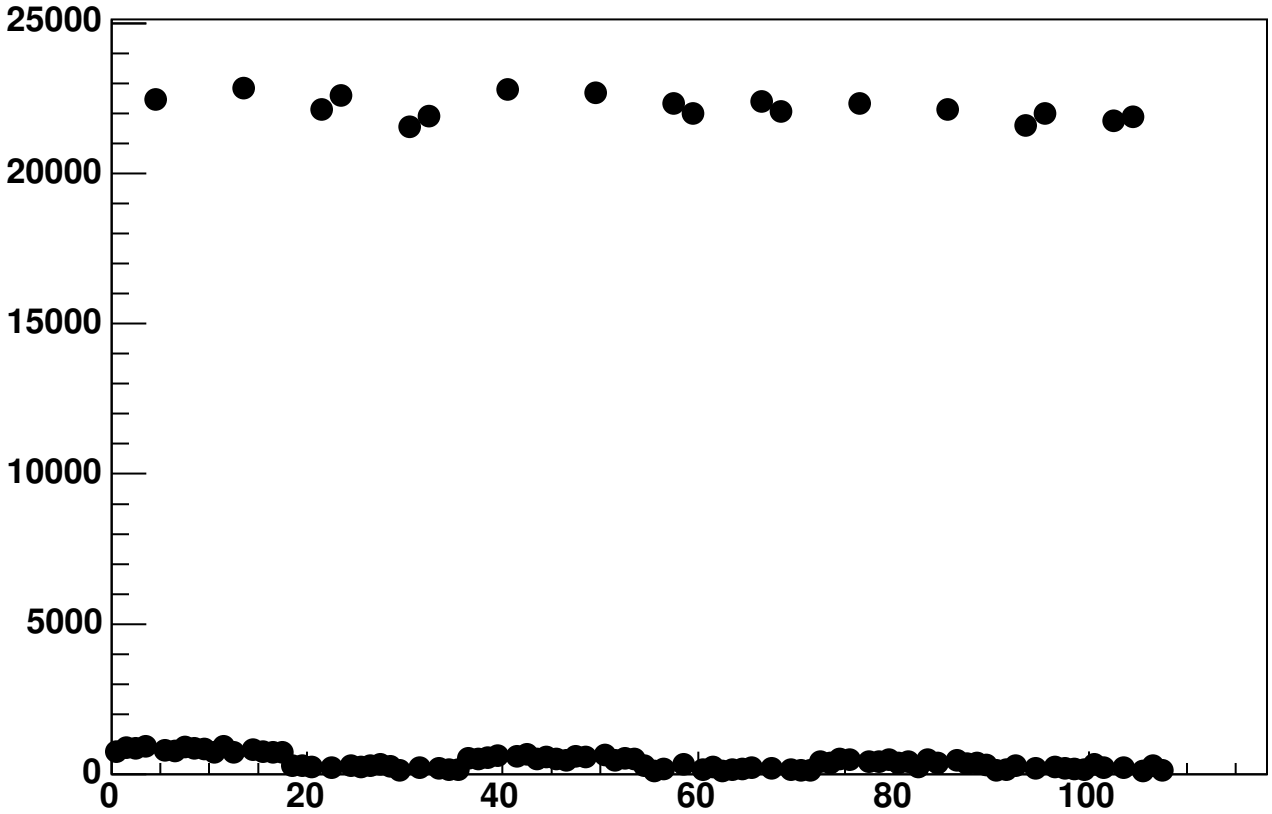
Enable 4, Hold=35, DAC=42000, ADC Mean vs 18*Chip+Chan



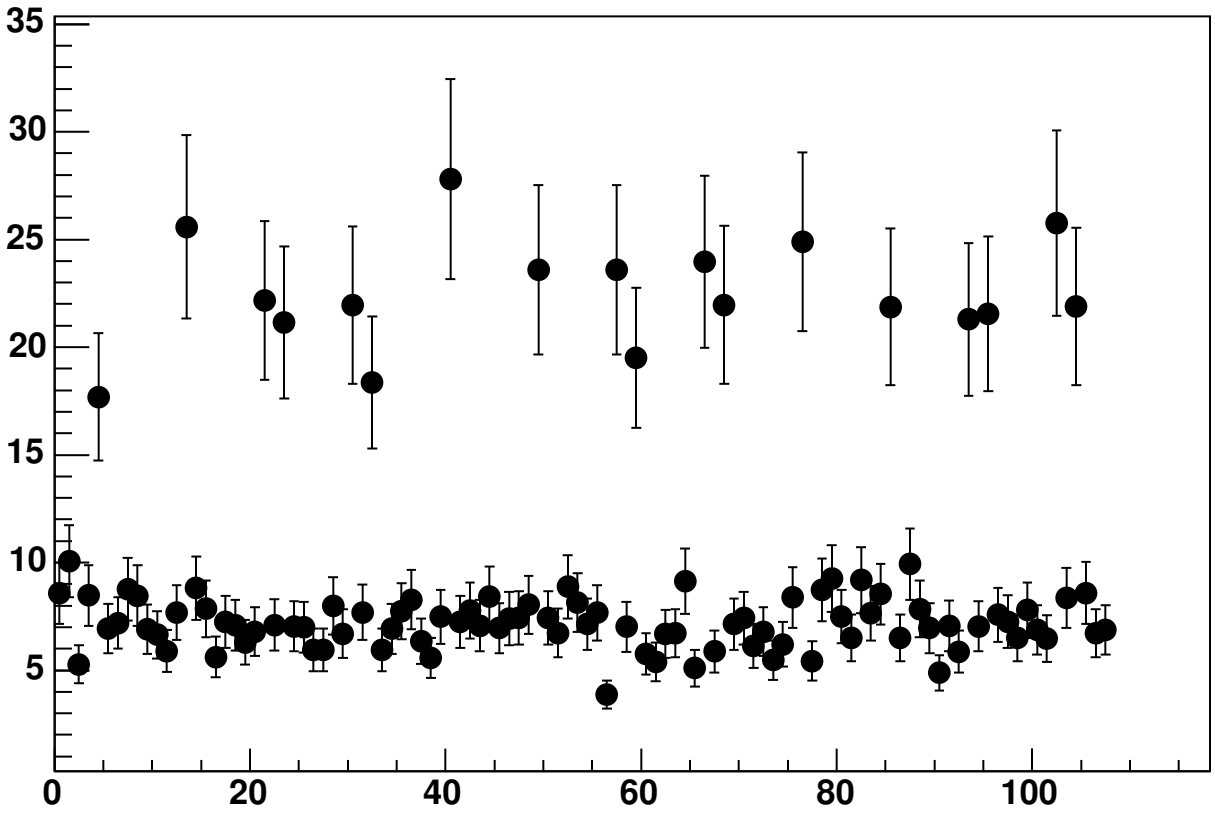
Enable 4, Hold=35, DAC=42000, ADC Noise vs 18*Chip+Chan



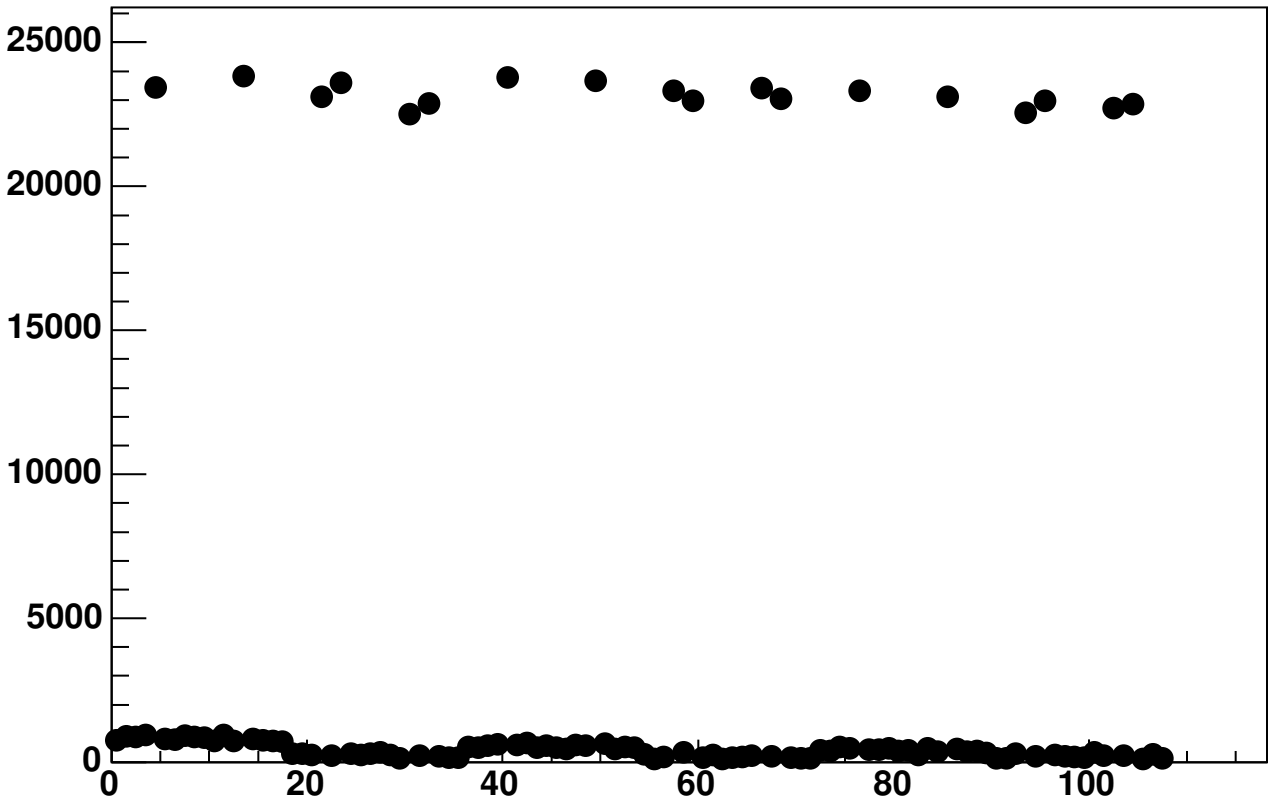
Enable 4, Hold=35, DAC=44000, ADC Mean vs 18*Chip+Chan



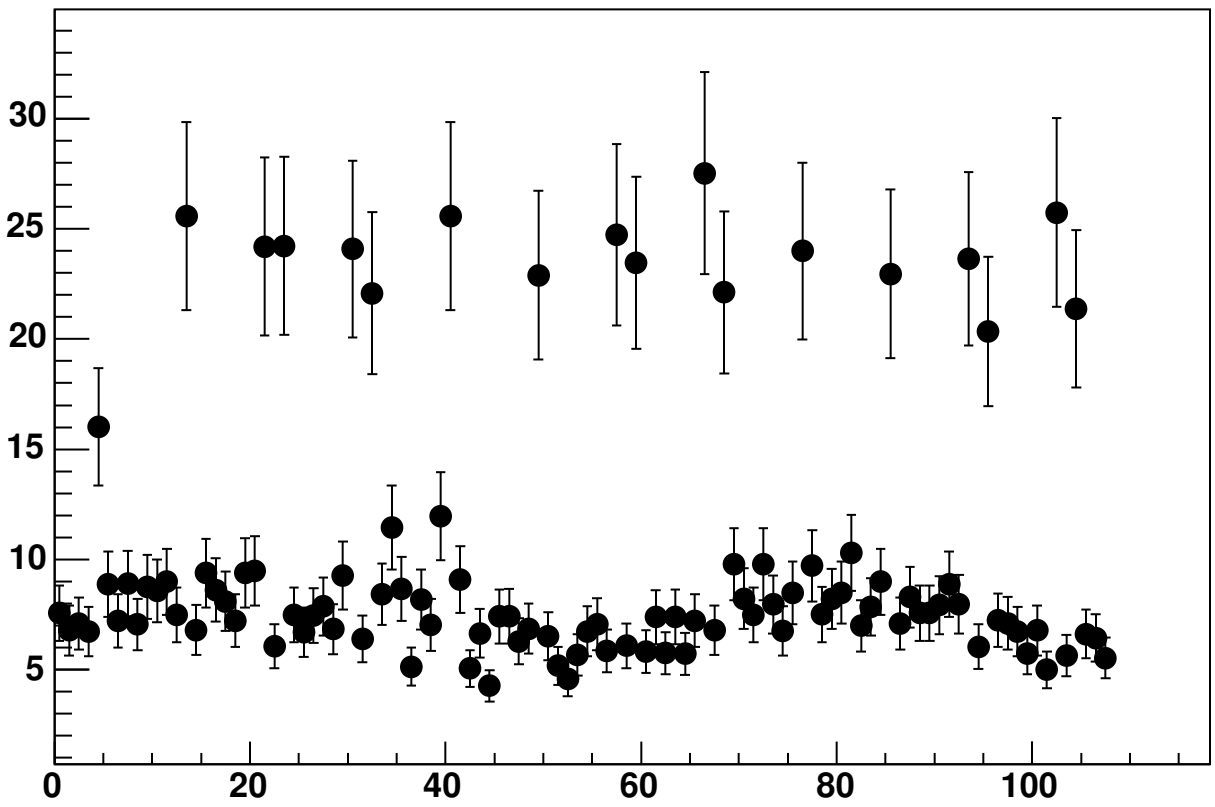
Enable 4, Hold=35, DAC=44000, ADC Noise vs 18*Chip+Chan



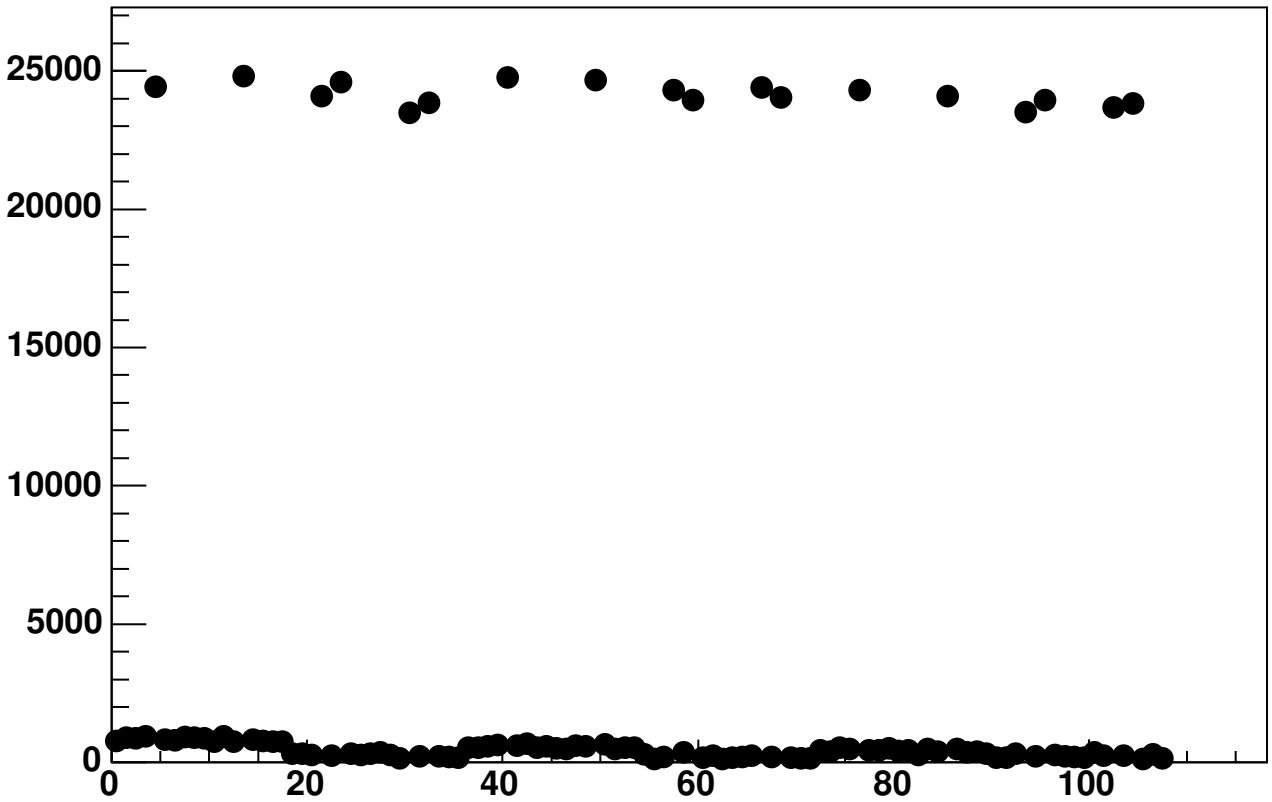
Enable 4, Hold=35, DAC=46000, ADC Mean vs 18*Chip+Chan



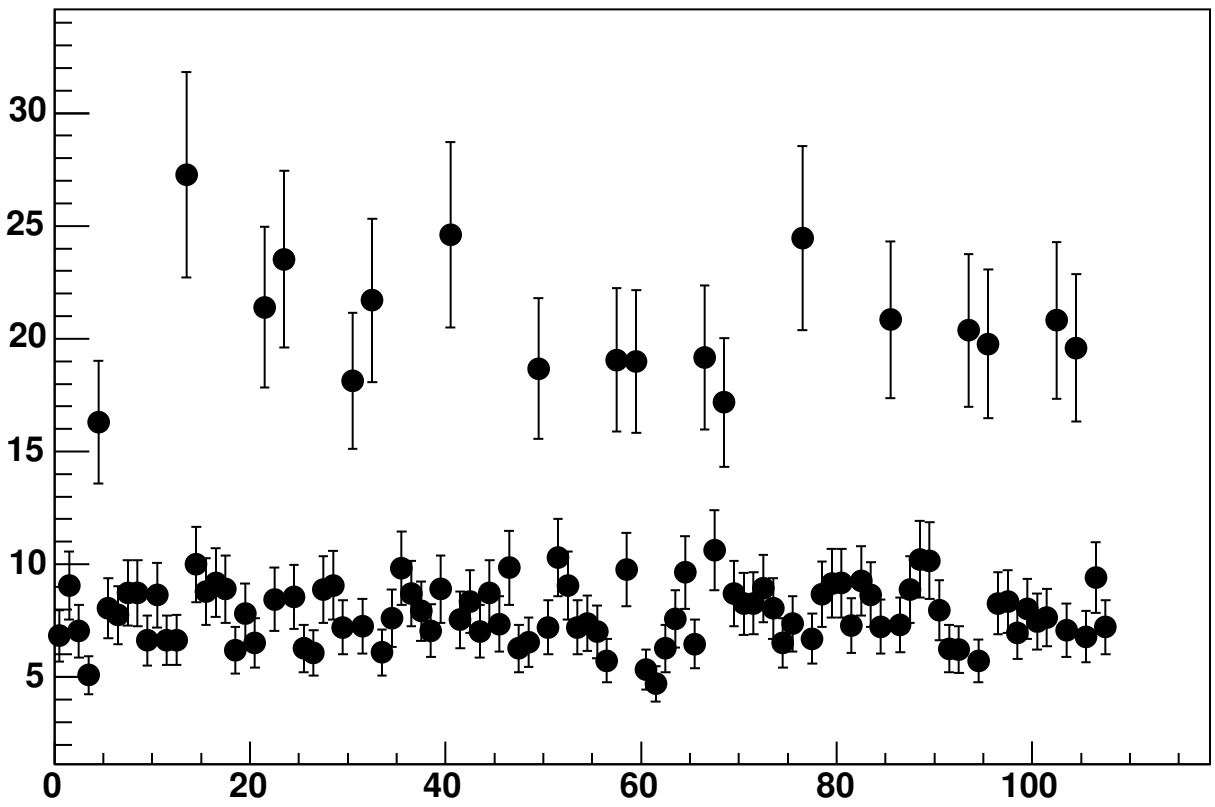
Enable 4, Hold=35, DAC=46000, ADC Noise vs 18*Chip+Chan



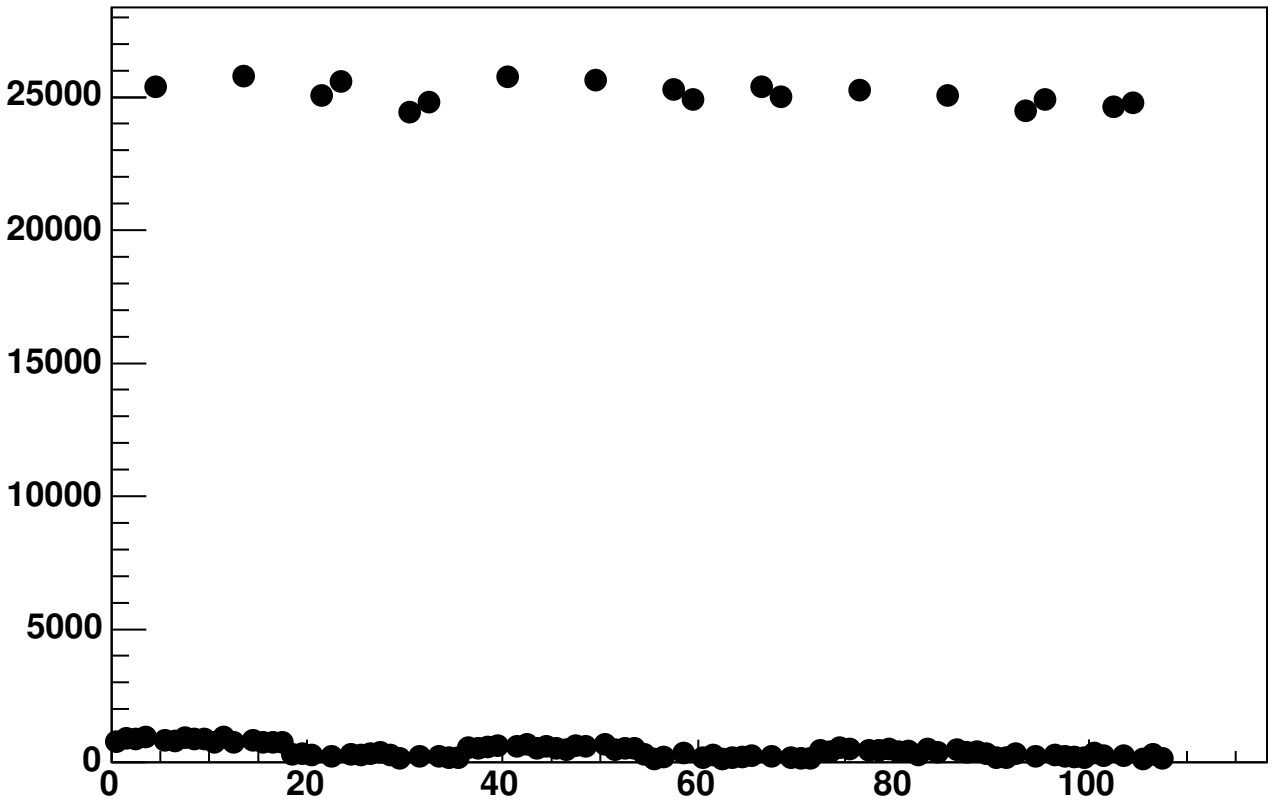
Enable 4, Hold=35, DAC=48000, ADC Mean vs 18*Chip+Chan



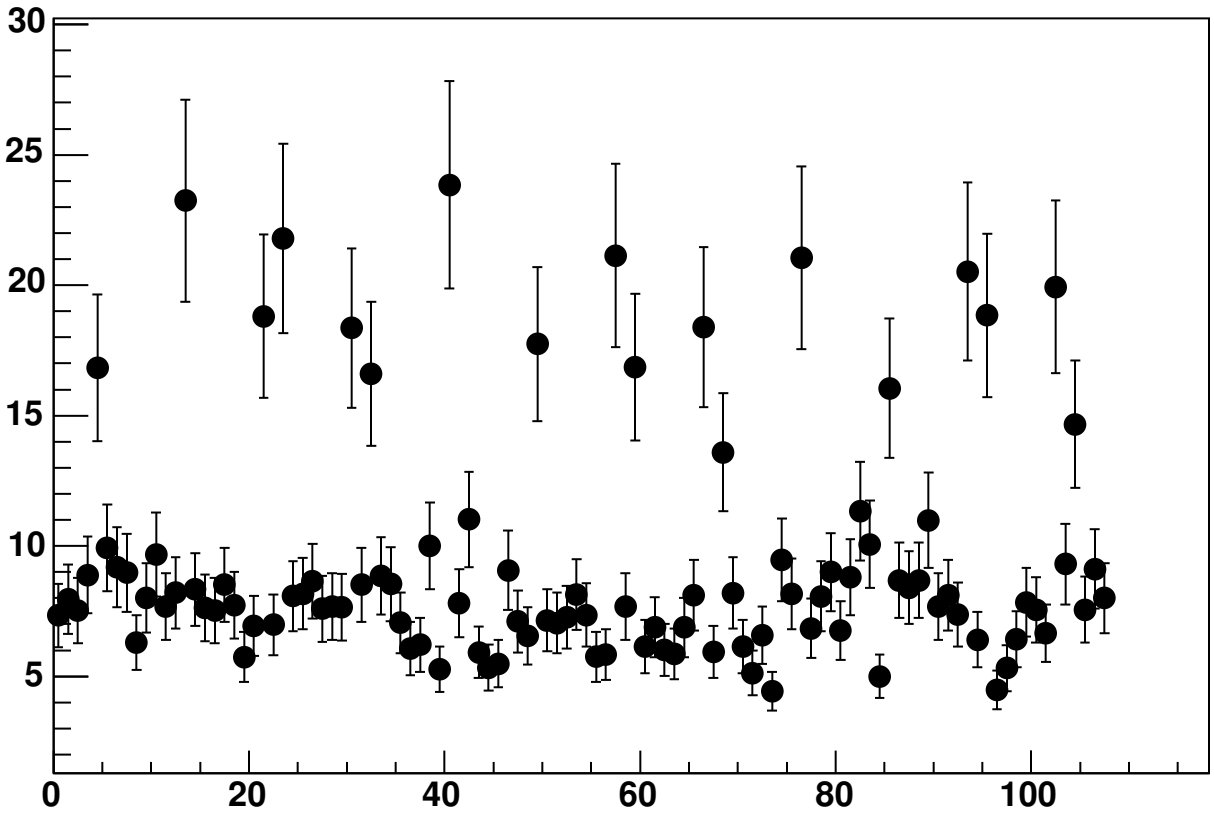
Enable 4, Hold=35, DAC=48000, ADC Noise vs 18*Chip+Chan



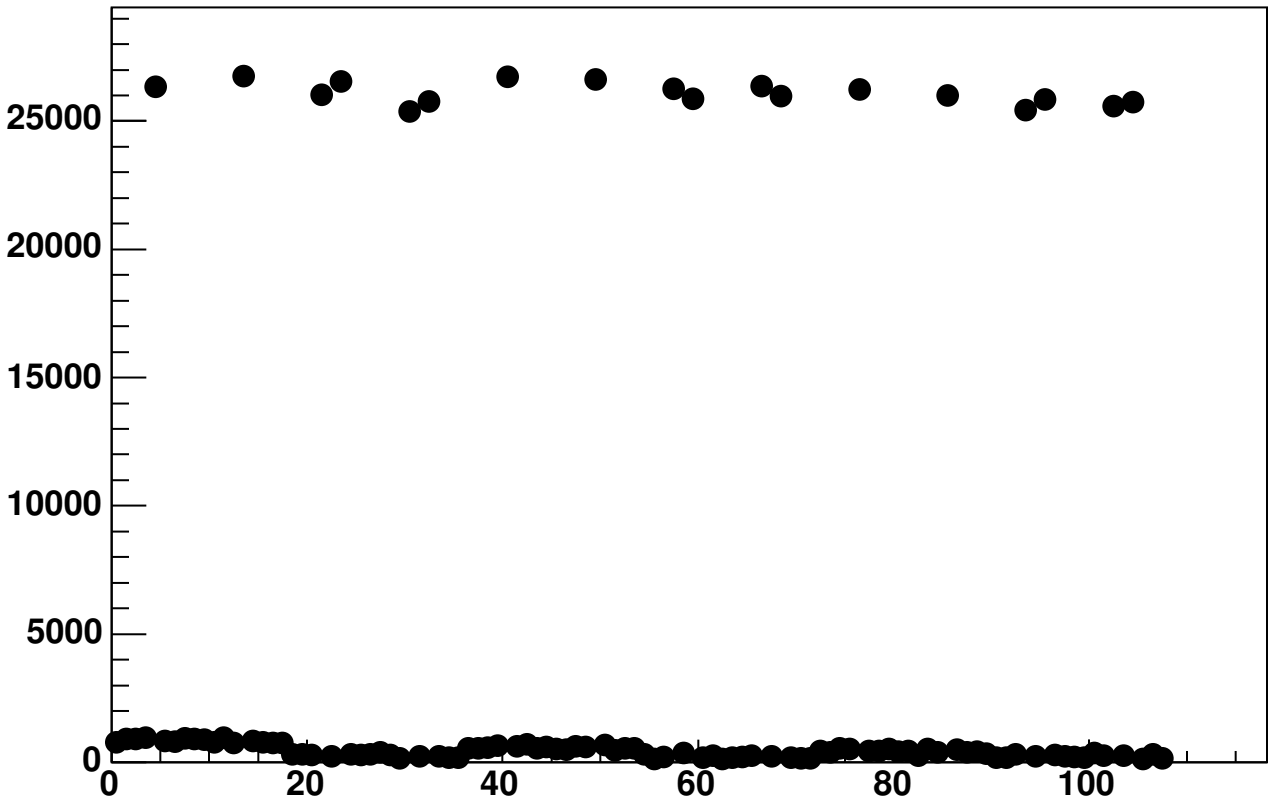
Enable 4, Hold=35, DAC=50000, ADC Mean vs 18*Chip+Chan



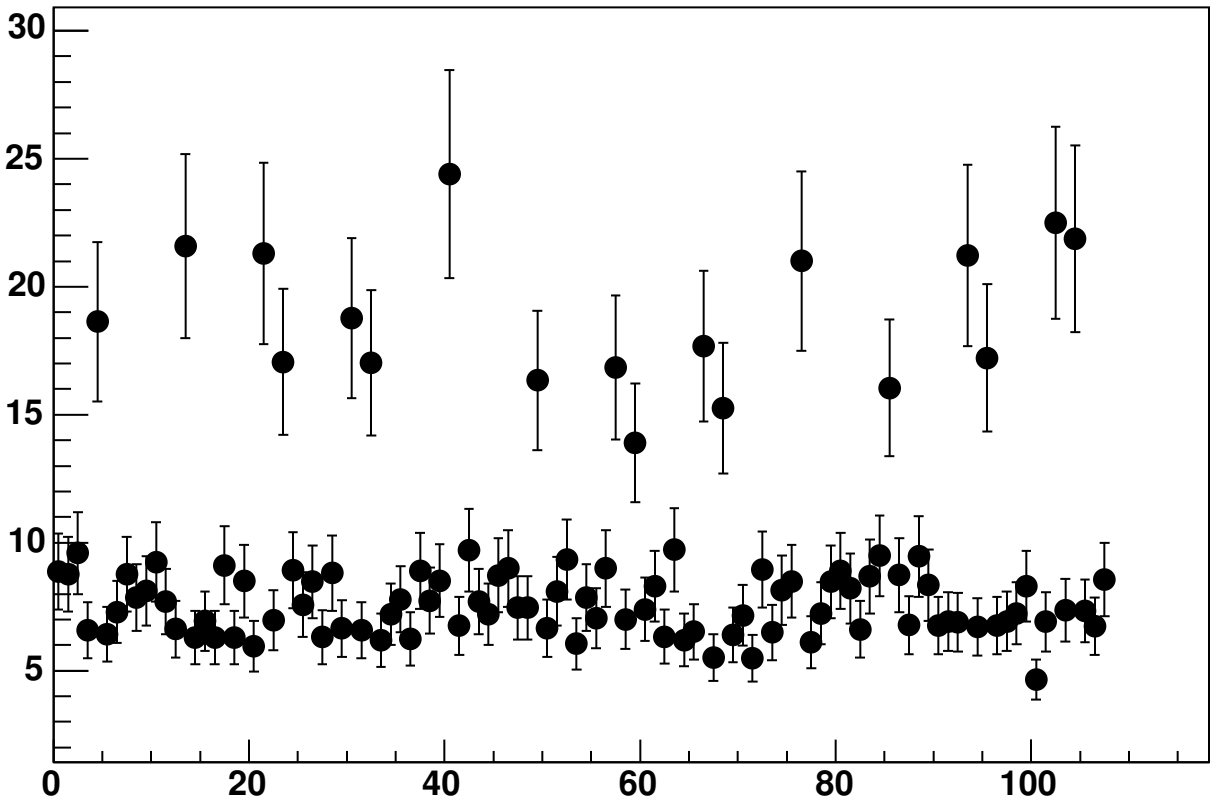
Enable 4, Hold=35, DAC=50000, ADC Noise vs 18*Chip+Chan



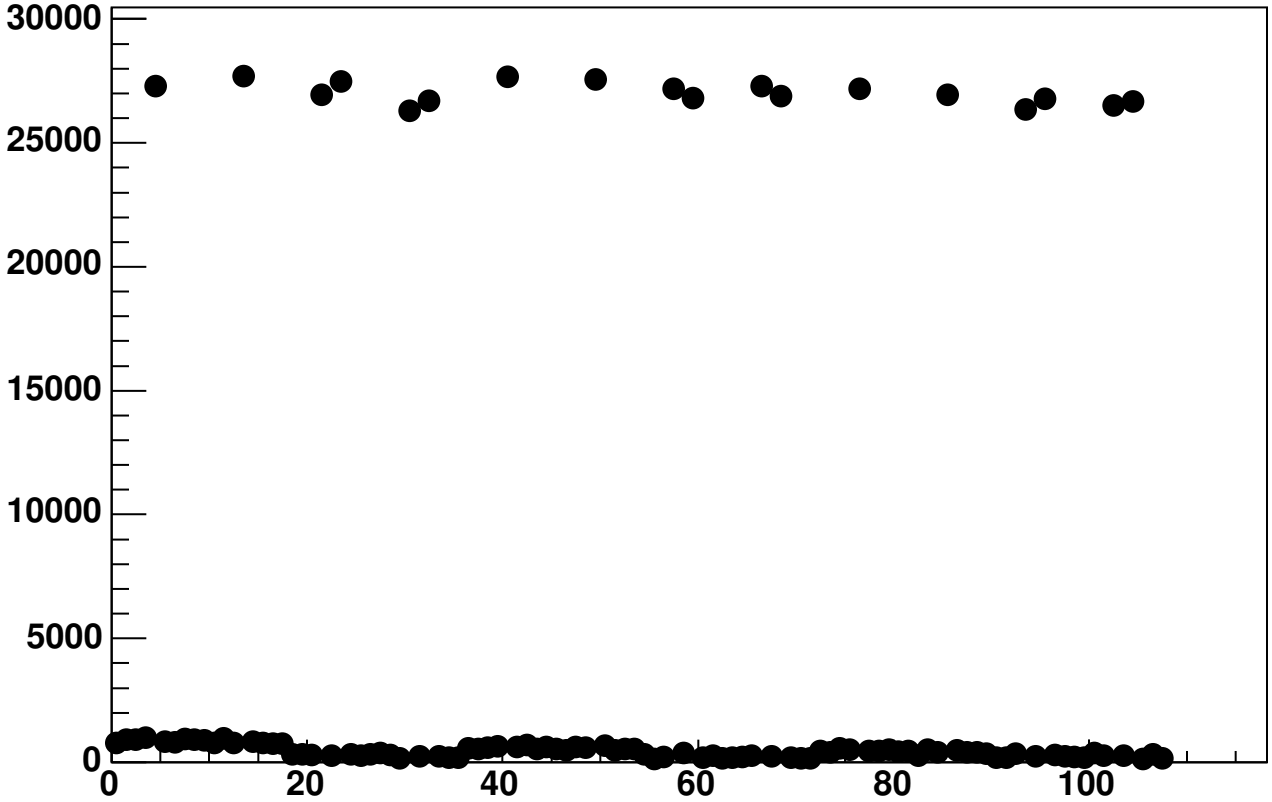
Enable 4, Hold=35, DAC=52000, ADC Mean vs 18*Chip+Chan



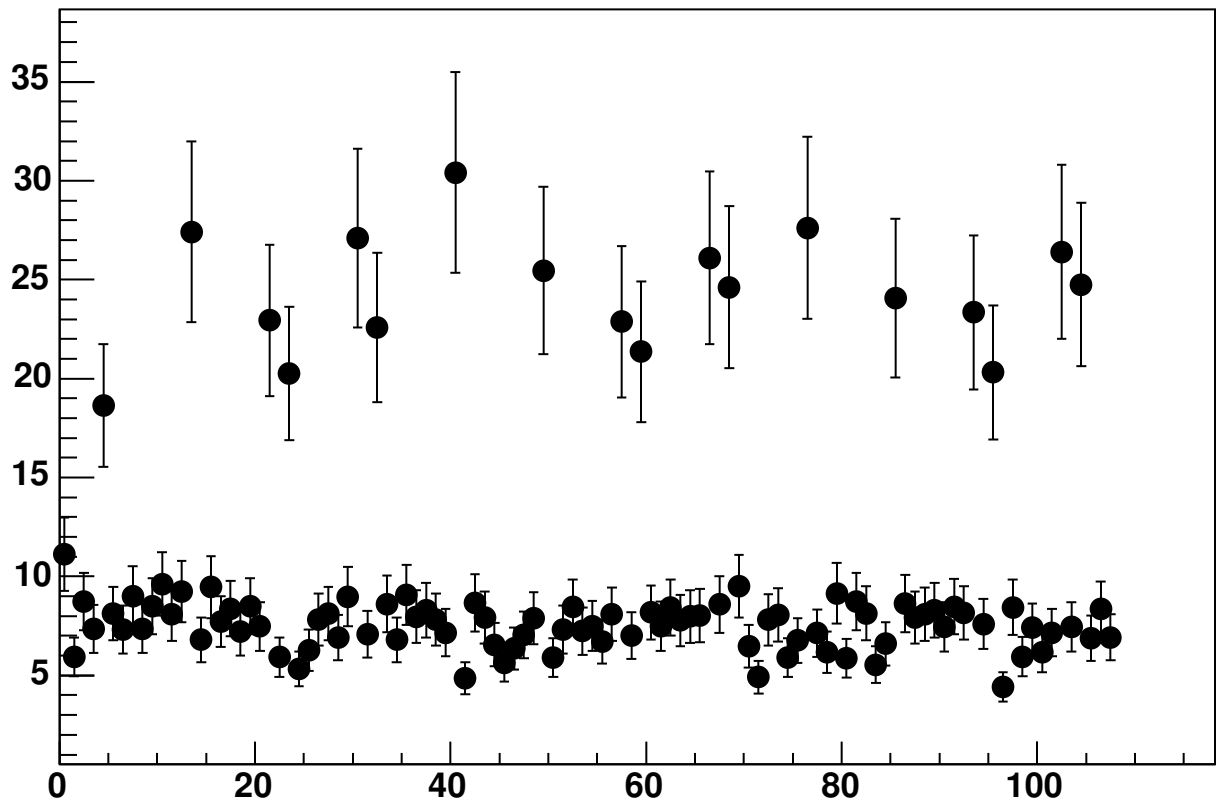
Enable 4, Hold=35, DAC=52000, ADC Noise vs 18*Chip+Chan



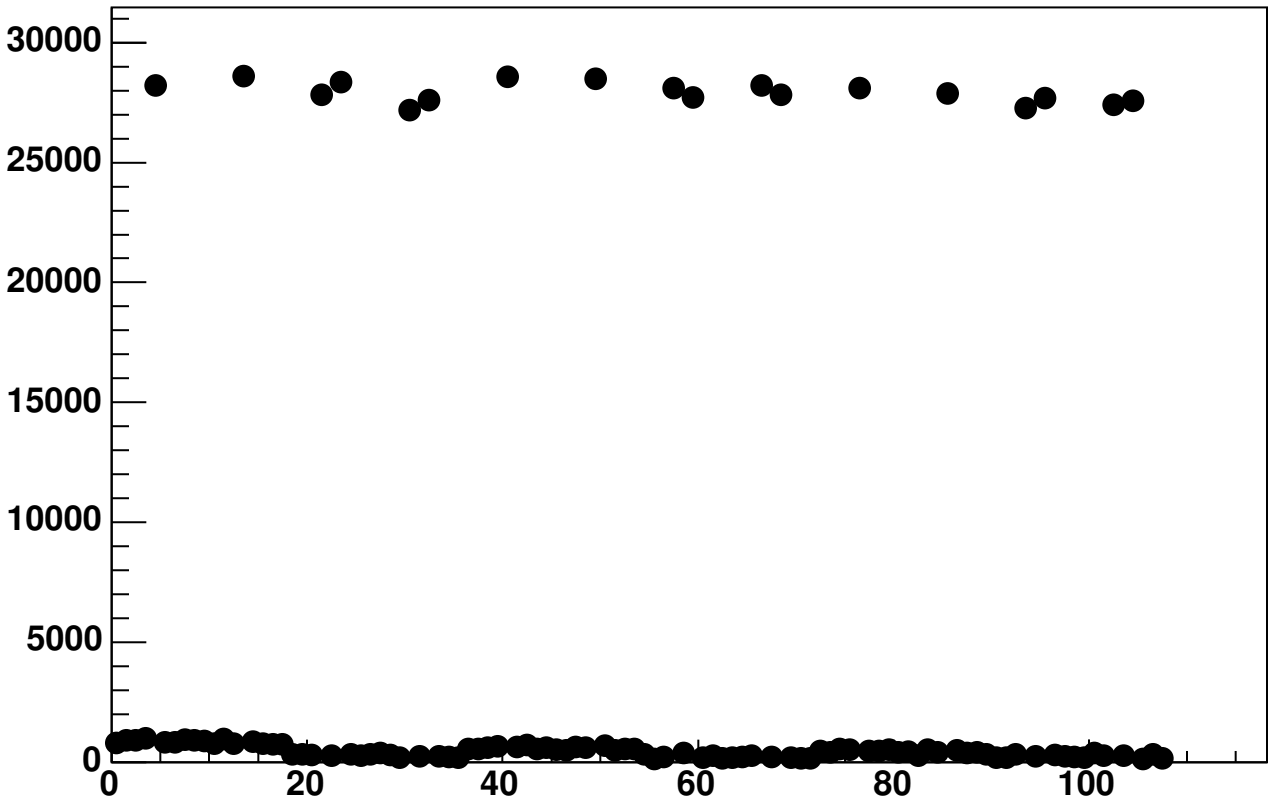
Enable 4, Hold=35, DAC=54000, ADC Mean vs 18*Chip+Chan



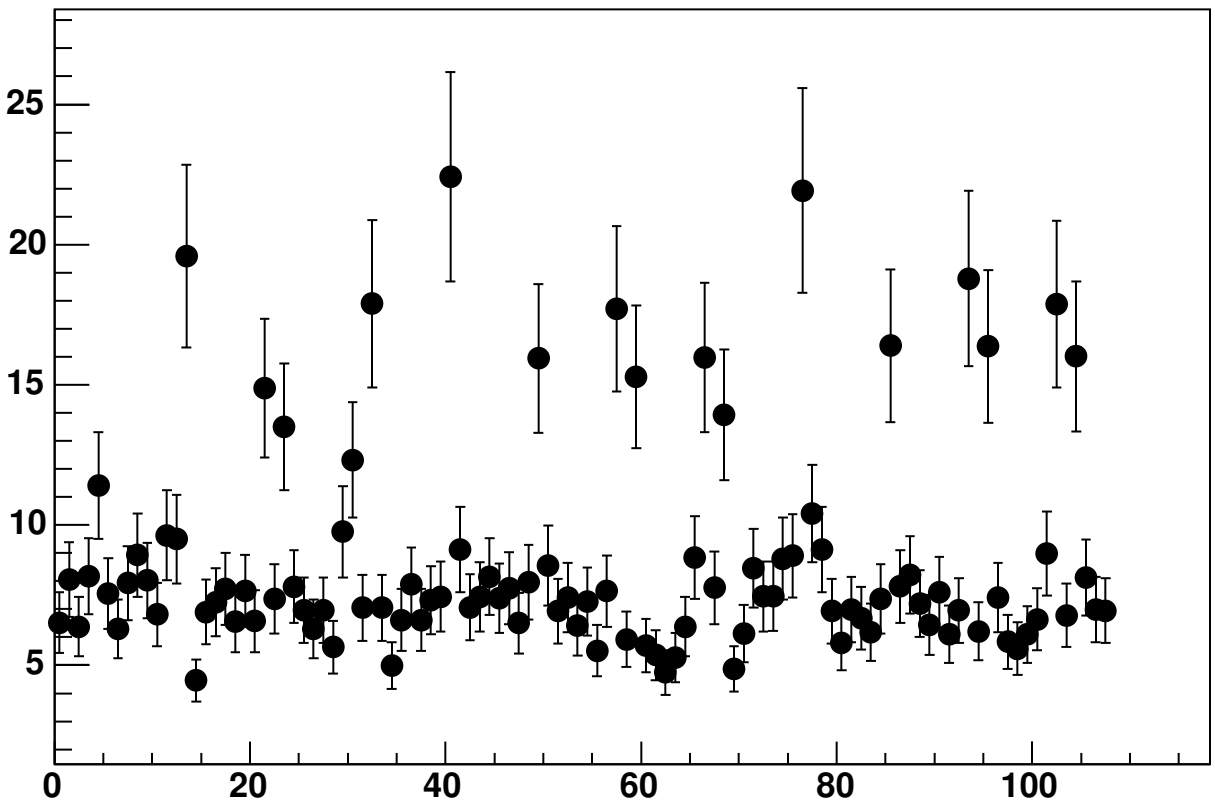
Enable 4, Hold=35, DAC=54000, ADC Noise vs 18*Chip+Chan



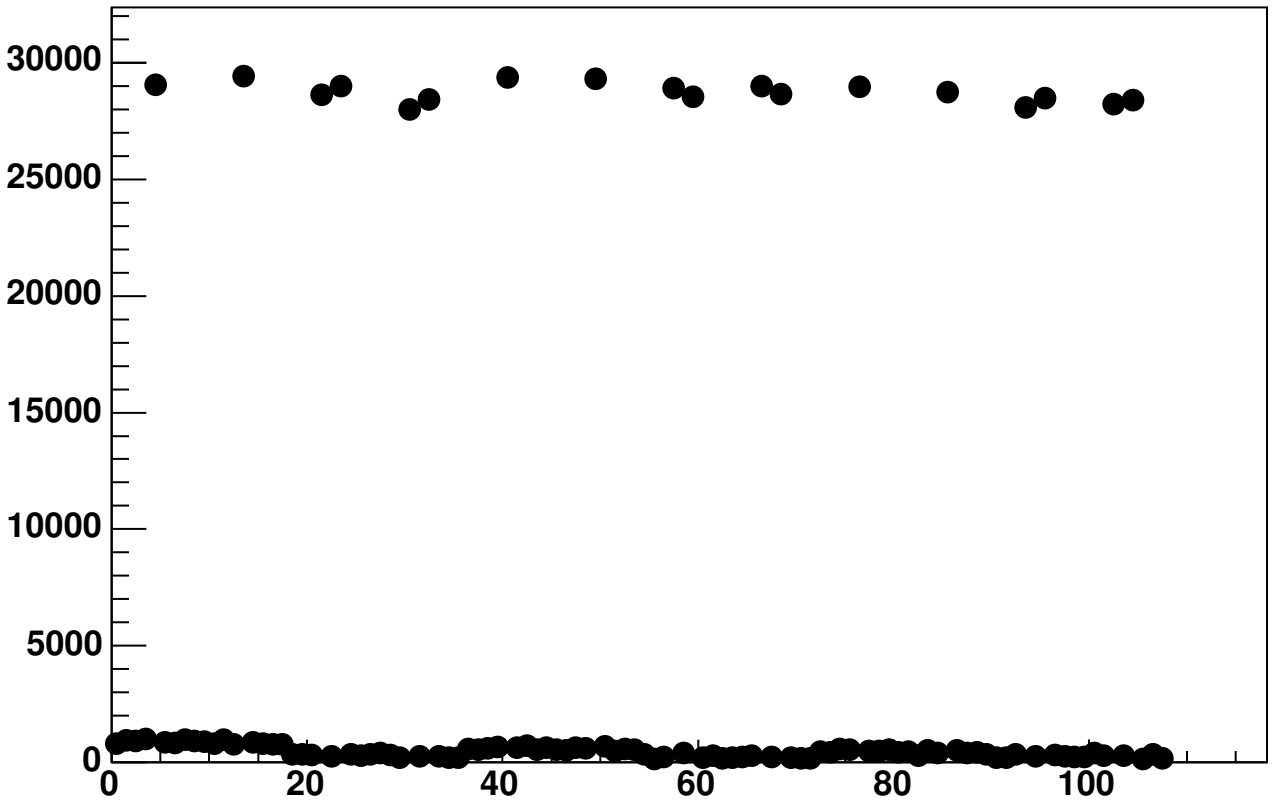
Enable 4, Hold=35, DAC=56000, ADC Mean vs 18*Chip+Chan



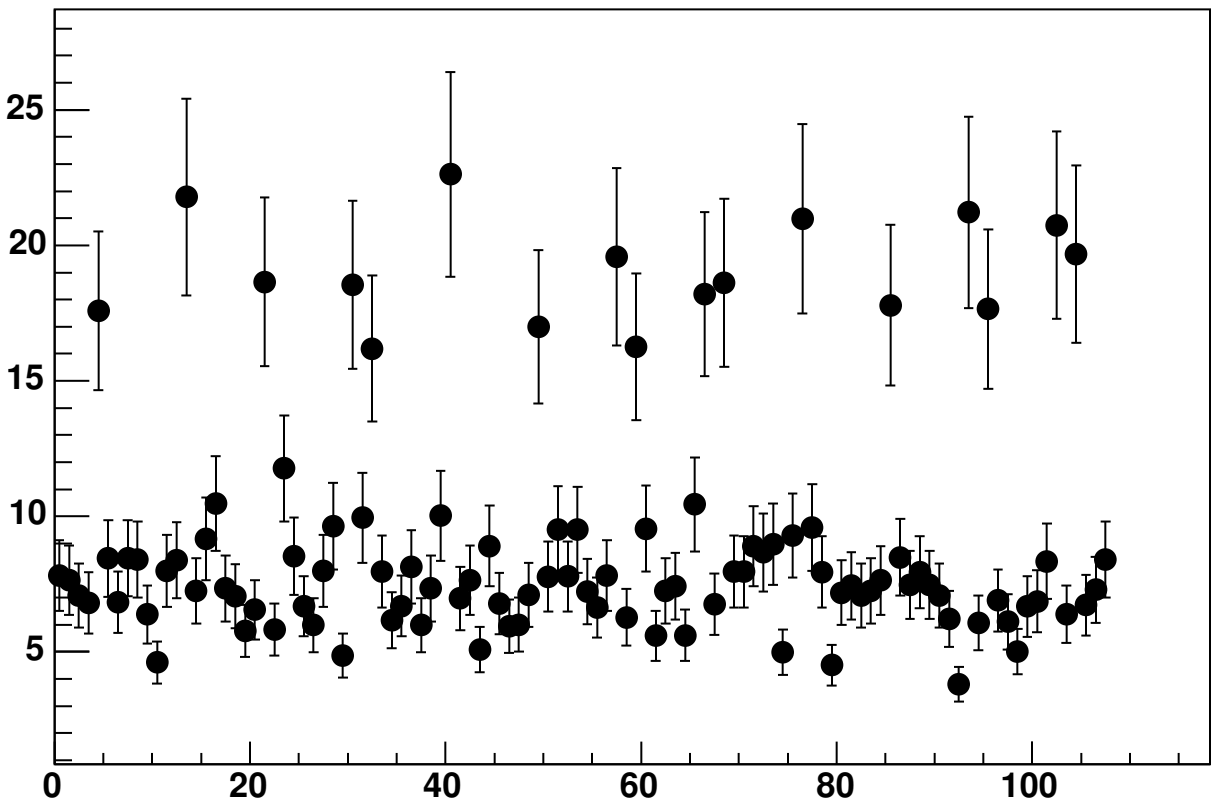
Enable 4, Hold=35, DAC=56000, ADC Noise vs 18*Chip+Chan



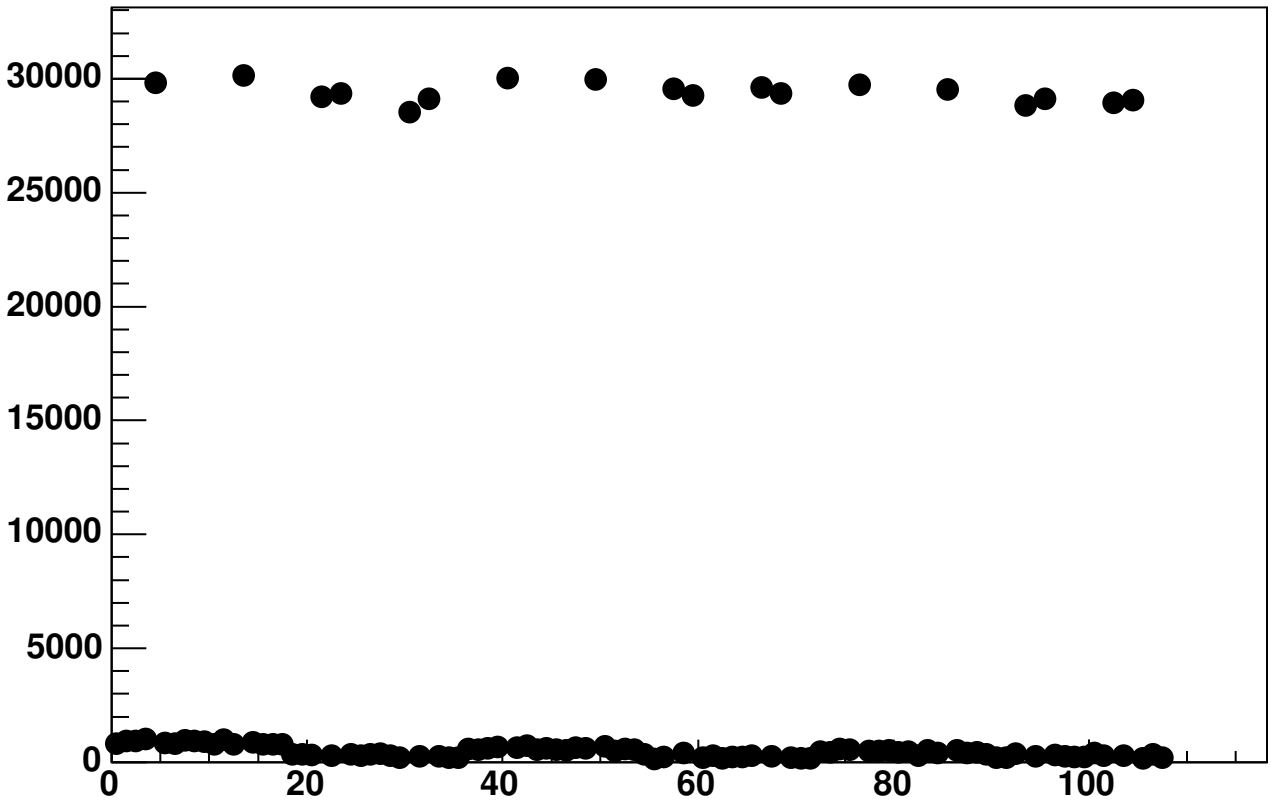
Enable 4, Hold=35, DAC=58000, ADC Mean vs 18*Chip+Chan



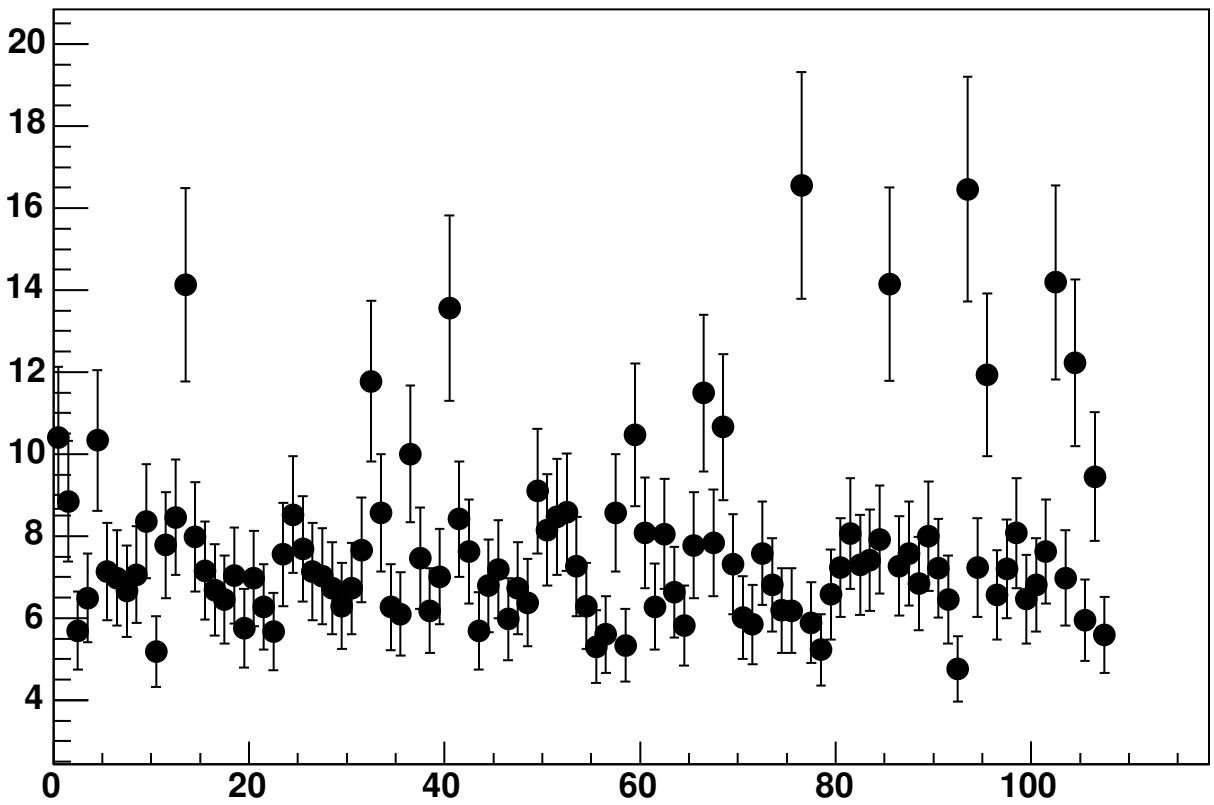
Enable 4, Hold=35, DAC=58000, ADC Noise vs 18*Chip+Chan



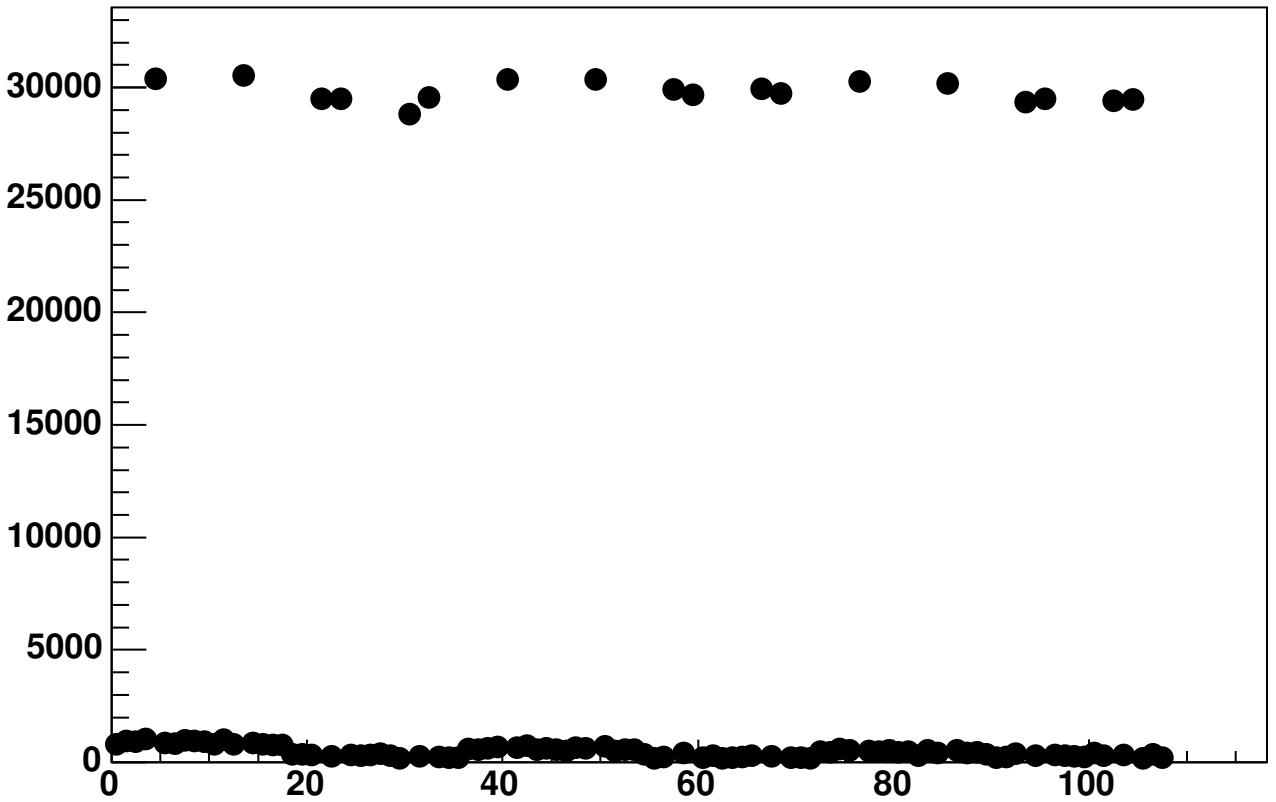
Enable 4, Hold=35, DAC=60000, ADC Mean vs 18*Chip+Chan



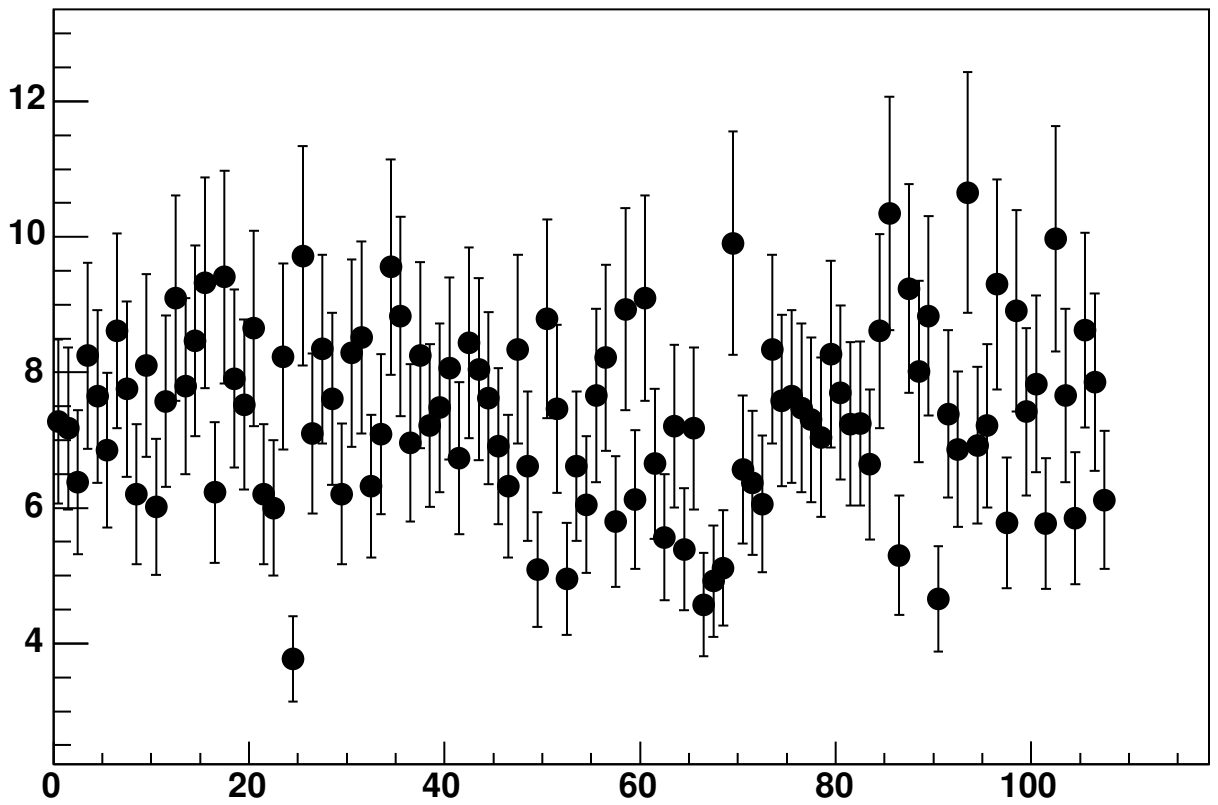
Enable 4, Hold=35, DAC=60000, ADC Noise vs 18*Chip+Chan



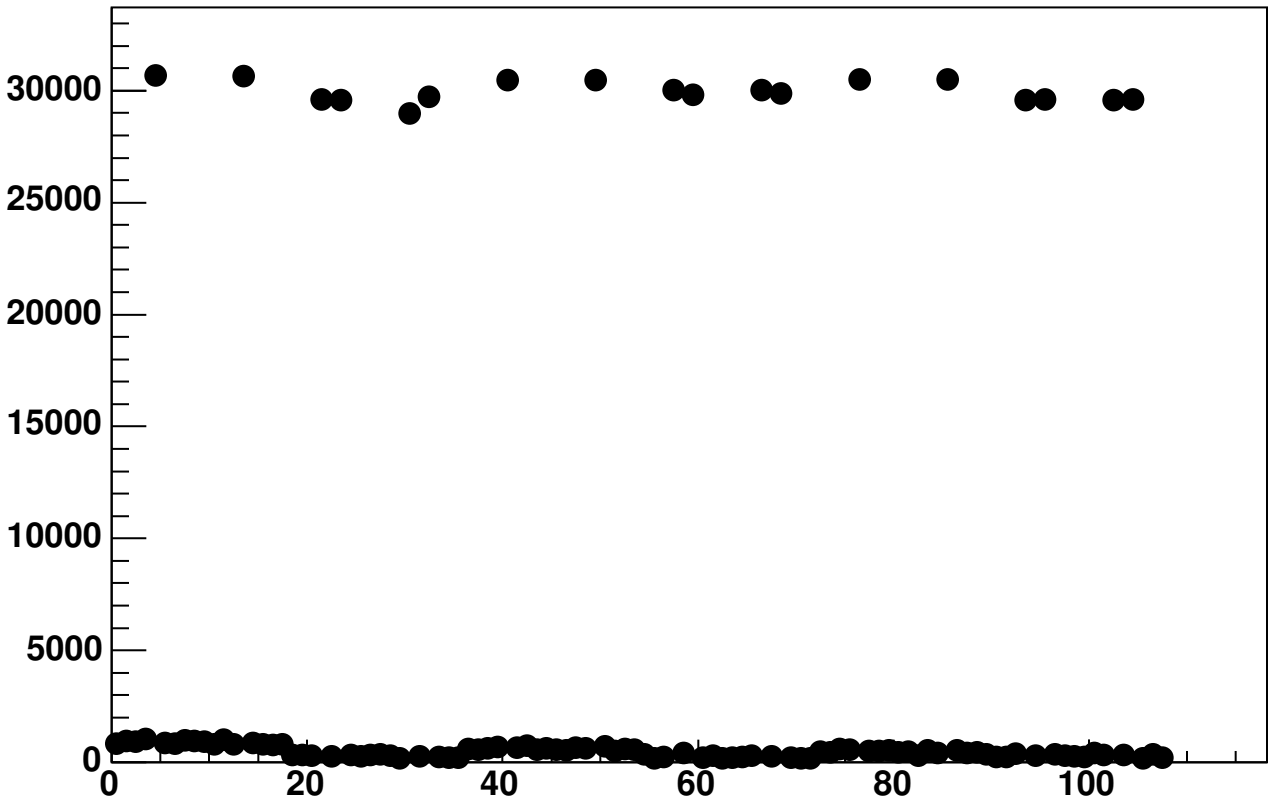
Enable 4, Hold=35, DAC=62000, ADC Mean vs 18*Chip+Chan



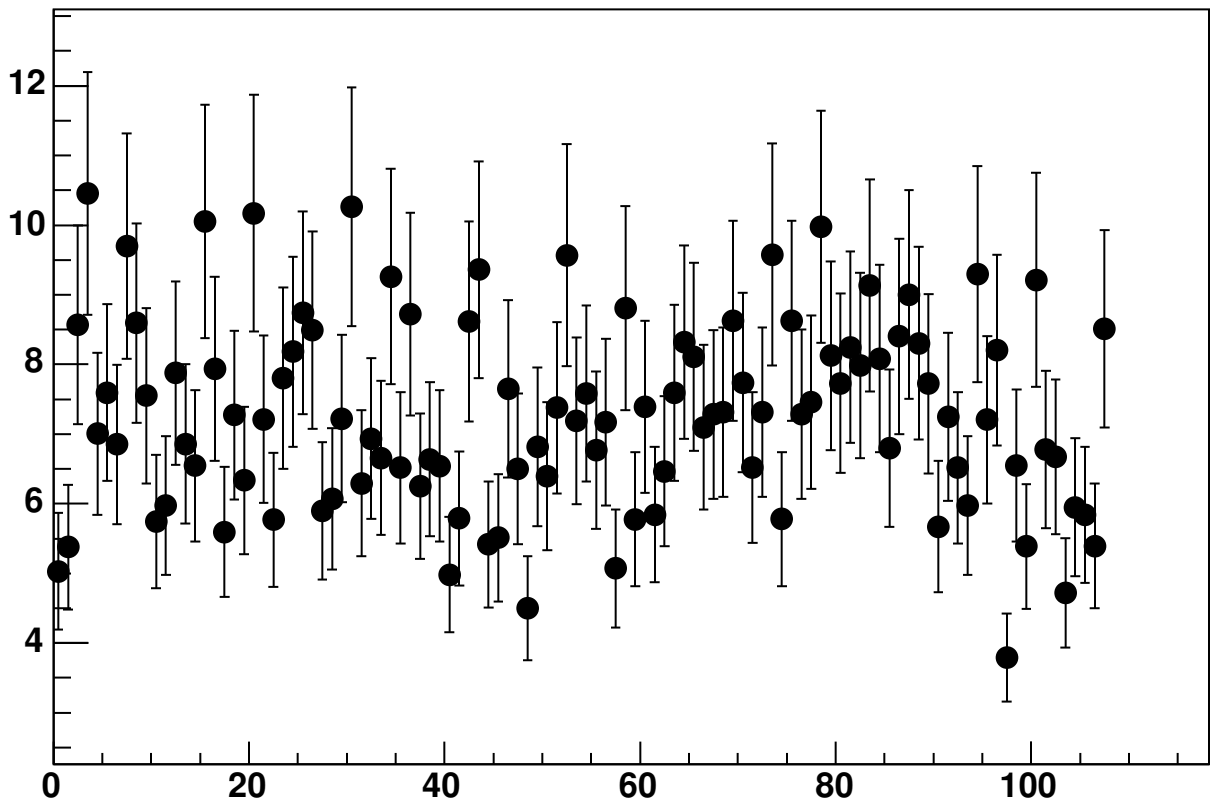
Enable 4, Hold=35, DAC=62000, ADC Noise vs 18*Chip+Chan



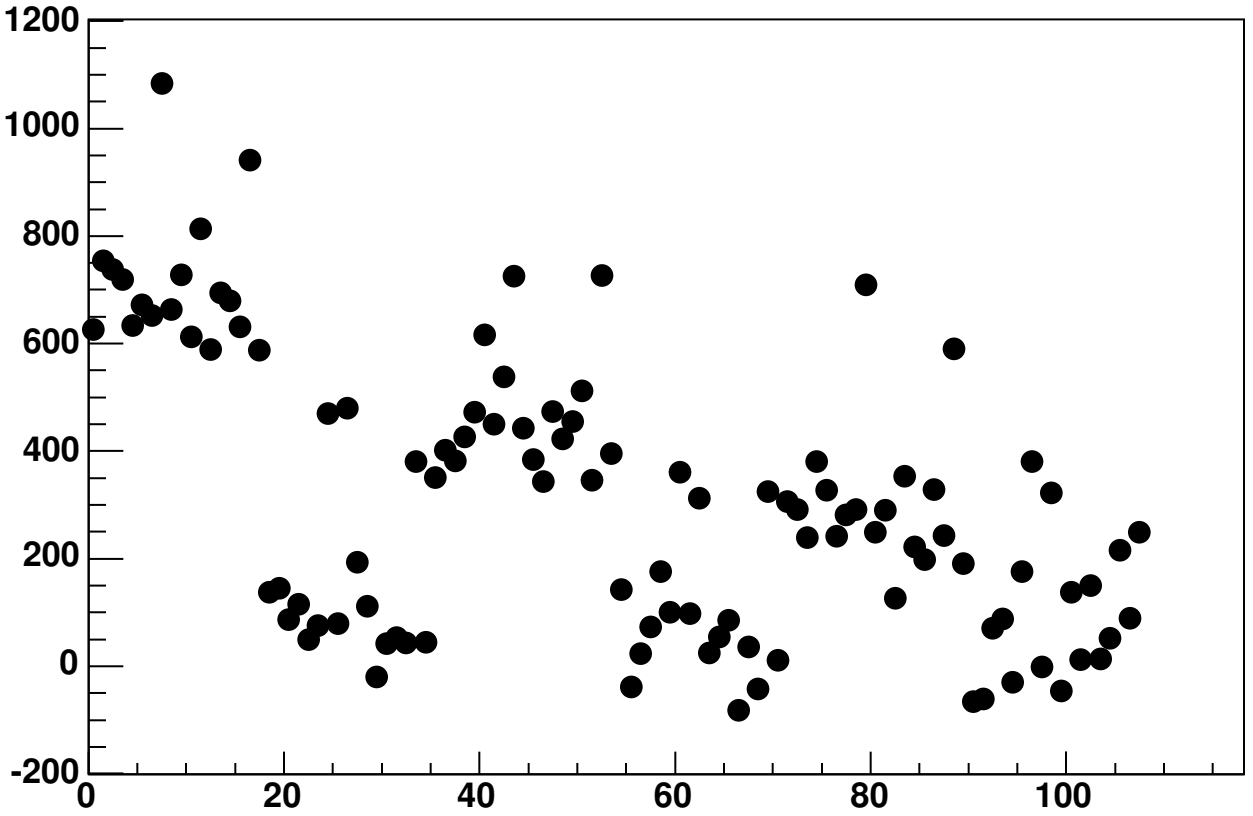
Enable 4, Hold=35, DAC=64000, ADC Mean vs 18*Chip+Chan



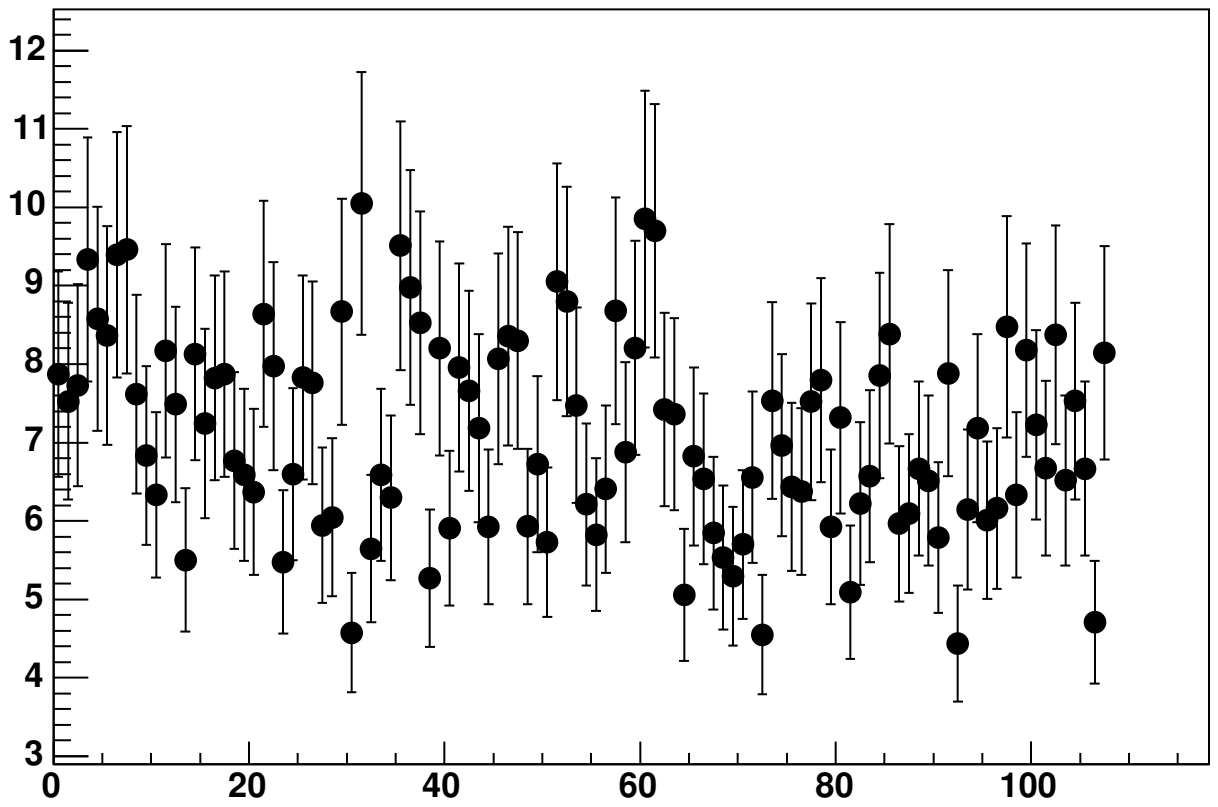
Enable 4, Hold=35, DAC=64000, ADC Noise vs 18*Chip+Chan



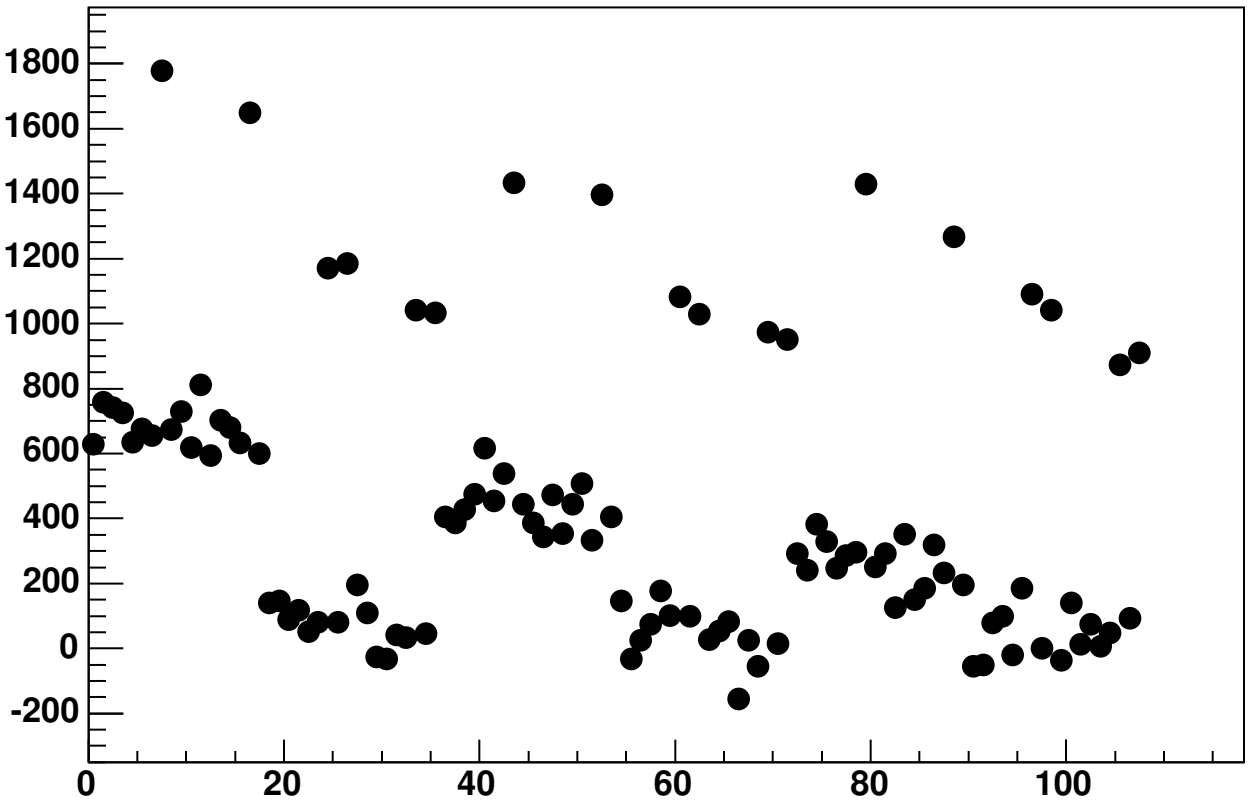
Enable 5, Hold=35, DAC=0, ADC Mean vs 18*Chip+Chan



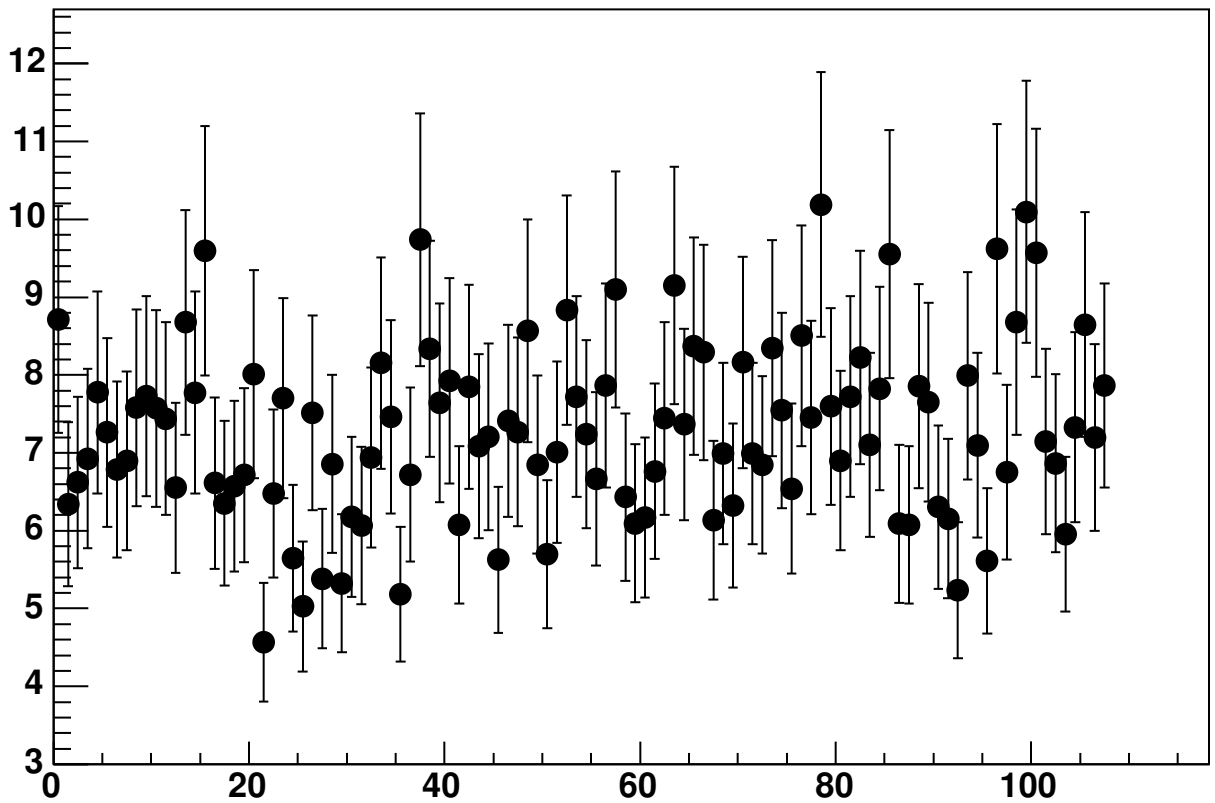
Enable 5, Hold=35, DAC=0, ADC Noise vs 18*Chip+Chan



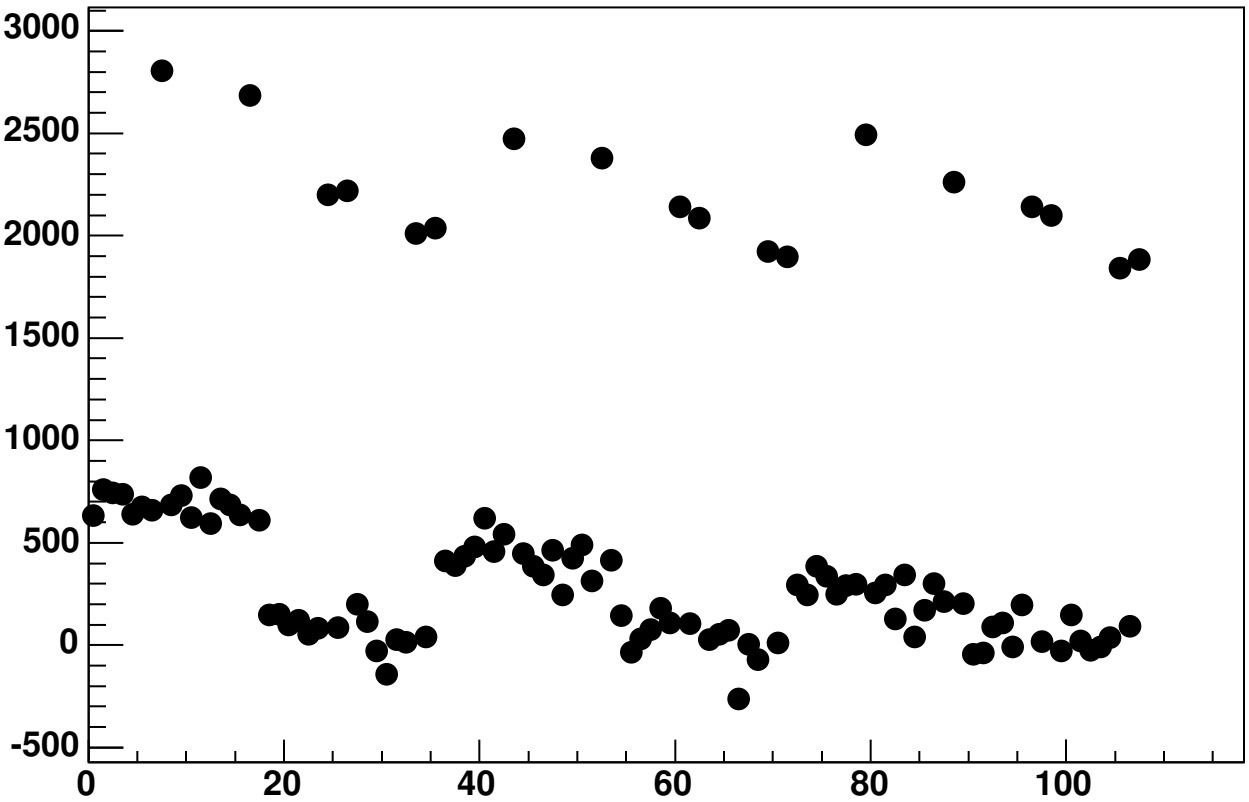
Enable 5, Hold=35, DAC=2000, ADC Mean vs 18*Chip+Chan



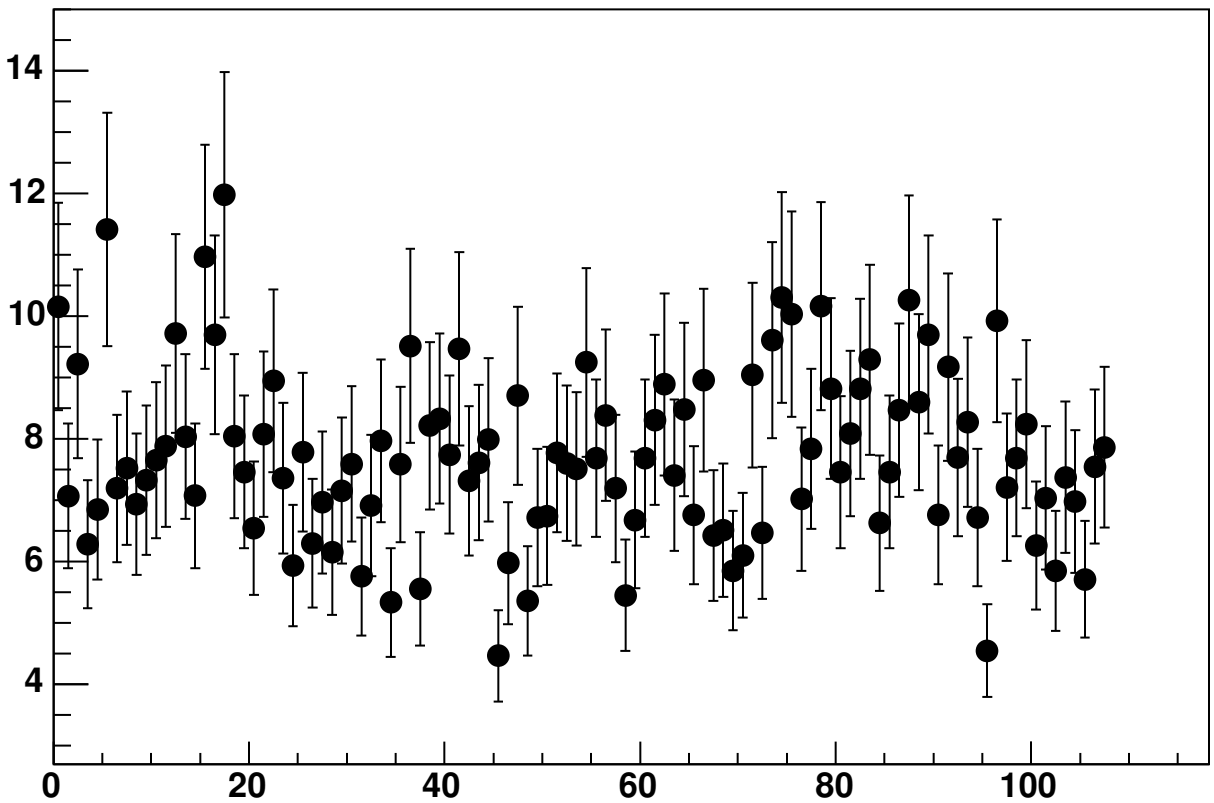
Enable 5, Hold=35, DAC=2000, ADC Noise vs 18*Chip+Chan



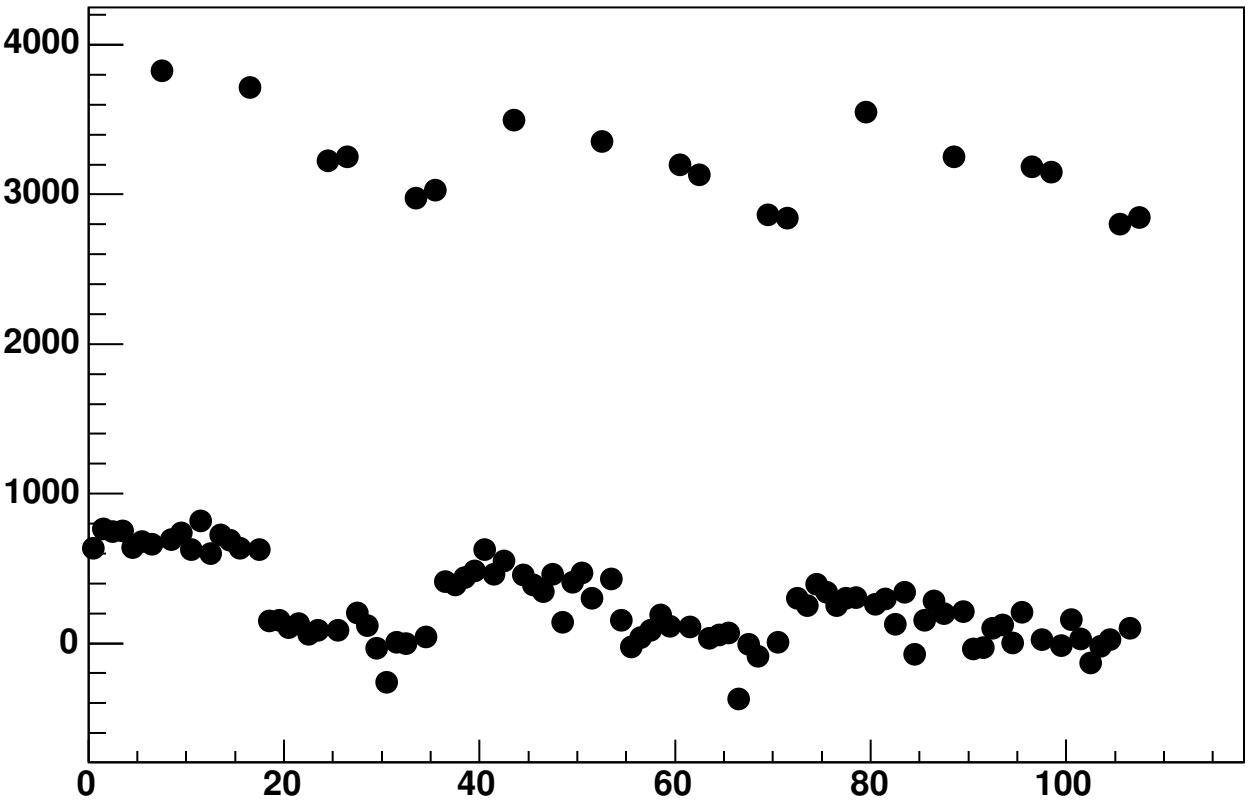
Enable 5, Hold=35, DAC=4000, ADC Mean vs 18*Chip+Chan



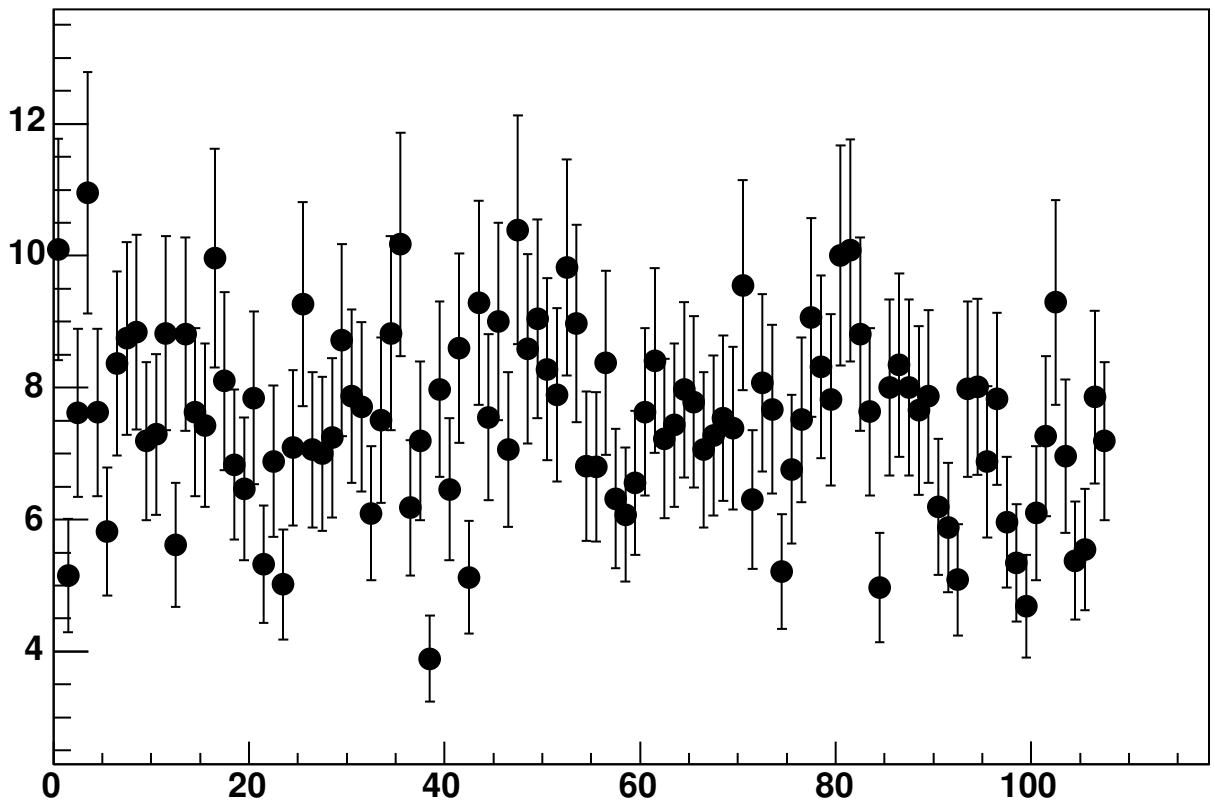
Enable 5, Hold=35, DAC=4000, ADC Noise vs 18*Chip+Chan



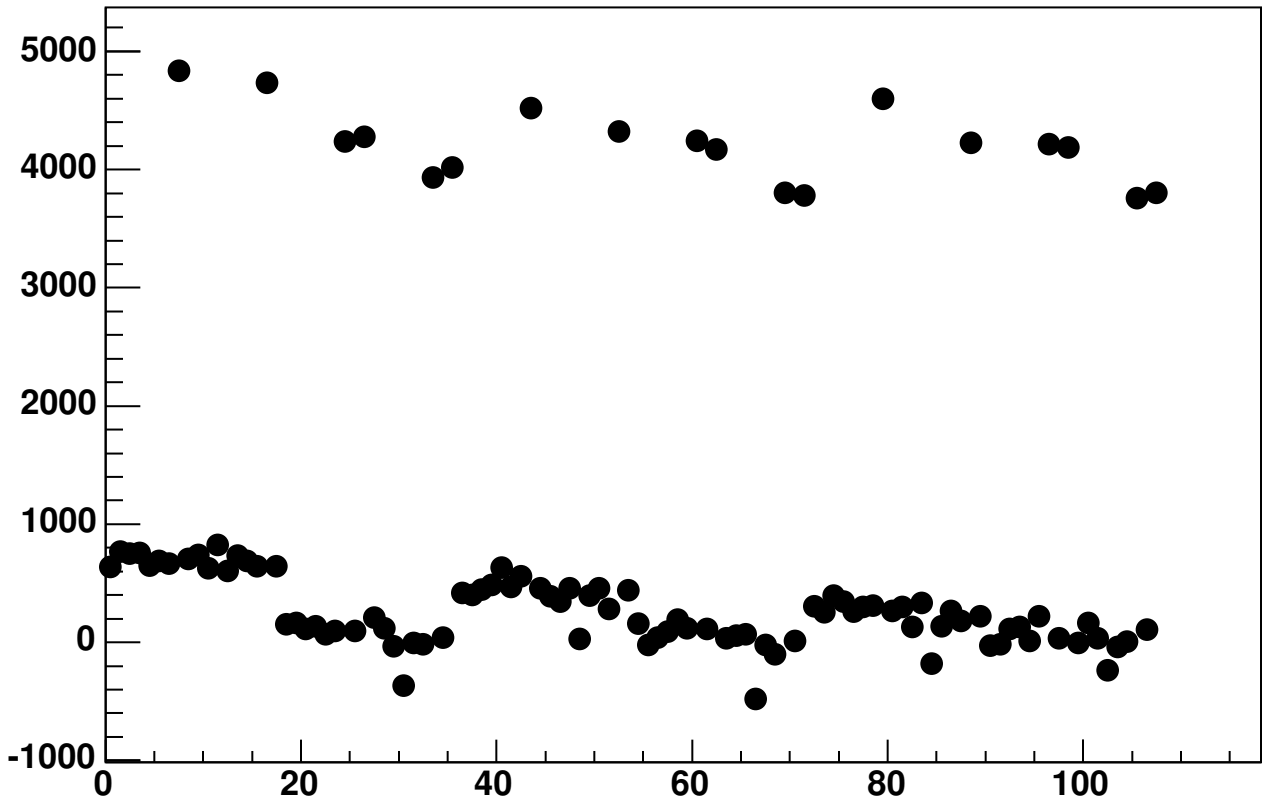
Enable 5, Hold=35, DAC=6000, ADC Mean vs 18*Chip+Chan



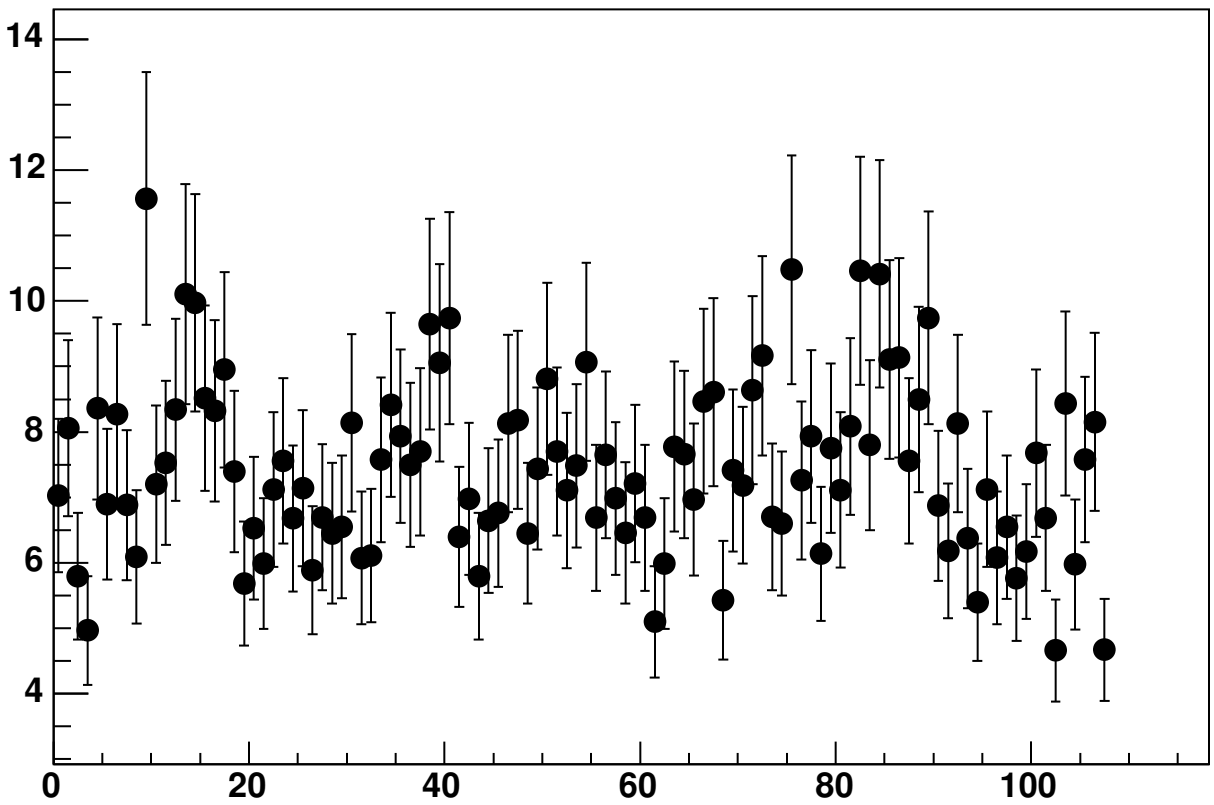
Enable 5, Hold=35, DAC=6000, ADC Noise vs 18*Chip+Chan



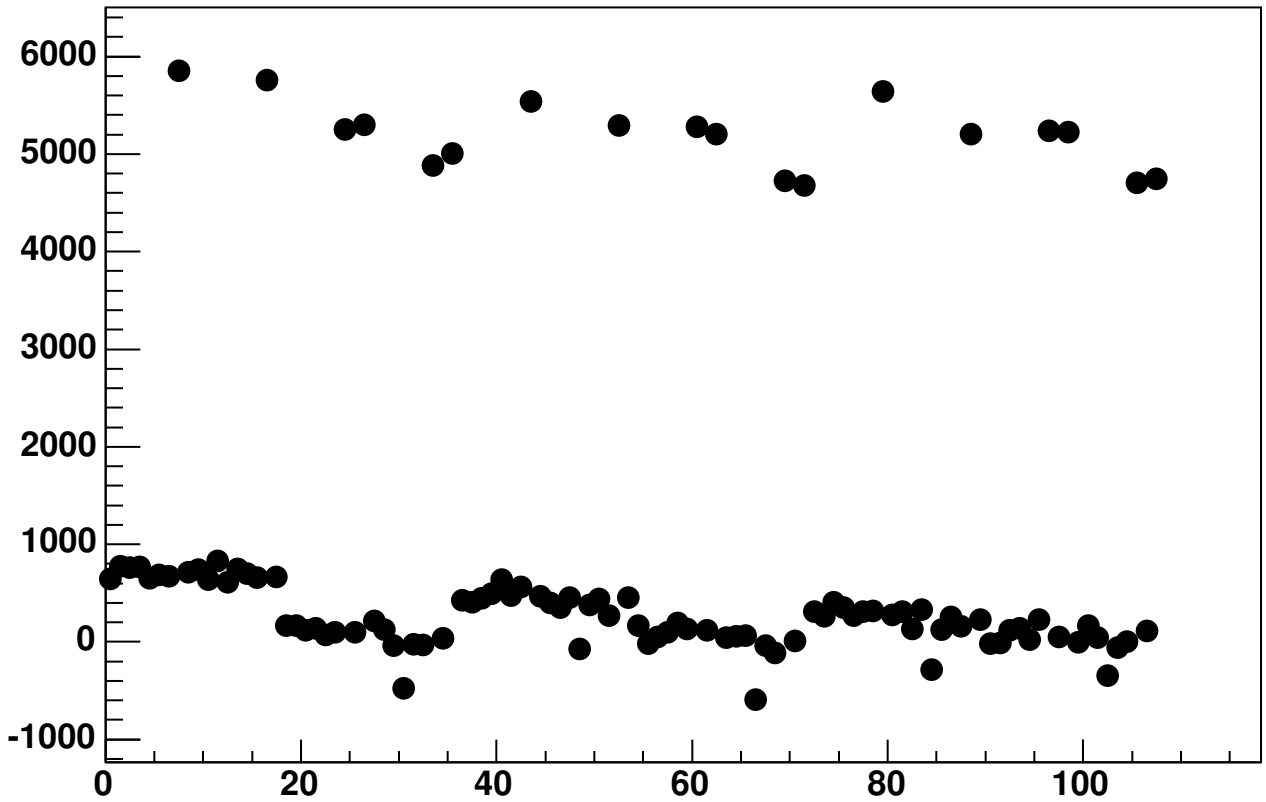
Enable 5, Hold=35, DAC=8000, ADC Mean vs 18*Chip+Chan



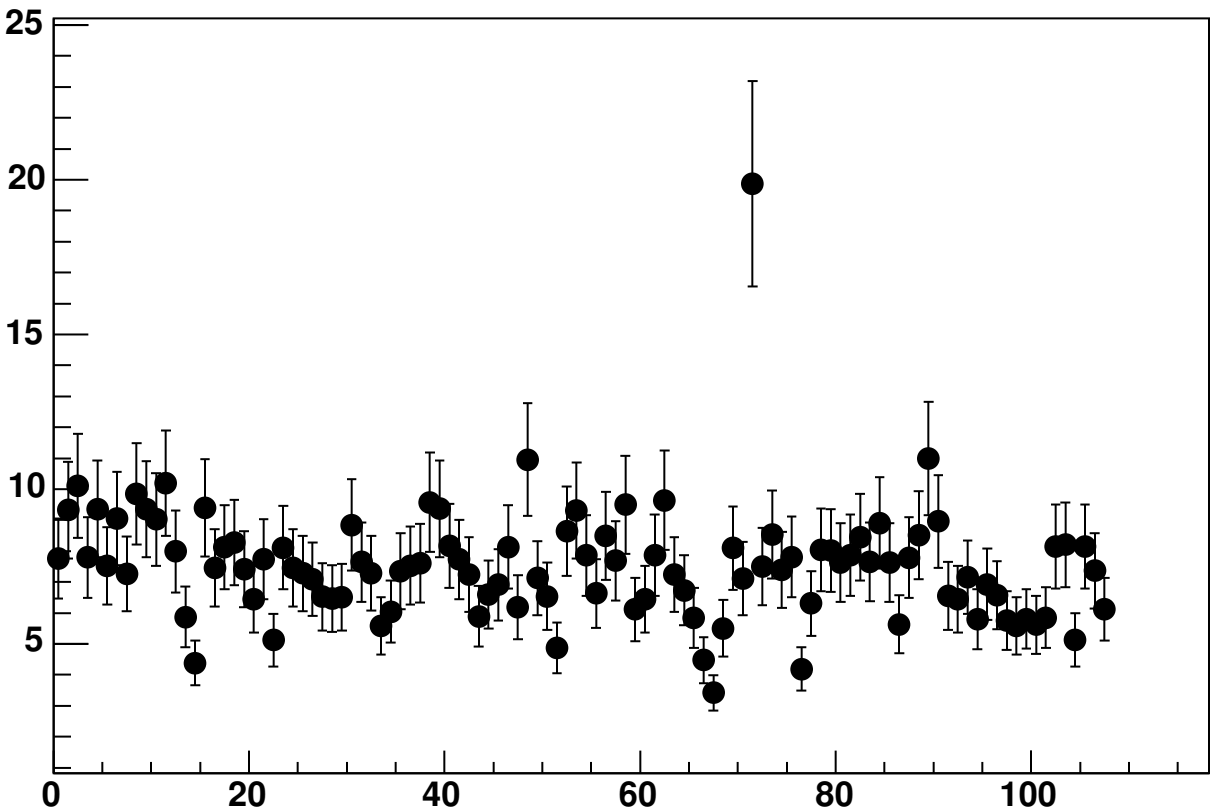
Enable 5, Hold=35, DAC=8000, ADC Noise vs 18*Chip+Chan



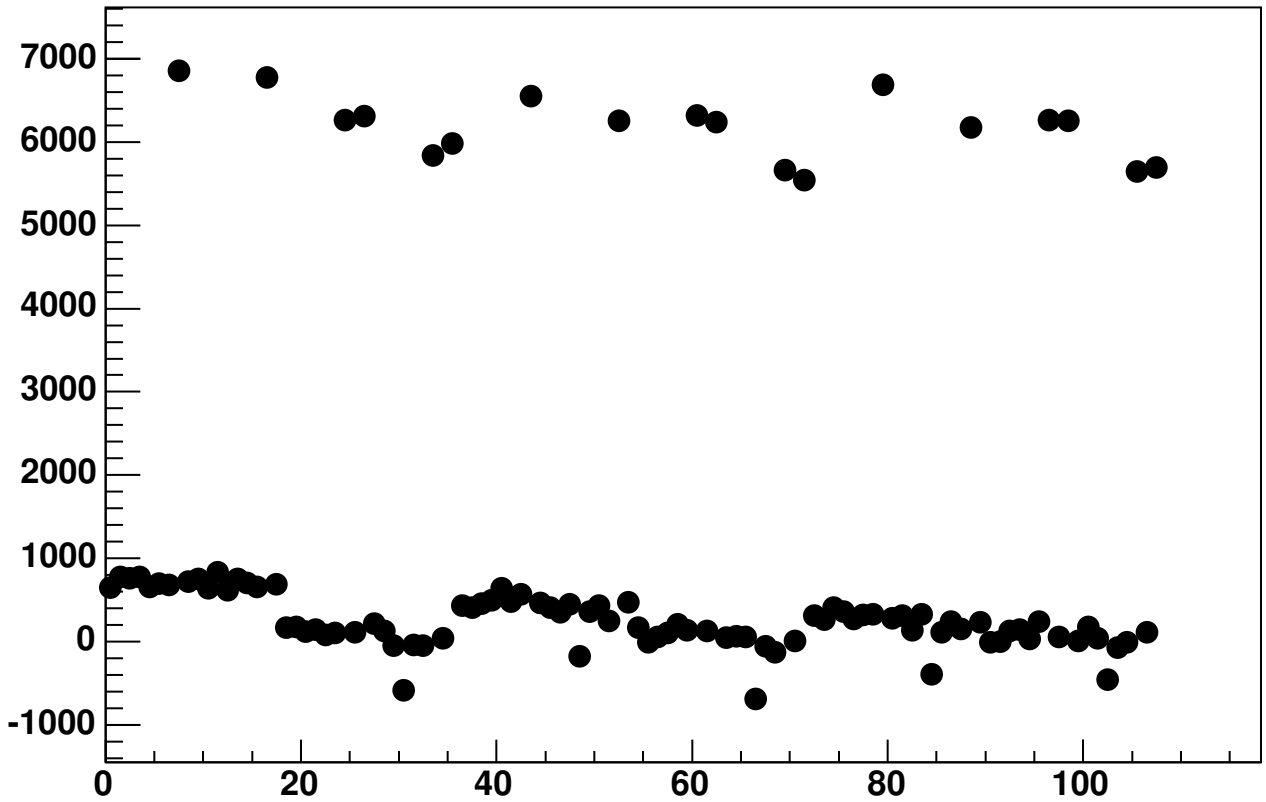
Enable 5, Hold=35, DAC=10000, ADC Mean vs 18*Chip+Chan



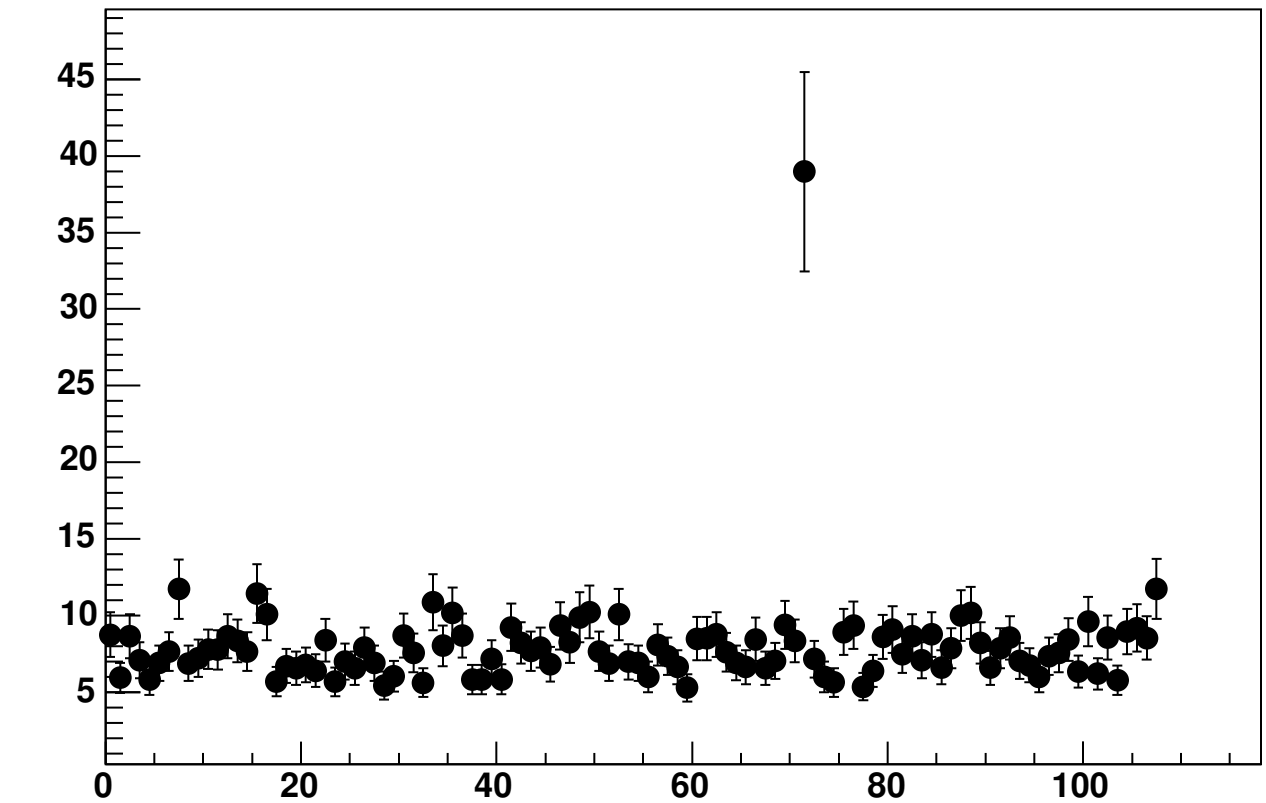
Enable 5, Hold=35, DAC=10000, ADC Noise vs 18*Chip+Chan



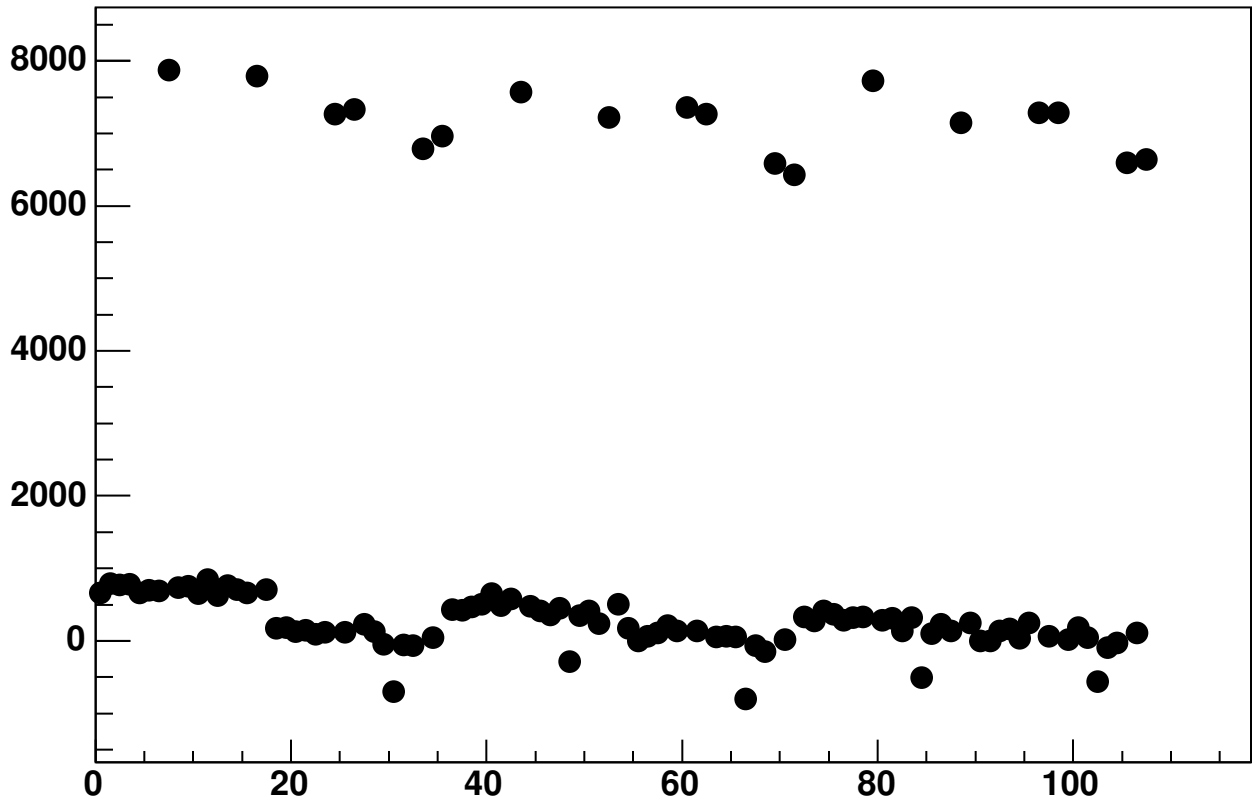
Enable 5, Hold=35, DAC=12000, ADC Mean vs 18*Chip+Chan



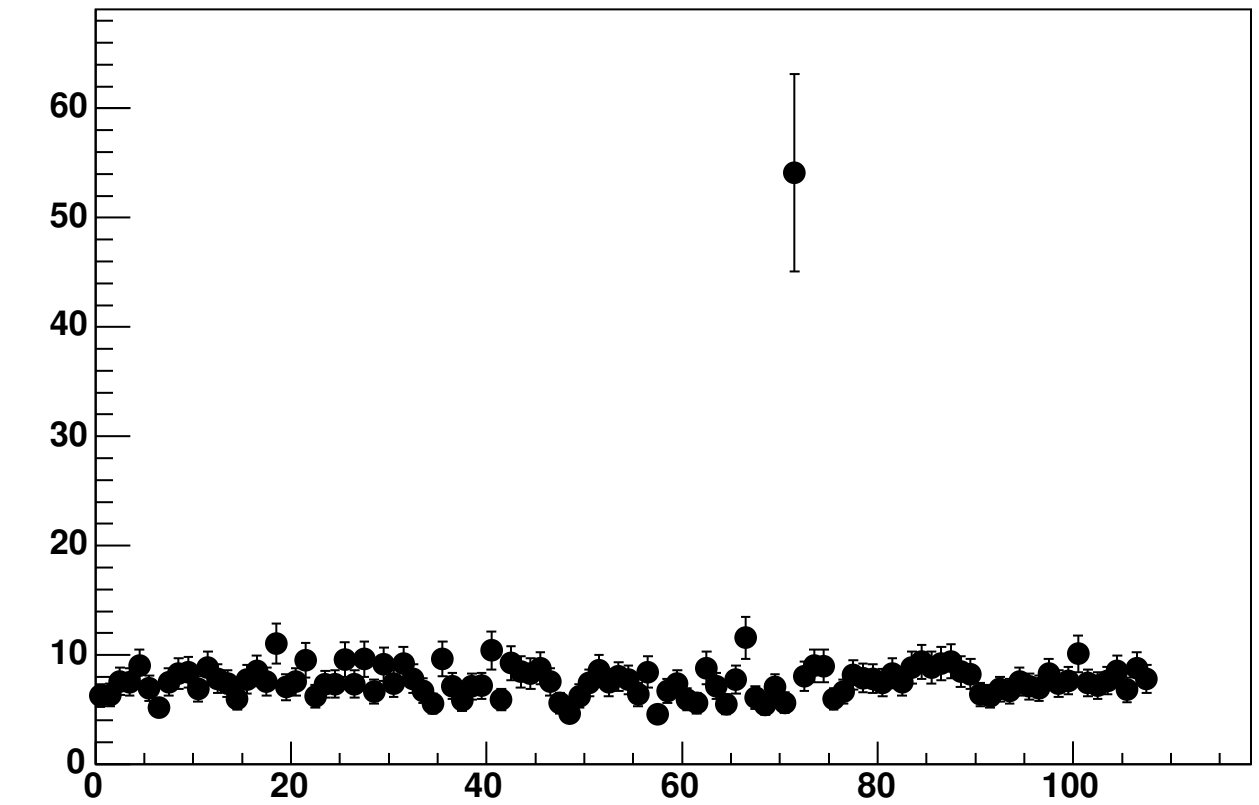
Enable 5, Hold=35, DAC=12000, ADC Noise vs 18*Chip+Chan



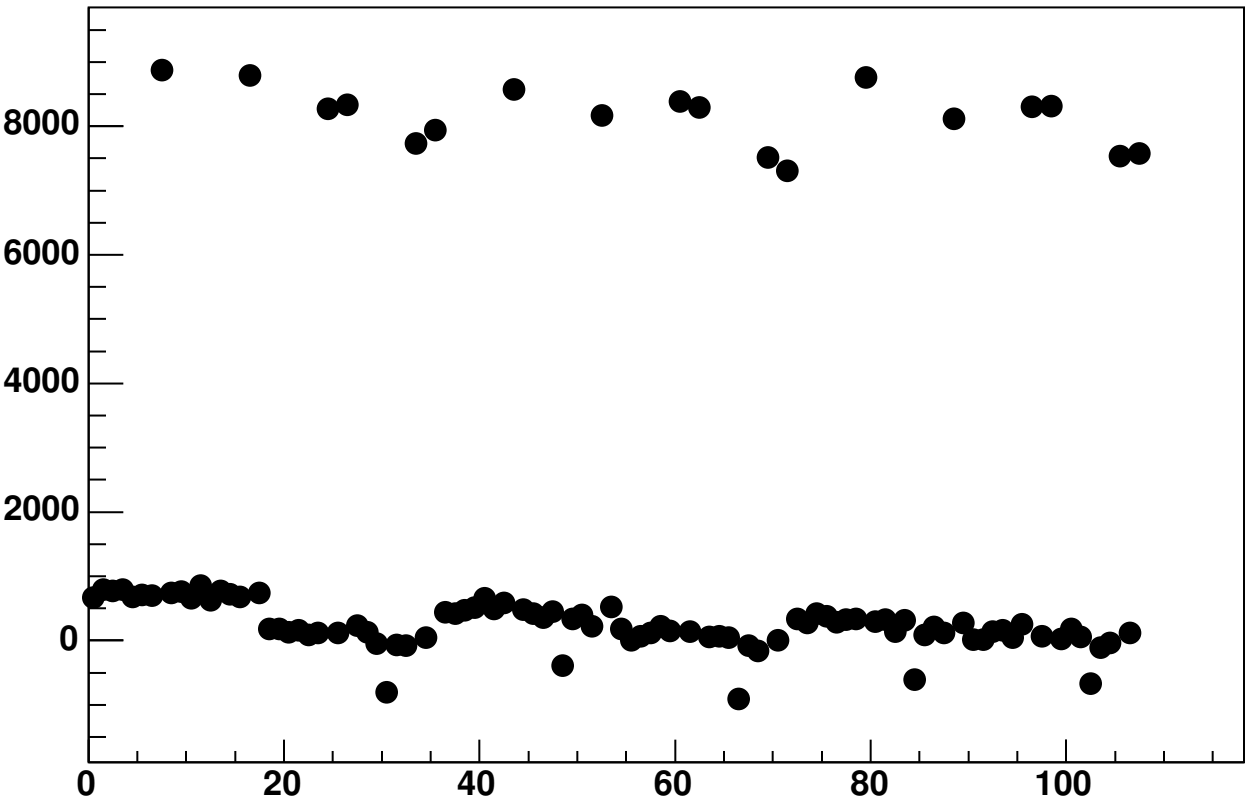
Enable 5, Hold=35, DAC=14000, ADC Mean vs 18*Chip+Chan



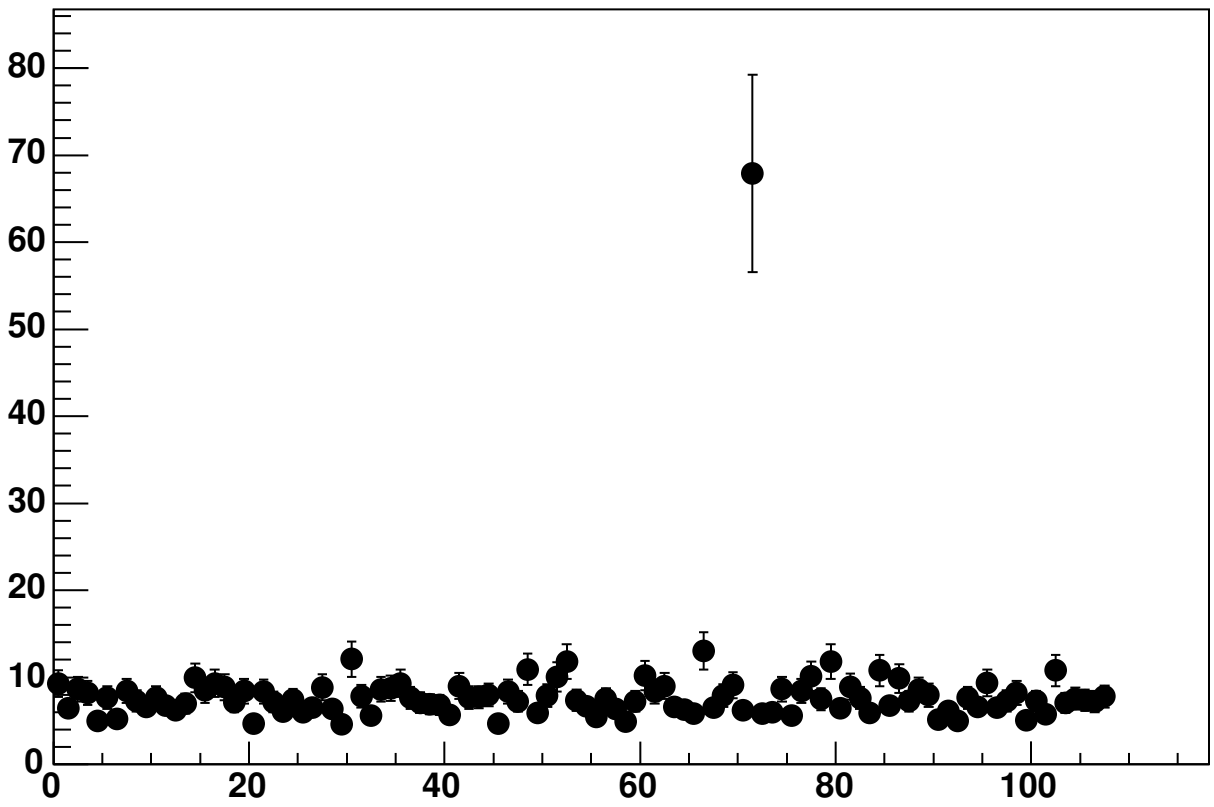
Enable 5, Hold=35, DAC=14000, ADC Noise vs 18*Chip+Chan



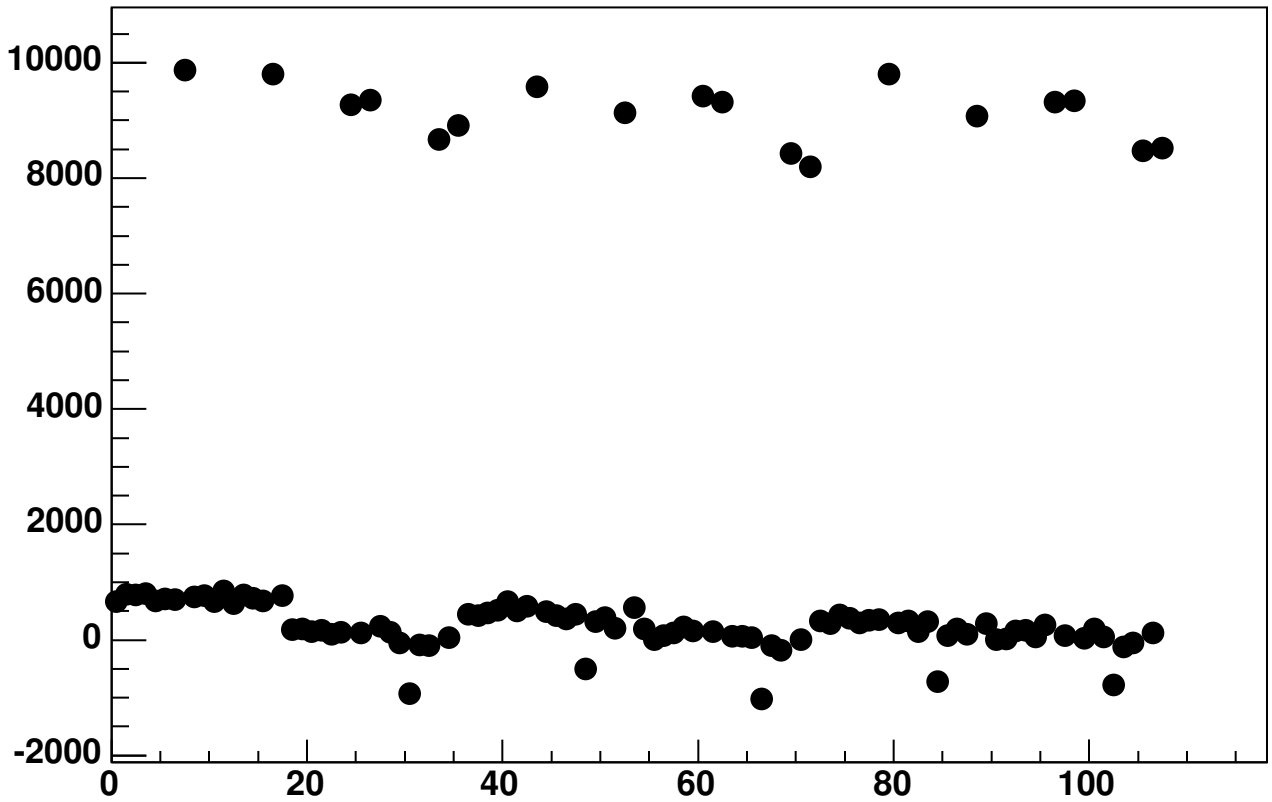
Enable 5, Hold=35, DAC=16000, ADC Mean vs 18*Chip+Chan



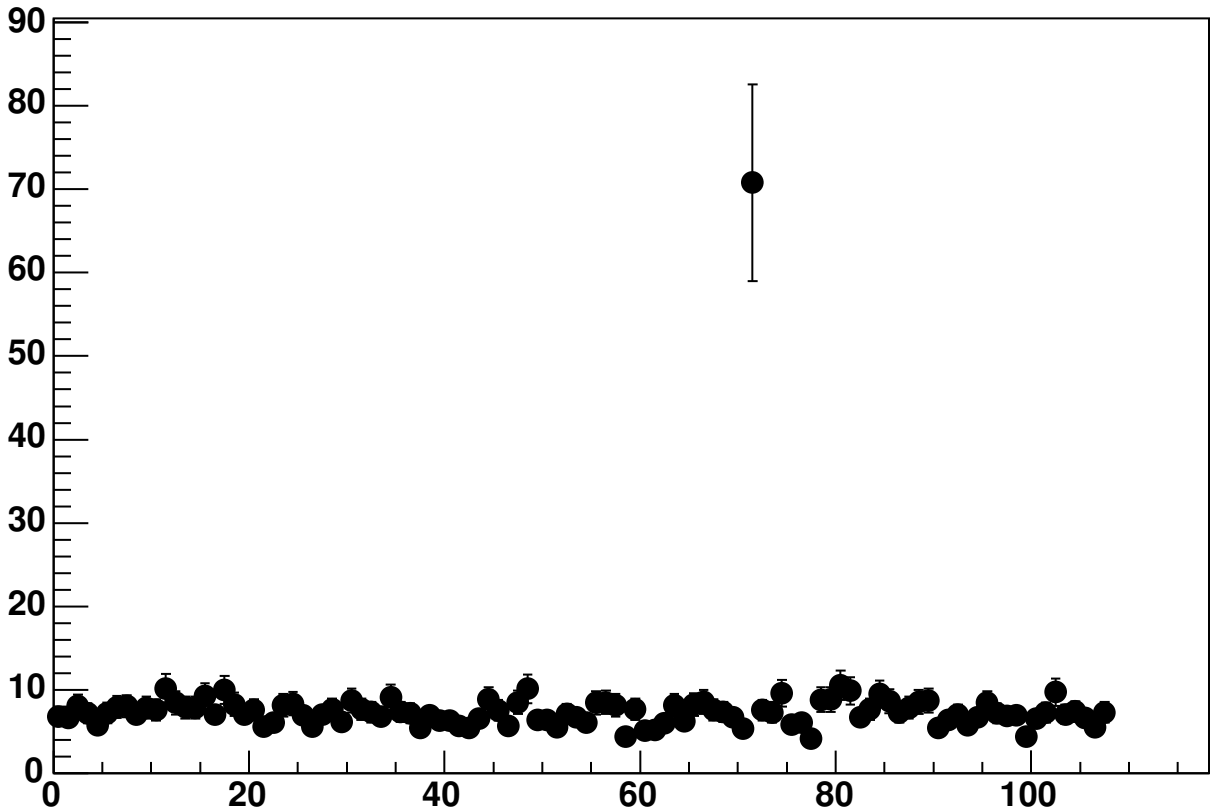
Enable 5, Hold=35, DAC=16000, ADC Noise vs 18*Chip+Chan



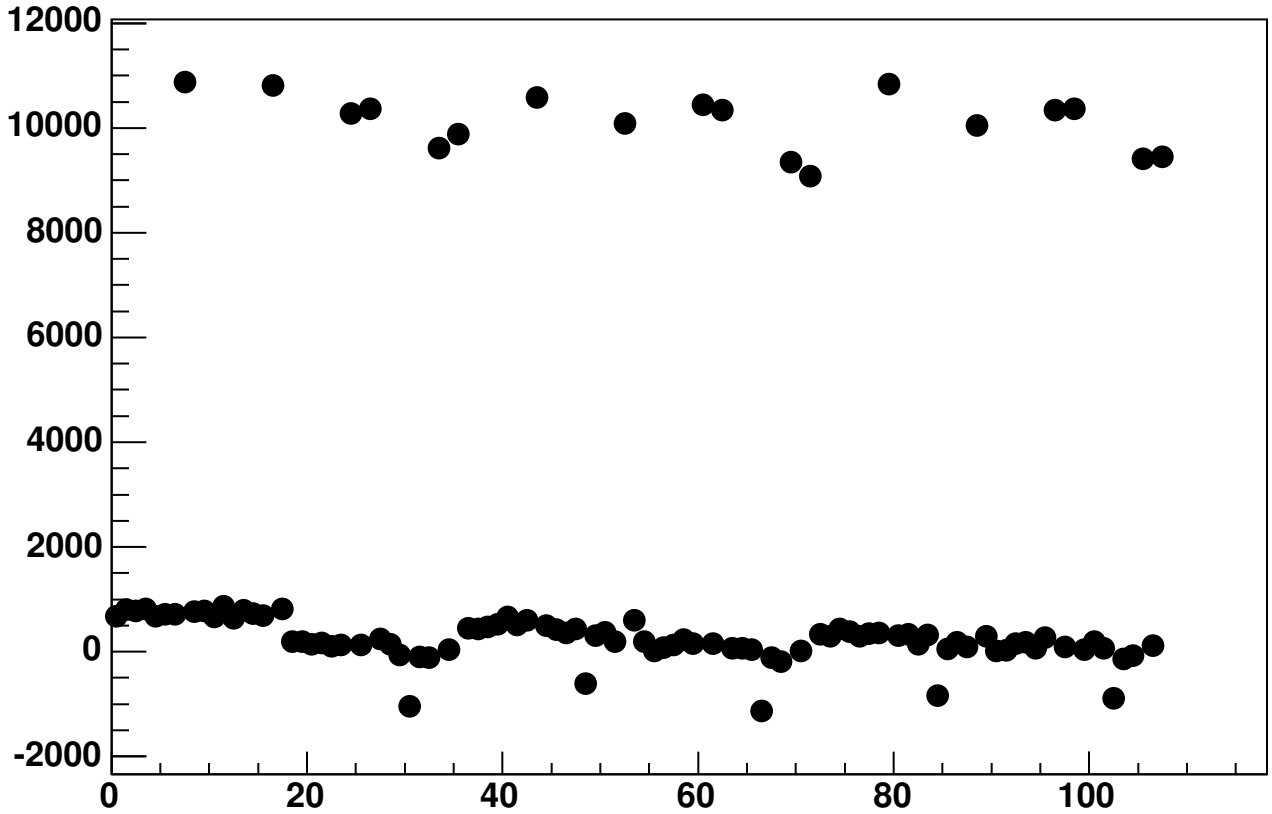
Enable 5, Hold=35, DAC=18000, ADC Mean vs 18*Chip+Chan



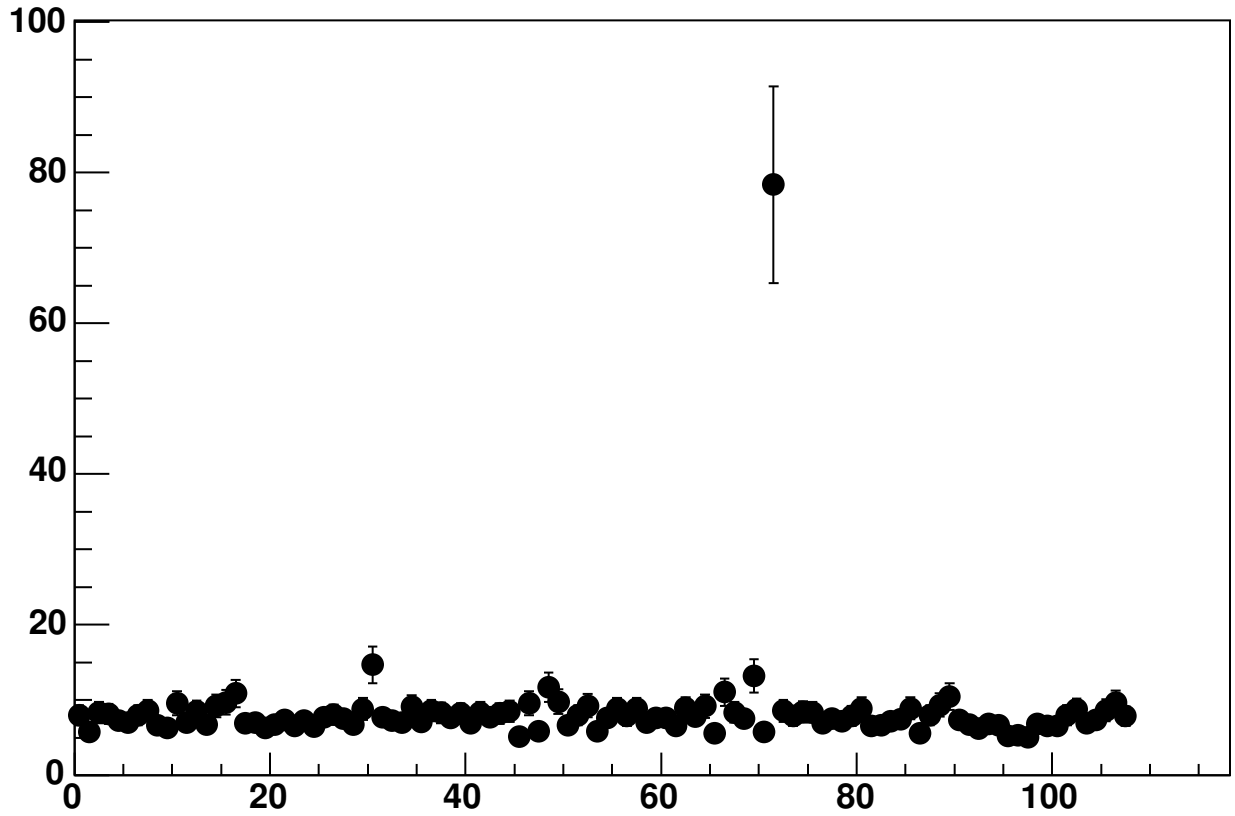
Enable 5, Hold=35, DAC=18000, ADC Noise vs 18*Chip+Chan



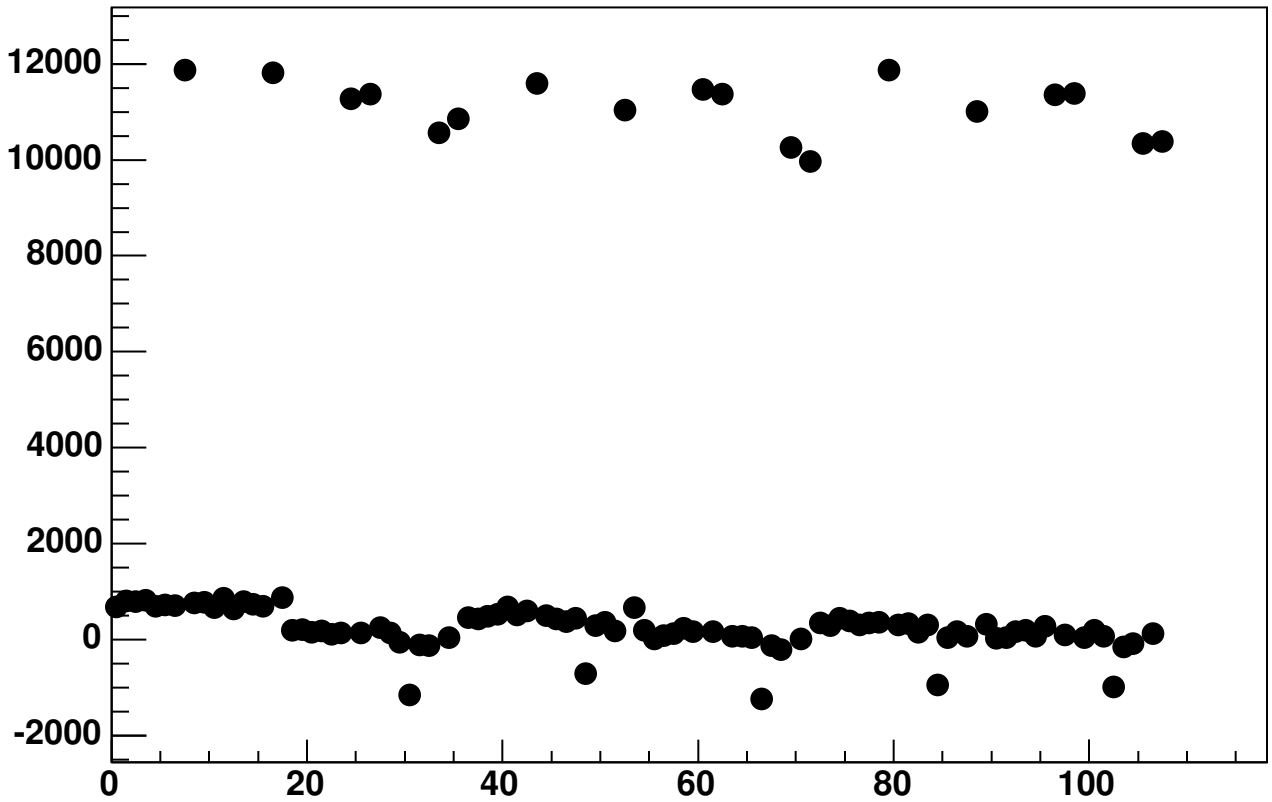
Enable 5, Hold=35, DAC=20000, ADC Mean vs 18*Chip+Chan



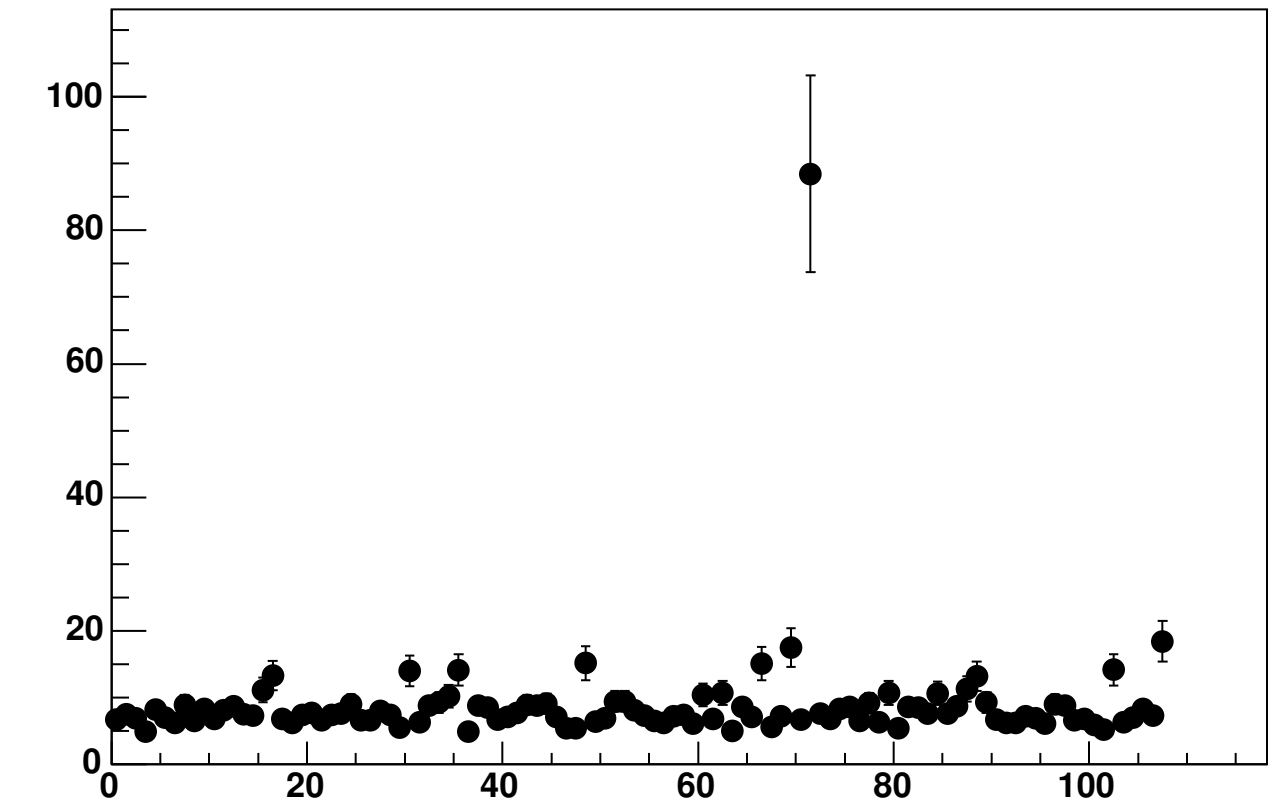
Enable 5, Hold=35, DAC=20000, ADC Noise vs 18*Chip+Chan



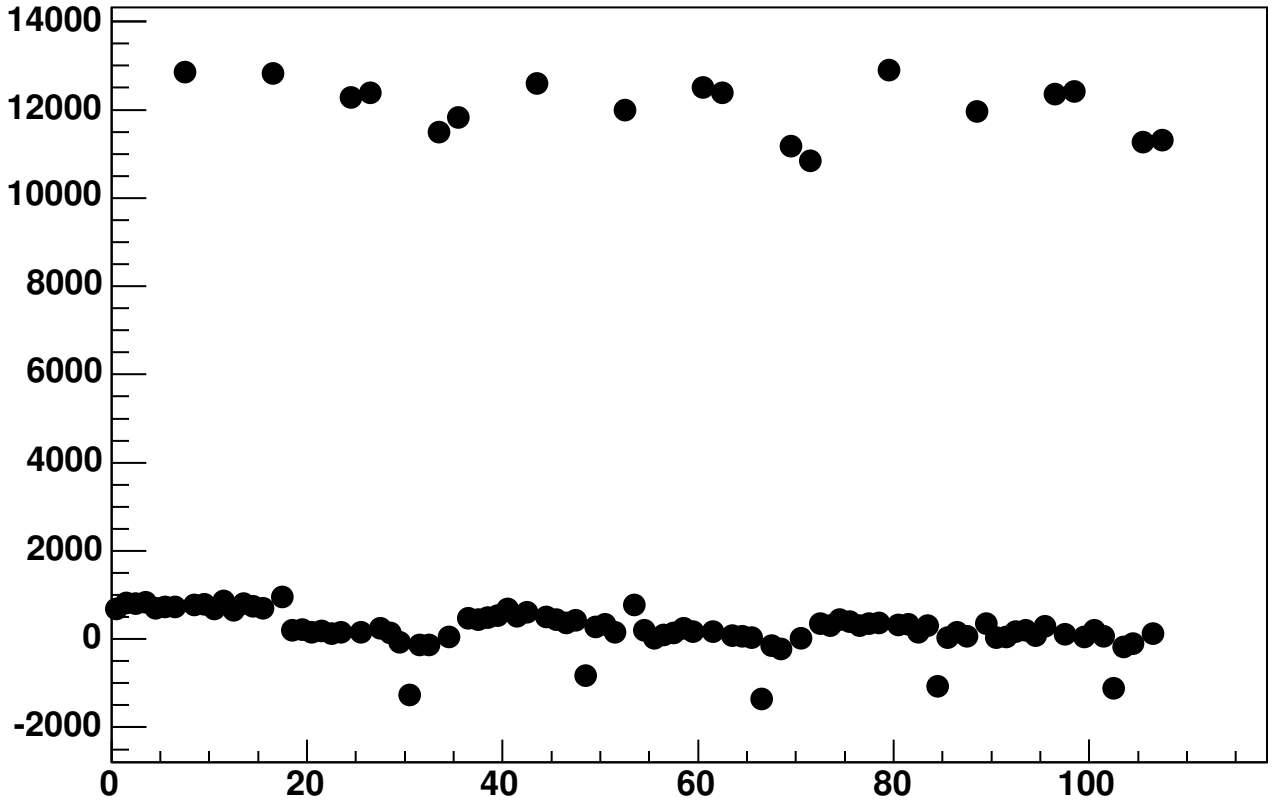
Enable 5, Hold=35, DAC=22000, ADC Mean vs 18*Chip+Chan



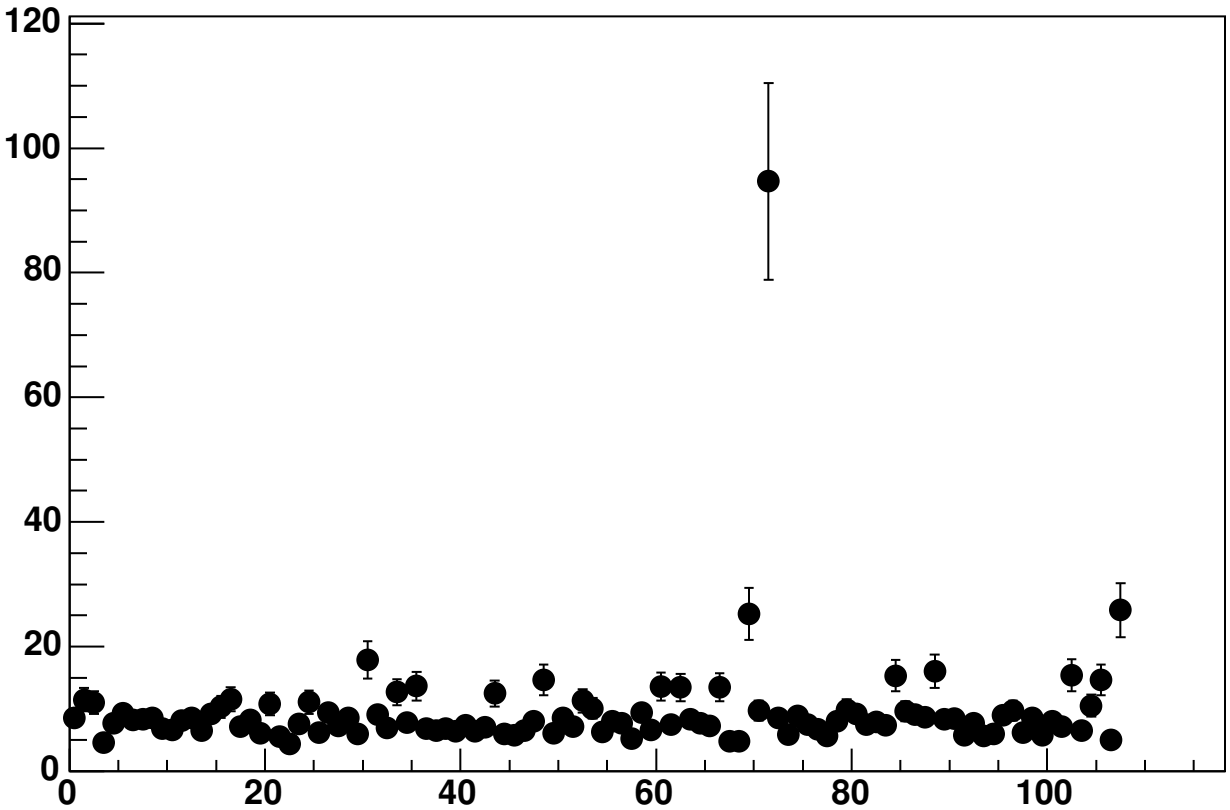
Enable 5, Hold=35, DAC=22000, ADC Noise vs 18*Chip+Chan



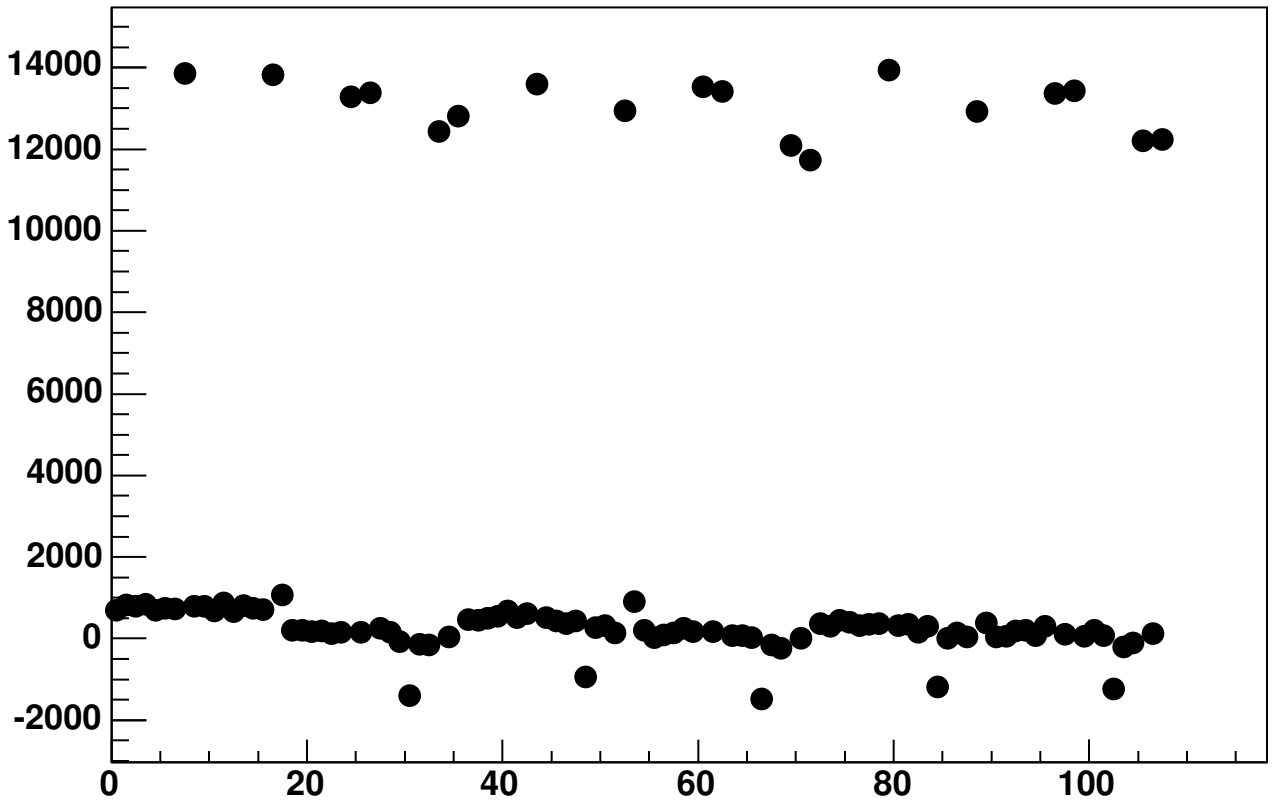
Enable 5, Hold=35, DAC=24000, ADC Mean vs 18*Chip+Chan



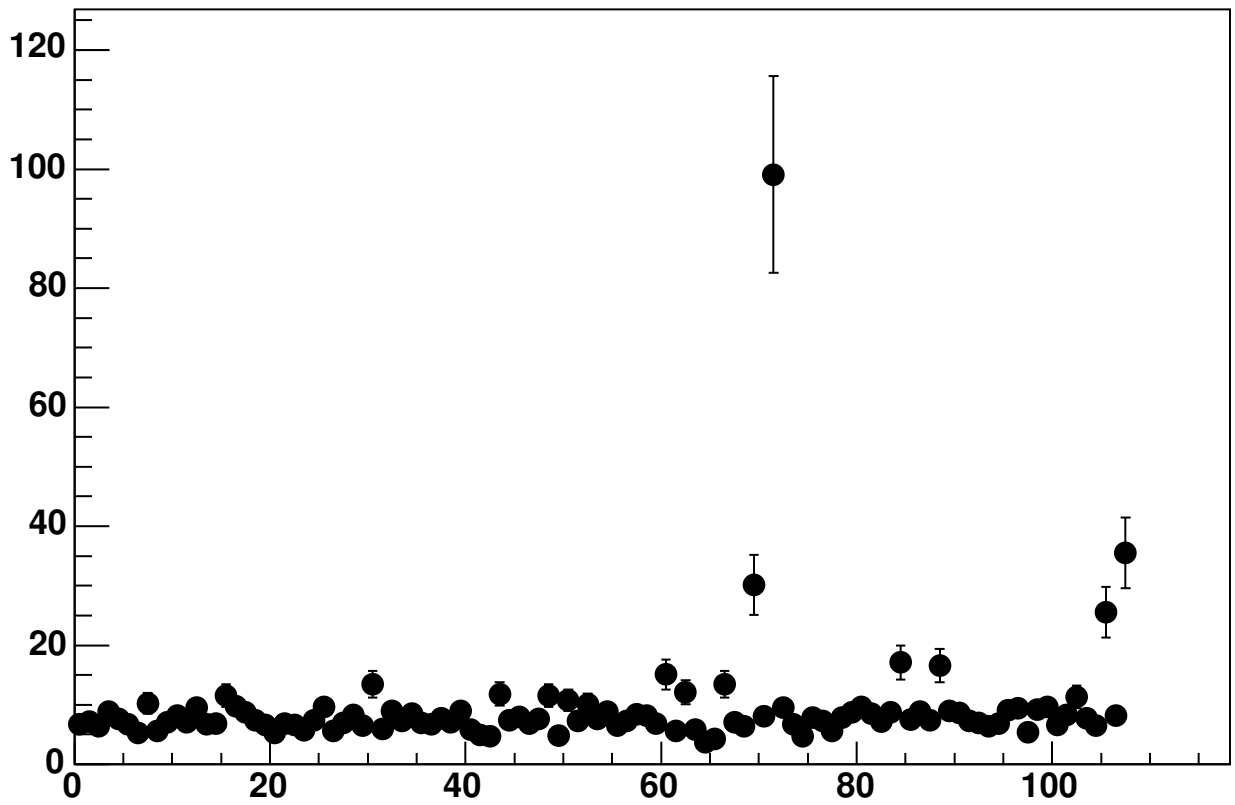
Enable 5, Hold=35, DAC=24000, ADC Noise vs 18*Chip+Chan



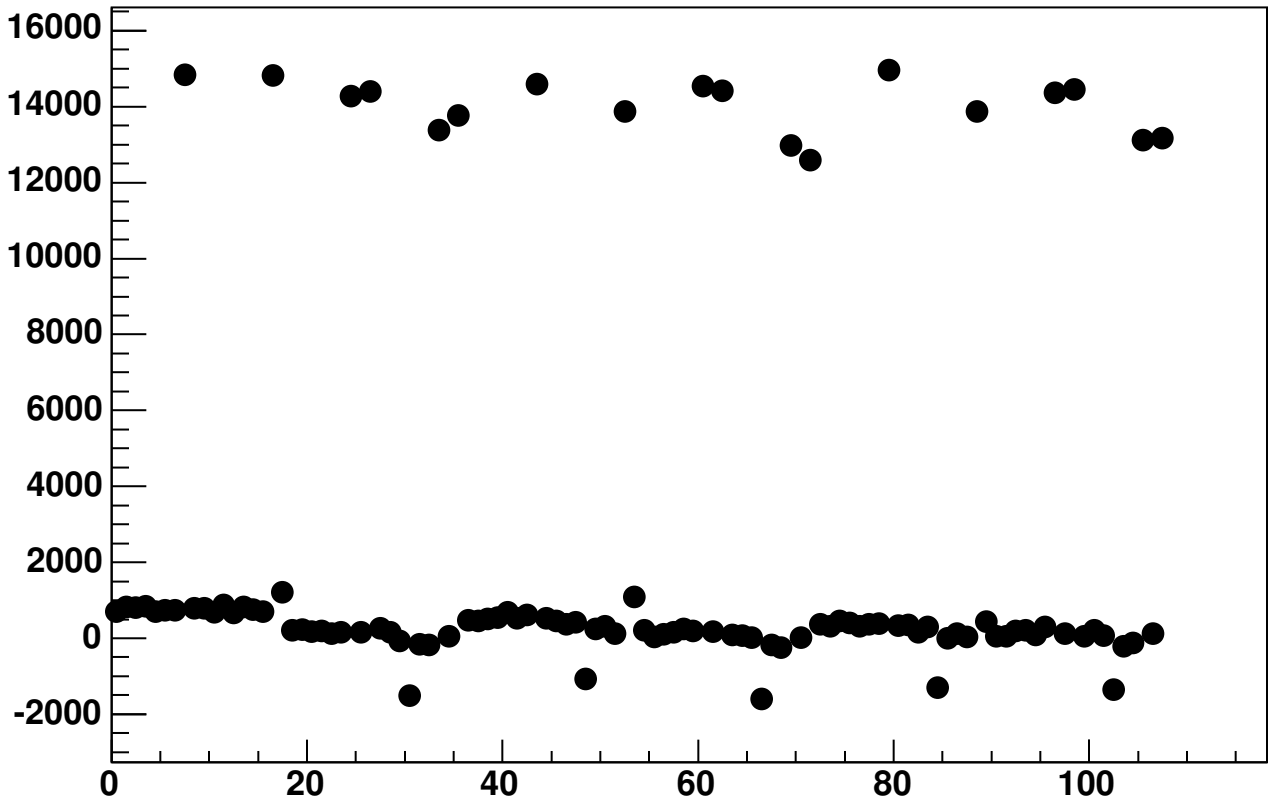
Enable 5, Hold=35, DAC=26000, ADC Mean vs 18*Chip+Chan



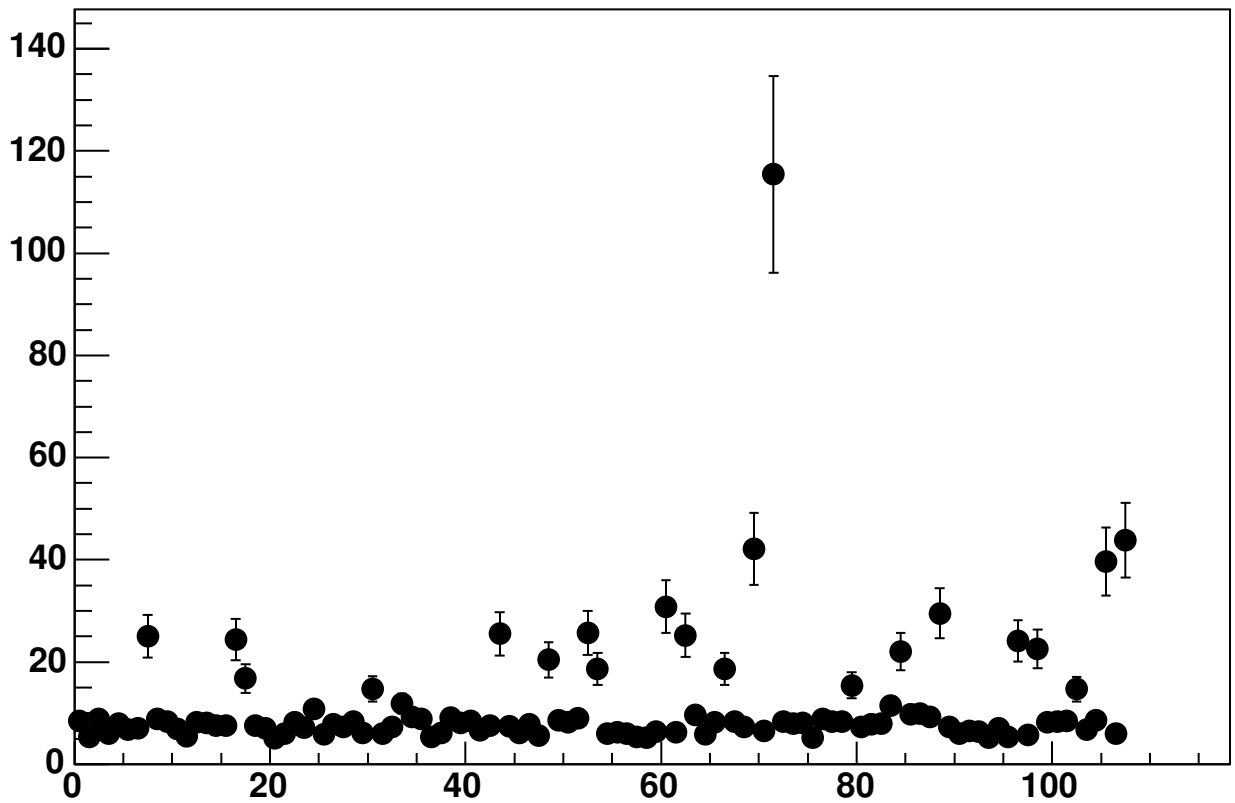
Enable 5, Hold=35, DAC=26000, ADC Noise vs 18*Chip+Chan



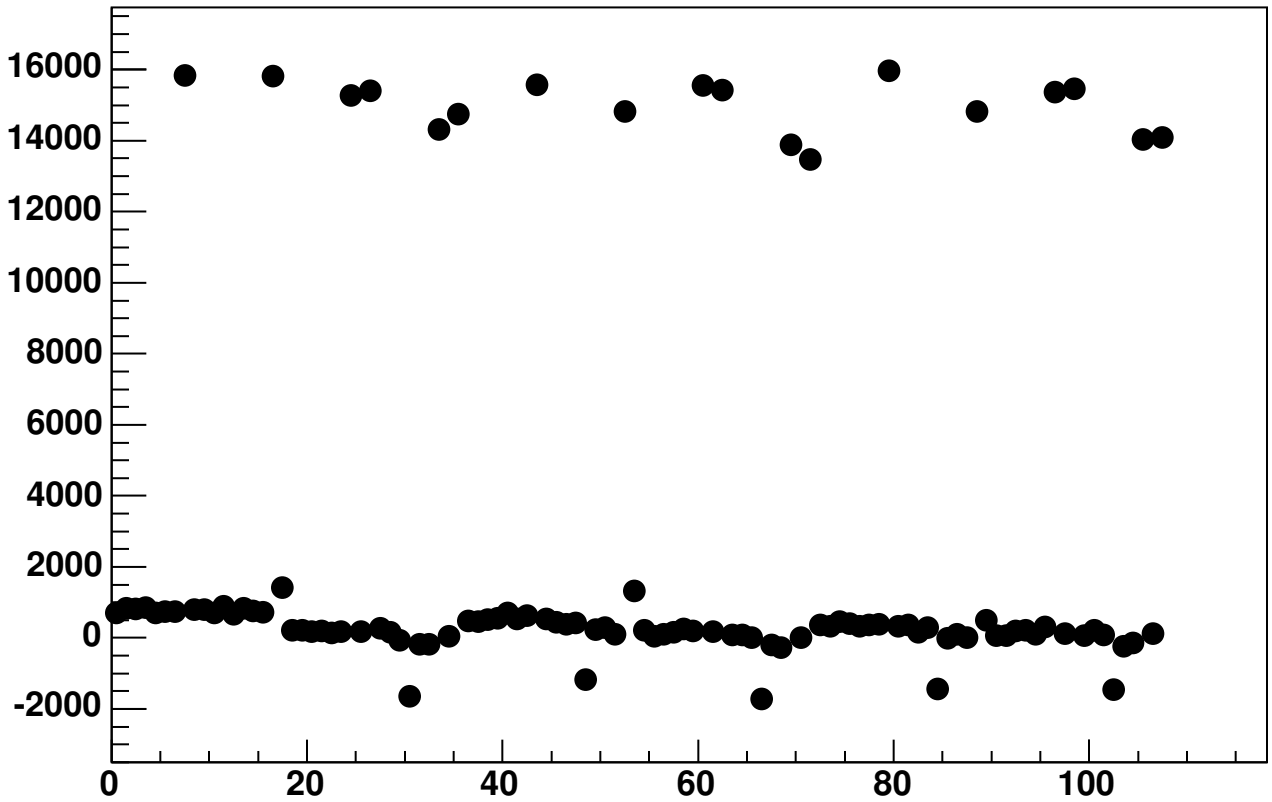
Enable 5, Hold=35, DAC=28000, ADC Mean vs 18*Chip+Chan



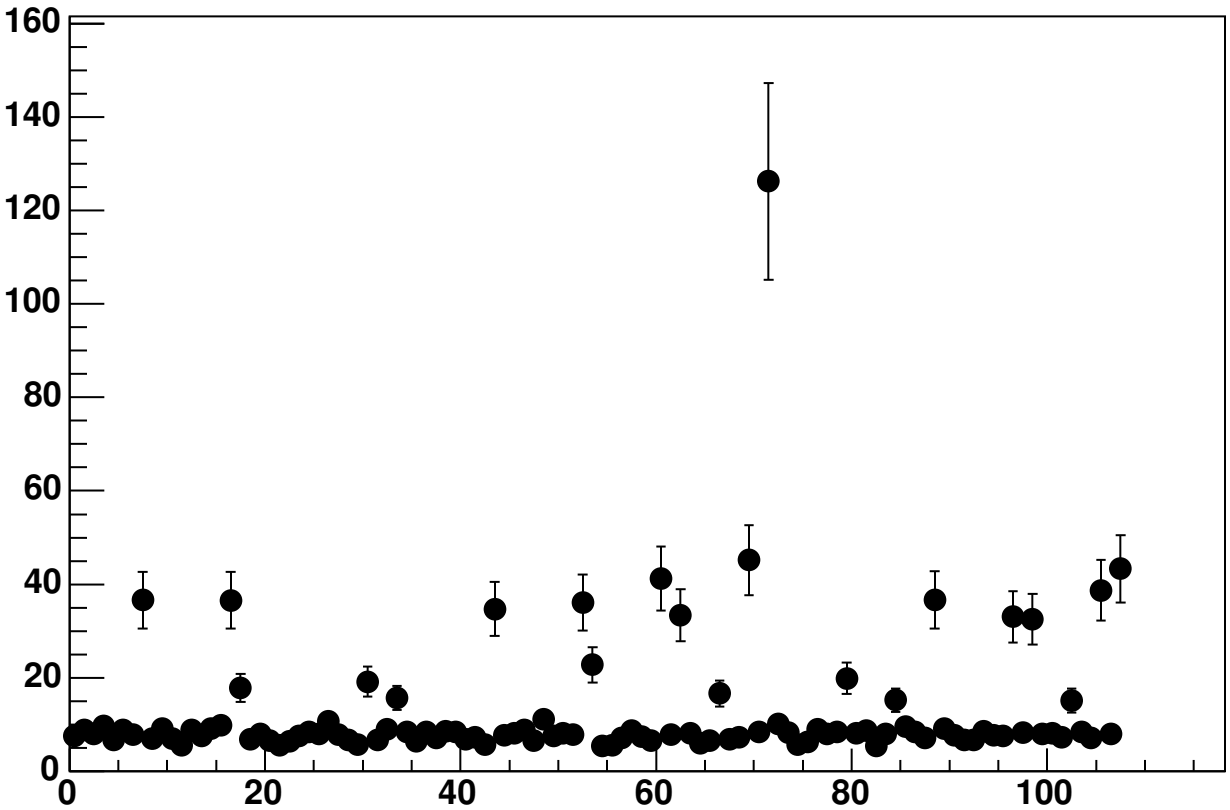
Enable 5, Hold=35, DAC=28000, ADC Noise vs 18*Chip+Chan



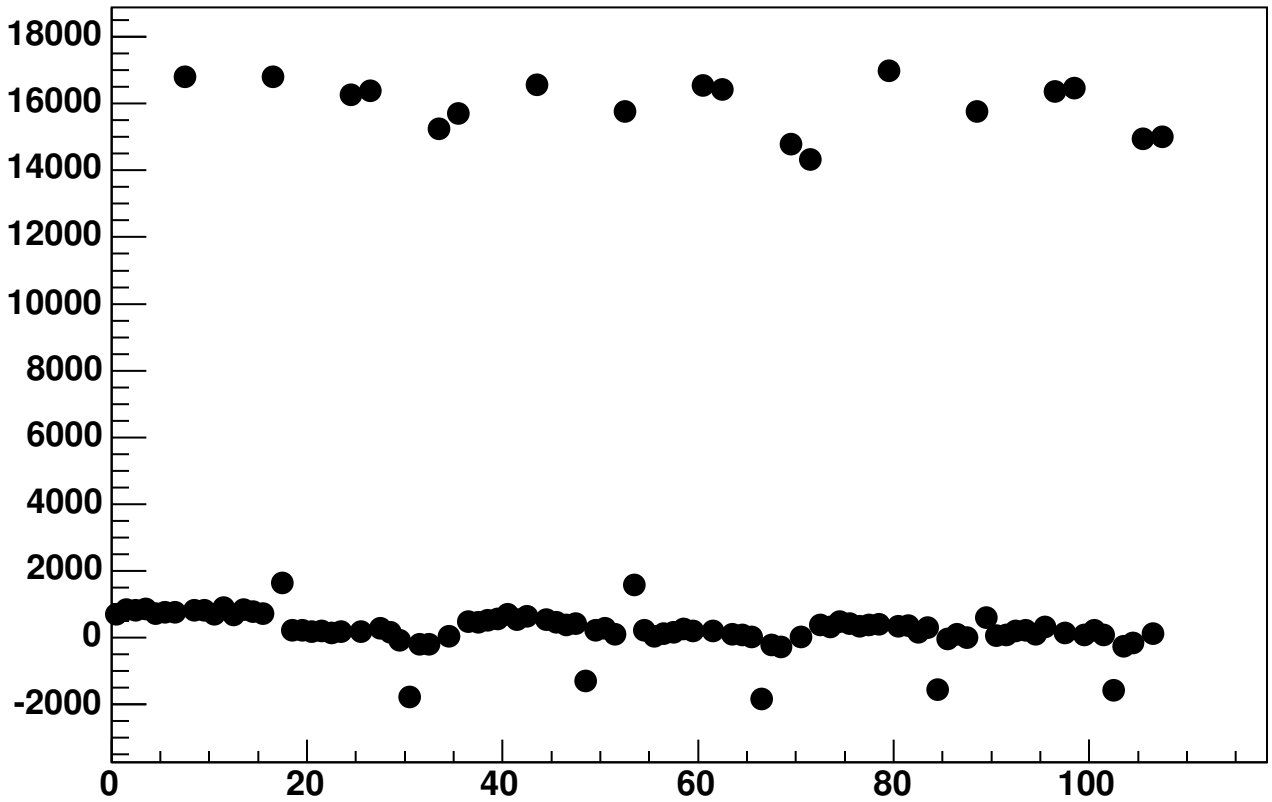
Enable 5, Hold=35, DAC=30000, ADC Mean vs 18*Chip+Chan



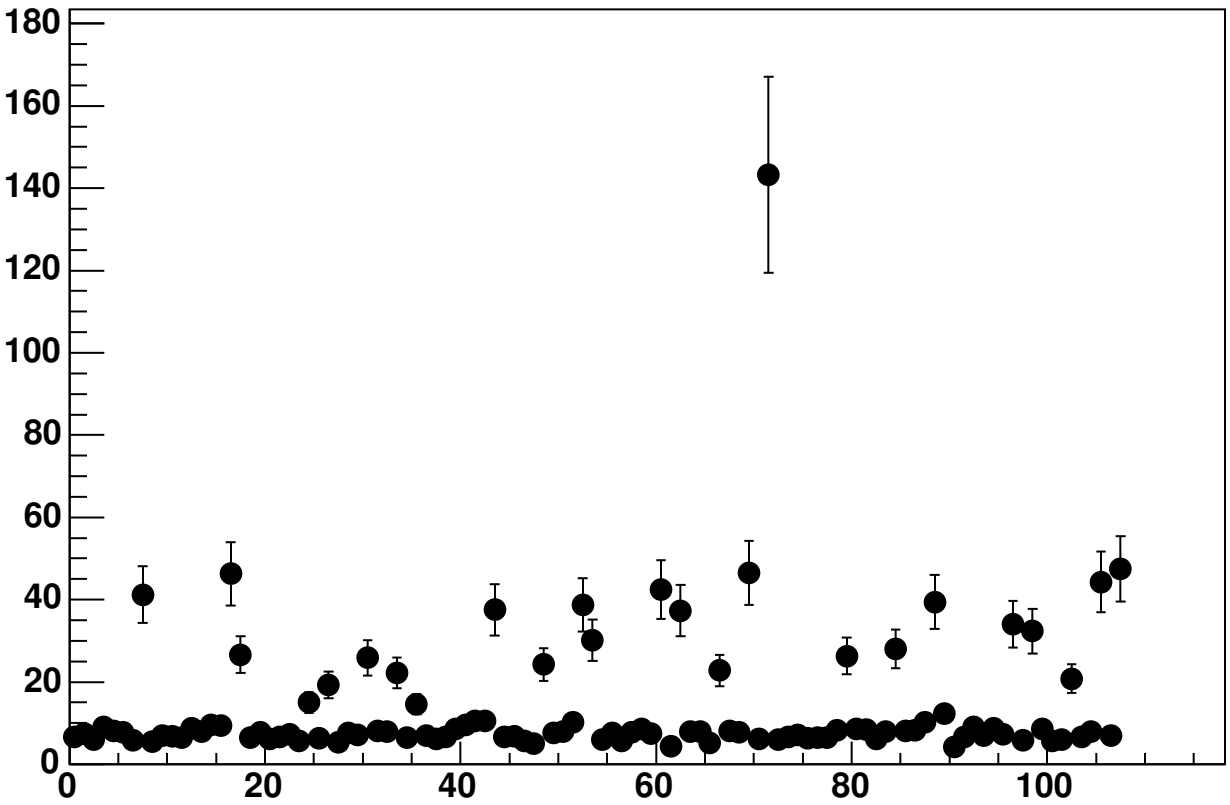
Enable 5, Hold=35, DAC=30000, ADC Noise vs 18*Chip+Chan



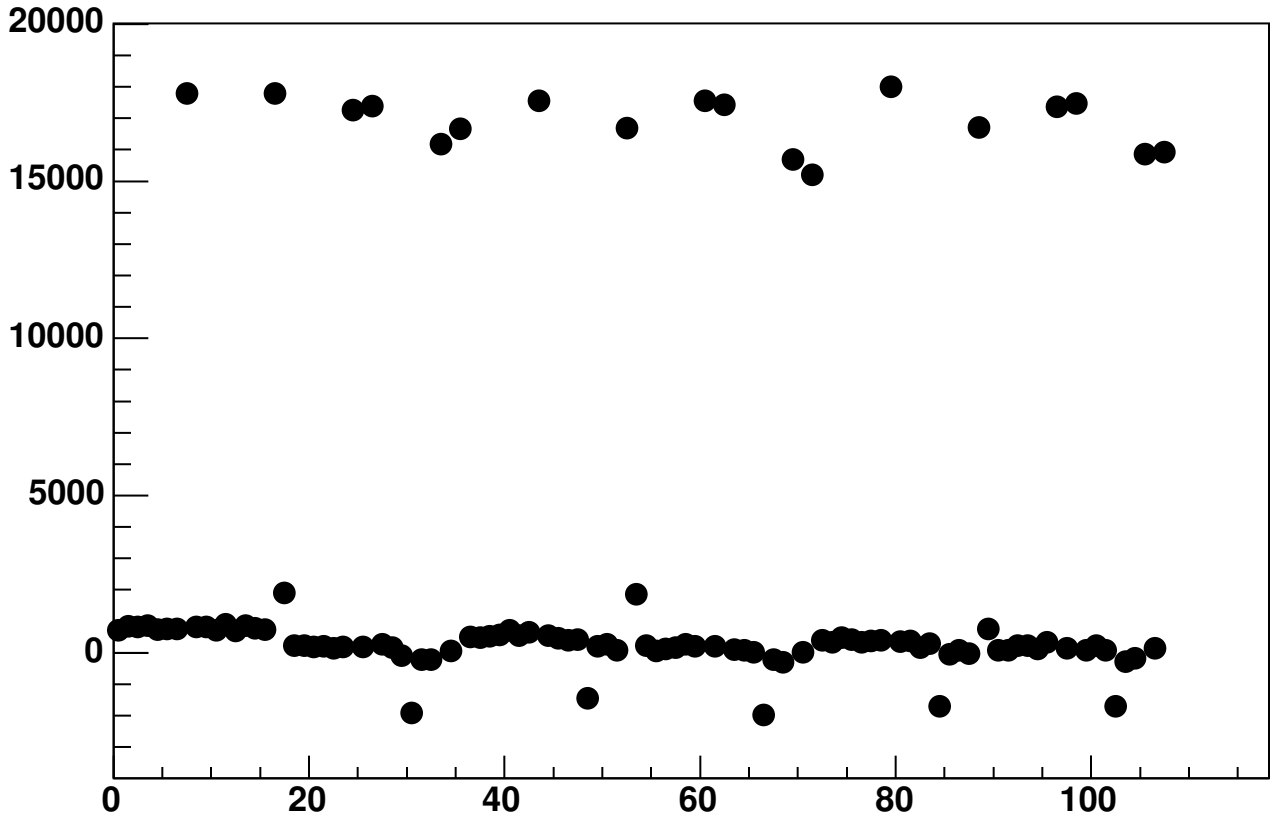
Enable 5, Hold=35, DAC=32000, ADC Mean vs 18*Chip+Chan



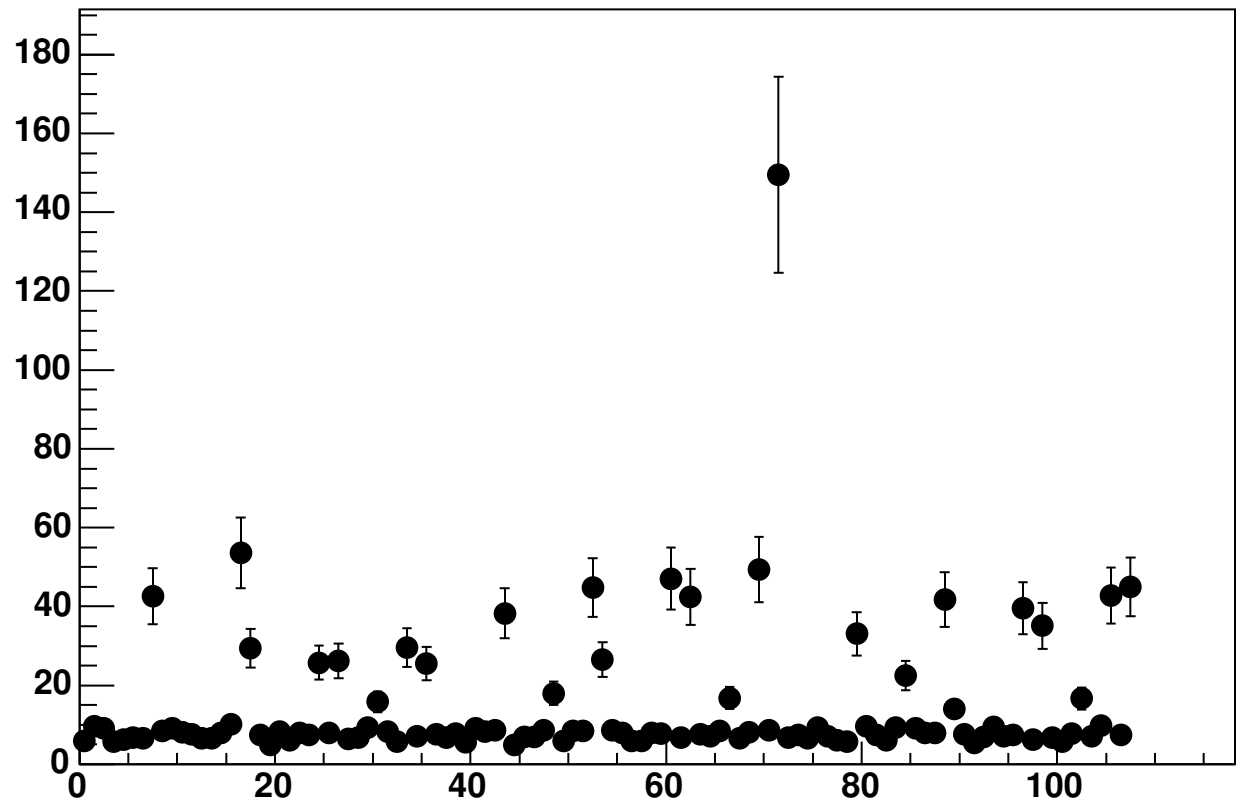
Enable 5, Hold=35, DAC=32000, ADC Noise vs 18*Chip+Chan



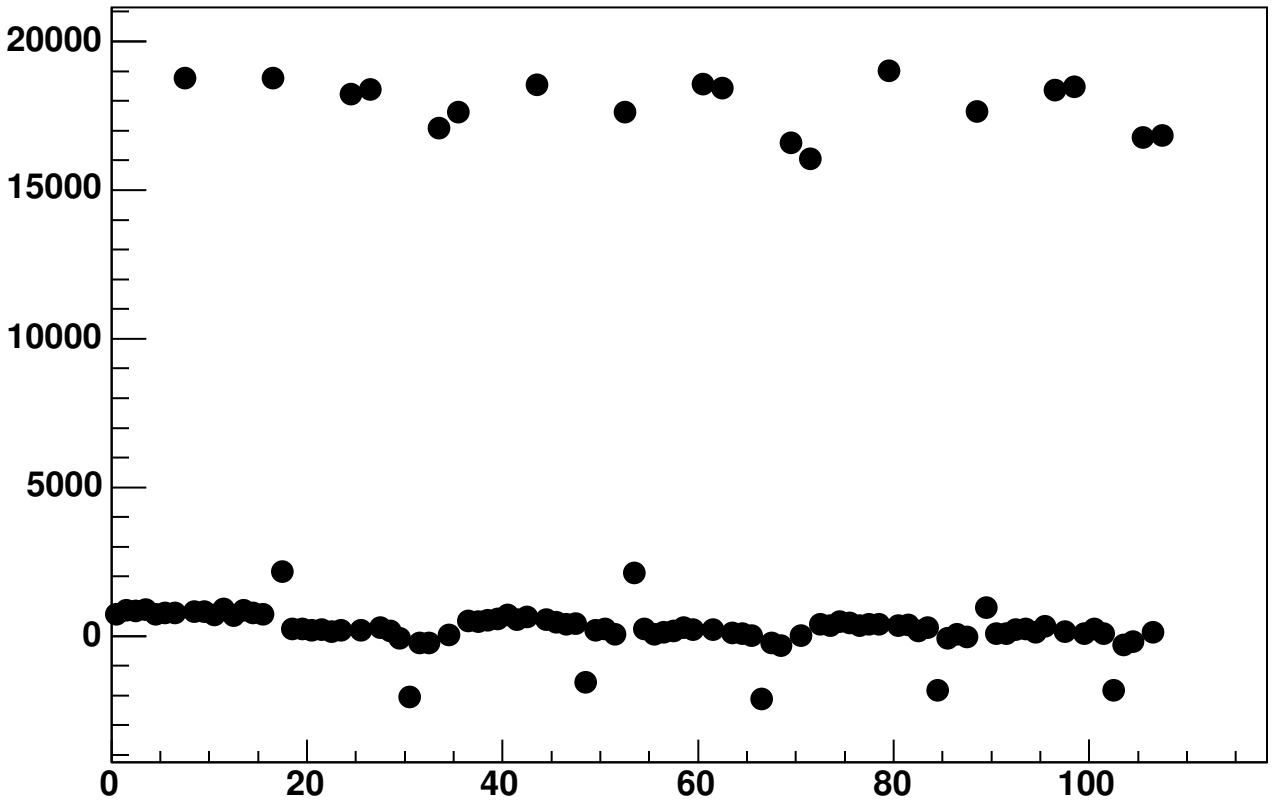
Enable 5, Hold=35, DAC=34000, ADC Mean vs 18*Chip+Chan



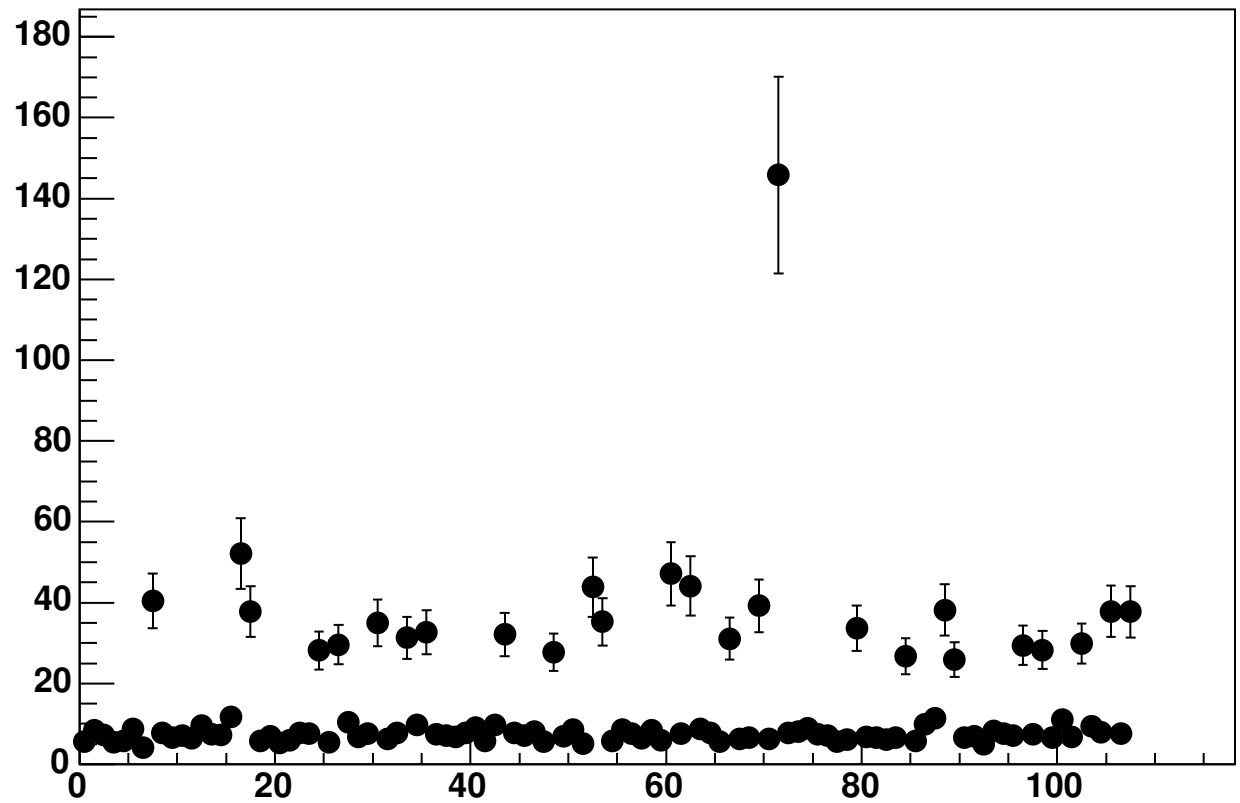
Enable 5, Hold=35, DAC=34000, ADC Noise vs 18*Chip+Chan



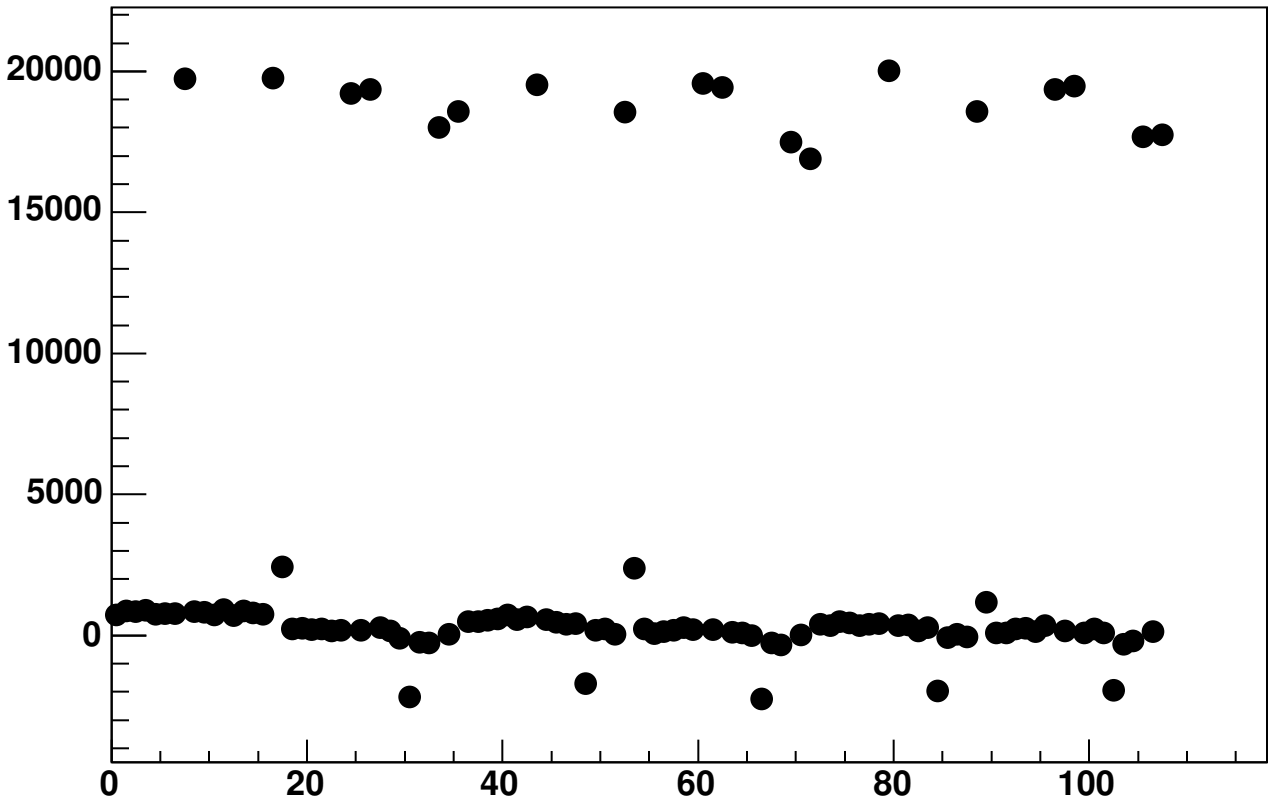
Enable 5, Hold=35, DAC=36000, ADC Mean vs 18*Chip+Chan



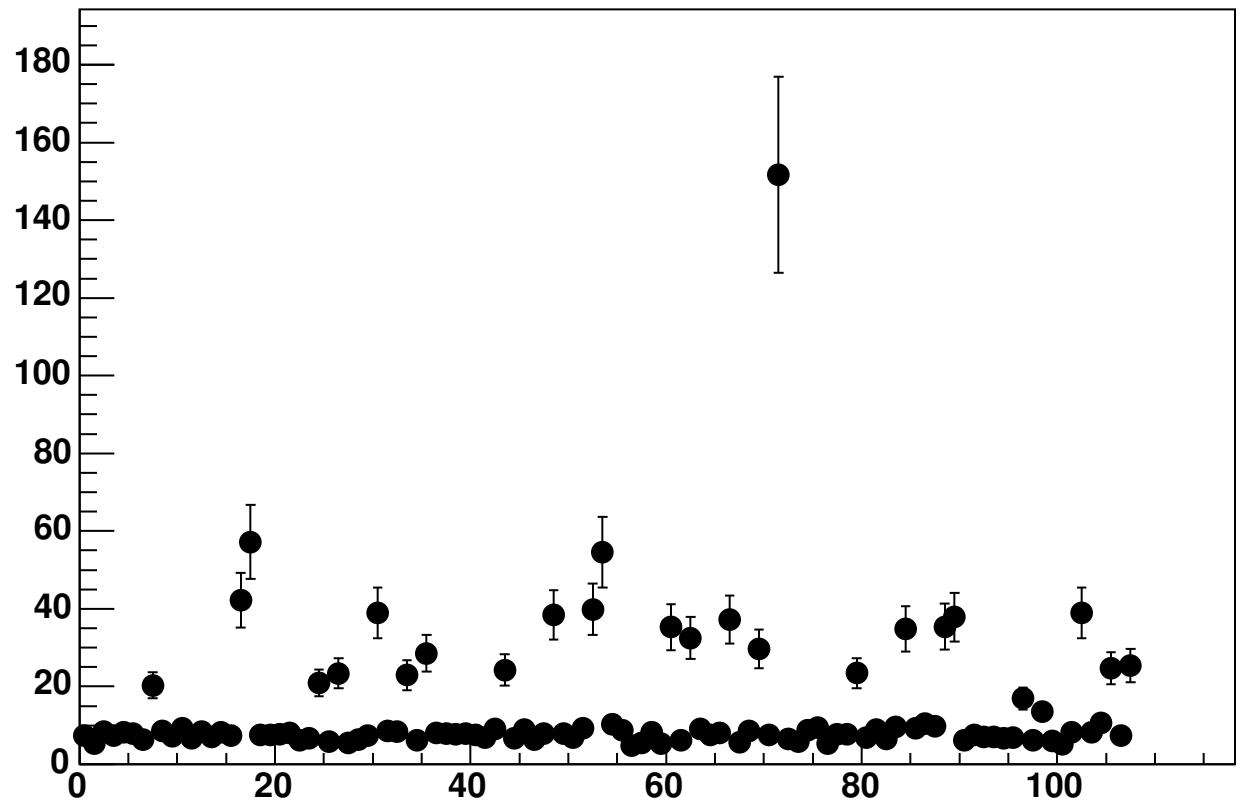
Enable 5, Hold=35, DAC=36000, ADC Noise vs 18*Chip+Chan



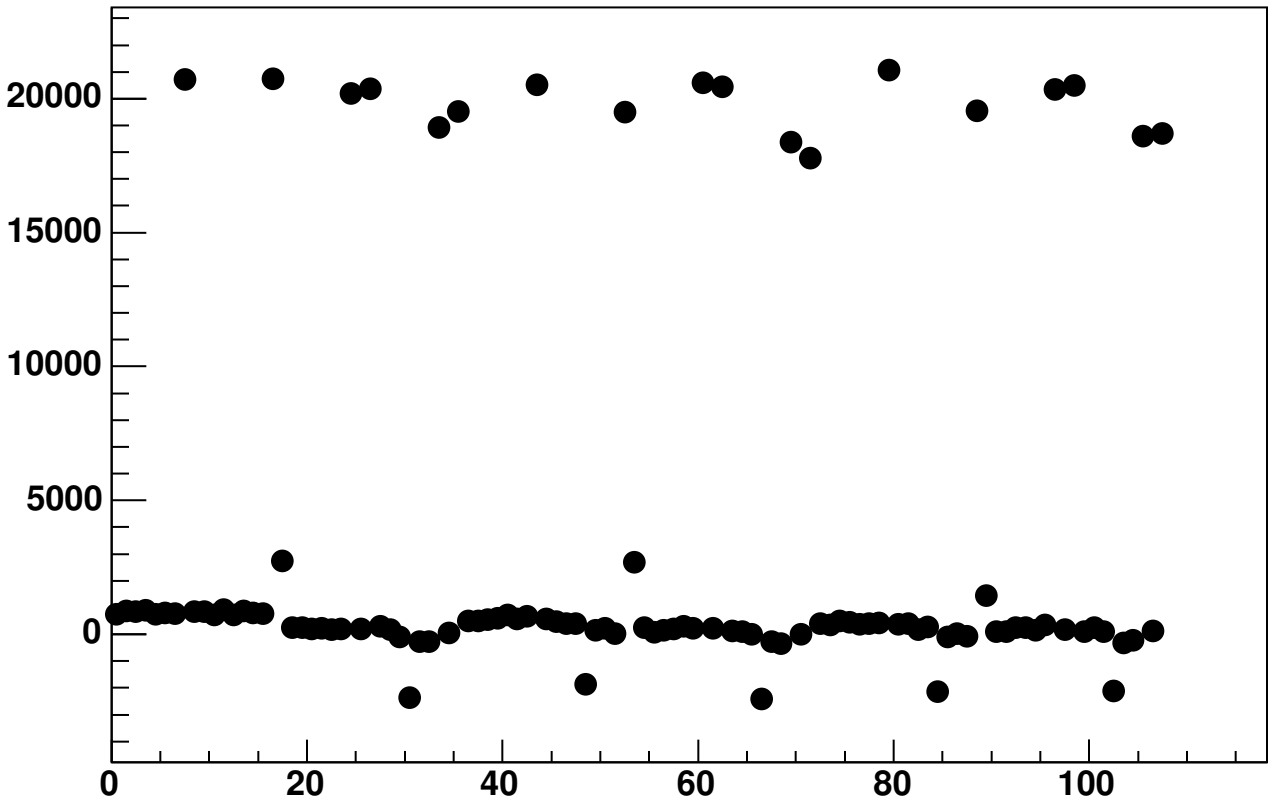
Enable 5, Hold=35, DAC=38000, ADC Mean vs 18*Chip+Chan



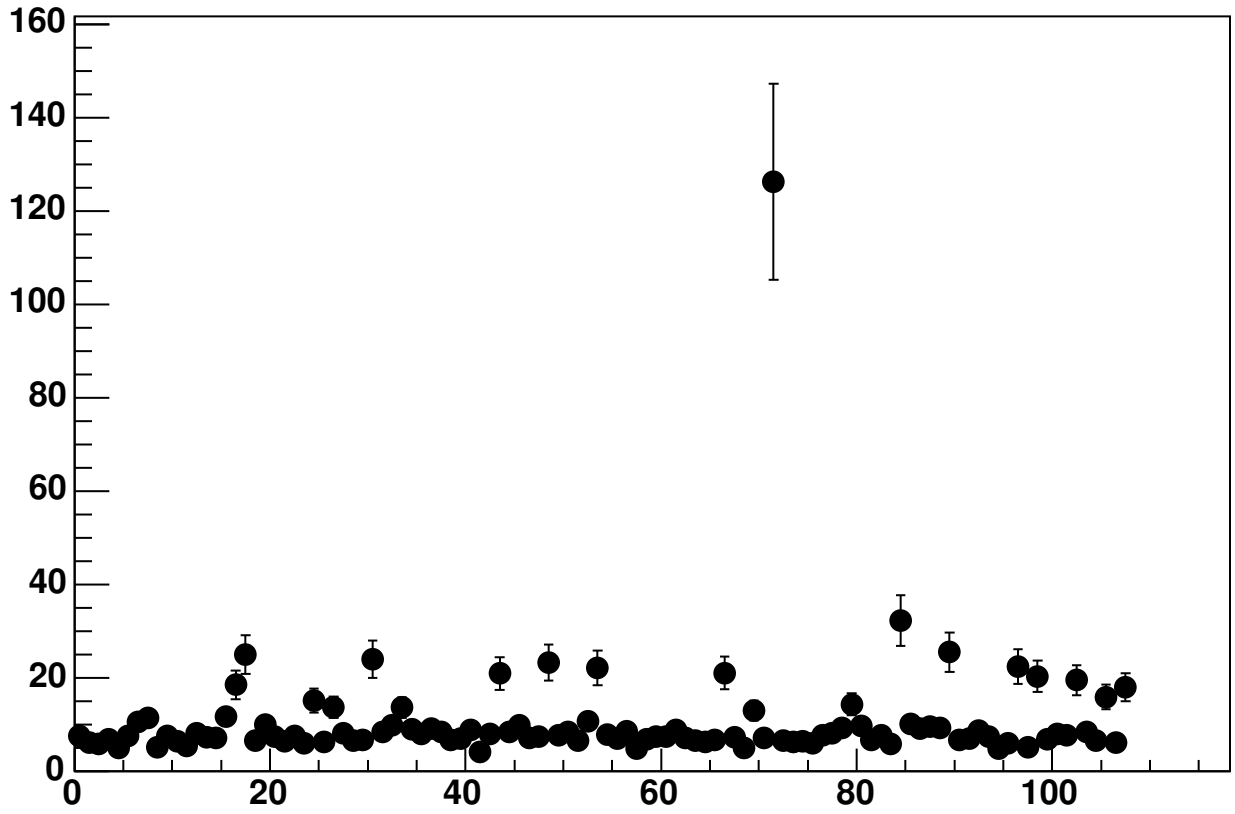
Enable 5, Hold=35, DAC=38000, ADC Noise vs 18*Chip+Chan



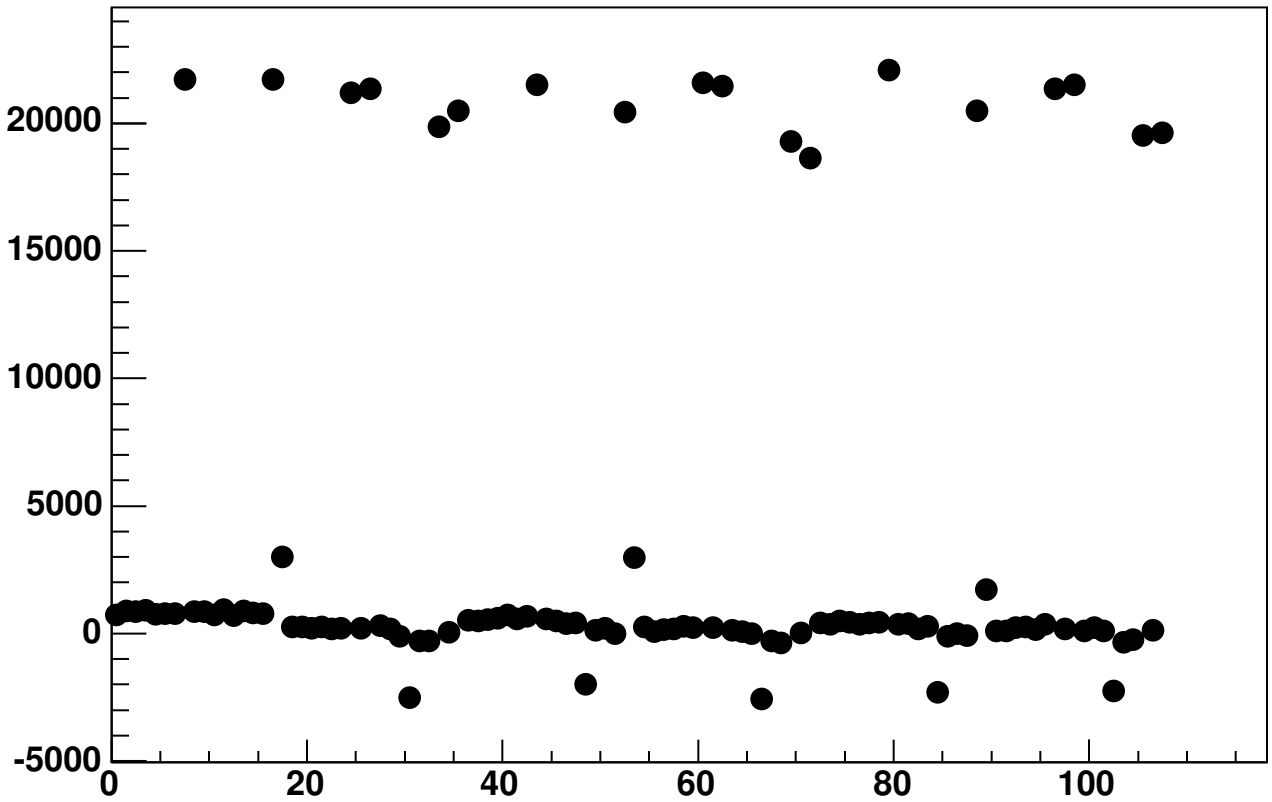
Enable 5, Hold=35, DAC=40000, ADC Mean vs 18*Chip+Chan



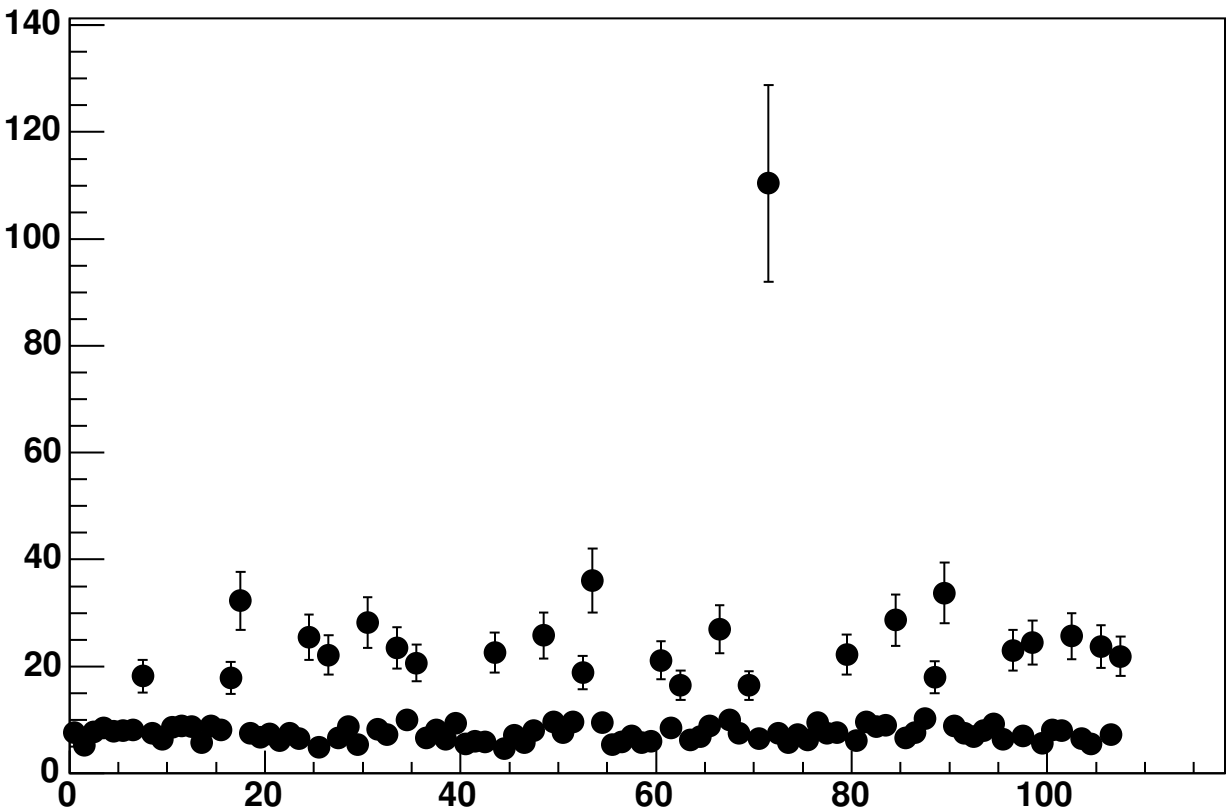
Enable 5, Hold=35, DAC=40000, ADC Noise vs 18*Chip+Chan



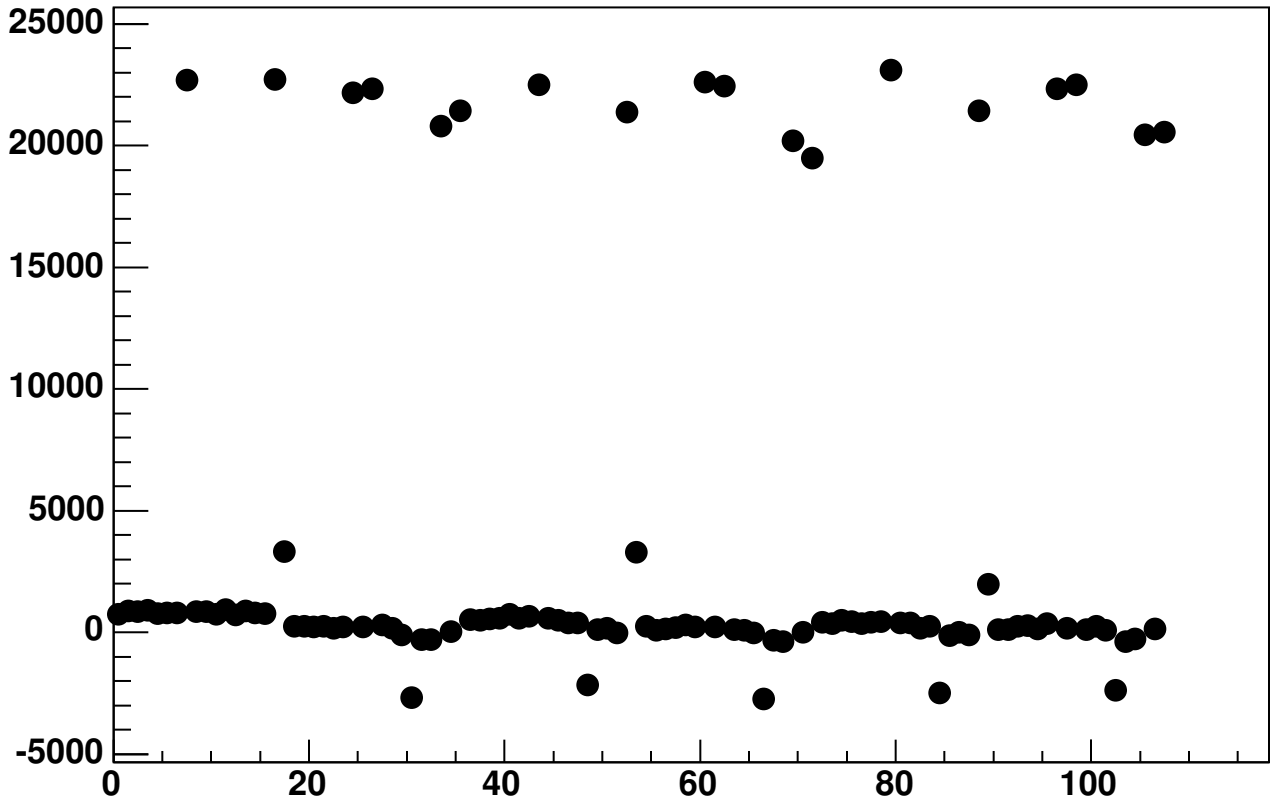
Enable 5, Hold=35, DAC=42000, ADC Mean vs 18*Chip+Chan



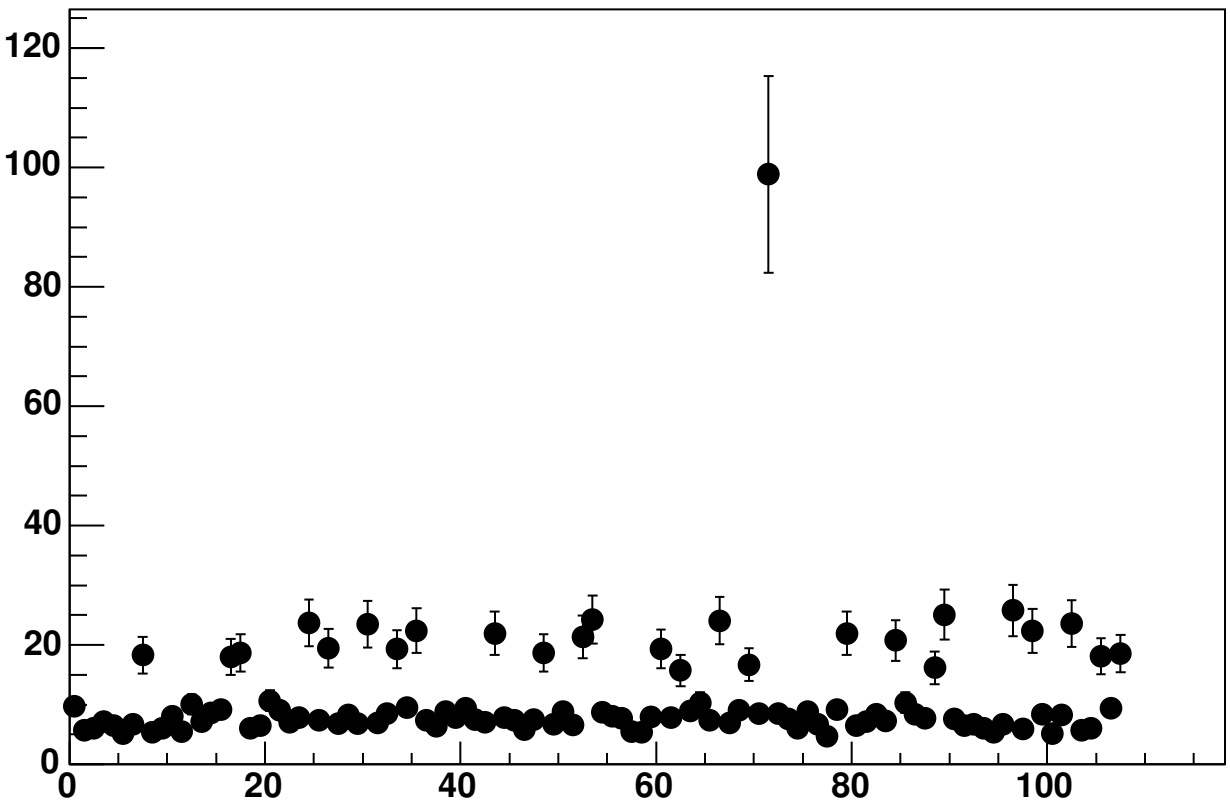
Enable 5, Hold=35, DAC=42000, ADC Noise vs 18*Chip+Chan



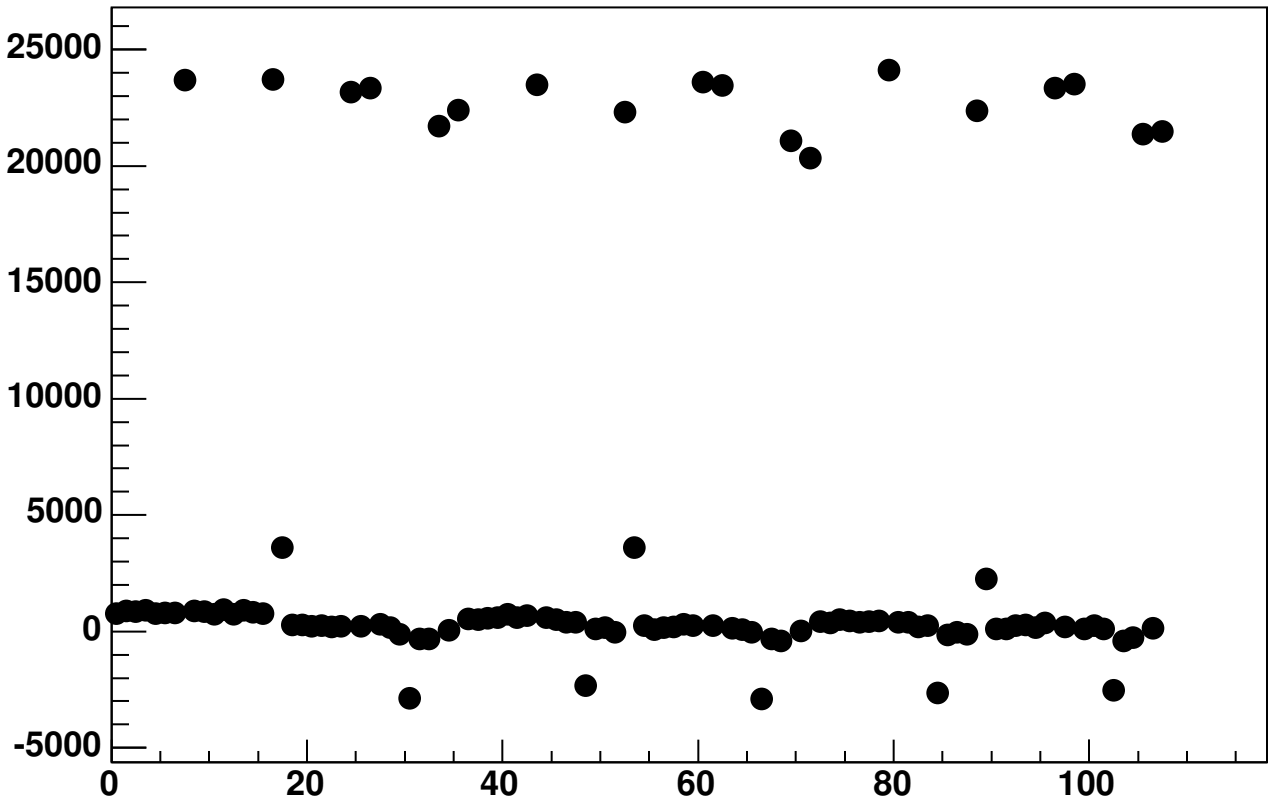
Enable 5, Hold=35, DAC=44000, ADC Mean vs 18*Chip+Chan



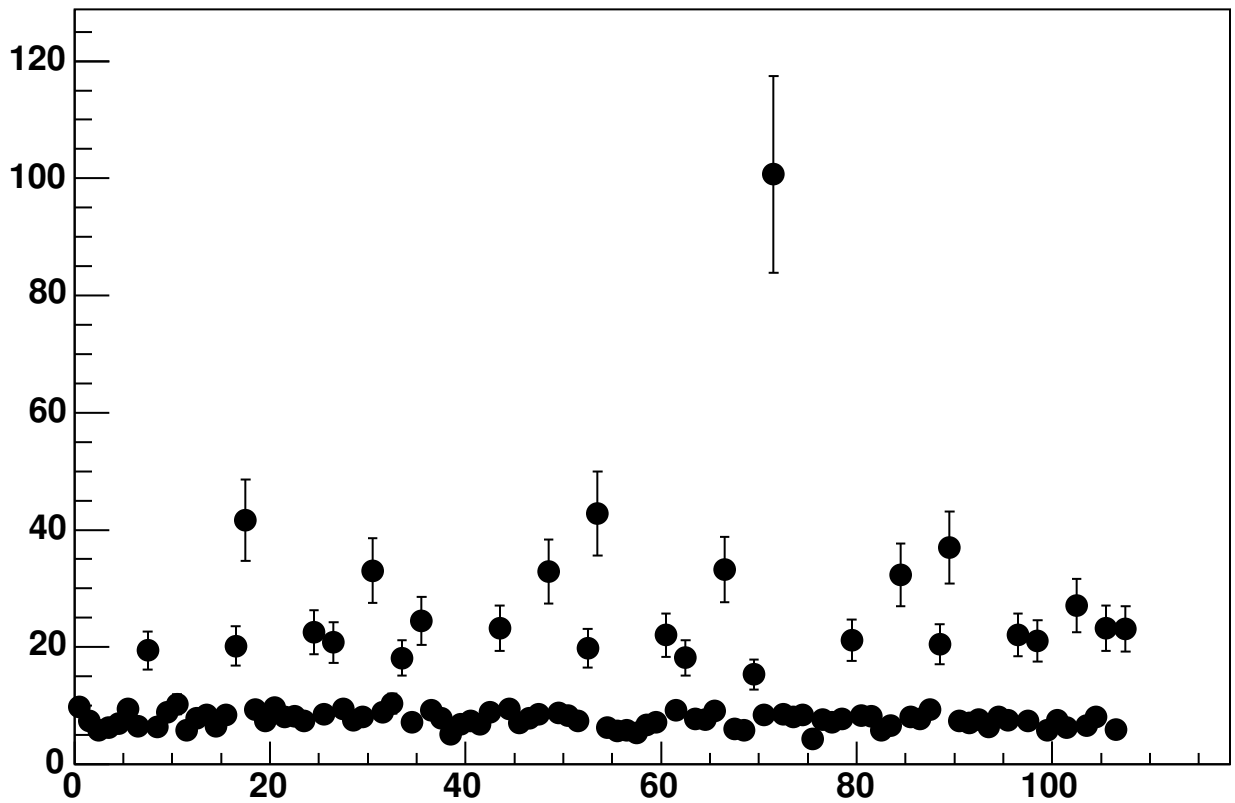
Enable 5, Hold=35, DAC=44000, ADC Noise vs 18*Chip+Chan



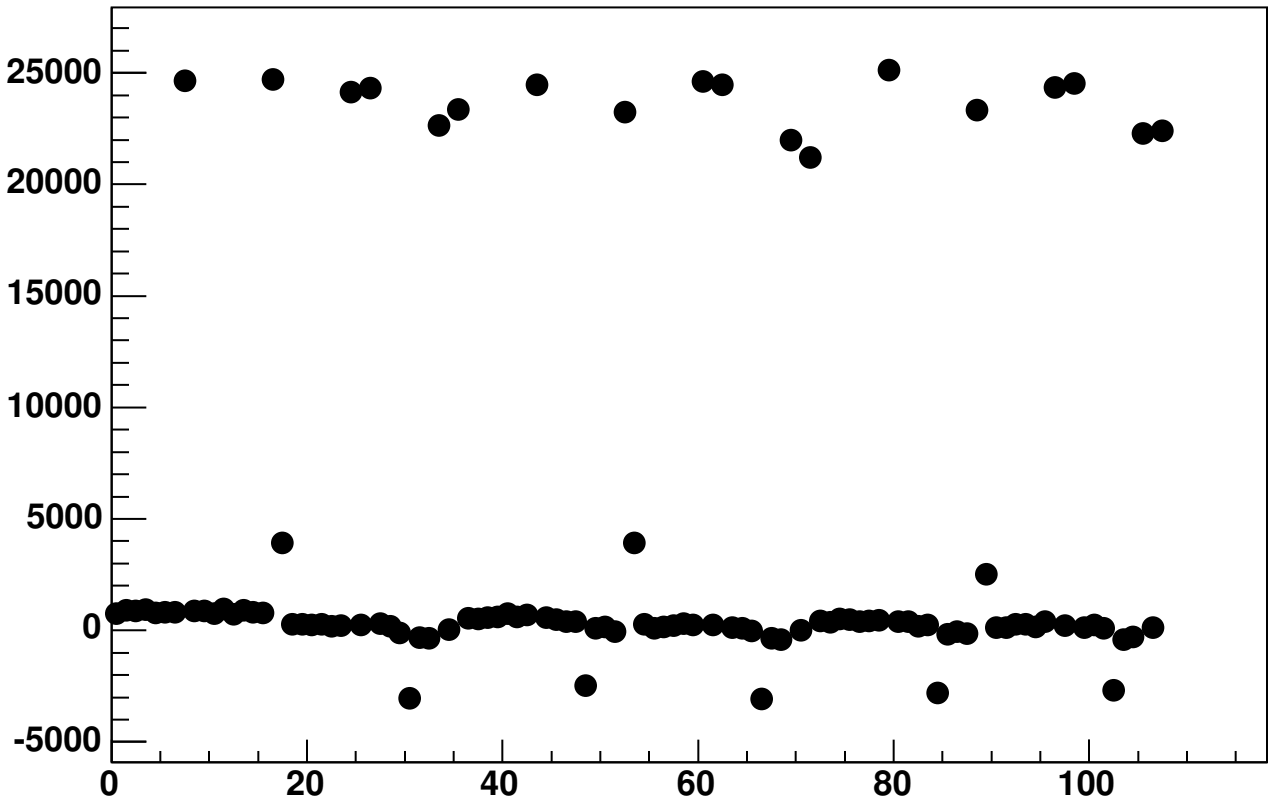
Enable 5, Hold=35, DAC=46000, ADC Mean vs 18*Chip+Chan



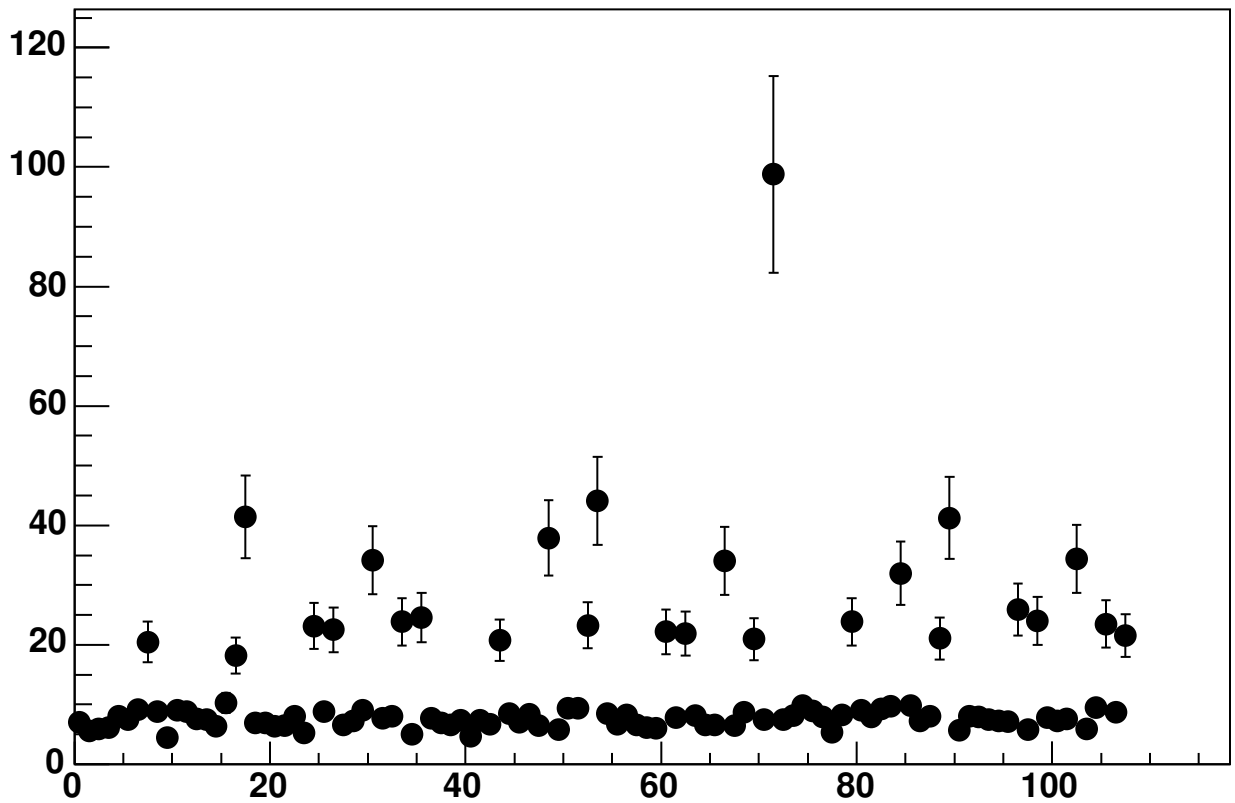
Enable 5, Hold=35, DAC=46000, ADC Noise vs 18*Chip+Chan



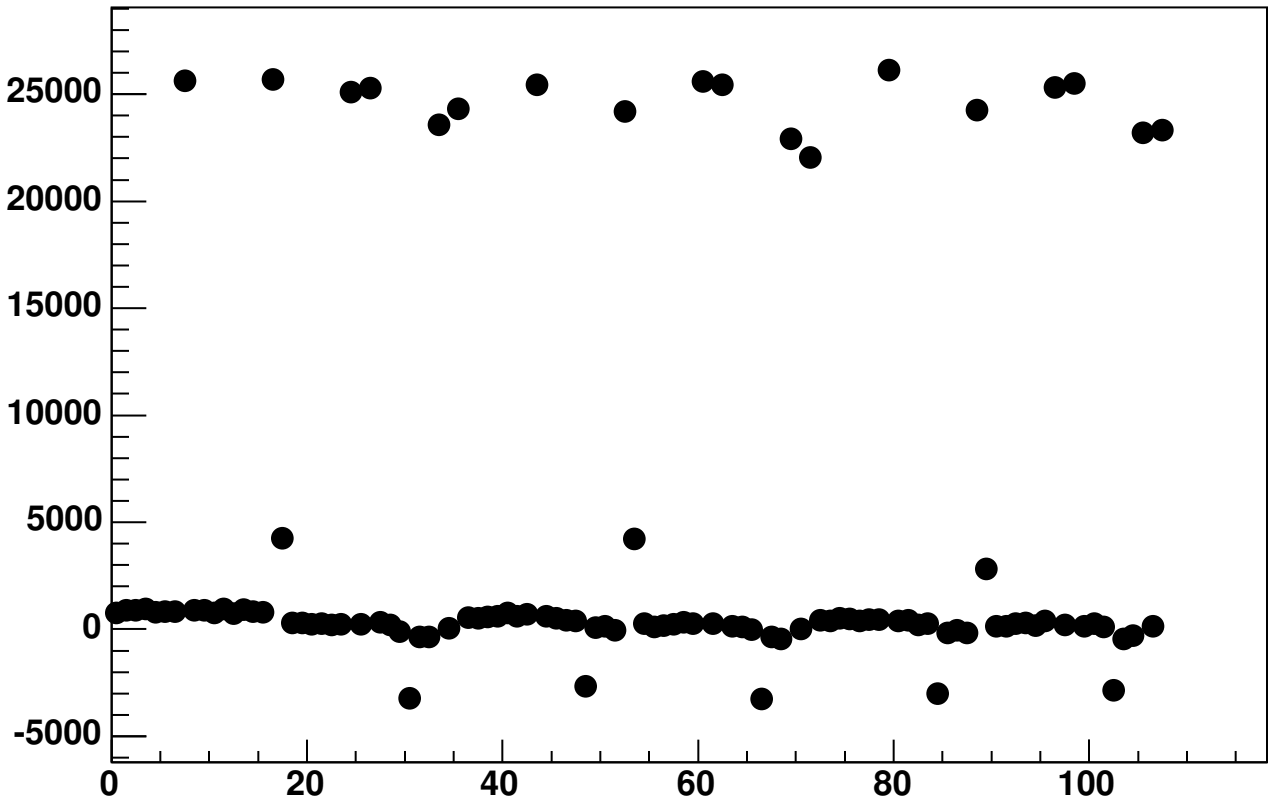
Enable 5, Hold=35, DAC=48000, ADC Mean vs 18*Chip+Chan



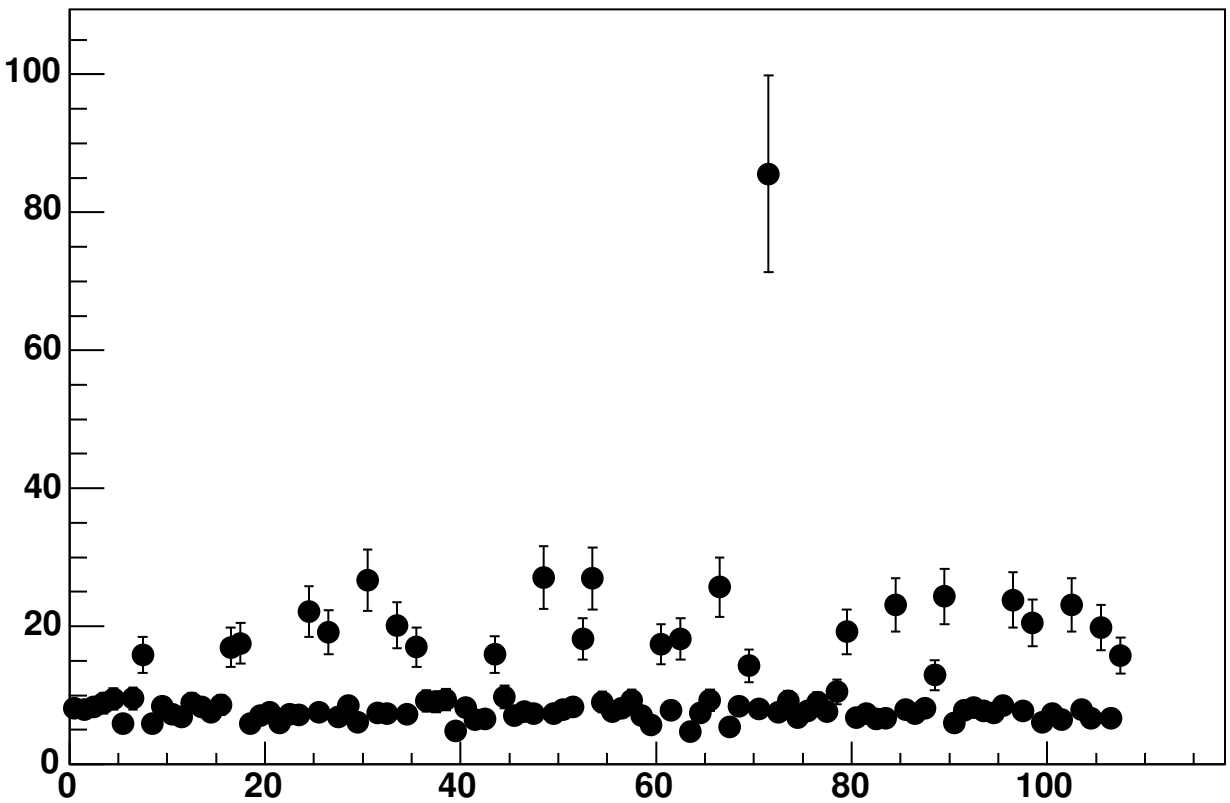
Enable 5, Hold=35, DAC=48000, ADC Noise vs 18*Chip+Chan



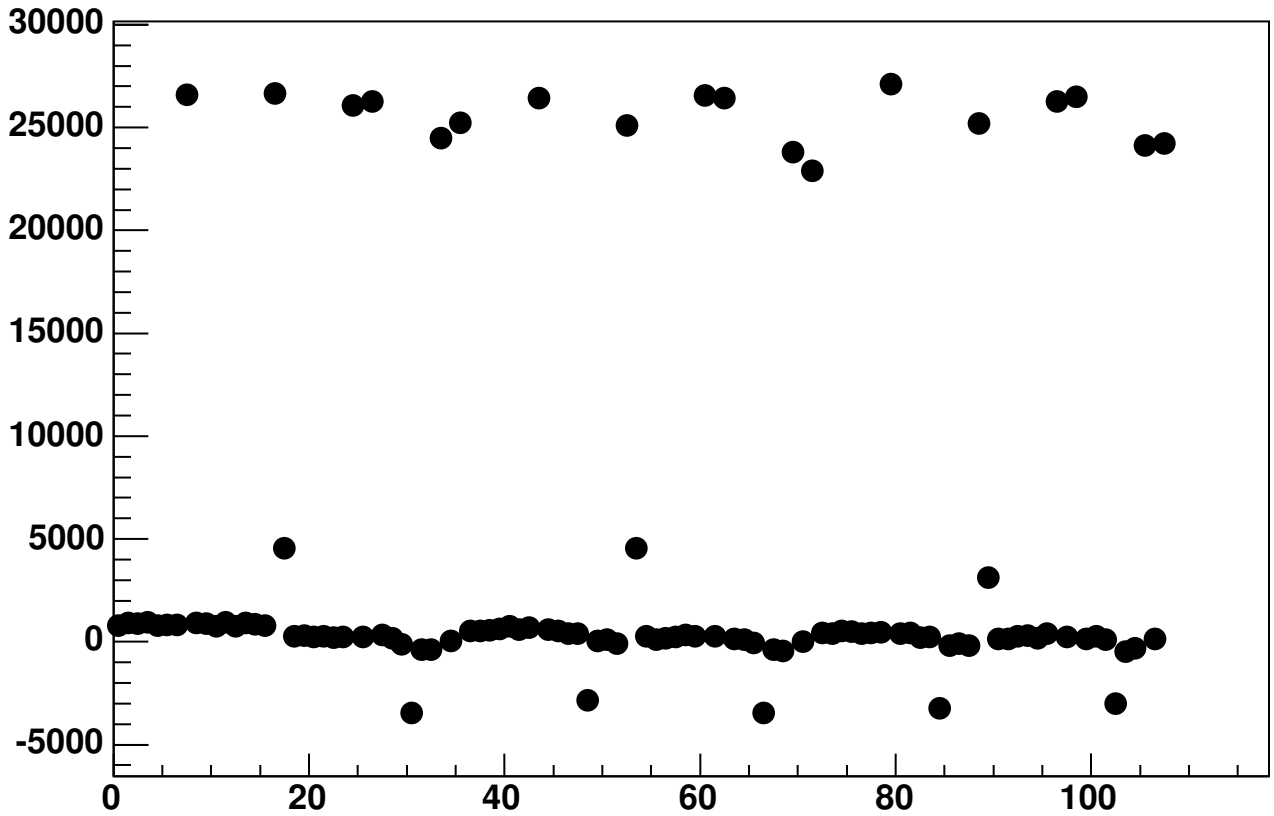
Enable 5, Hold=35, DAC=50000, ADC Mean vs 18*Chip+Chan



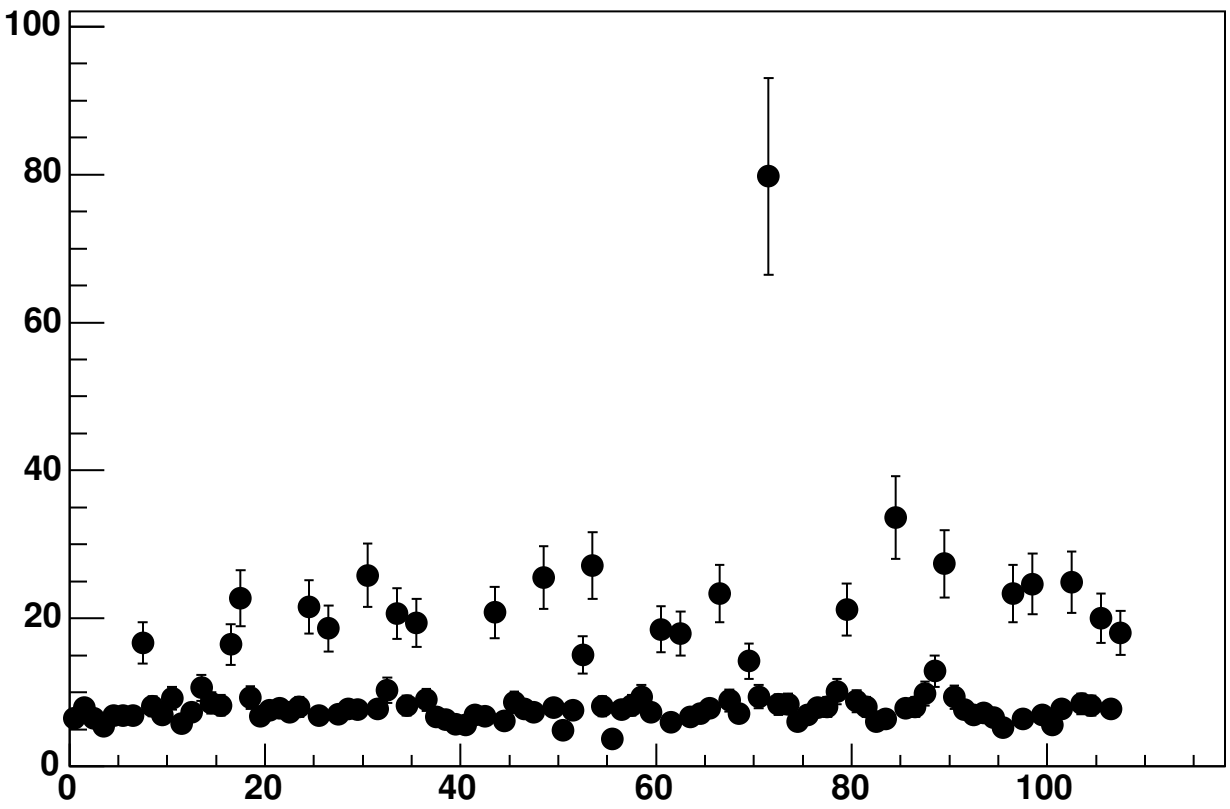
Enable 5, Hold=35, DAC=50000, ADC Noise vs 18*Chip+Chan



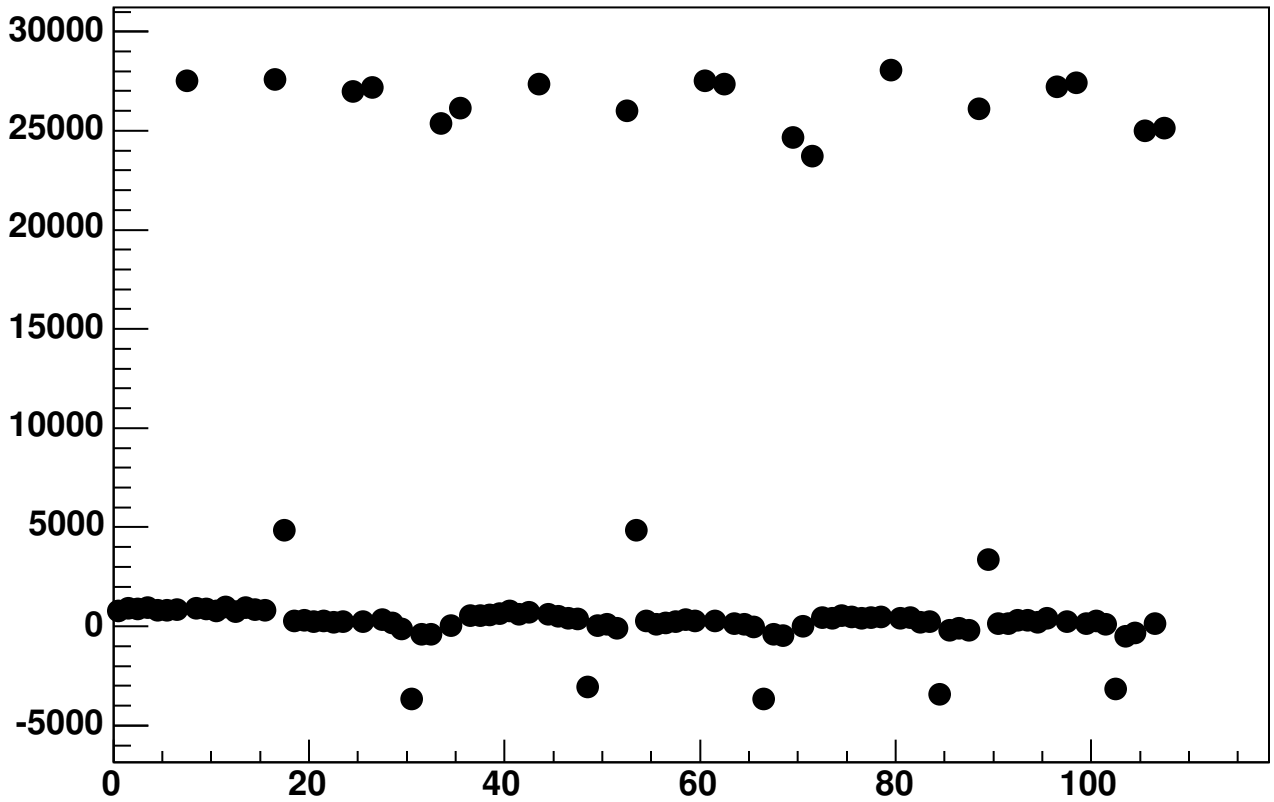
Enable 5, Hold=35, DAC=52000, ADC Mean vs 18*Chip+Chan



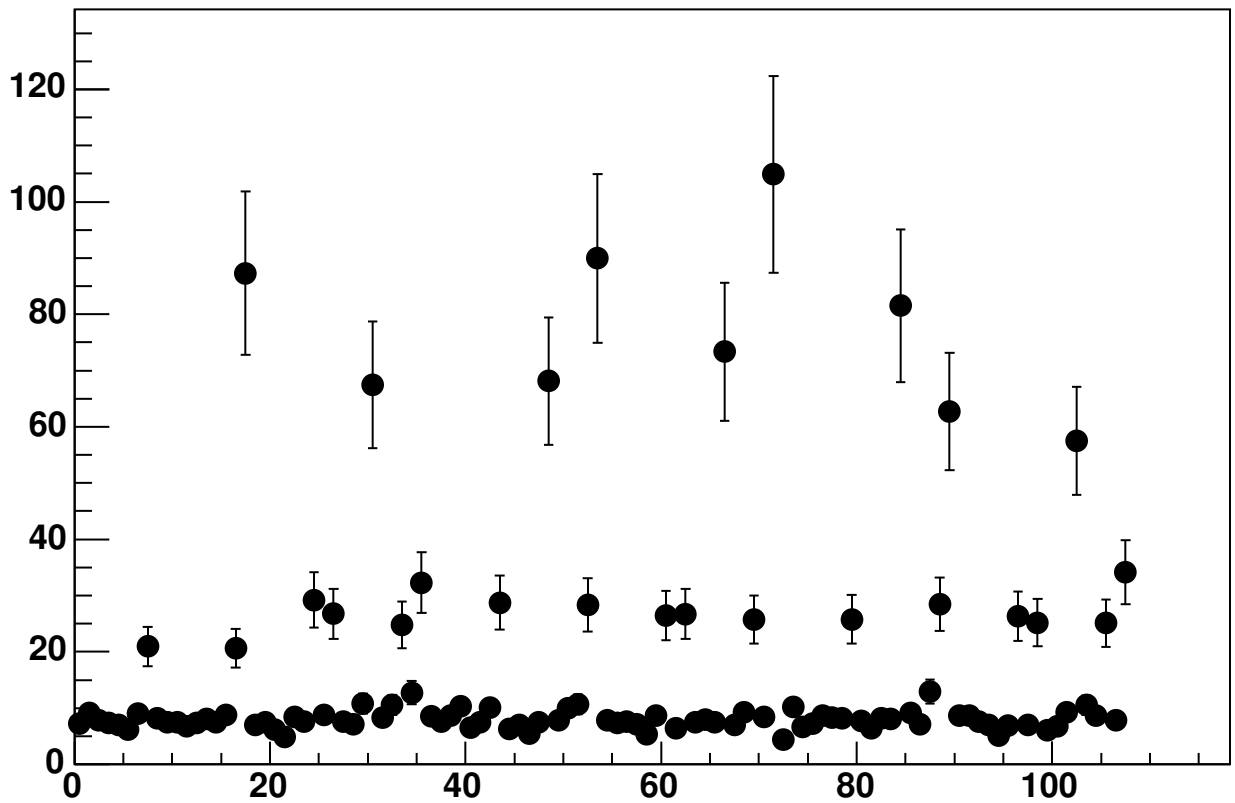
Enable 5, Hold=35, DAC=52000, ADC Noise vs 18*Chip+Chan



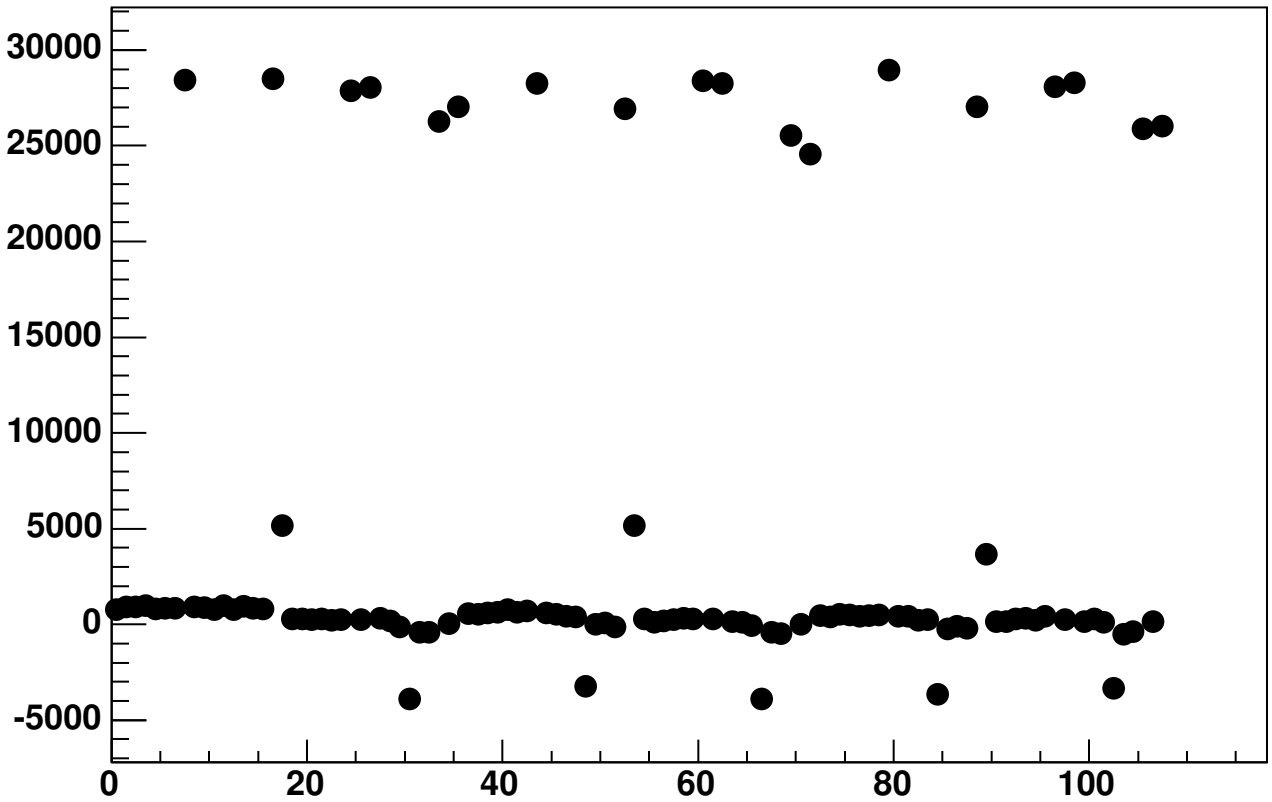
Enable 5, Hold=35, DAC=54000, ADC Mean vs 18*Chip+Chan



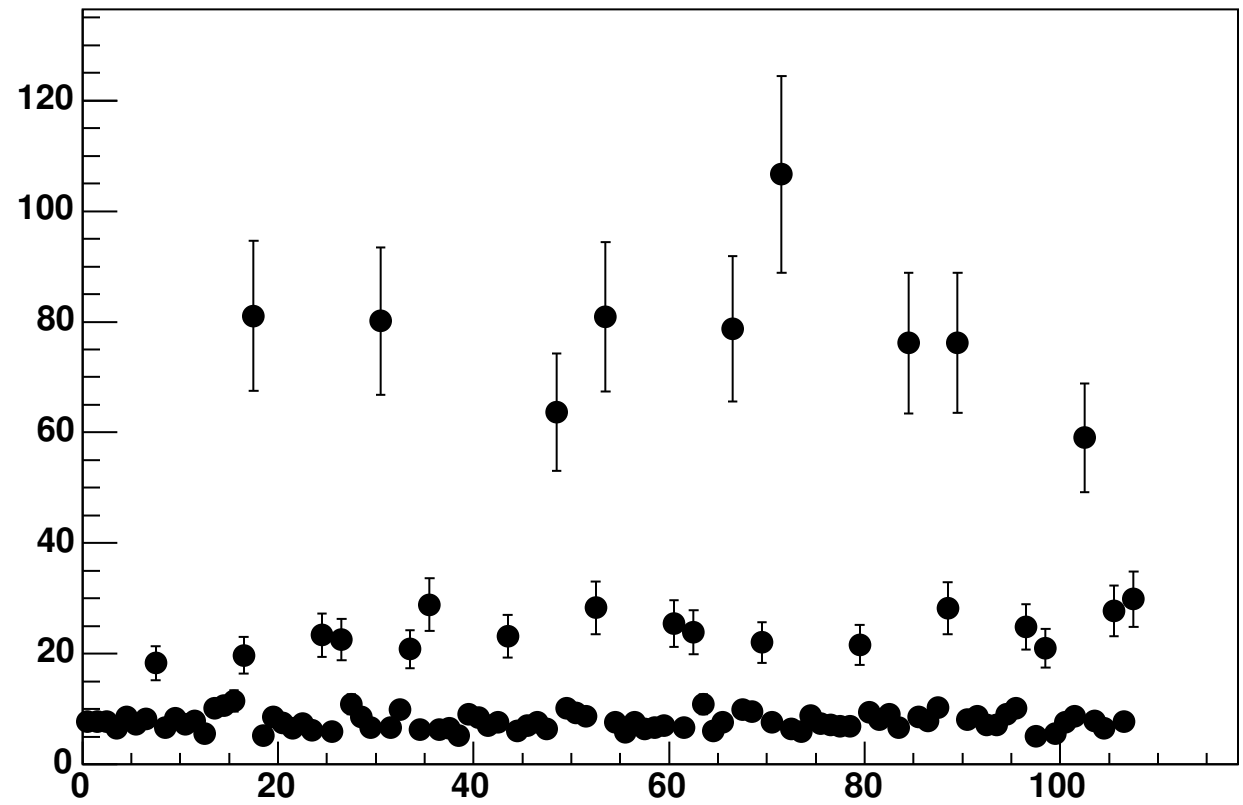
Enable 5, Hold=35, DAC=54000, ADC Noise vs 18*Chip+Chan



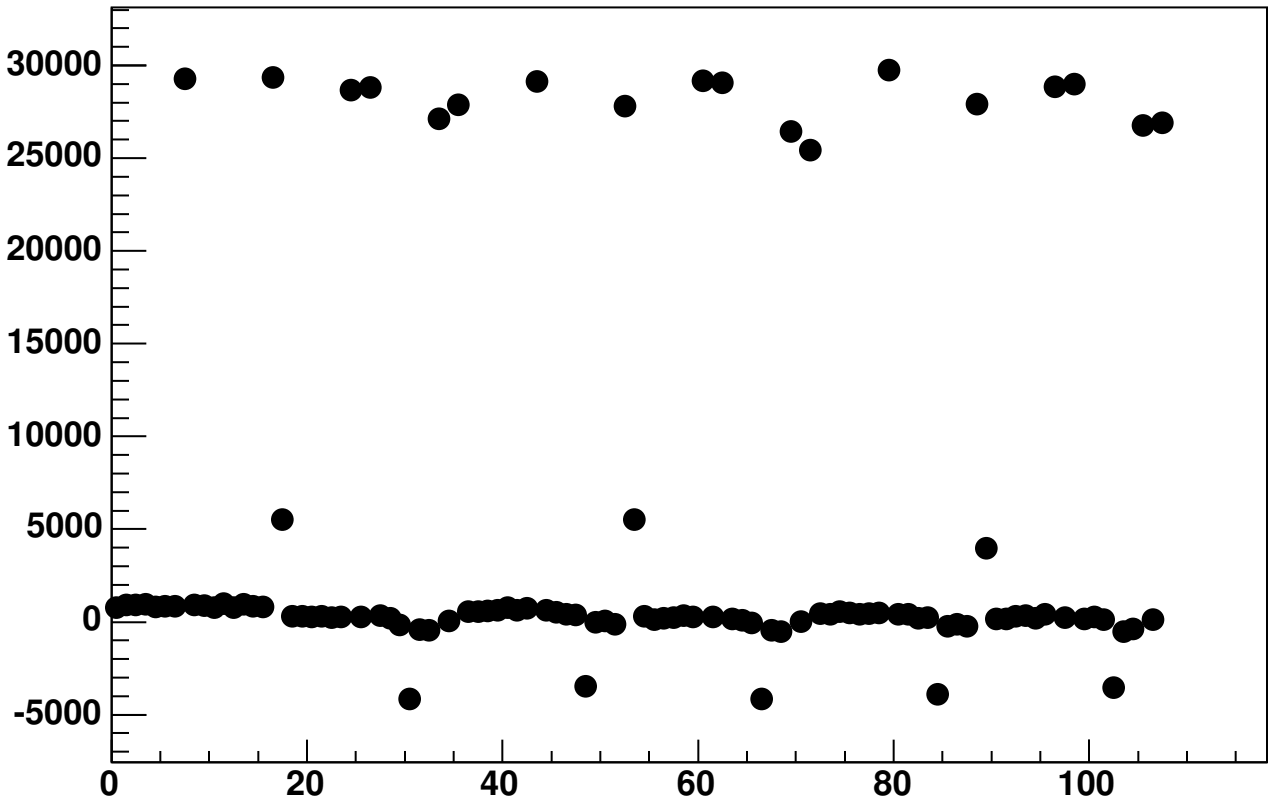
Enable 5, Hold=35, DAC=56000, ADC Mean vs 18*Chip+Chan



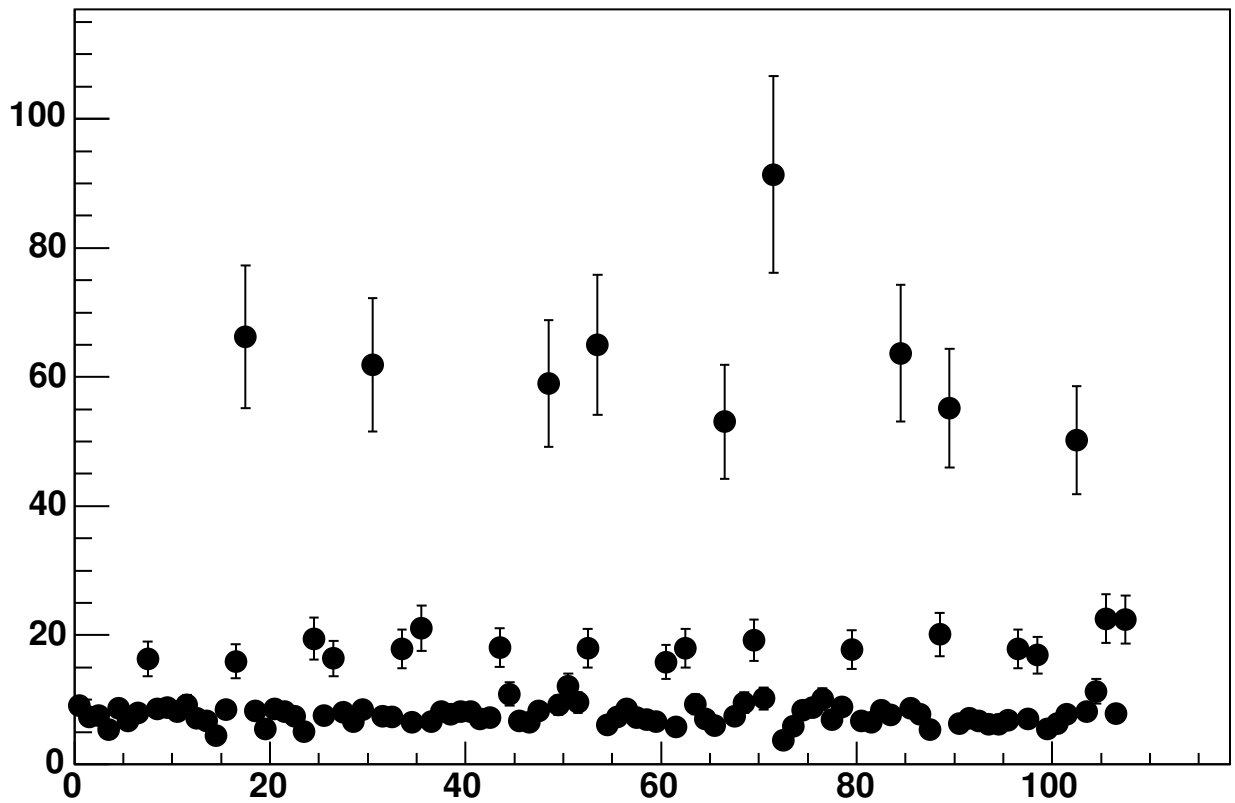
Enable 5, Hold=35, DAC=56000, ADC Noise vs 18*Chip+Chan



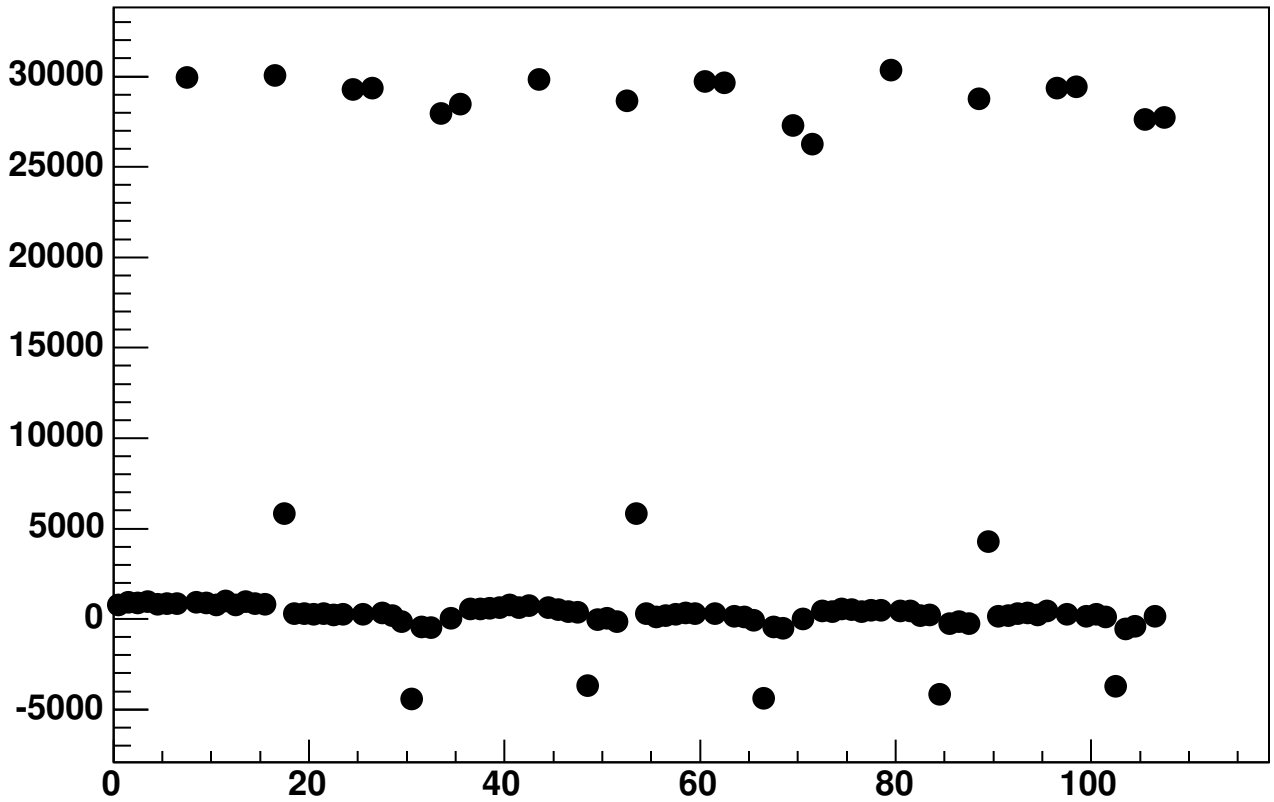
Enable 5, Hold=35, DAC=58000, ADC Mean vs 18*Chip+Chan



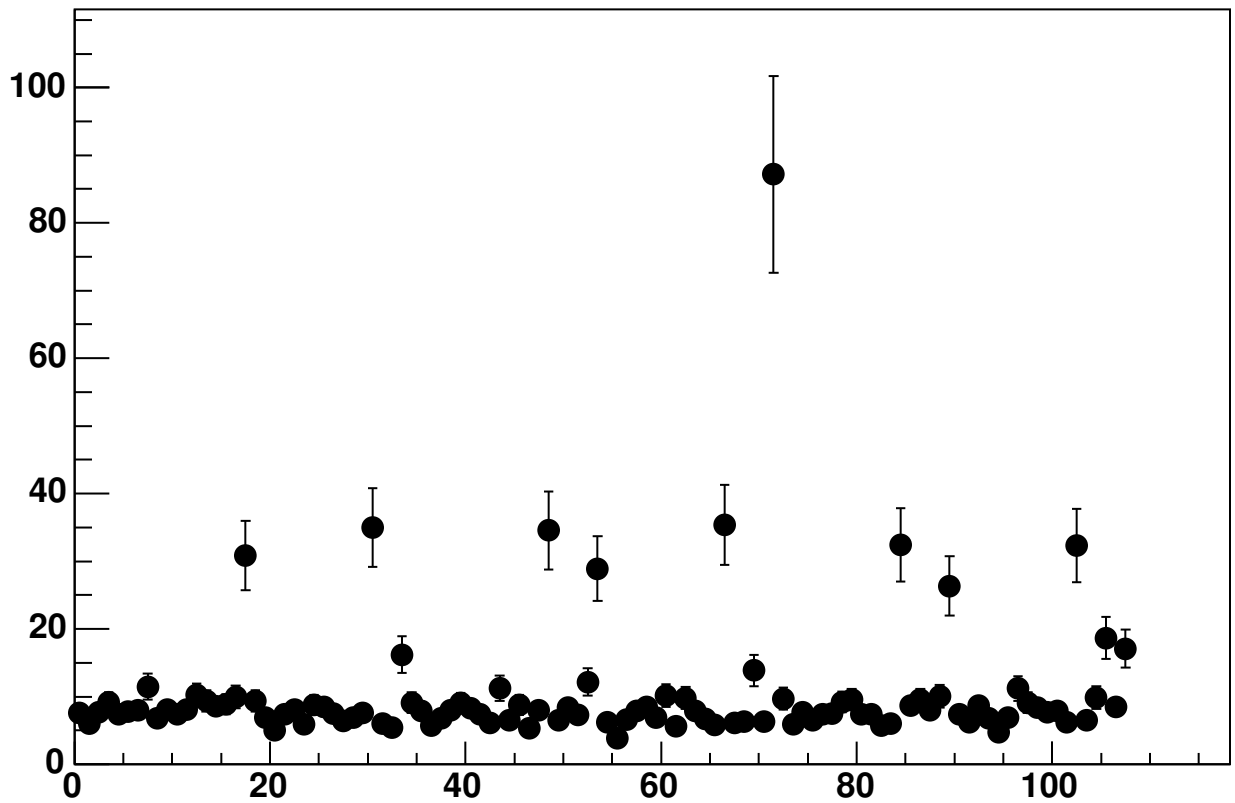
Enable 5, Hold=35, DAC=58000, ADC Noise vs 18*Chip+Chan



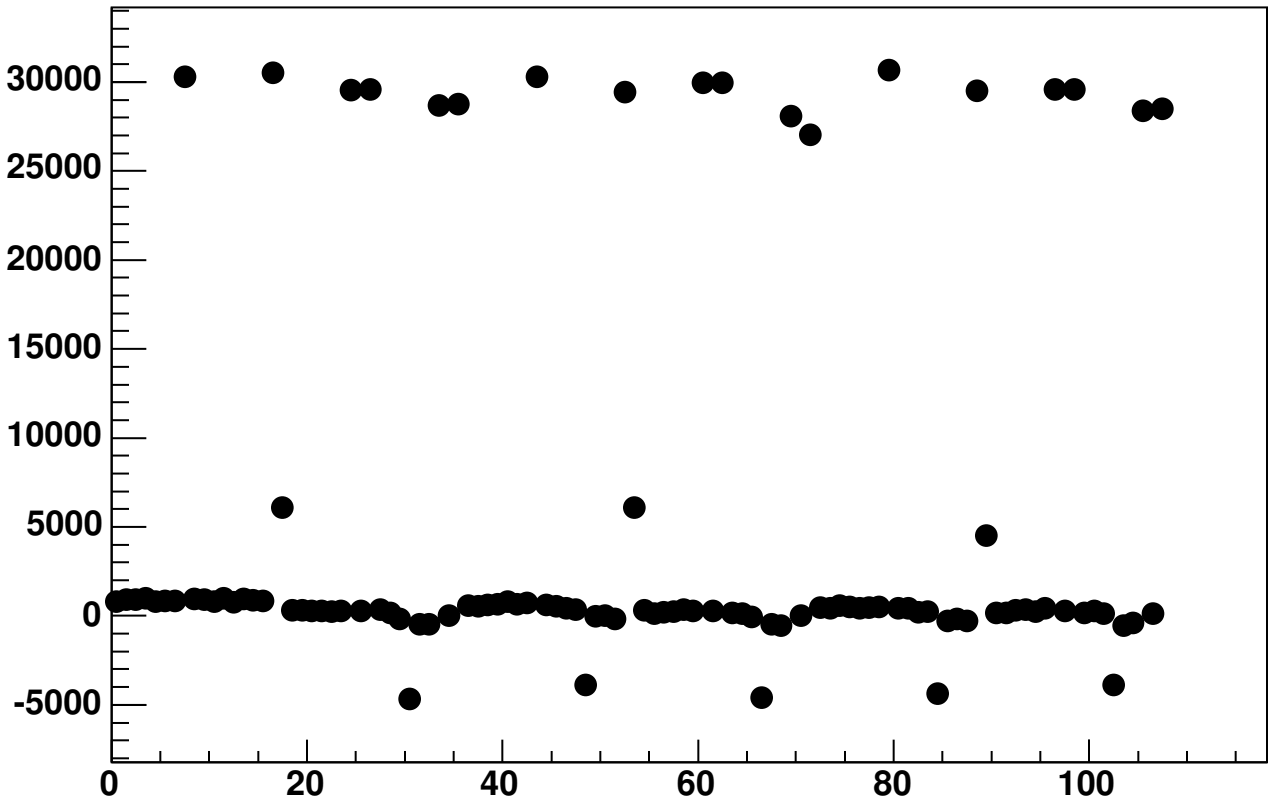
Enable 5, Hold=35, DAC=60000, ADC Mean vs 18*Chip+Chan



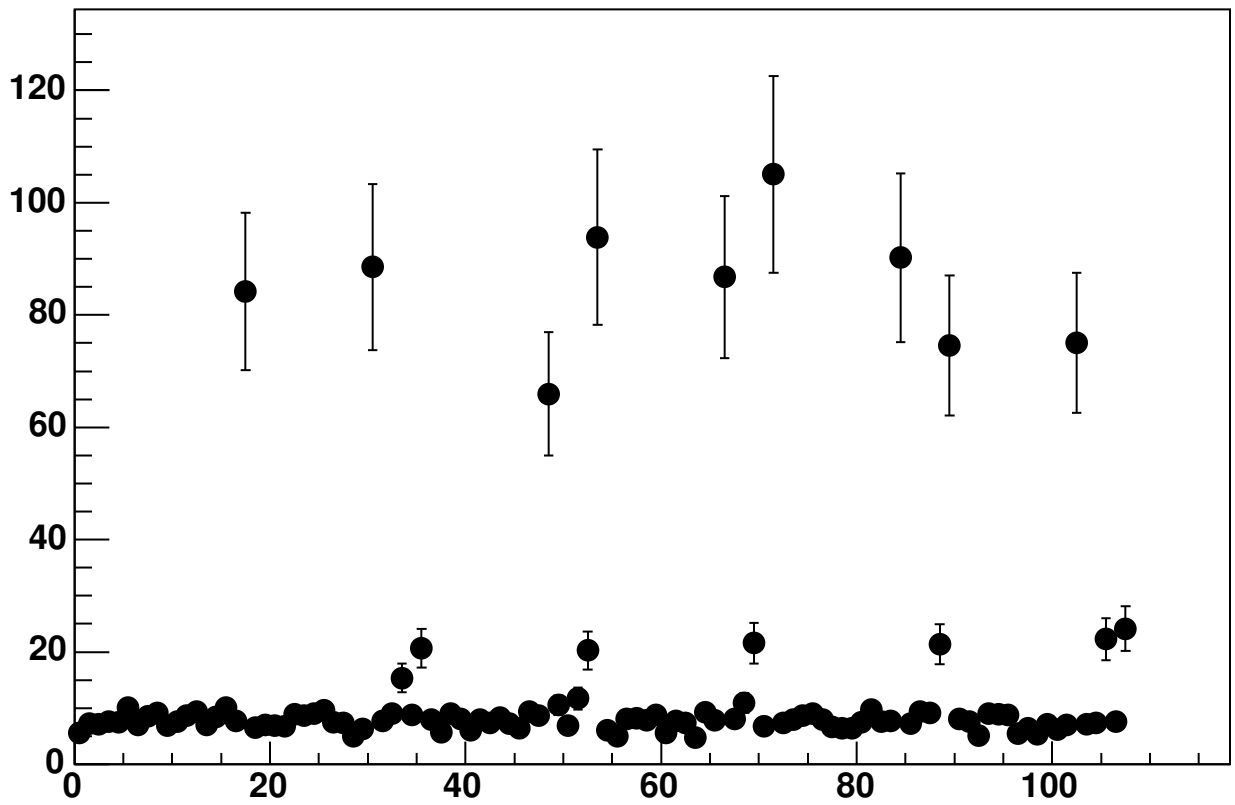
Enable 5, Hold=35, DAC=60000, ADC Noise vs 18*Chip+Chan

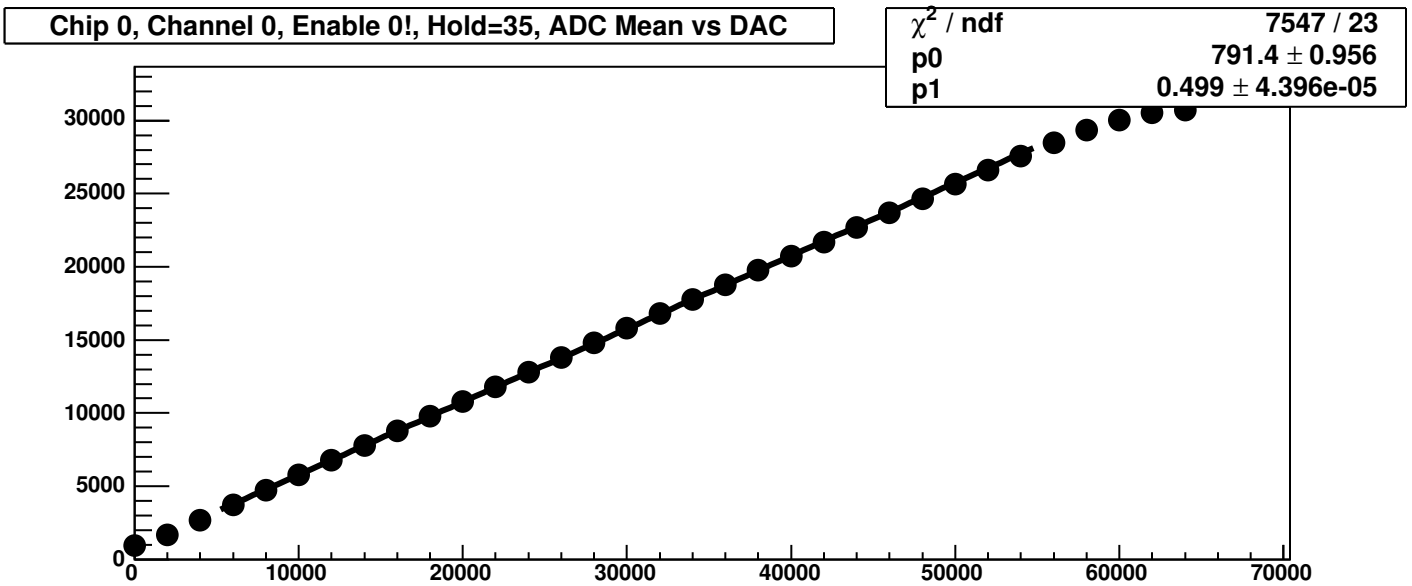


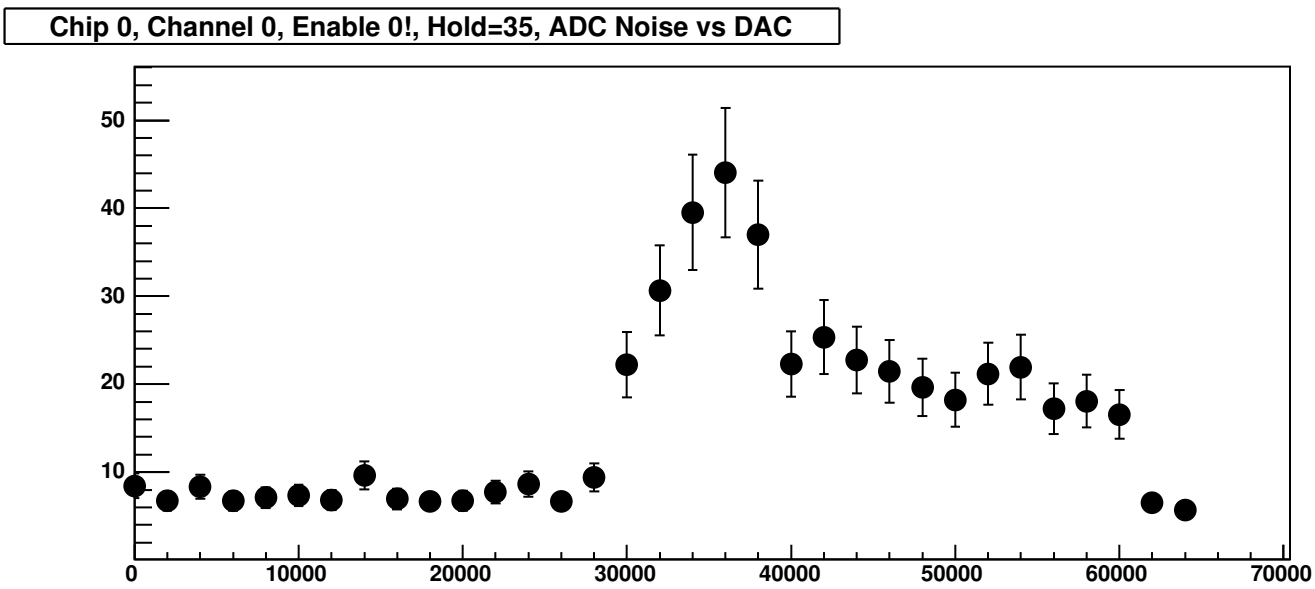
Enable 5, Hold=35, DAC=62000, ADC Mean vs 18*Chip+Chan

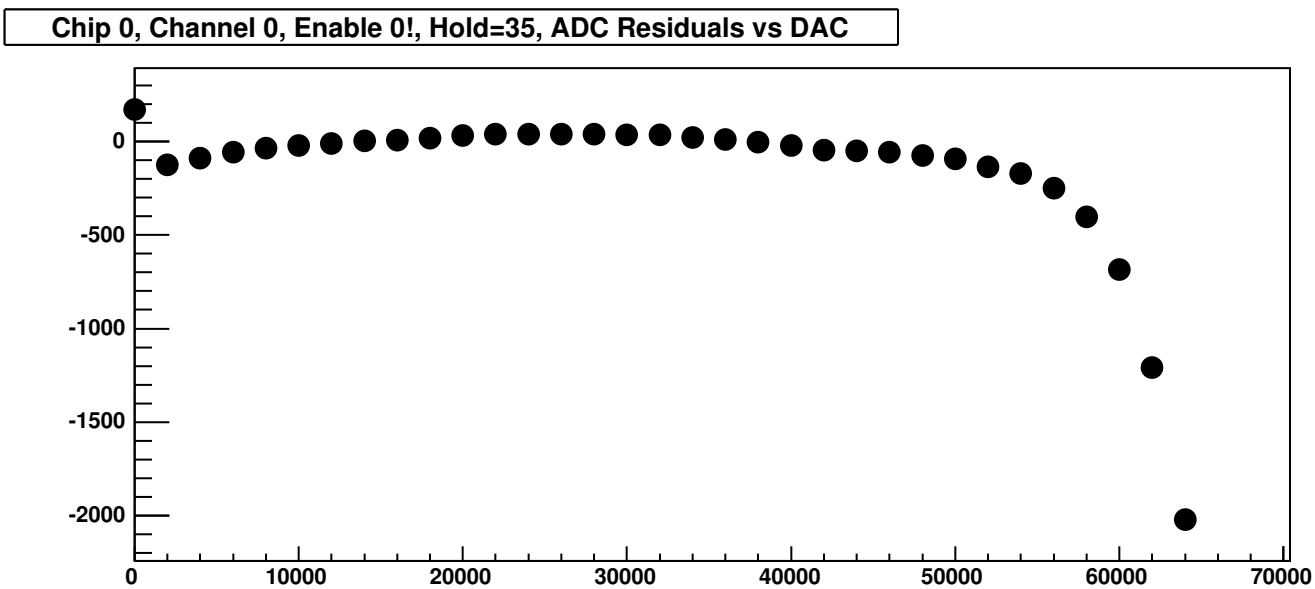


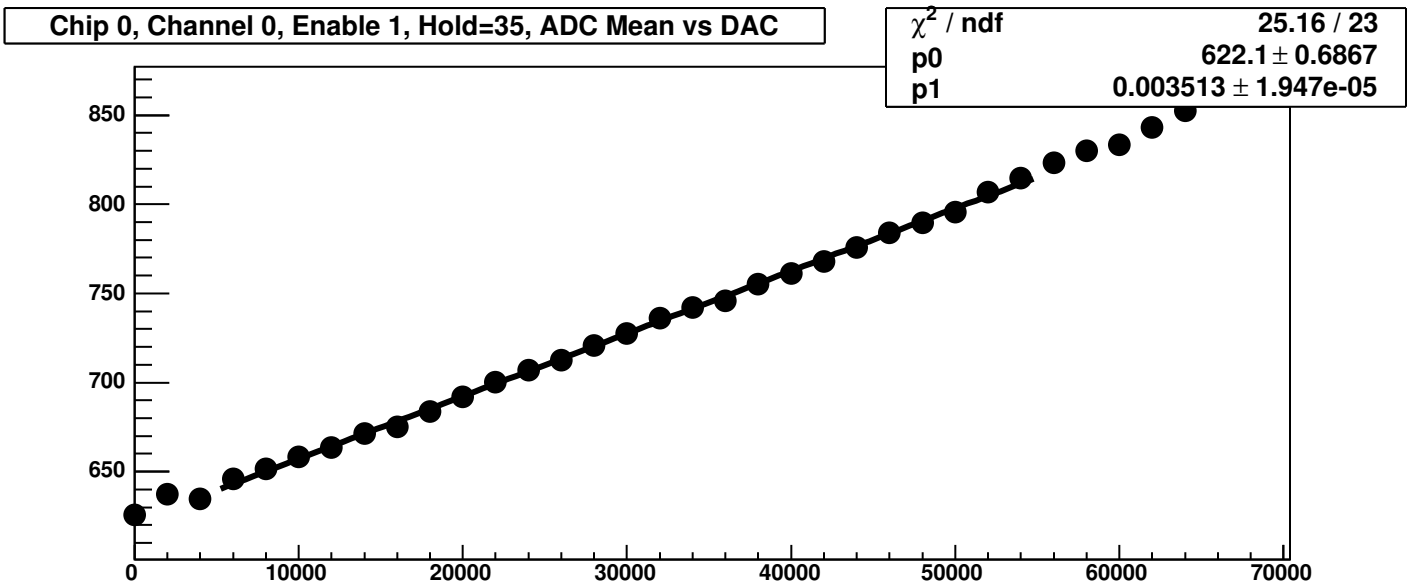
Enable 5, Hold=35, DAC=62000, ADC Noise vs 18*Chip+Chan

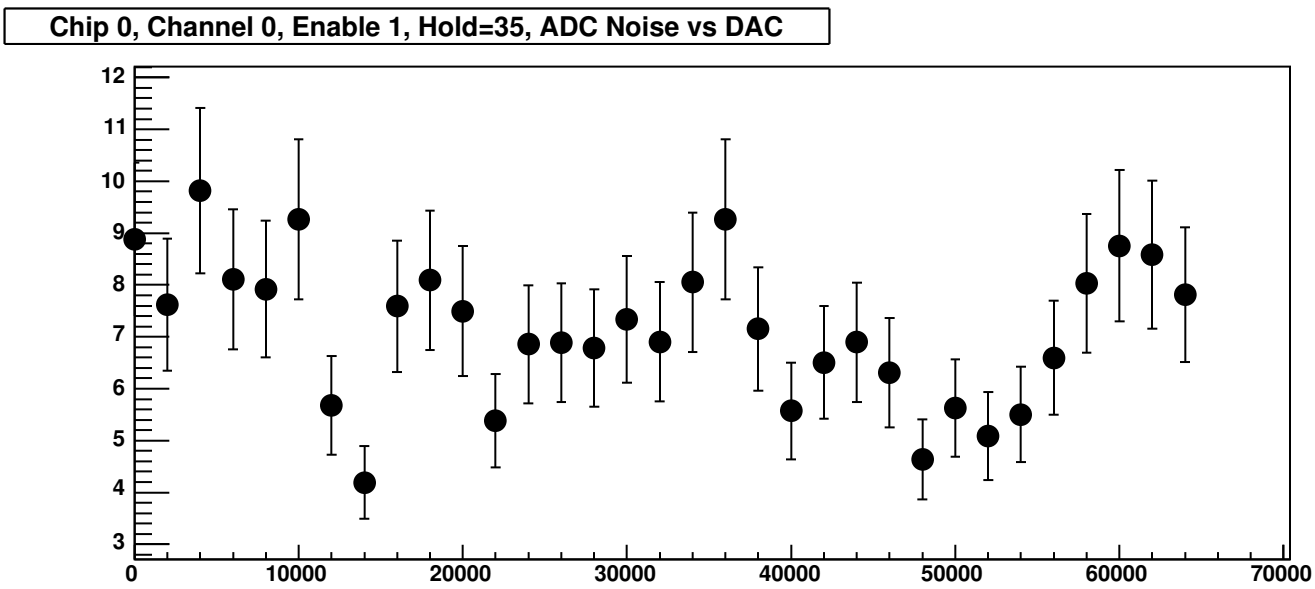


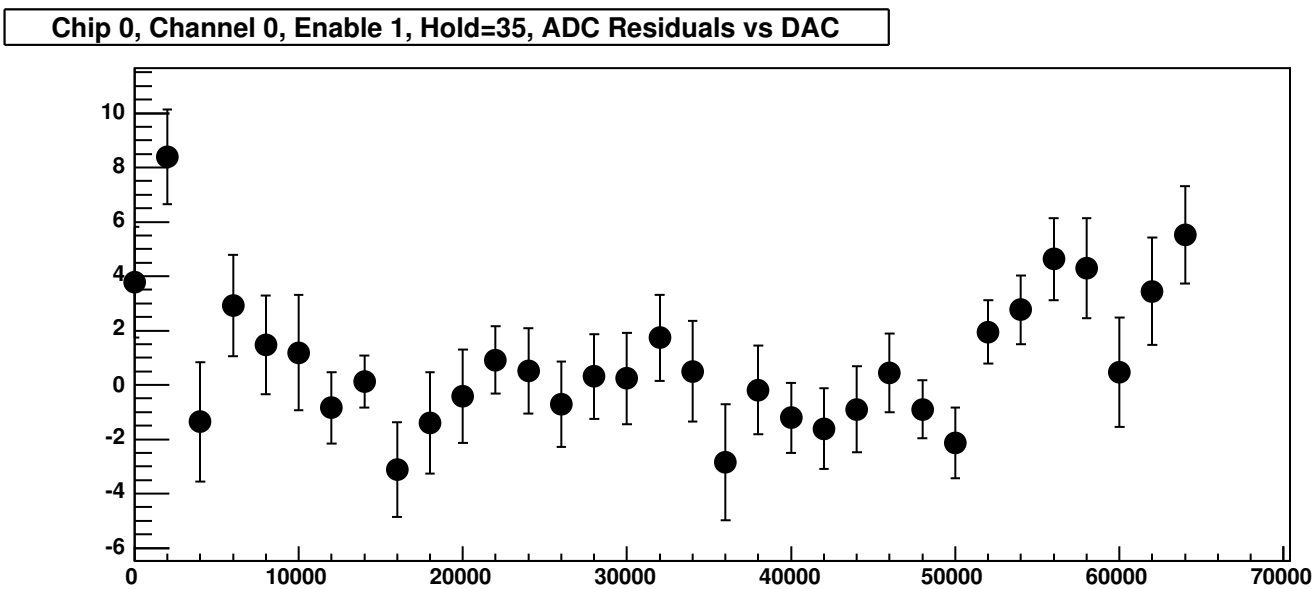

Chip 0, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 7547 / 23
p0 791.4 ± 0.956
p1 0.499 ± 4.396e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000

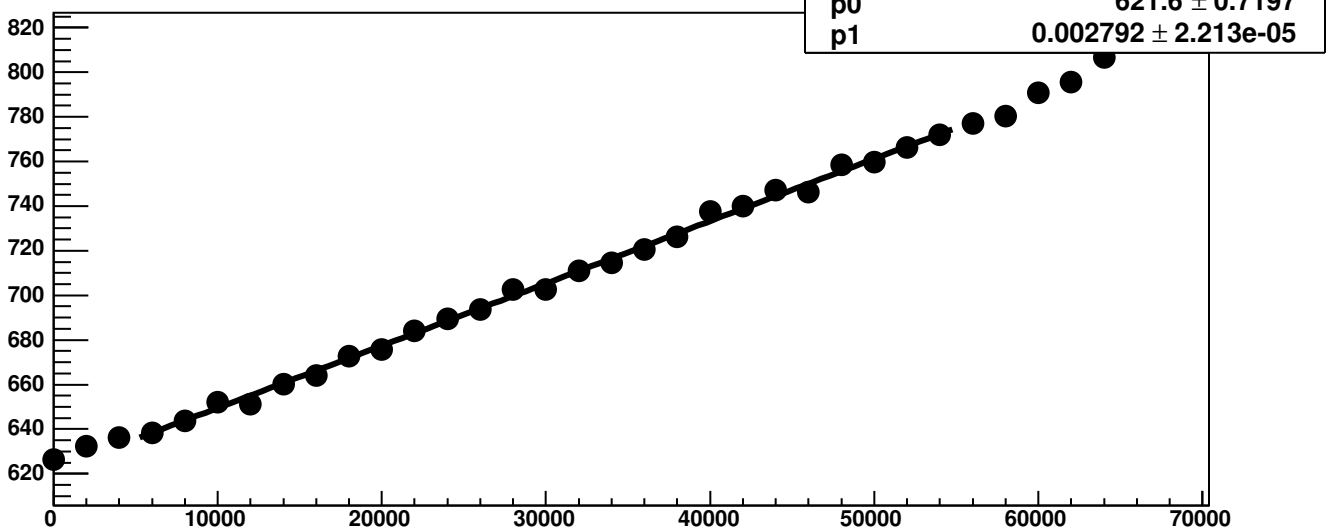

Chip 0, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 25.16 / 23
p0 622.1 ± 0.6867
p1 0.003513 ± 1.947e-05
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000
70000

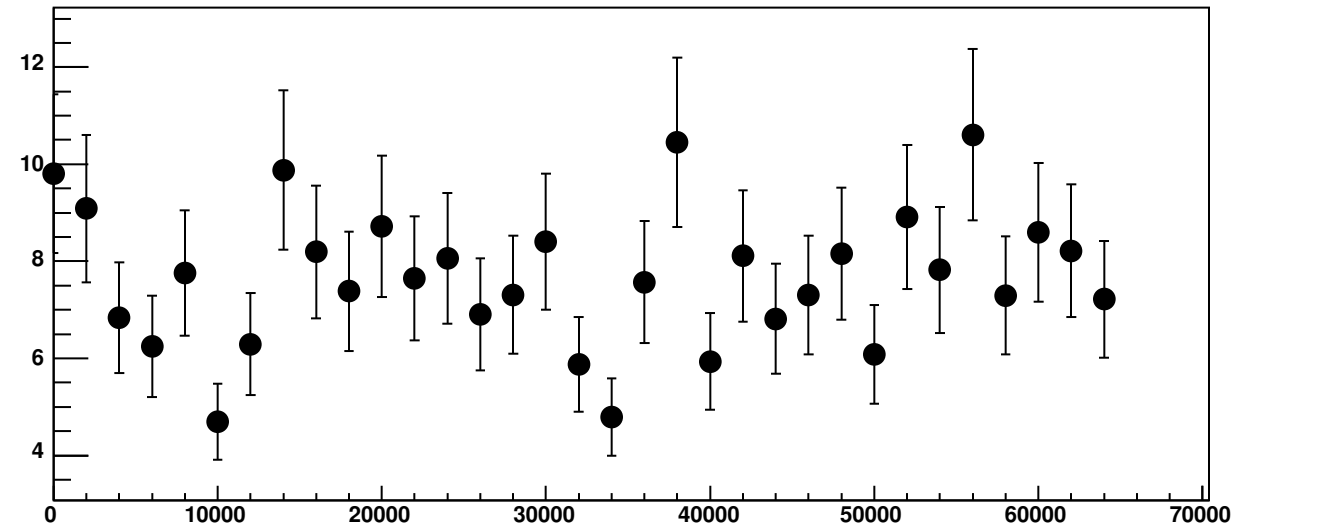

Chip 0, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

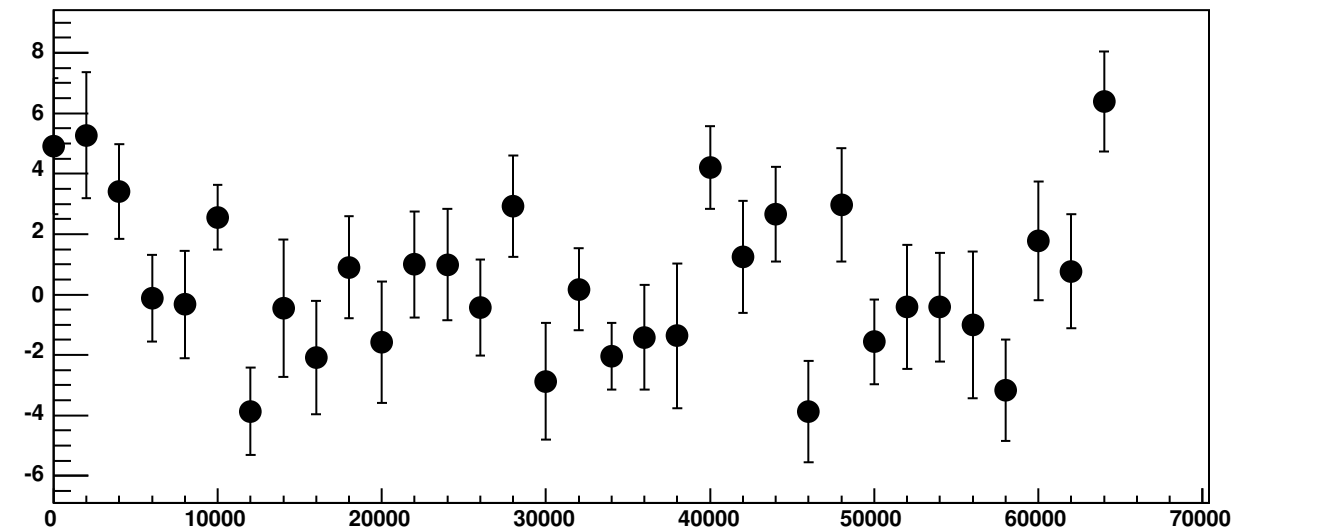
Chip 0, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

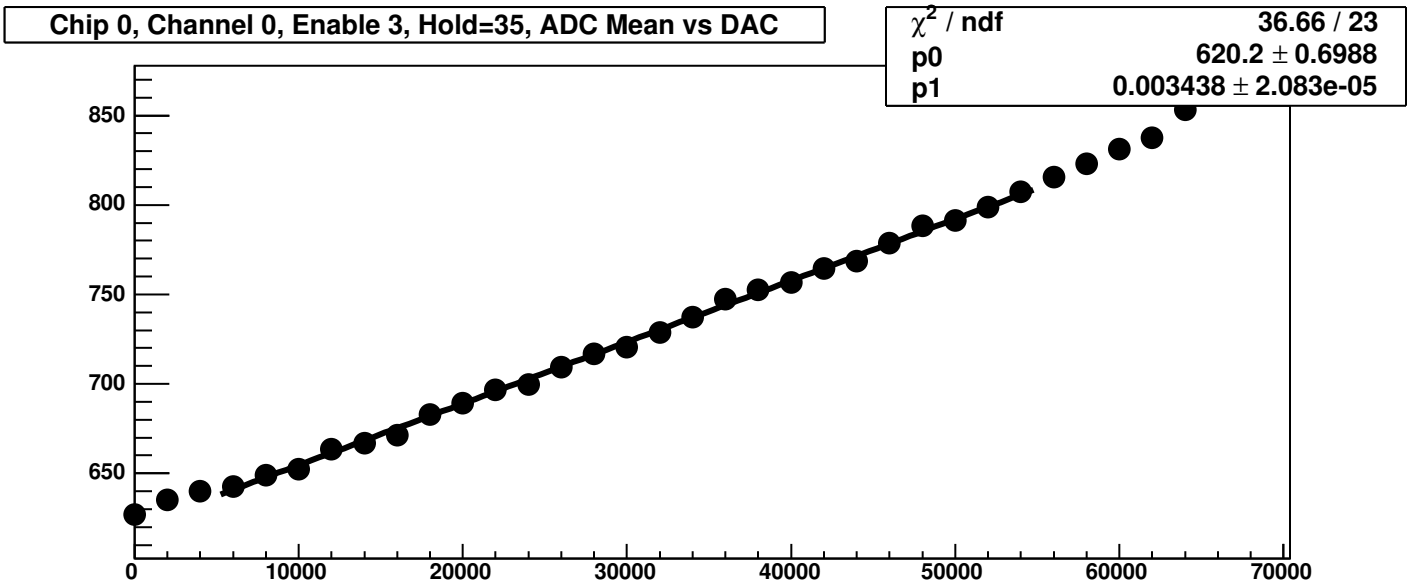


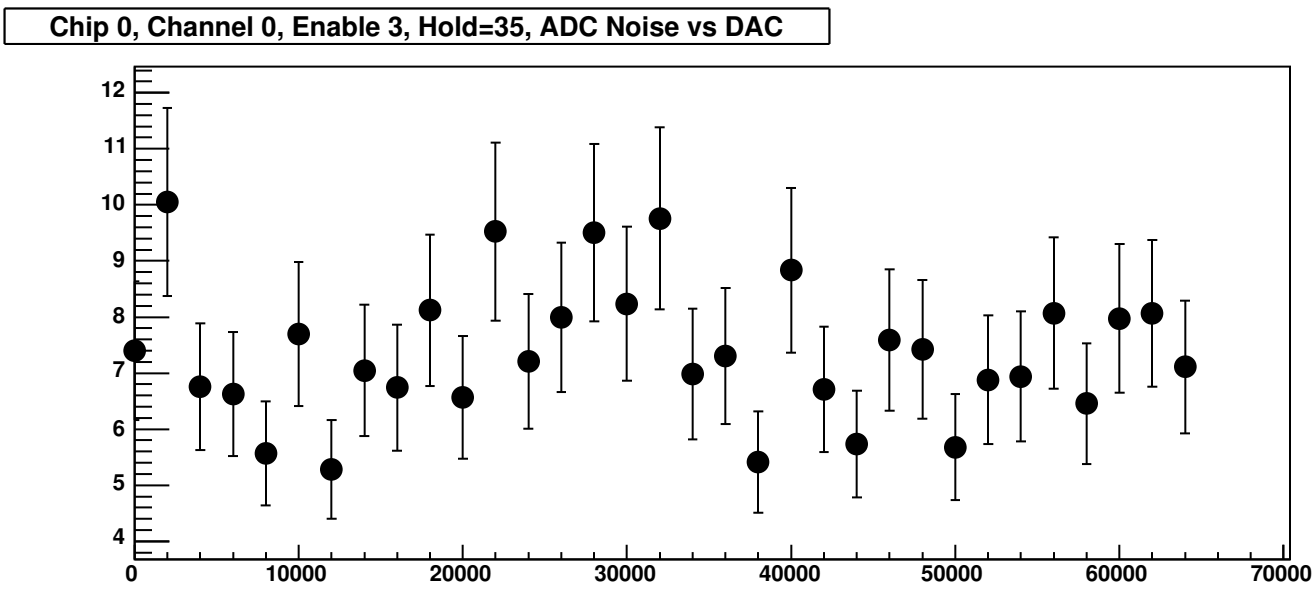
Chip 0, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC

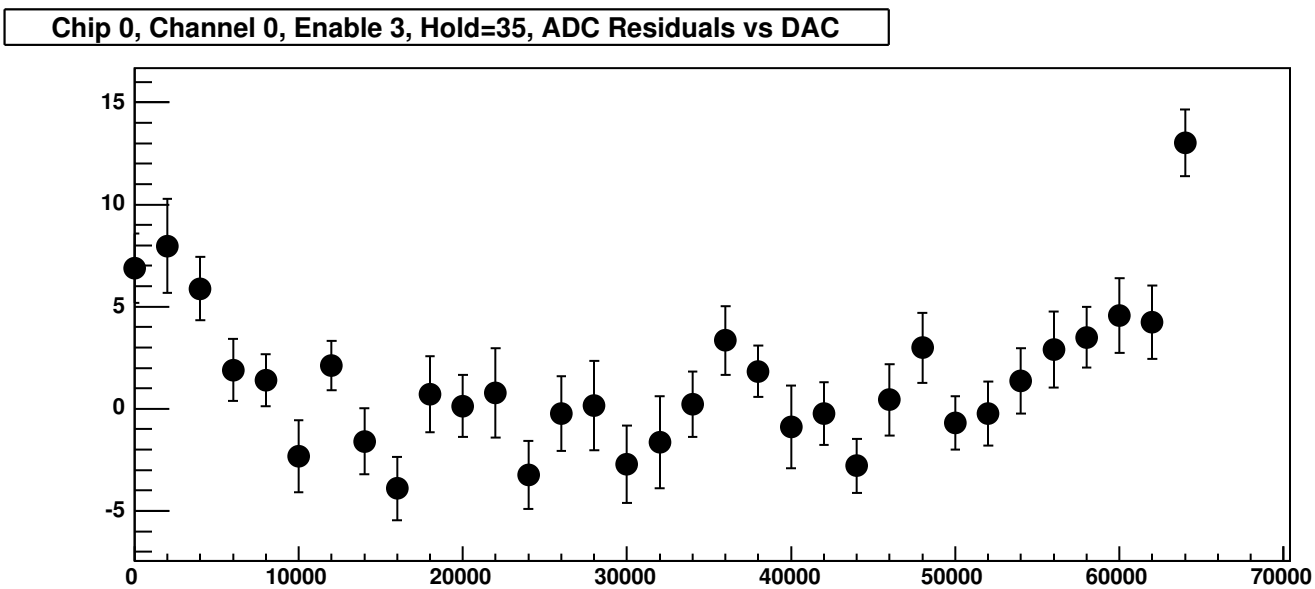


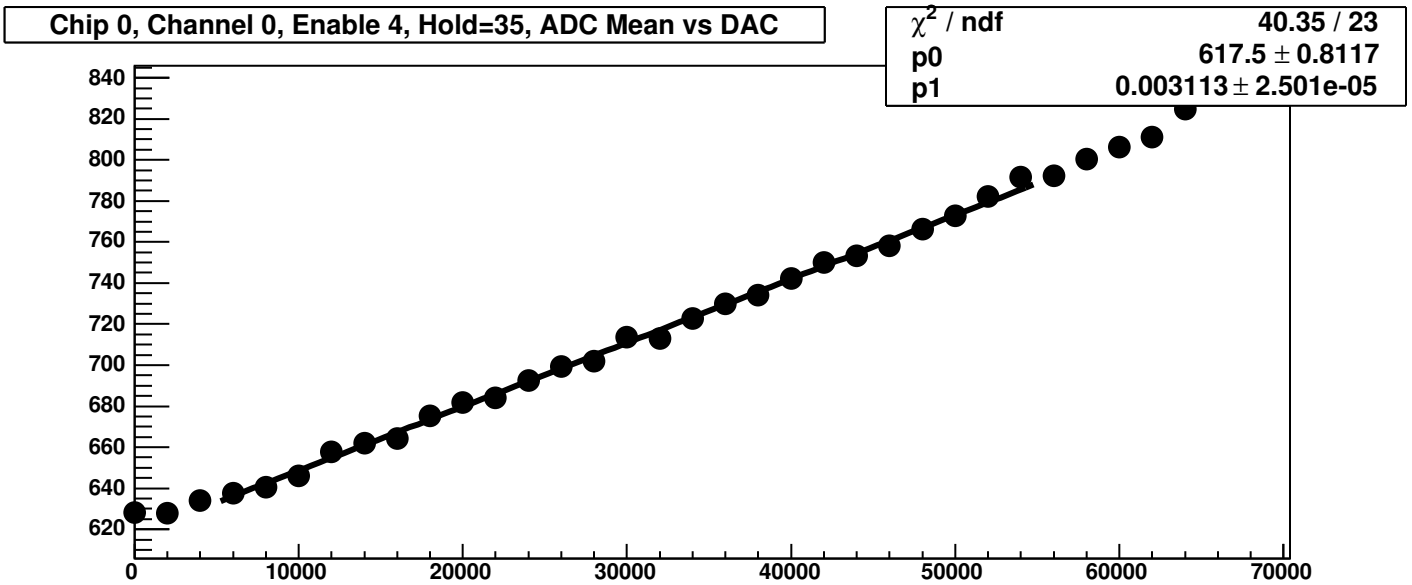
Chip 0, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

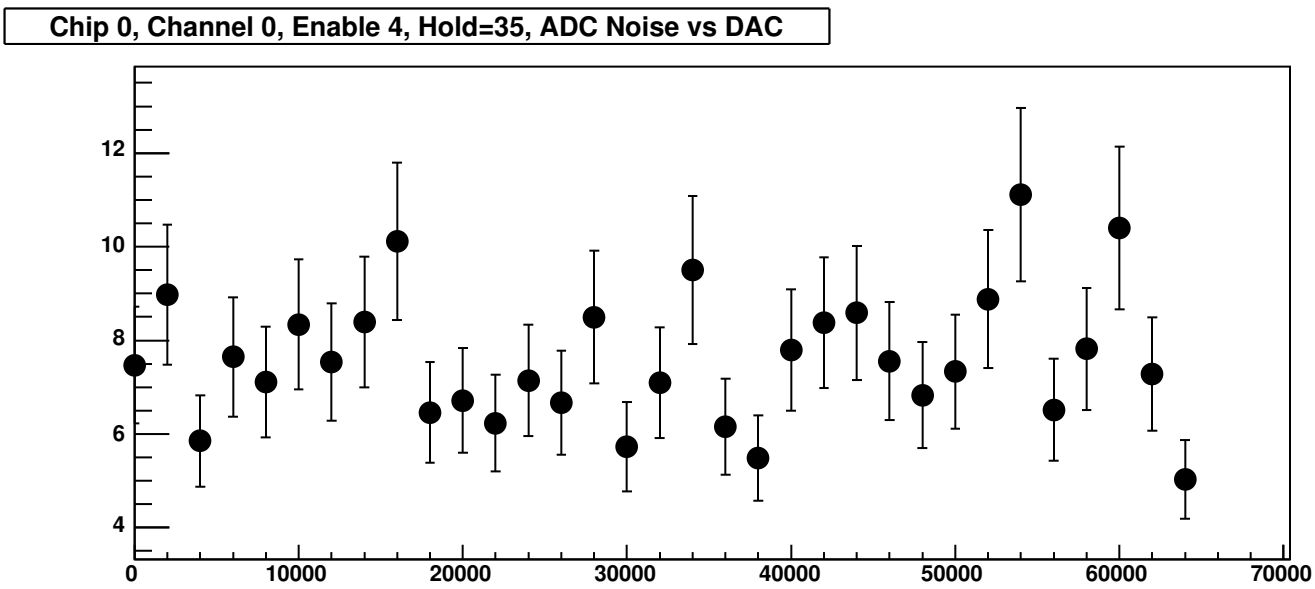


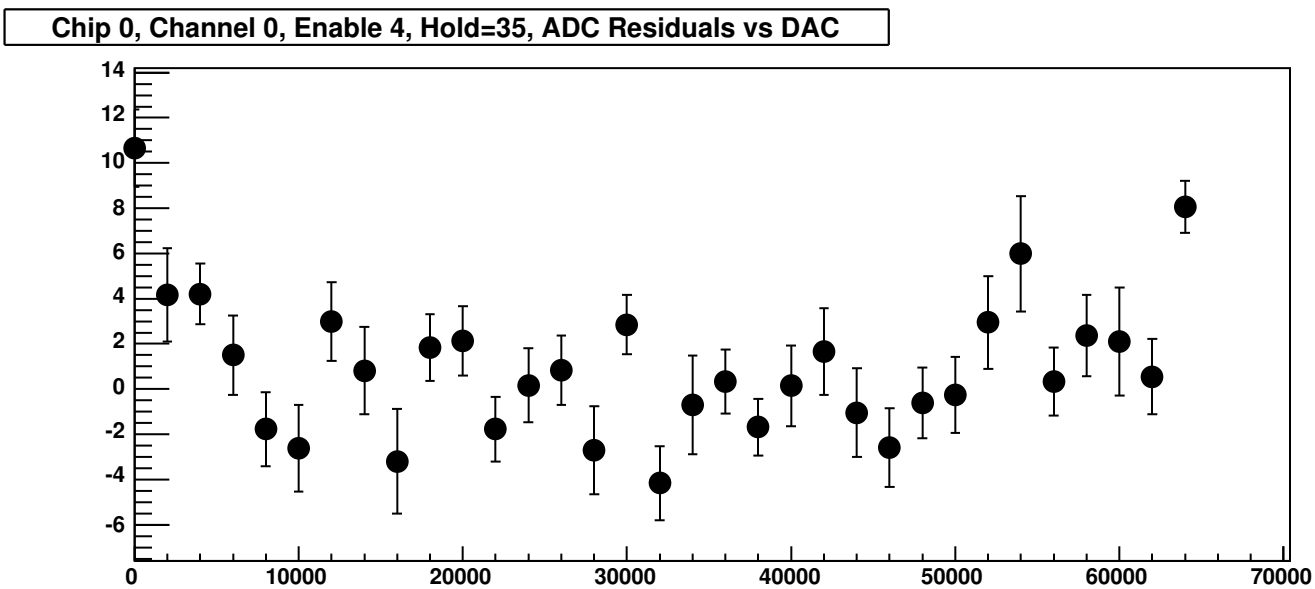

Chip 0, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.66 / 23
p0 620.2 ± 0.6988
p1 0.003438 ± 2.083e-05
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

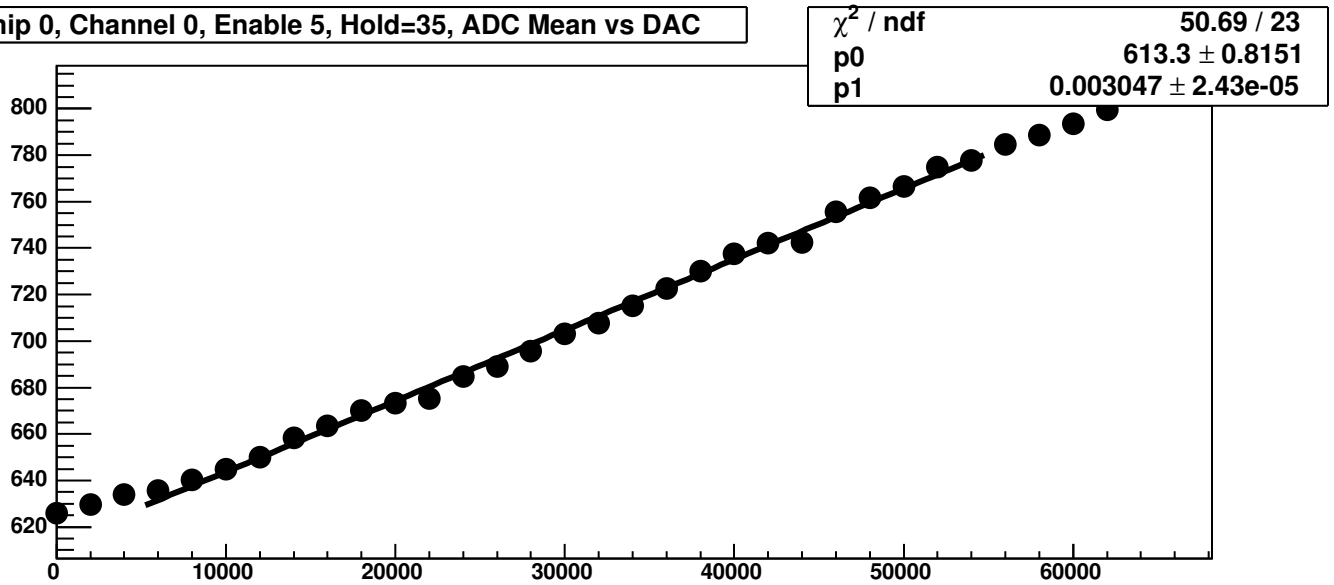

Chip 0, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 40.35 / 23
p0 617.5 ± 0.8117
p1 0.003113 ± 2.501e-05
840
820
800
780
760
740
720
700
680
660
640
620
0
10000
20000
30000
40000
50000
60000
70000

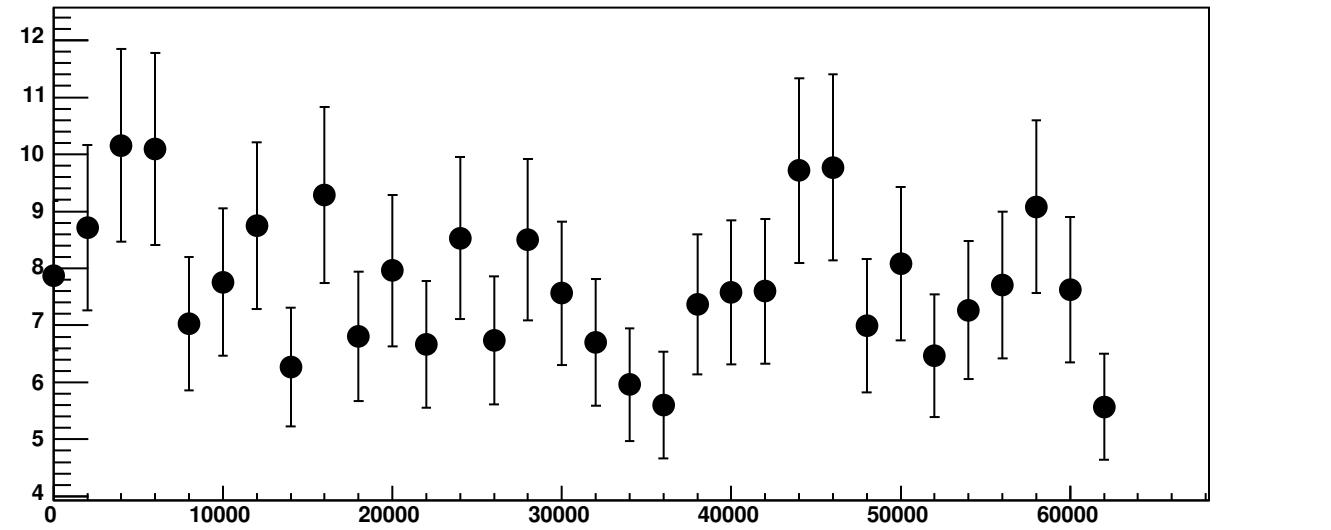

Chip 0, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

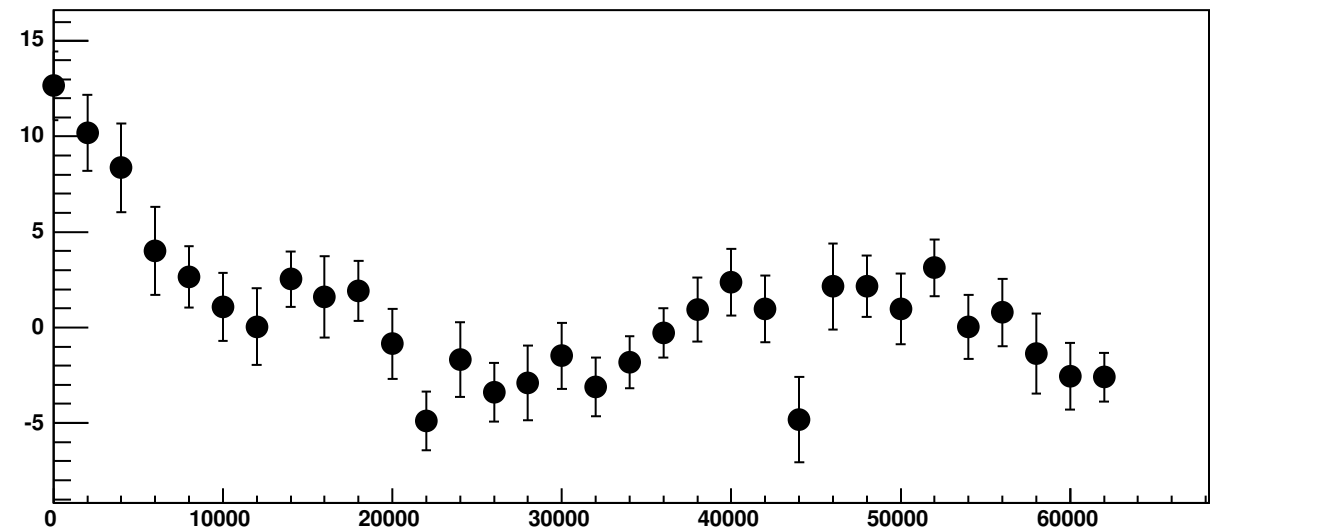
Chip 0, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

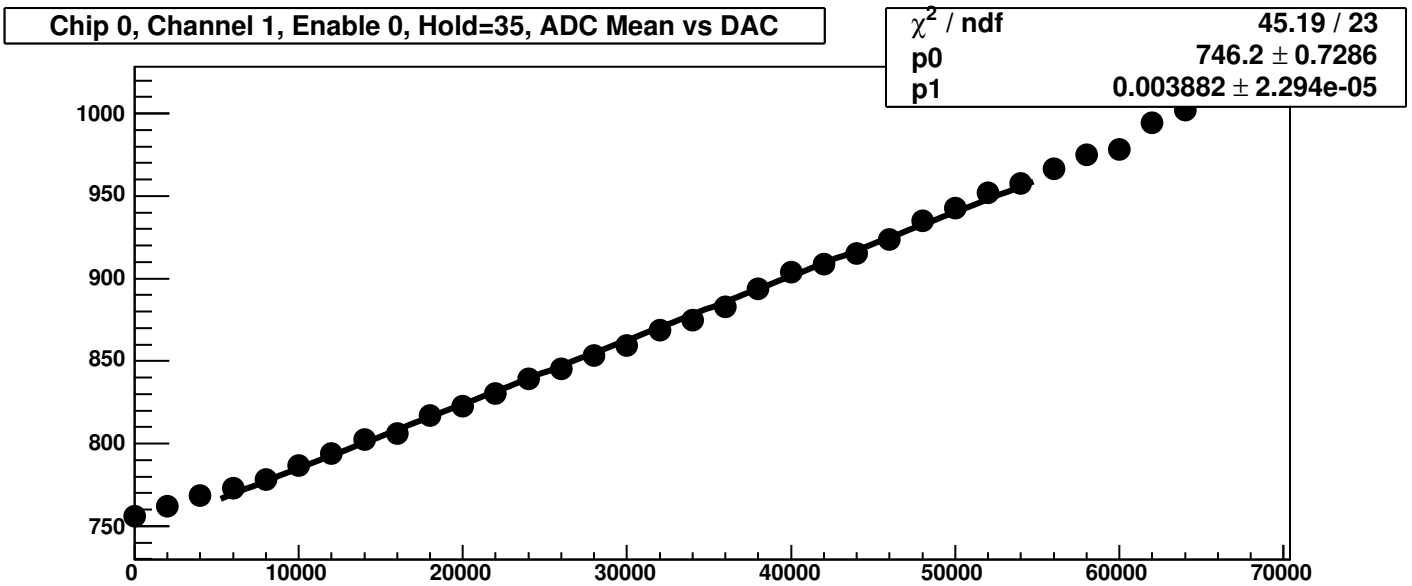


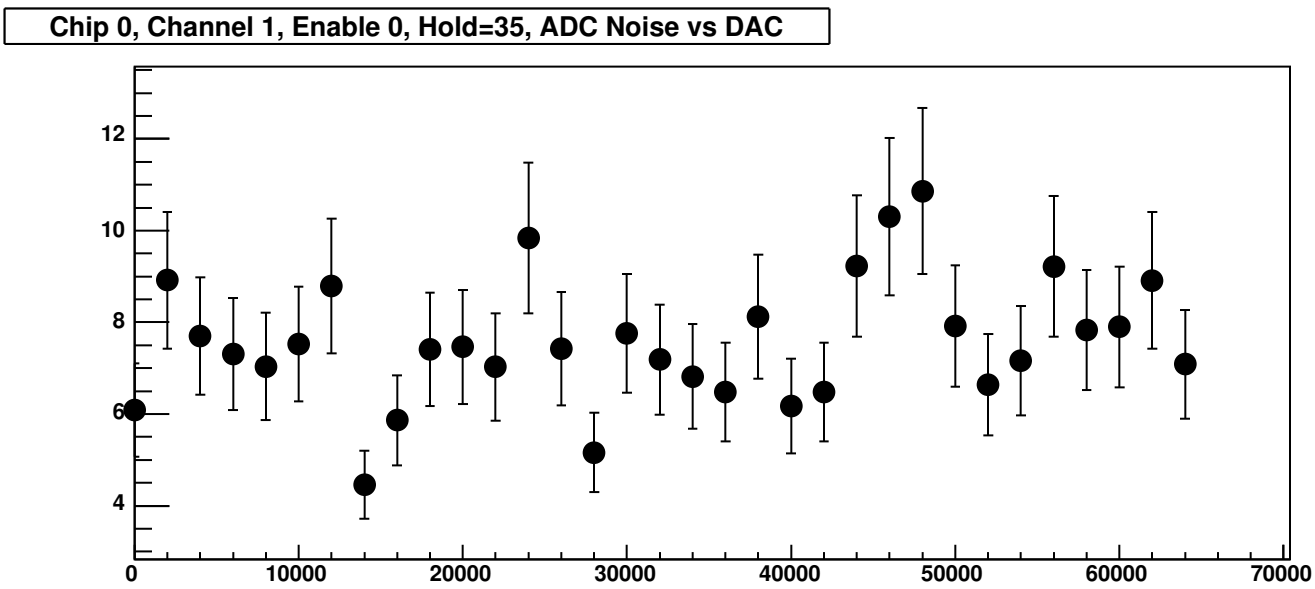
Chip 0, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC

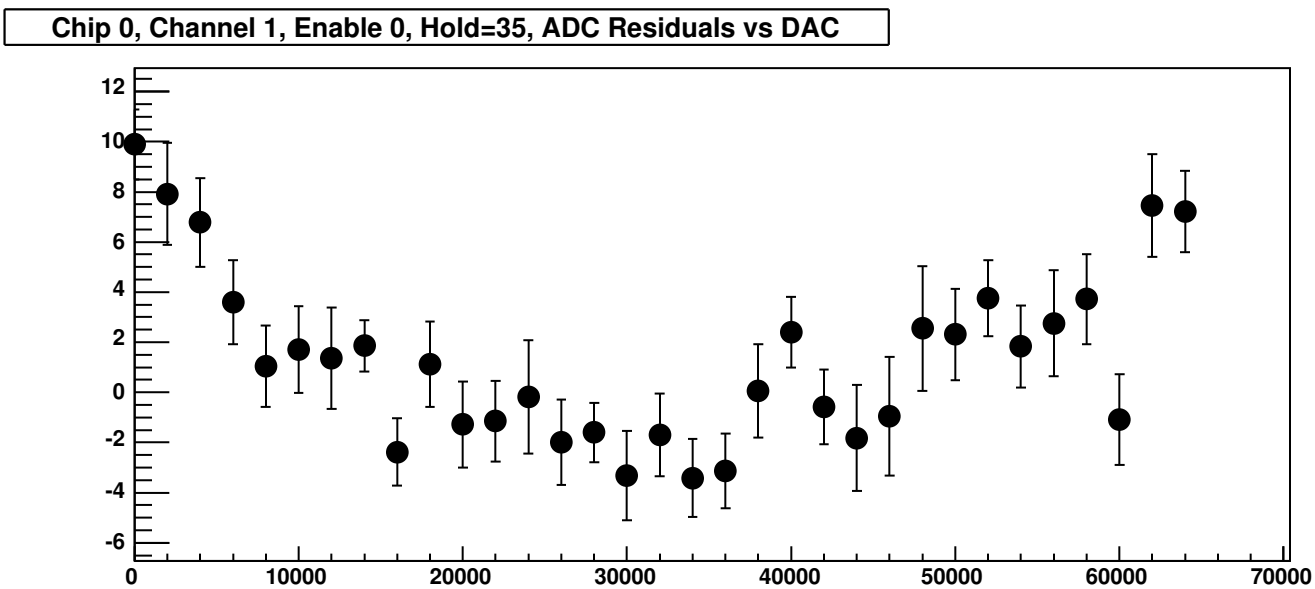


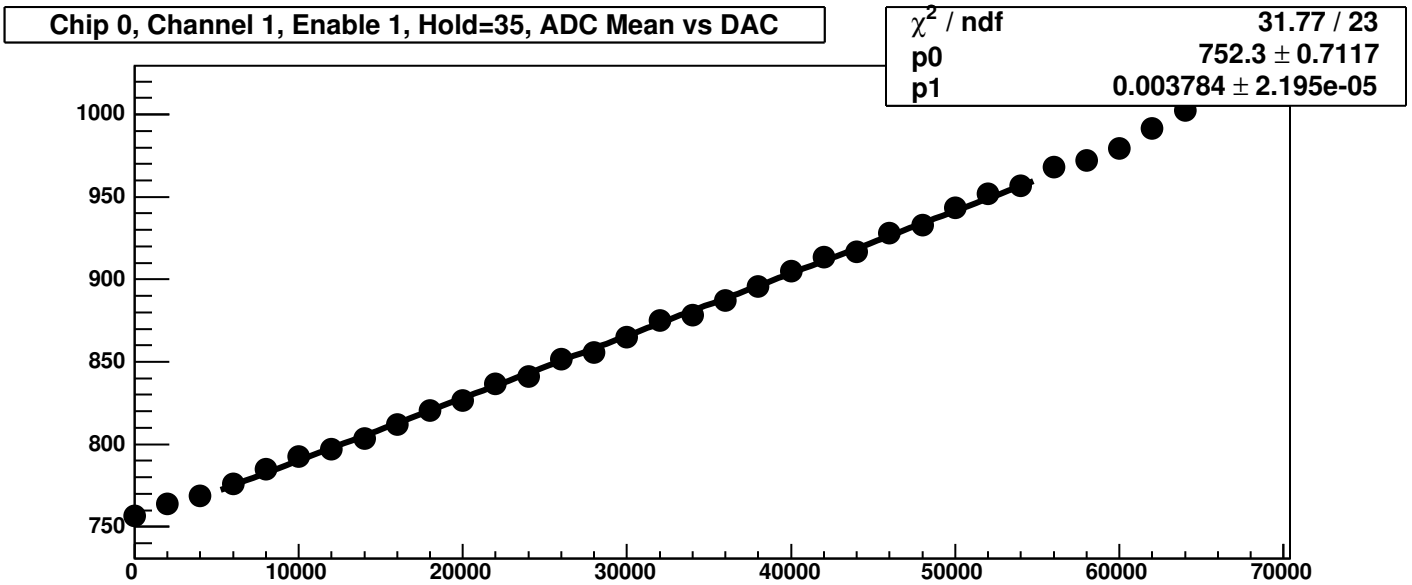
Chip 0, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC

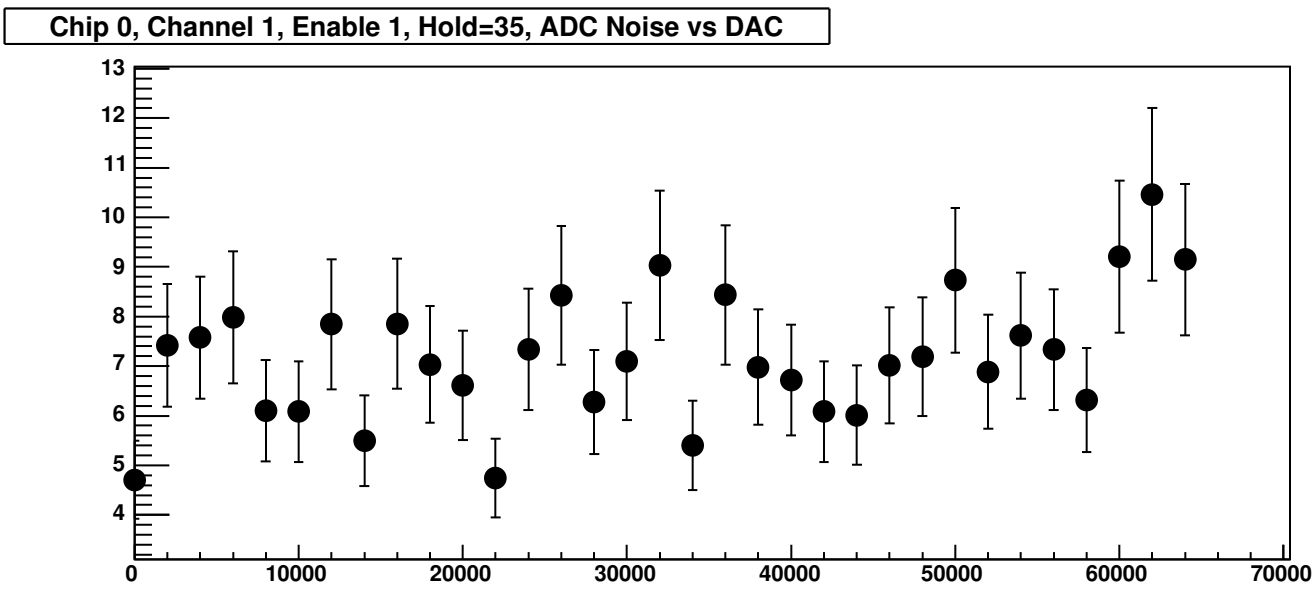


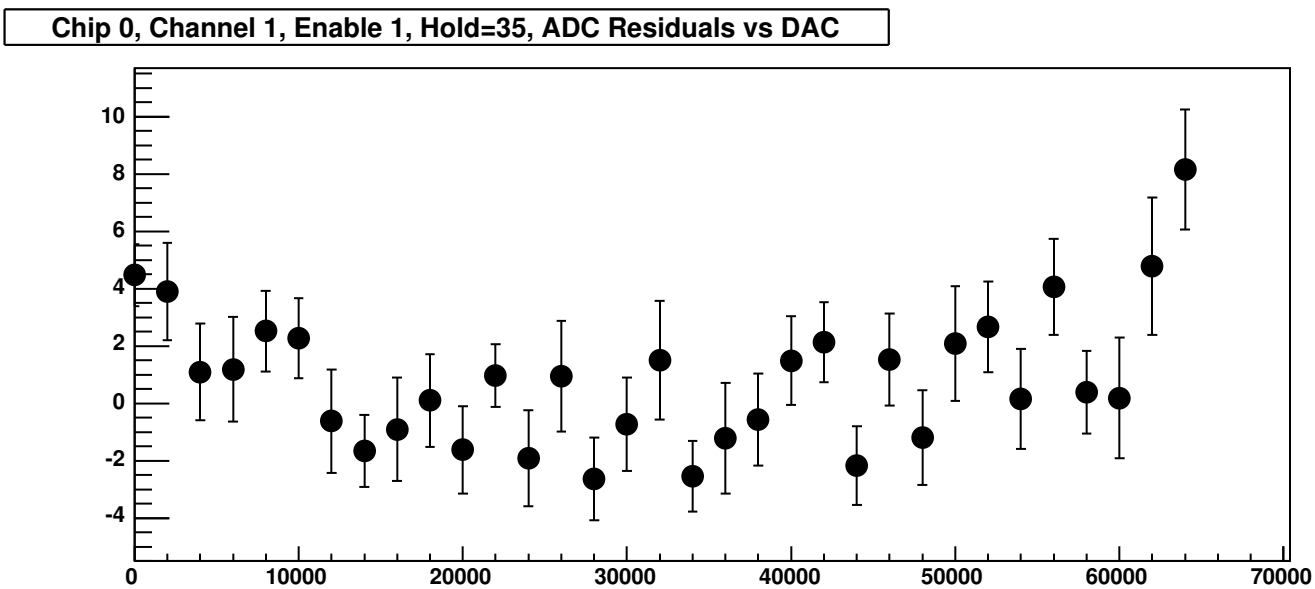

Chip 0, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 45.19 / 23
p0 746.2 ± 0.7286
p1 0.003882 ± 2.294e-05
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000
70000

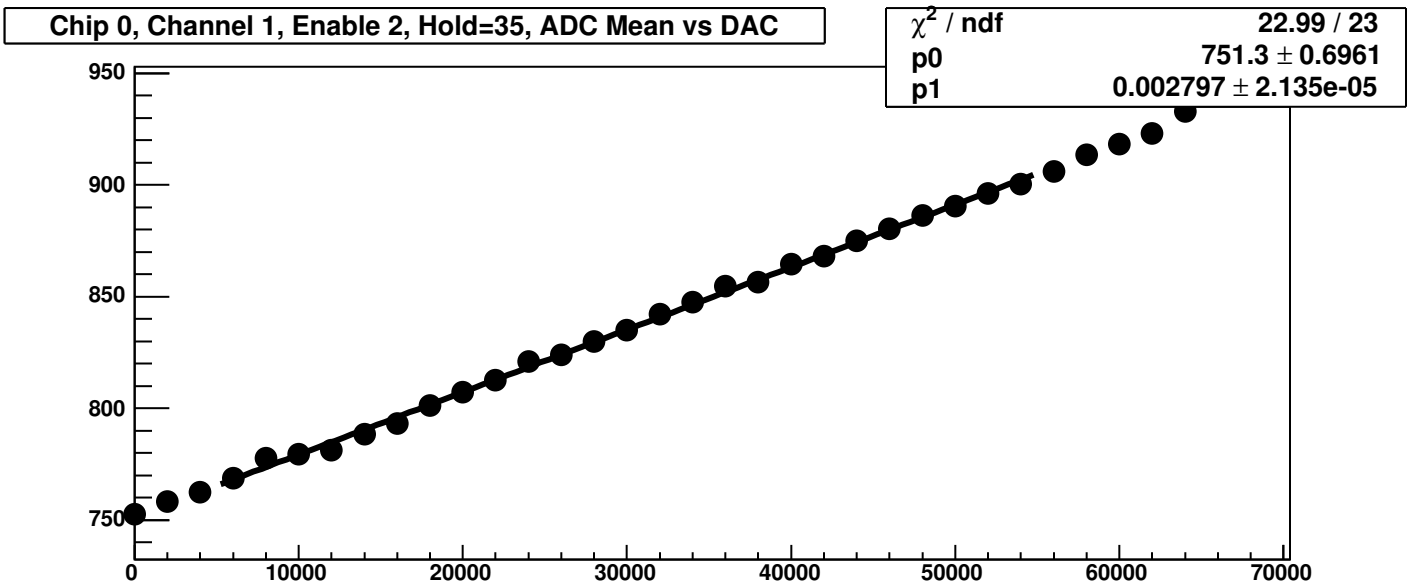

Chip 0, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

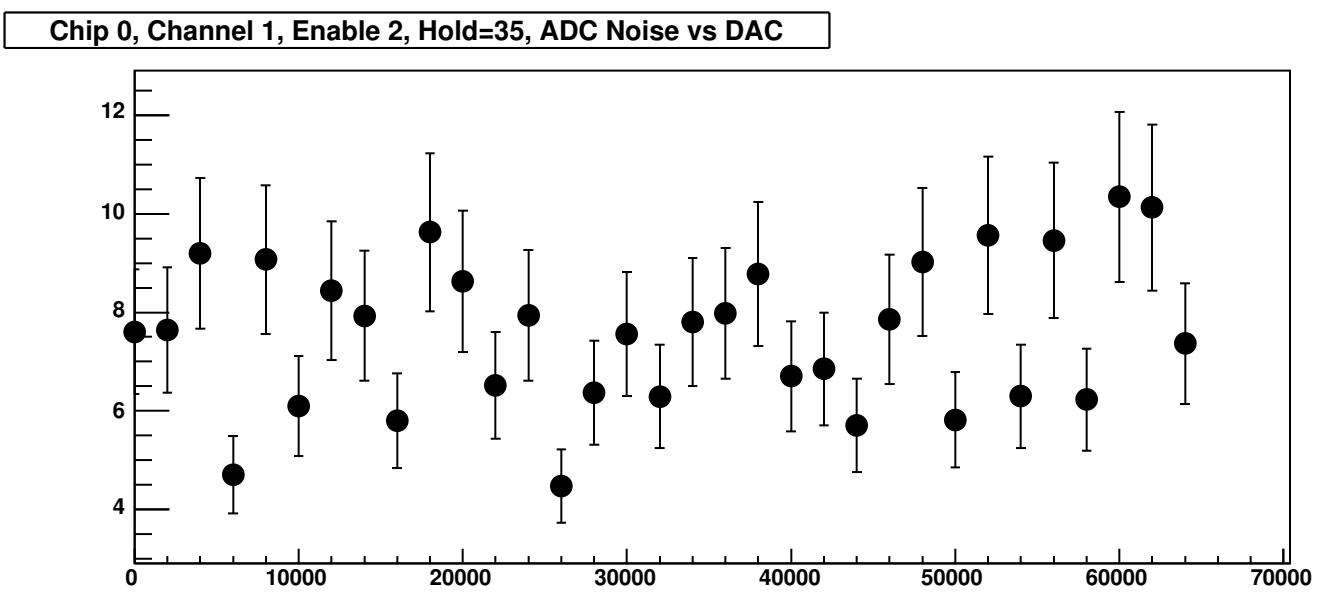

Chip 0, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

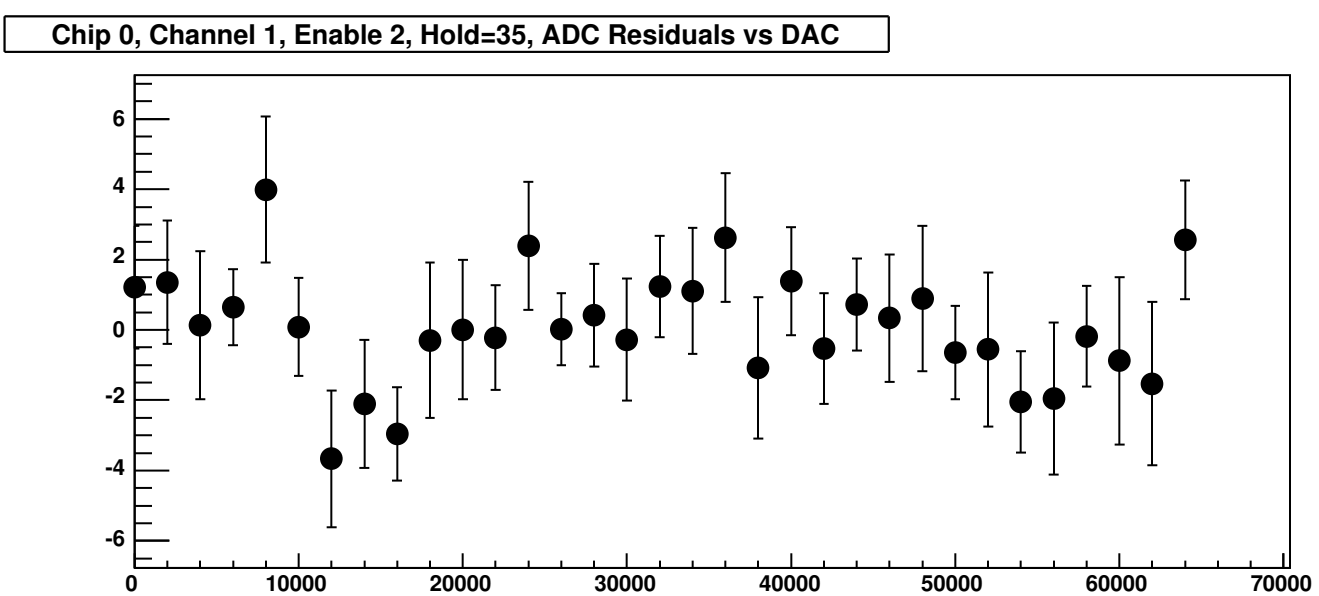

Chip 0, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.77 / 23
p0 752.3 ± 0.7117
p1 0.003784 ± 2.195e-05
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000
70000

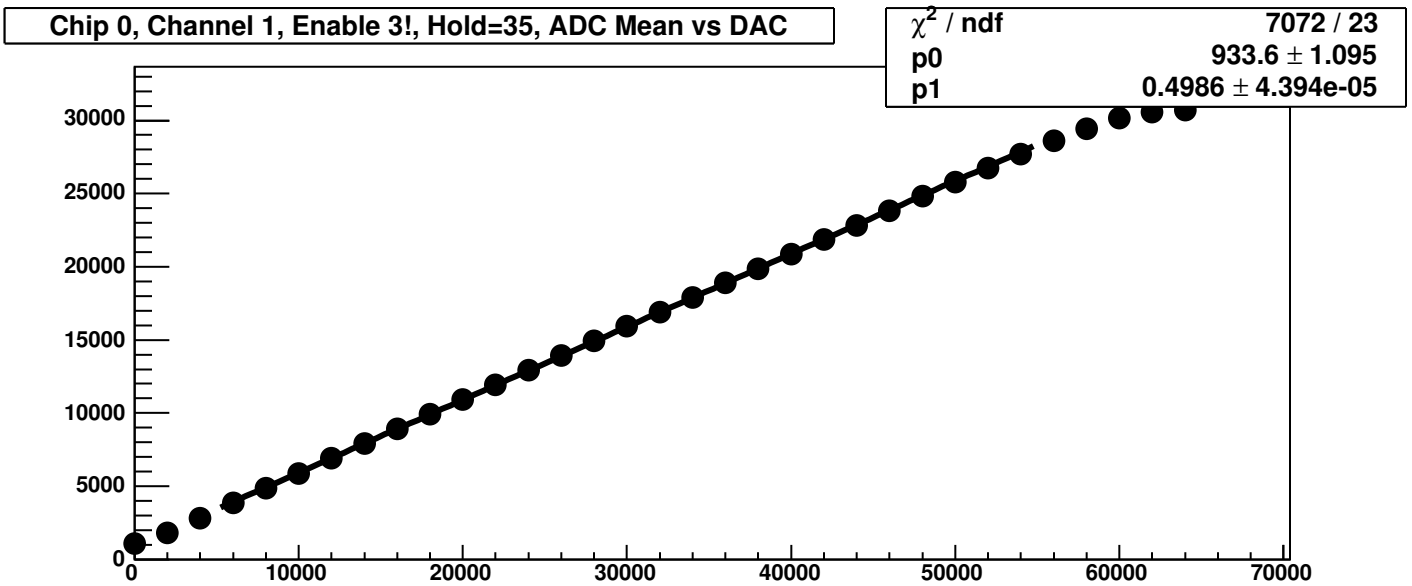

Chip 0, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

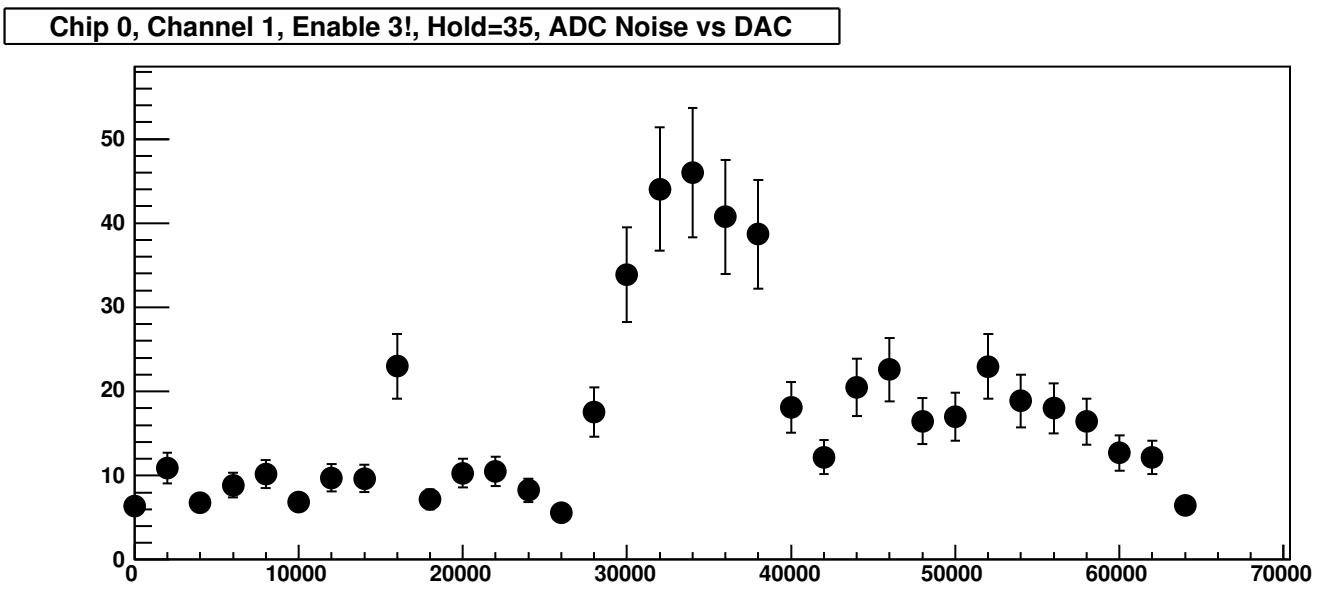

Chip 0, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
0
10000
20000
30000
40000
50000
60000
70000

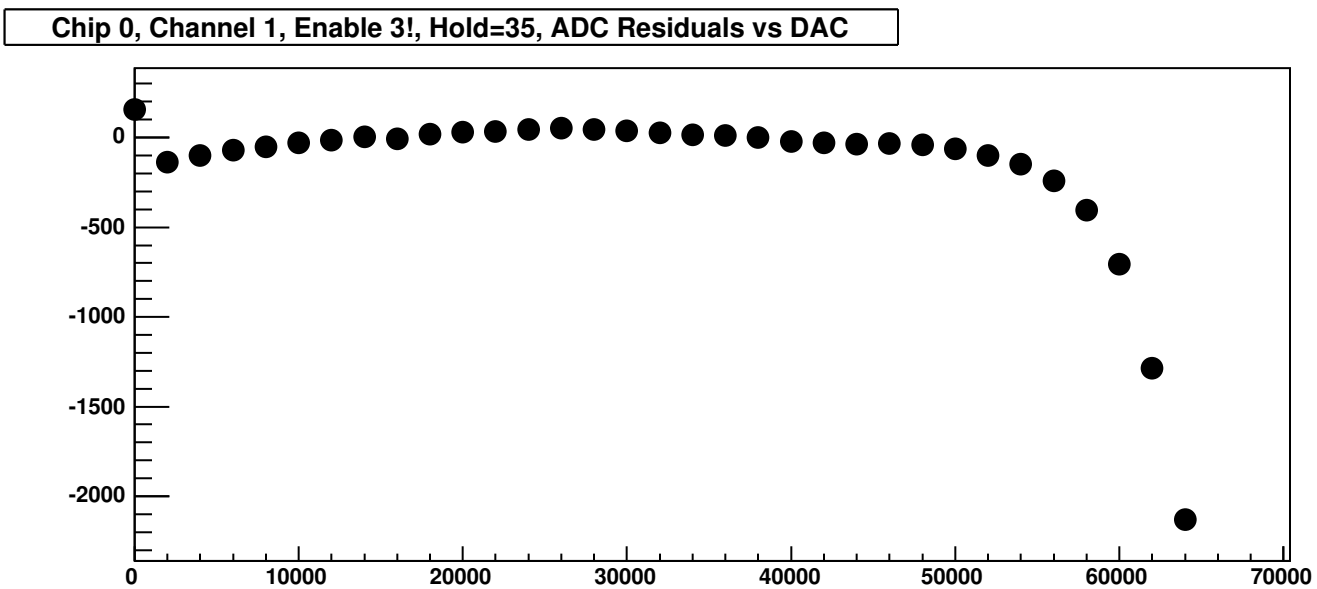

Chip 0, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 22.99 / 23
p0 751.3 ± 0.6961
p1 0.002797 ± 2.135e-05
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000
70000

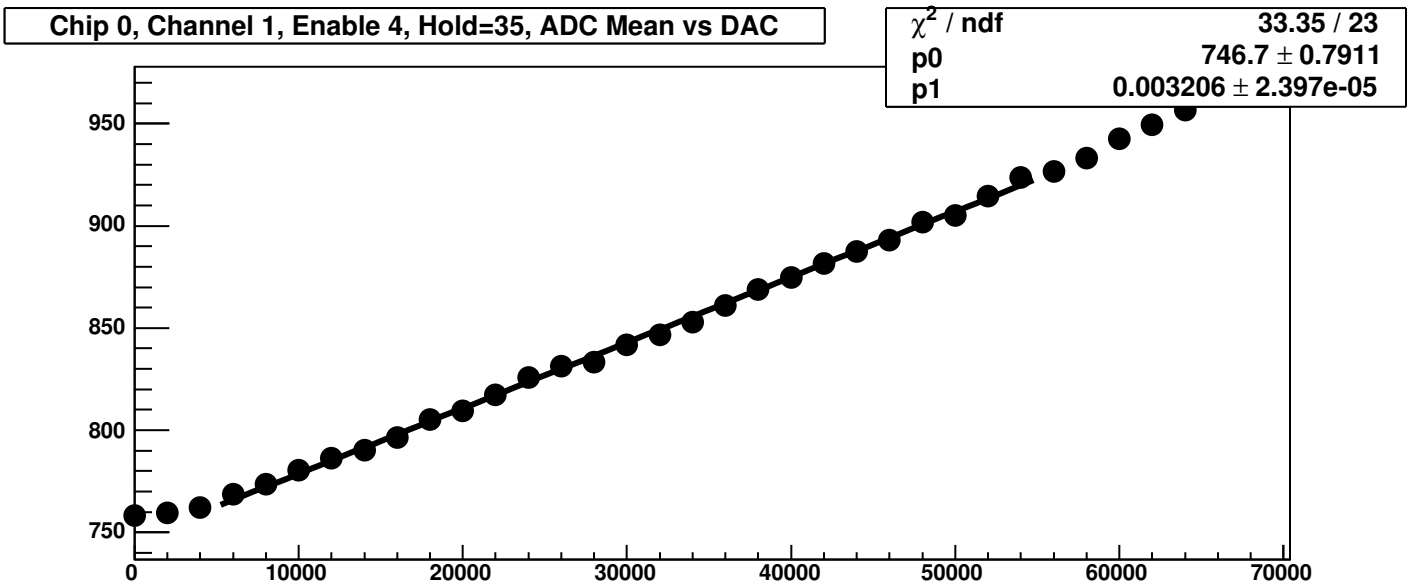

Chip 0, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

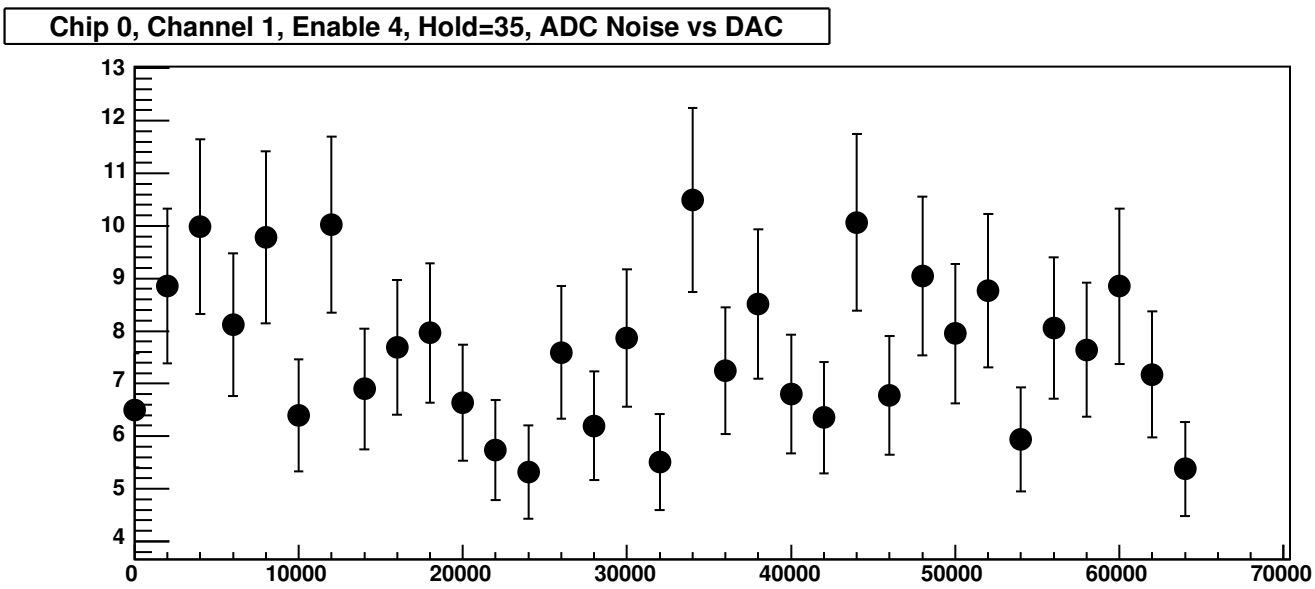

Chip 0, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

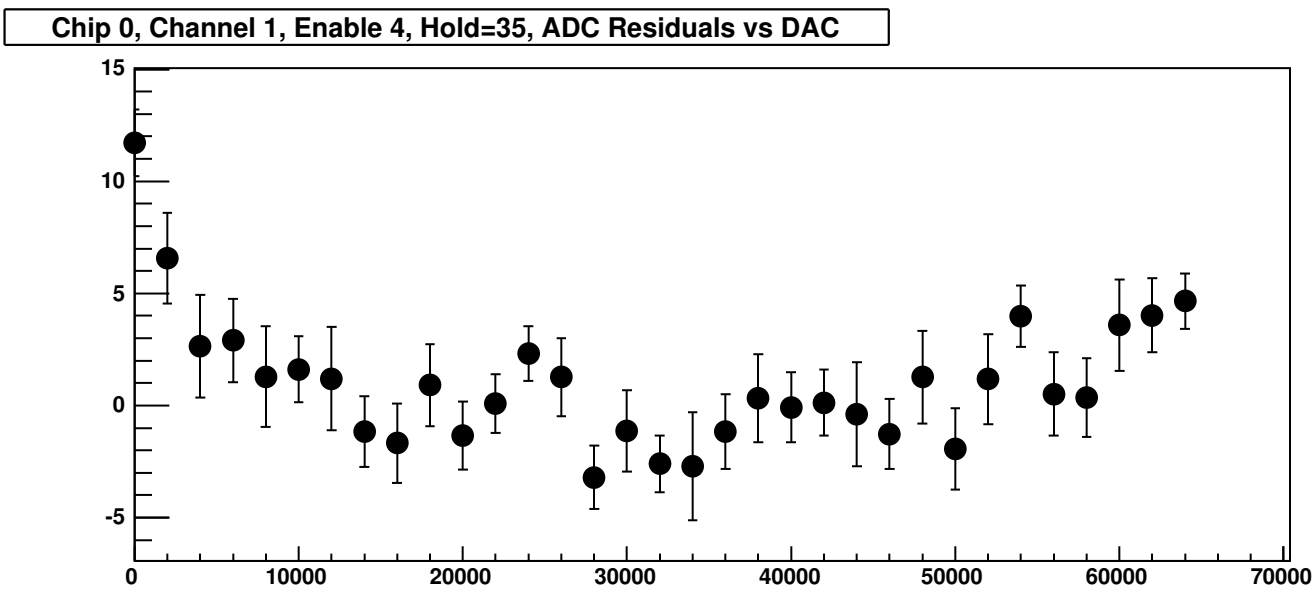

Chip 0, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 7072 / 23
p0 933.6 ± 1.095
p1 0.4986 ± 4.394e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

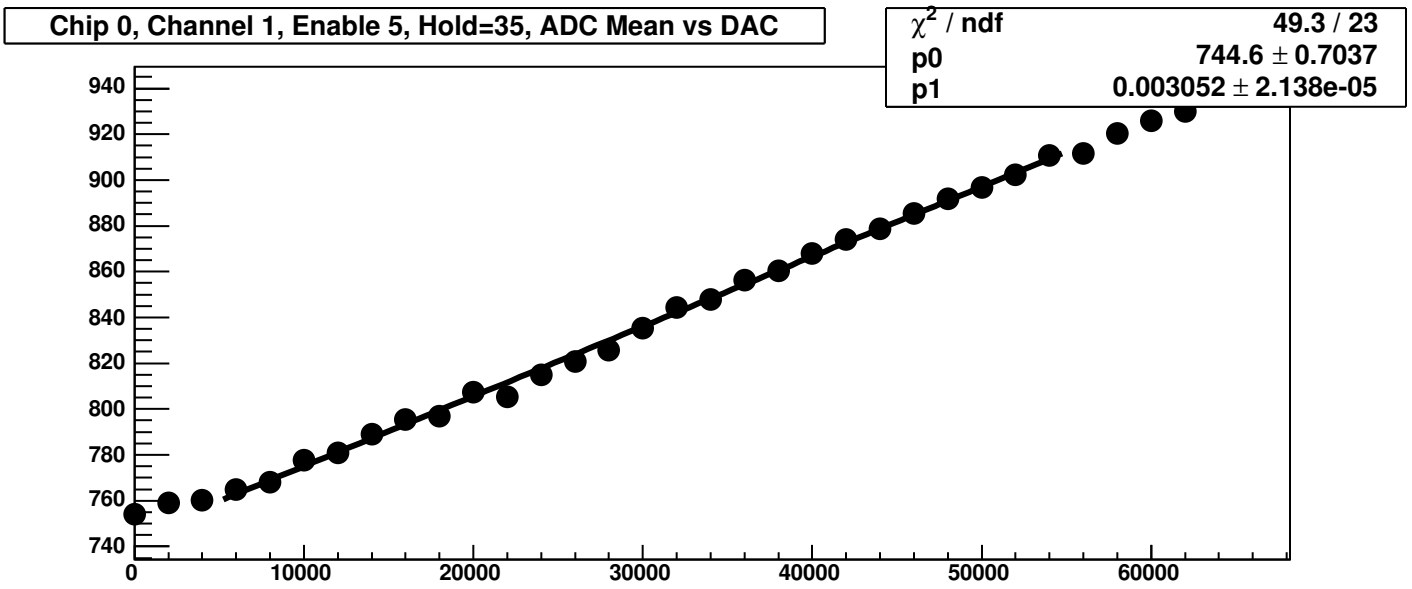

Chip 0, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

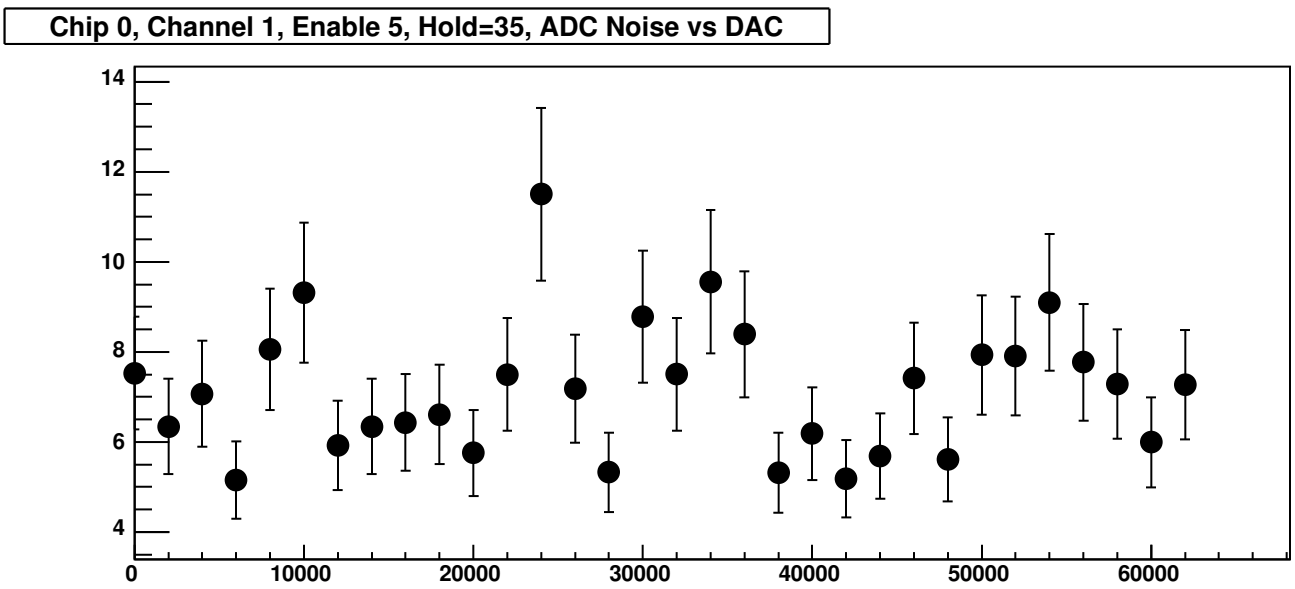

Chip 0, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000

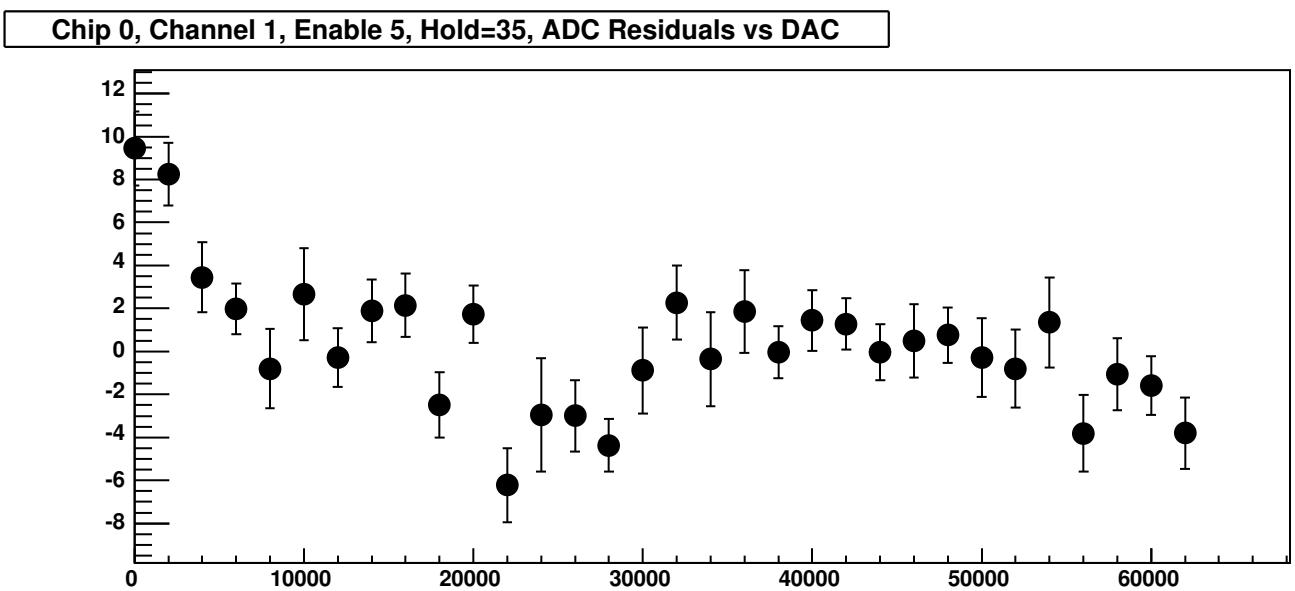

Chip 0, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 33.35 / 23
p0 746.7 ± 0.7911
p1 0.003206 ± 2.397e-05
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000
70000

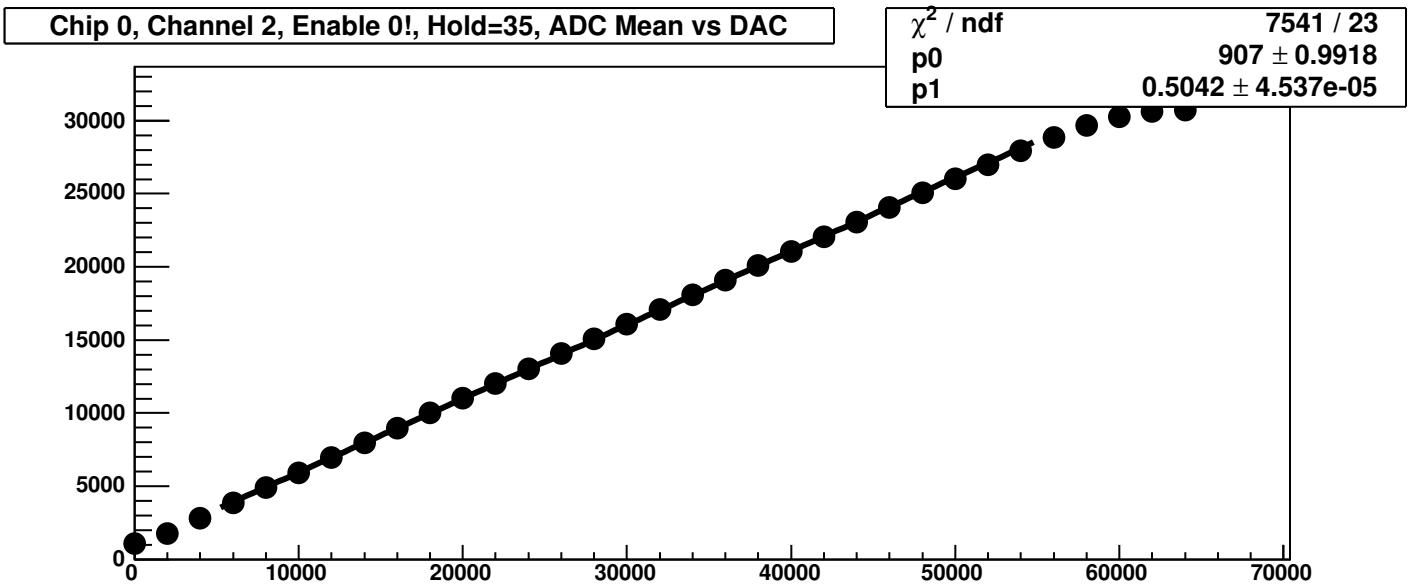

Chip 0, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

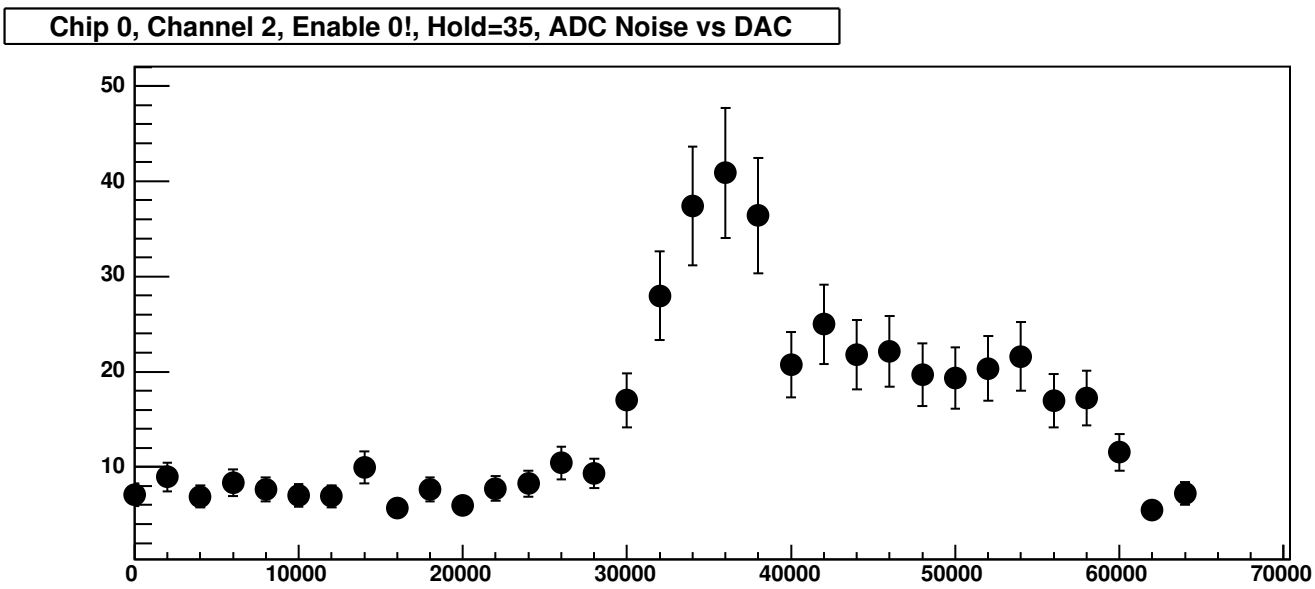

Chip 0, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

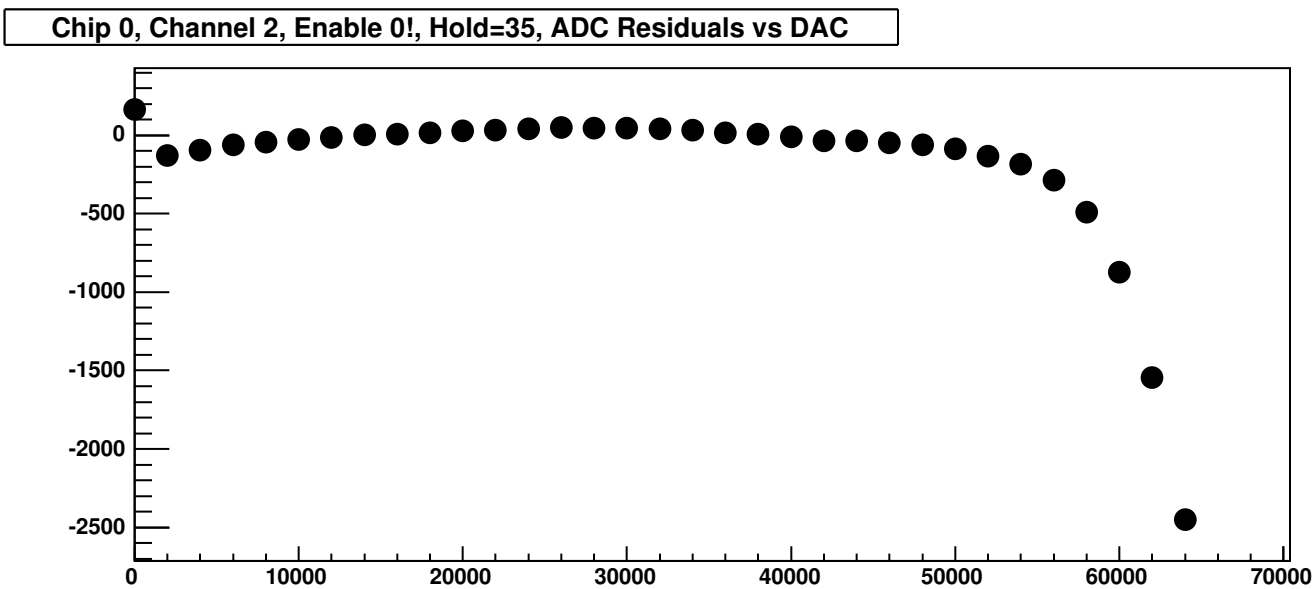

Chip 0, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 49.3 / 23
p0 744.6 ± 0.7037
p1 0.003052 ± 2.138e-05
940
920
900
880
860
840
820
800
780
760
740
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000

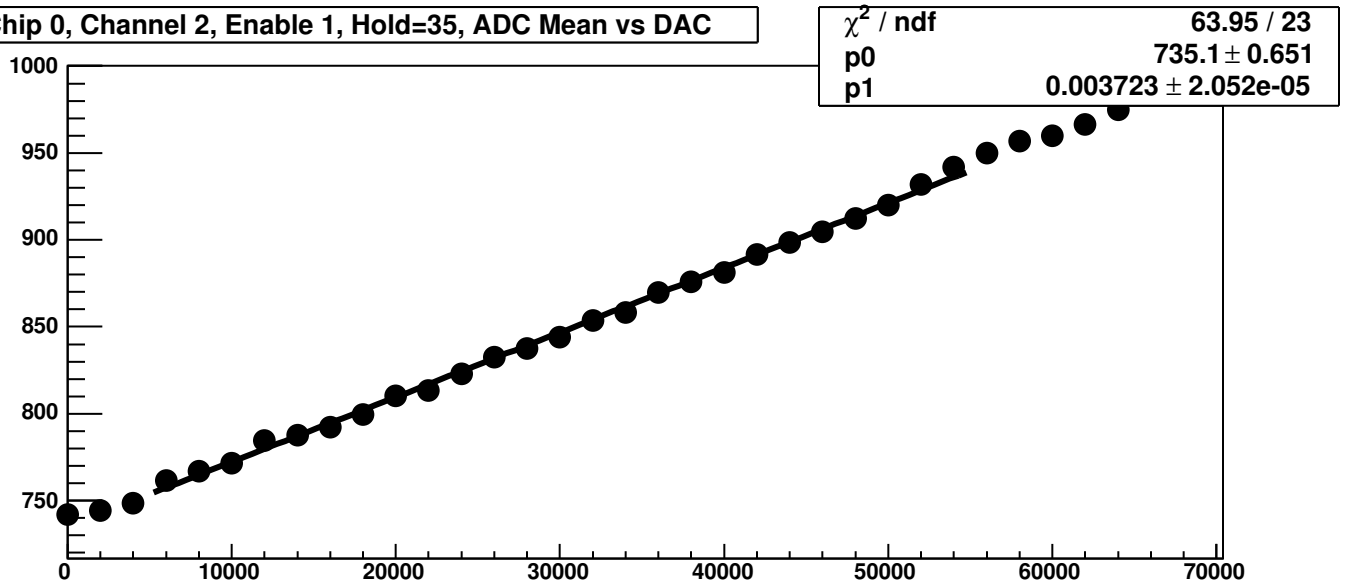

Chip 0, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000


Chip 0, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 7541 / 23
p0 907 ± 0.9918
p1 0.5042 ± 4.537e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

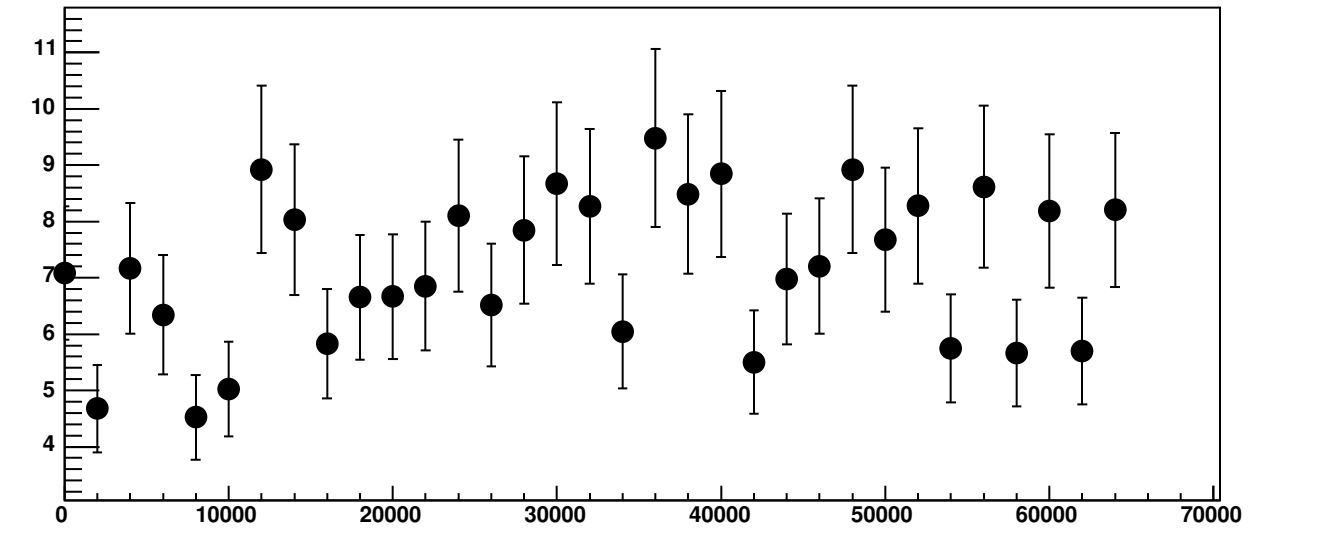

Chip 0, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
0
10000
20000
30000
40000
50000
60000
70000

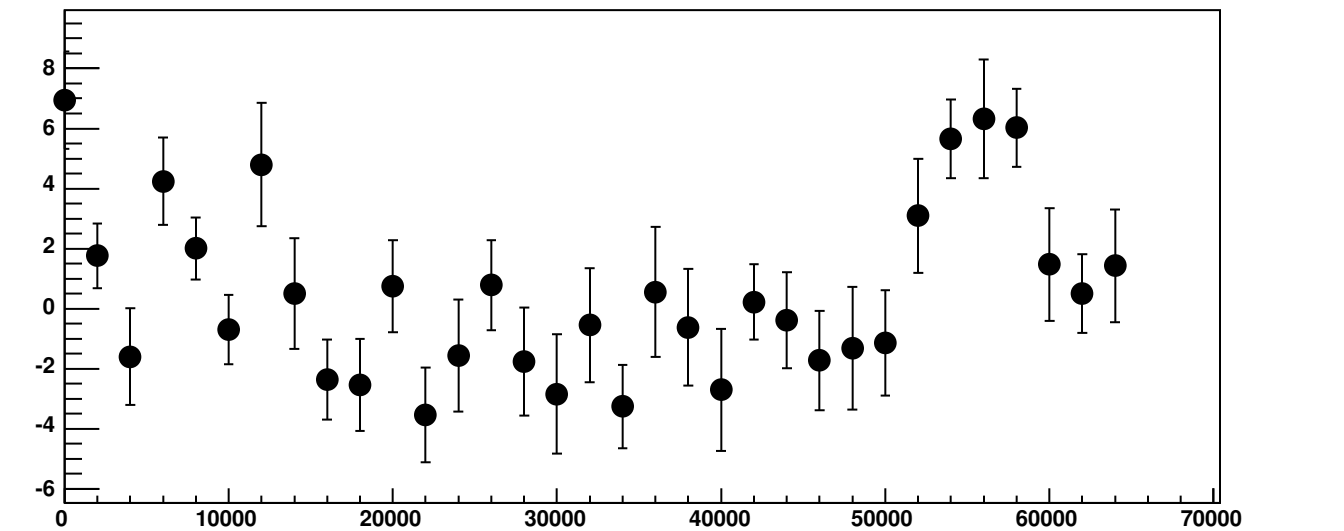
Chip 0, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC



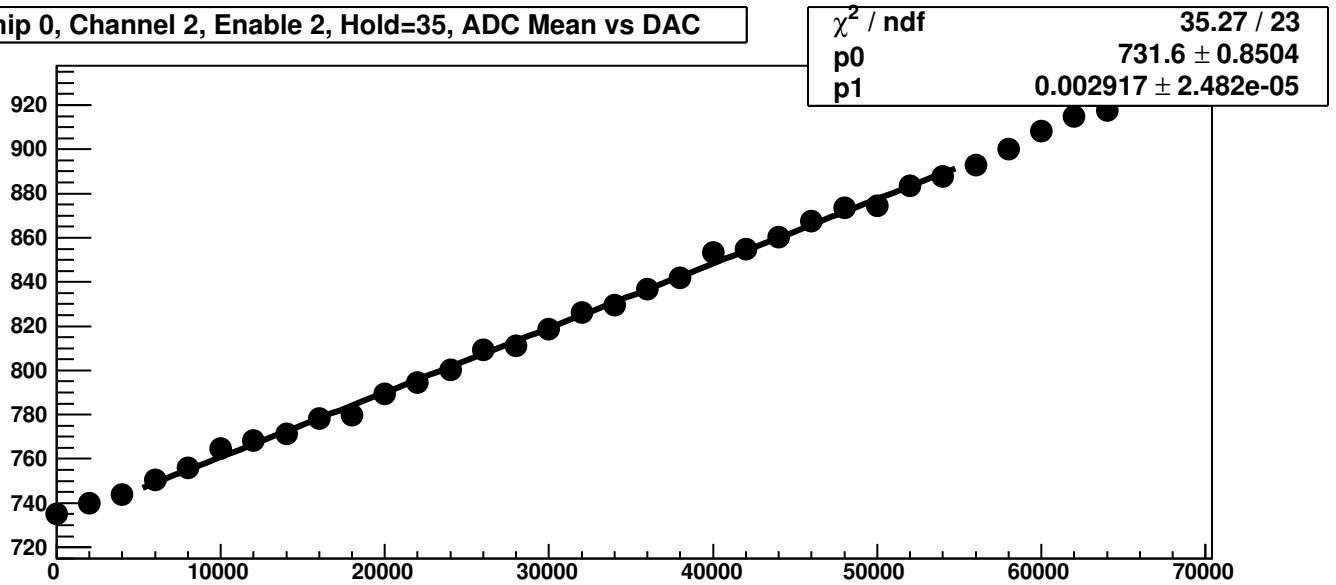
Chip 0, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC



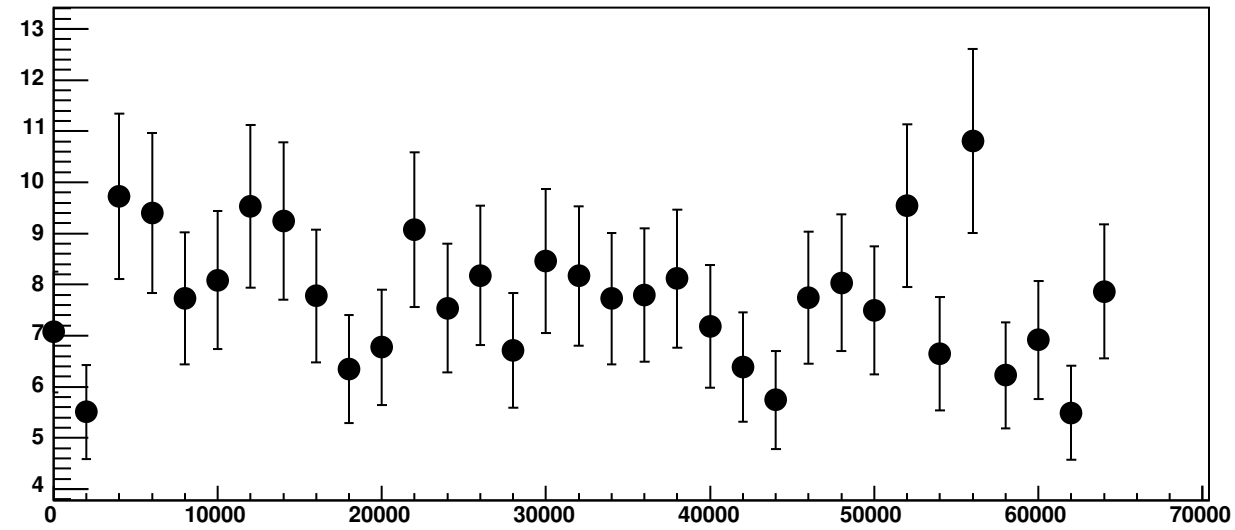
Chip 0, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC



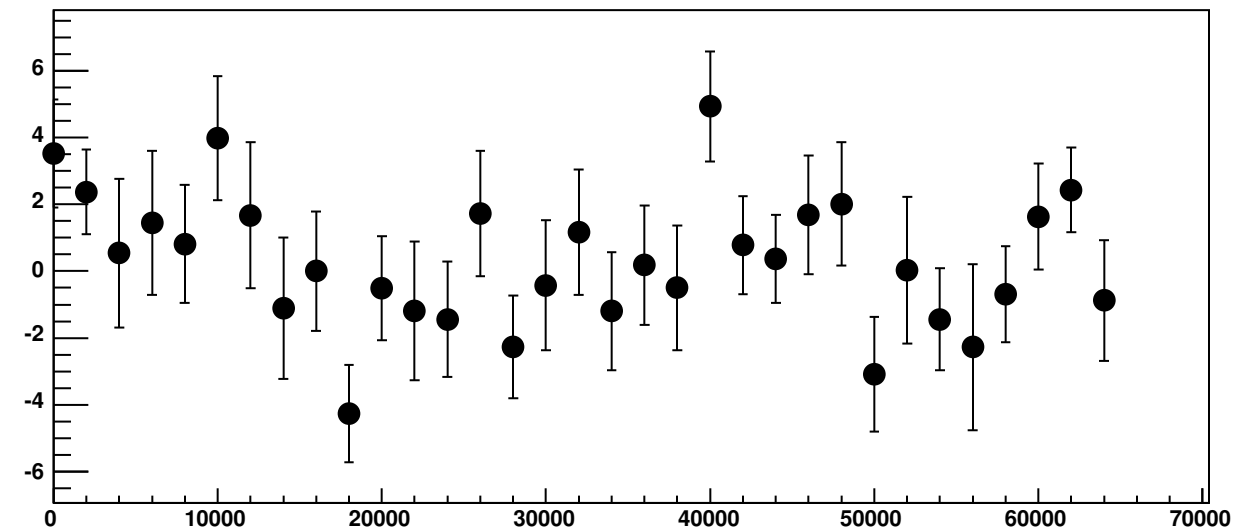
Chip 0, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC

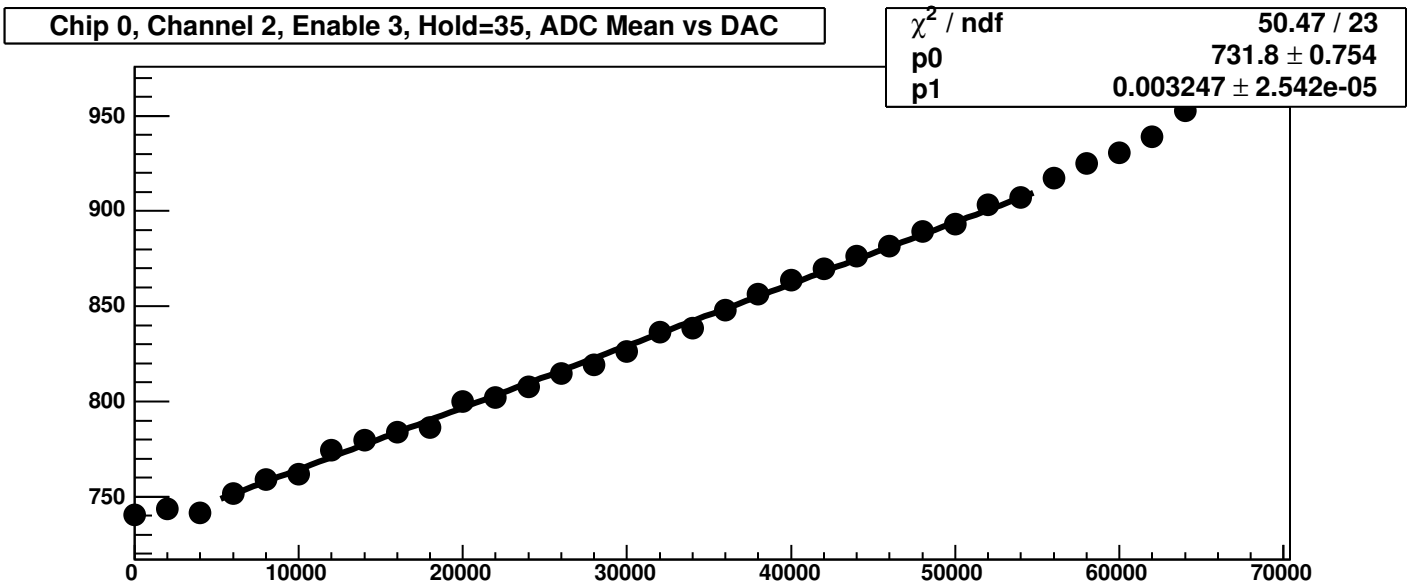


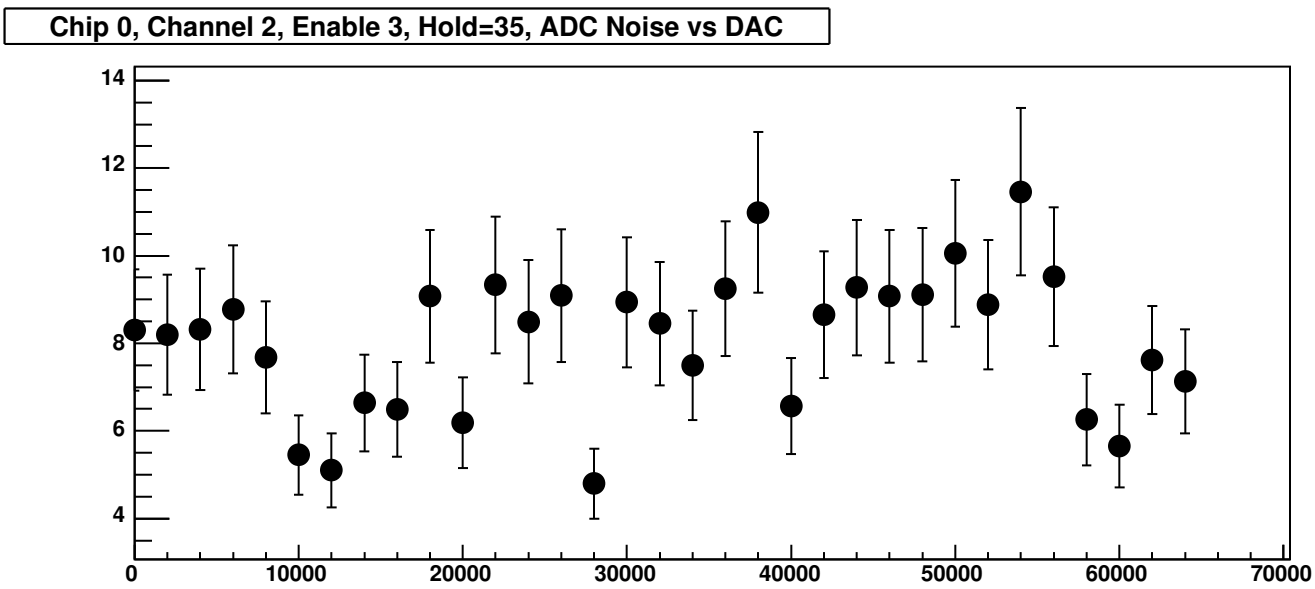
Chip 0, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC

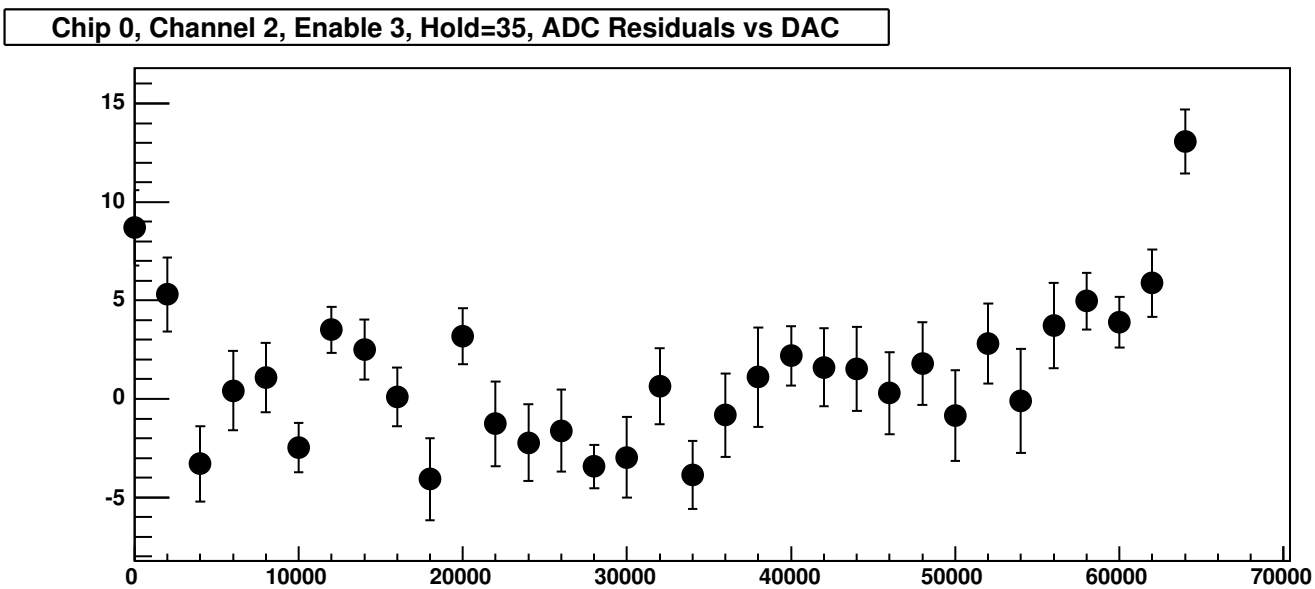


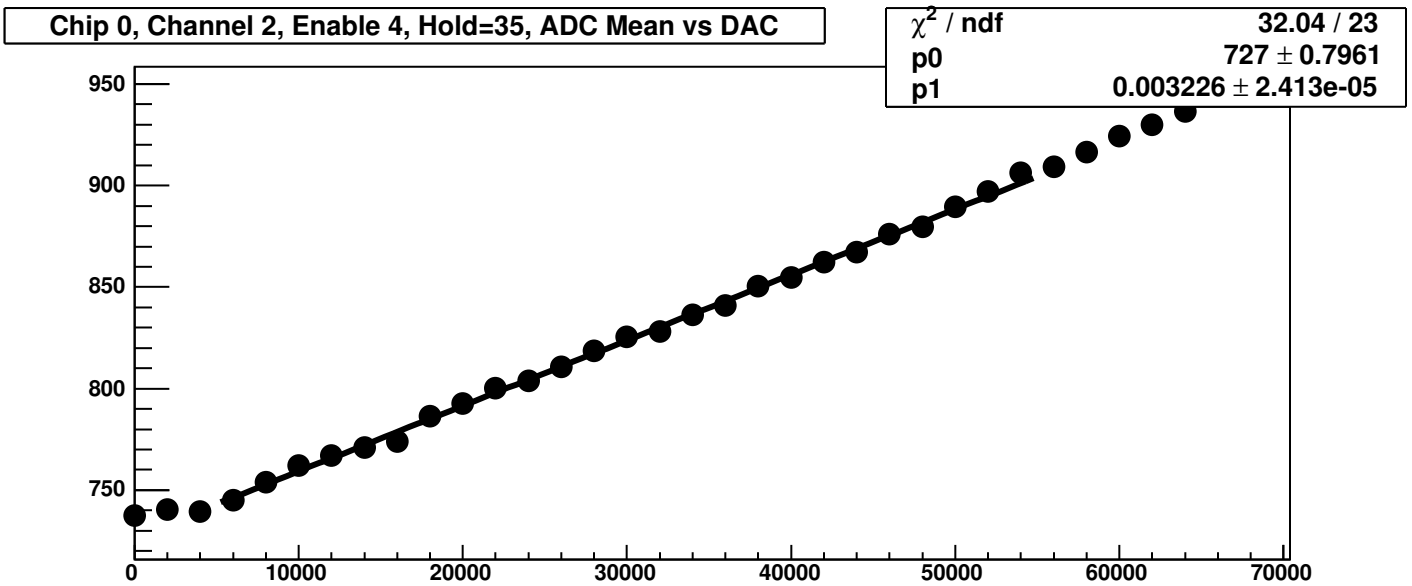
Chip 0, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC

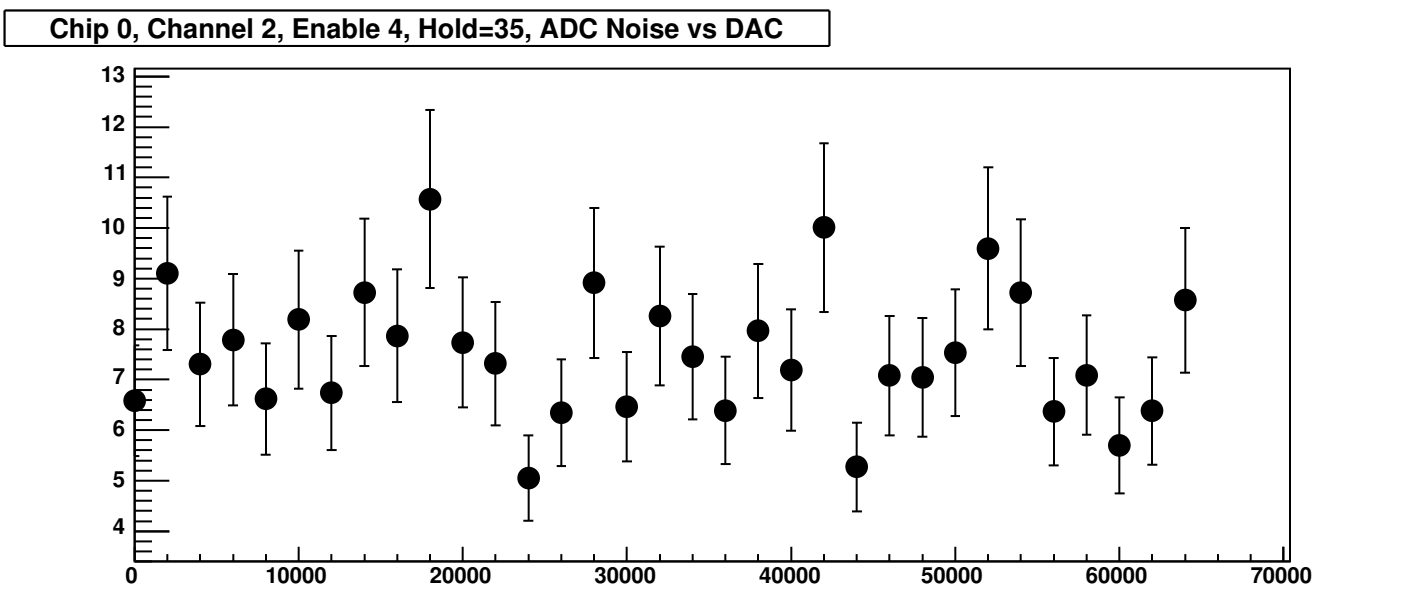


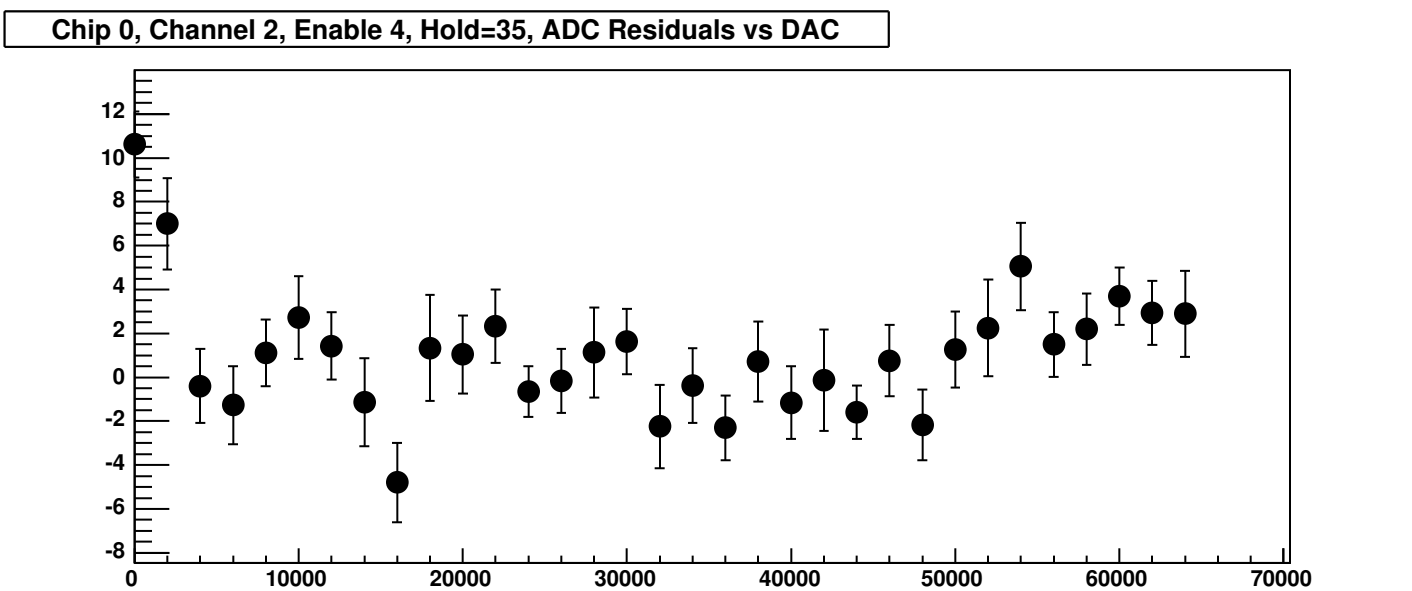

Chip 0, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 50.47 / 23
p0 731.8 ± 0.754
p1 0.003247 ± 2.542e-05
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000
70000

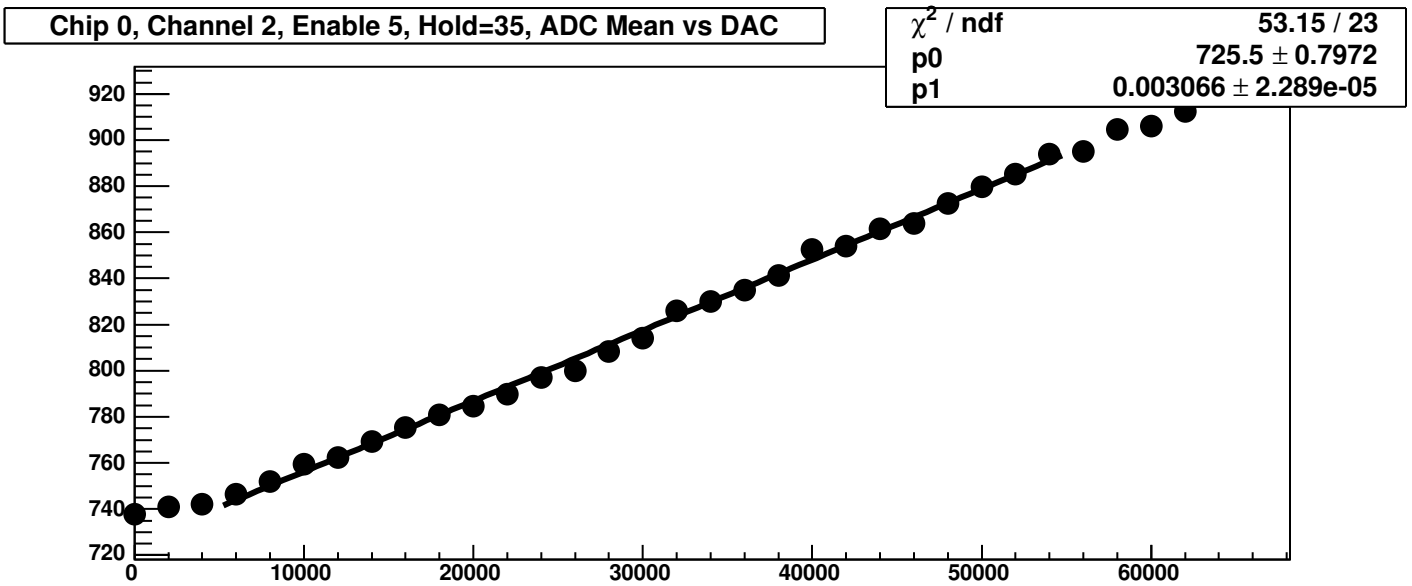

Chip 0, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

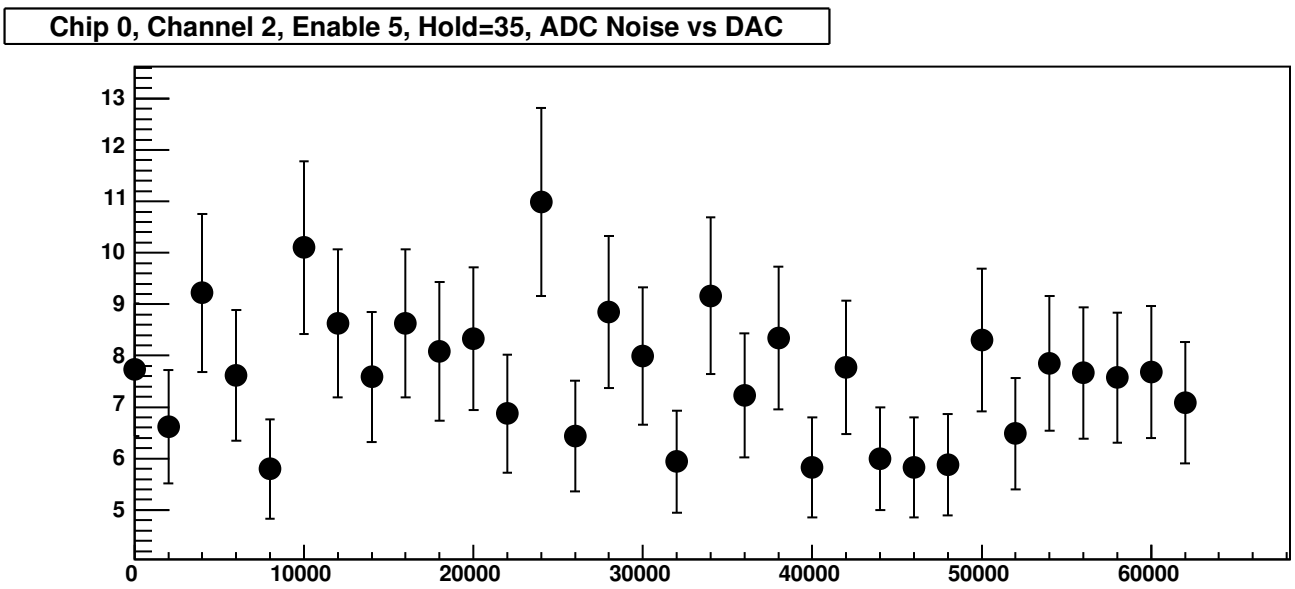

Chip 0, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

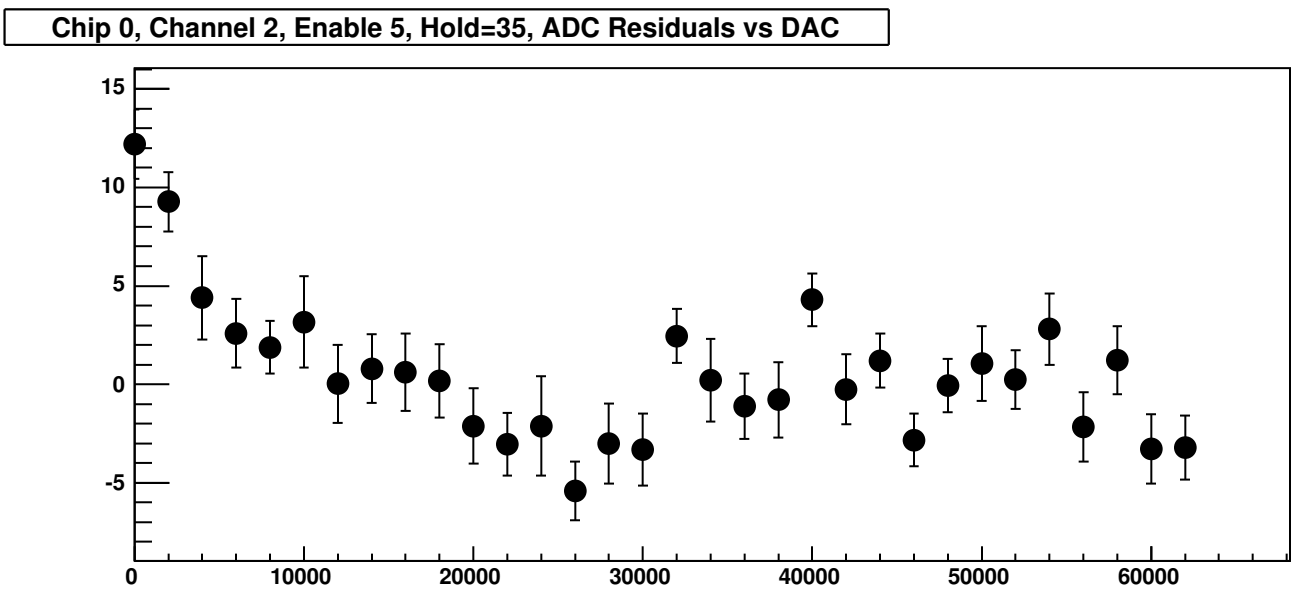

Chip 0, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 32.04 / 23
p0 727 ± 0.7961
p1 0.003226 ± 2.413e-05
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000
70000

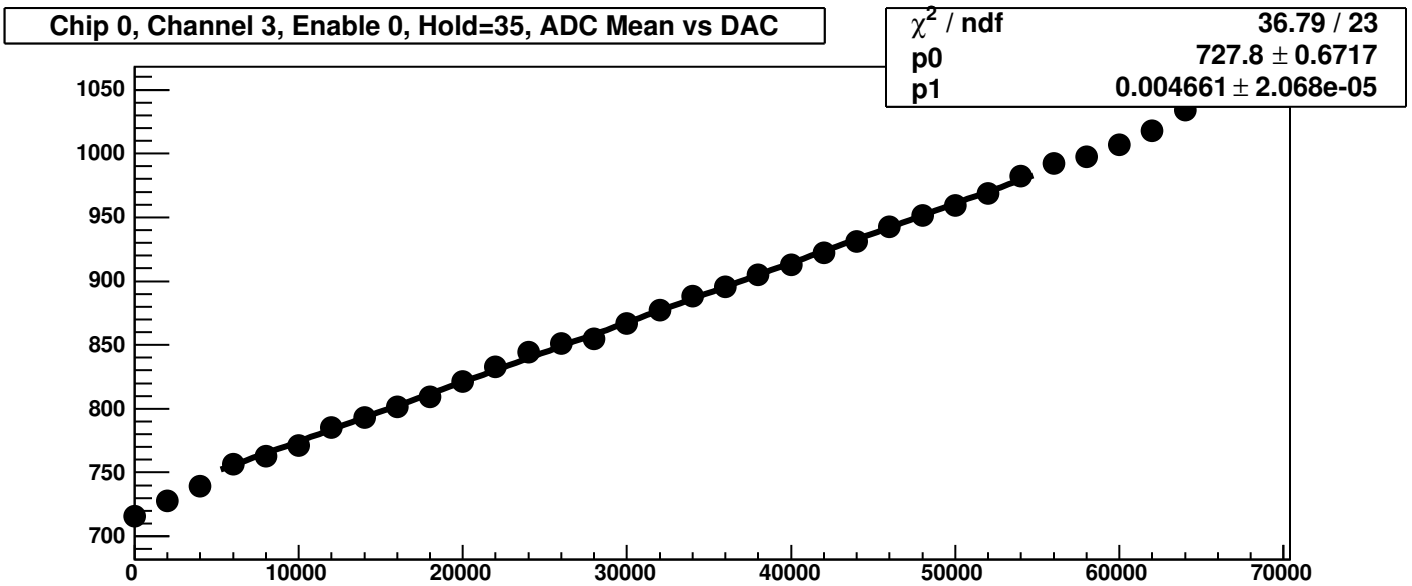

Chip 0, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

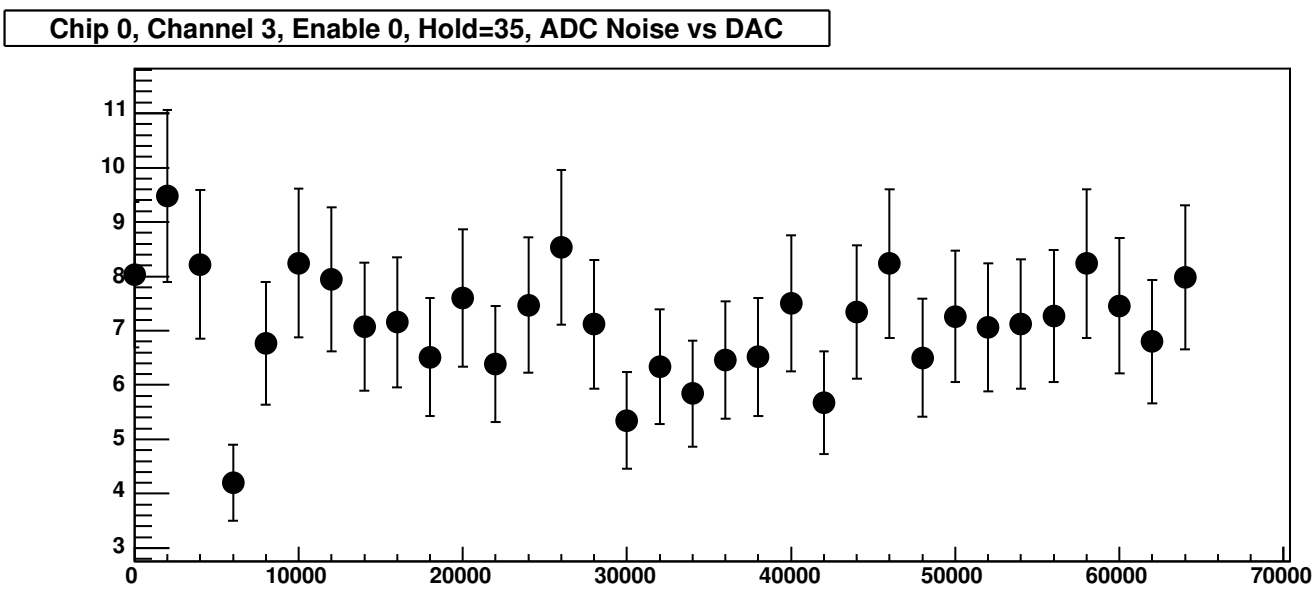

Chip 0, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

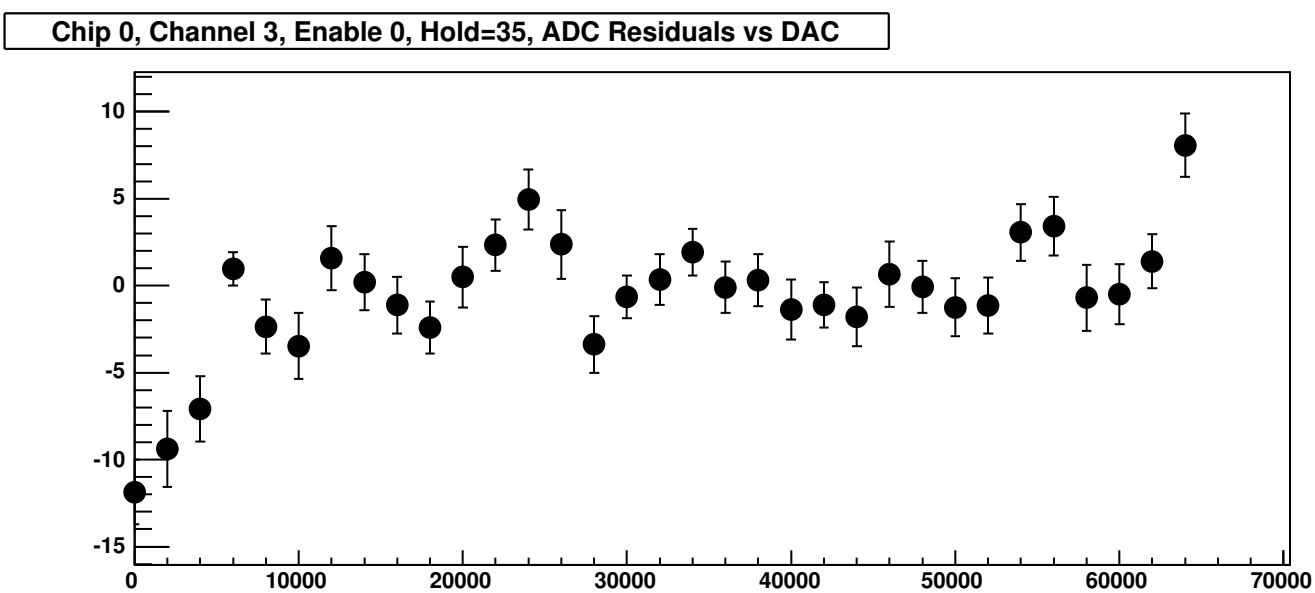

Chip 0, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 53.15 / 23
p0 725.5 ± 0.7972
p1 0.003066 ± 2.289e-05
920
900
880
860
840
820
800
780
760
740
720
0
10000
20000
30000
40000
50000
60000

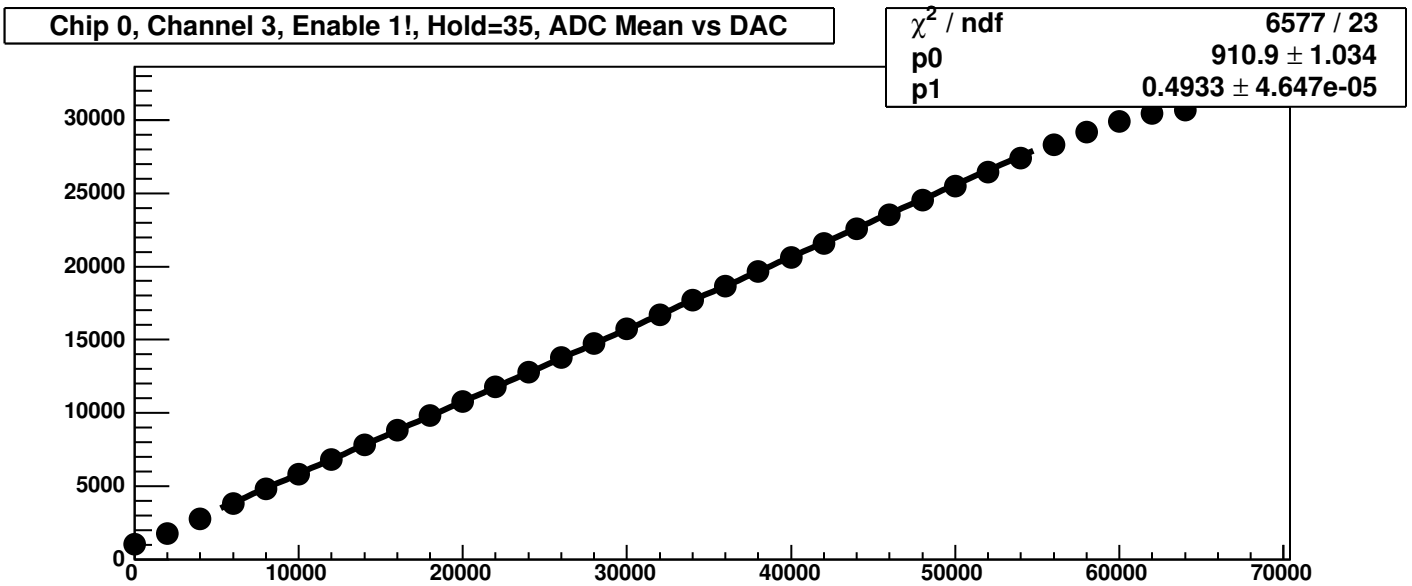

Chip 0, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000

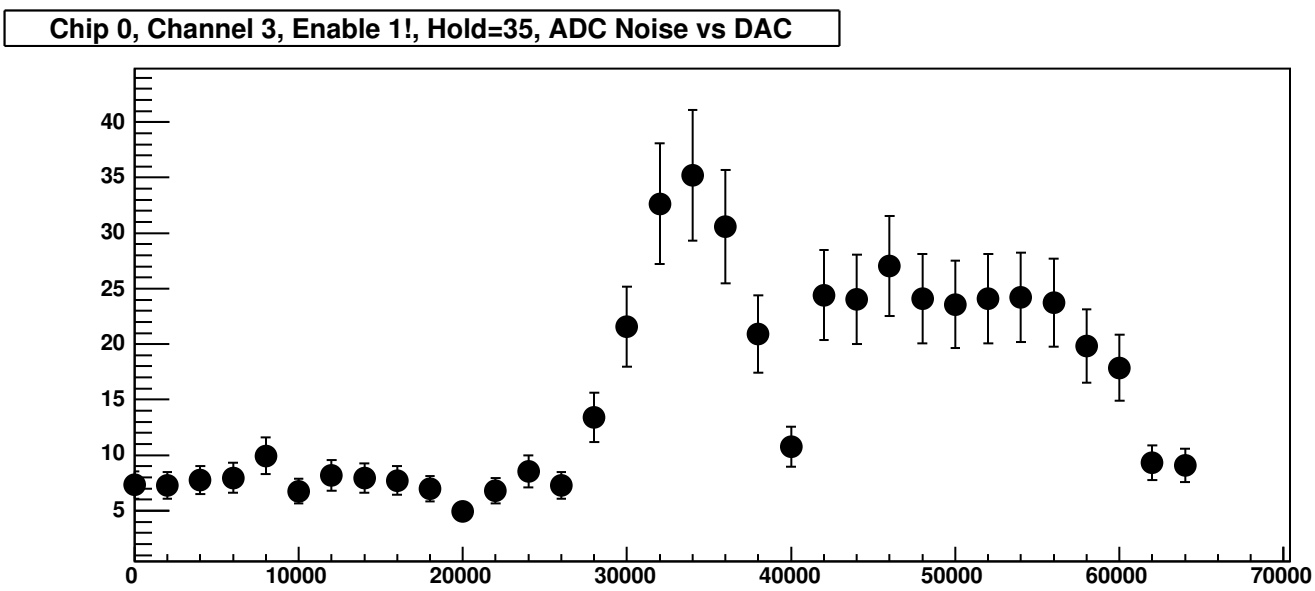

Chip 0, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000

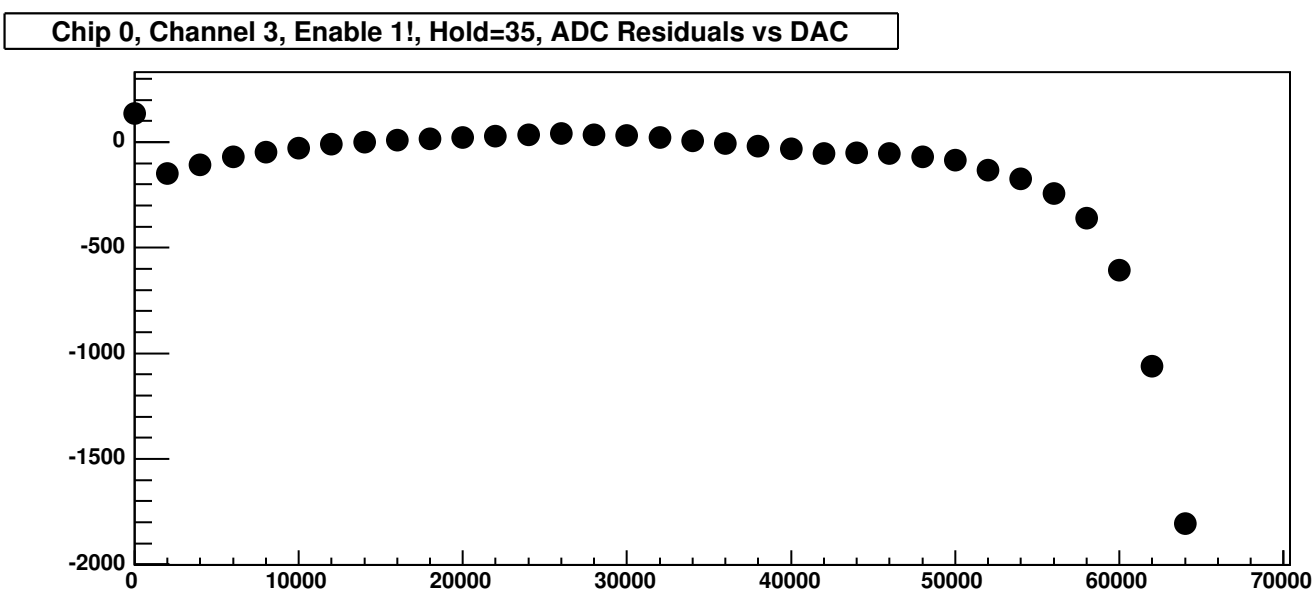

Chip 0, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 36.79 / 23
p0 727.8 ± 0.6717
p1 0.004661 ± 2.068e-05
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000
70000

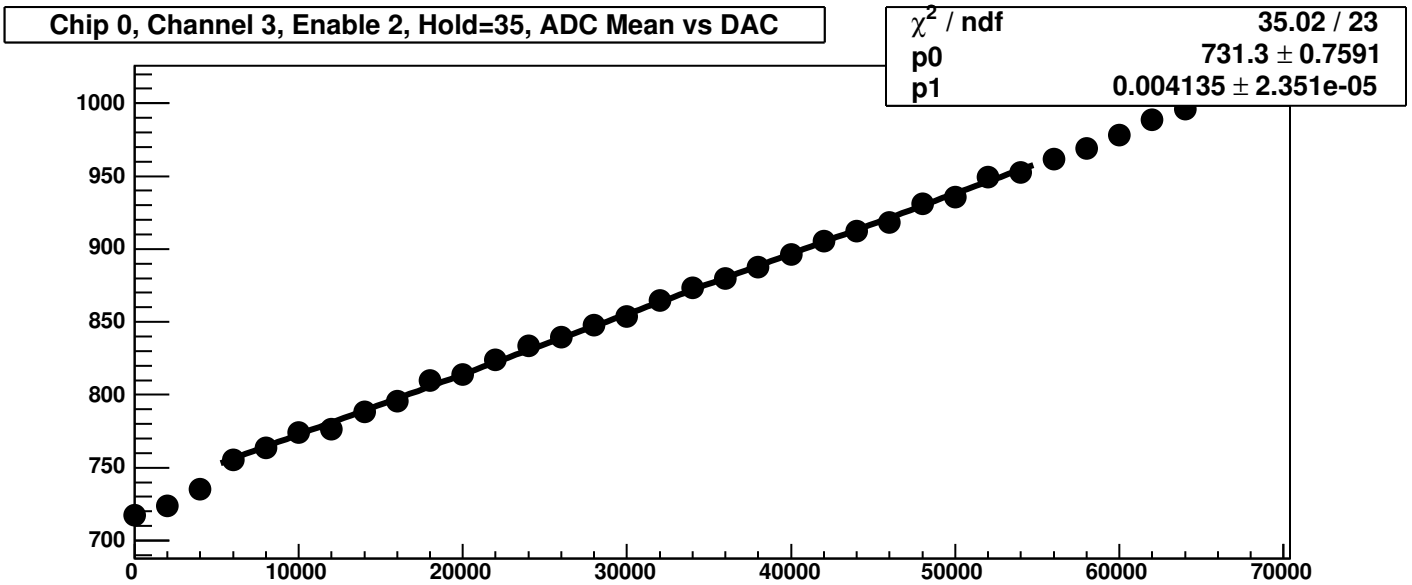

Chip 0, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

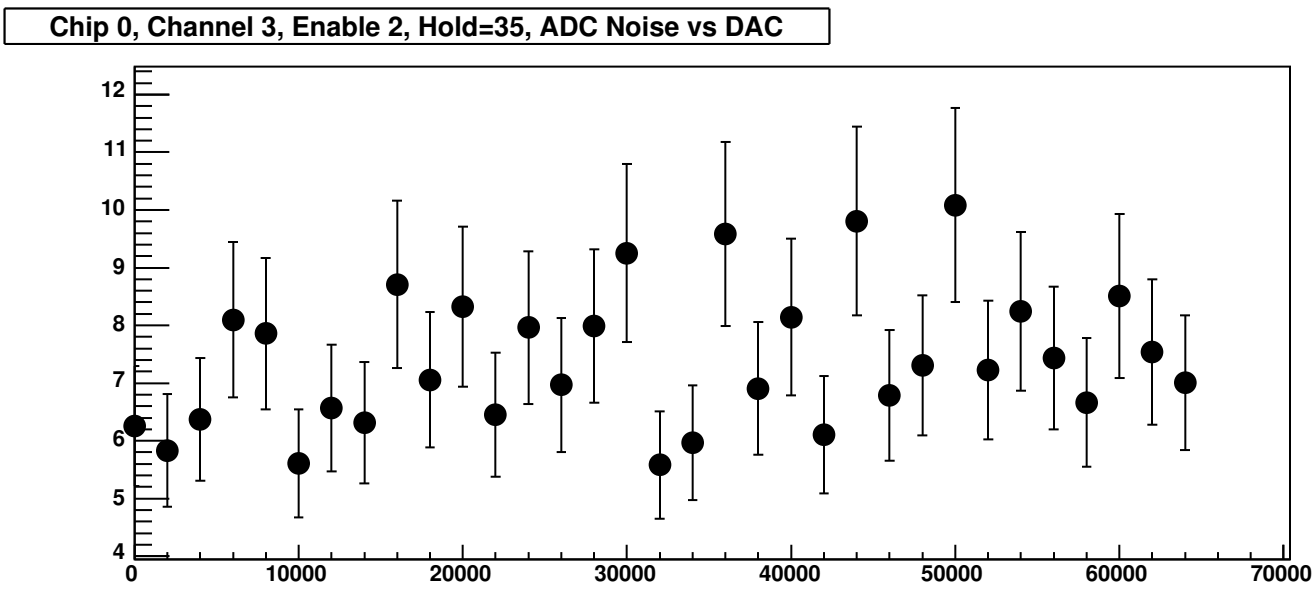

Chip 0, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

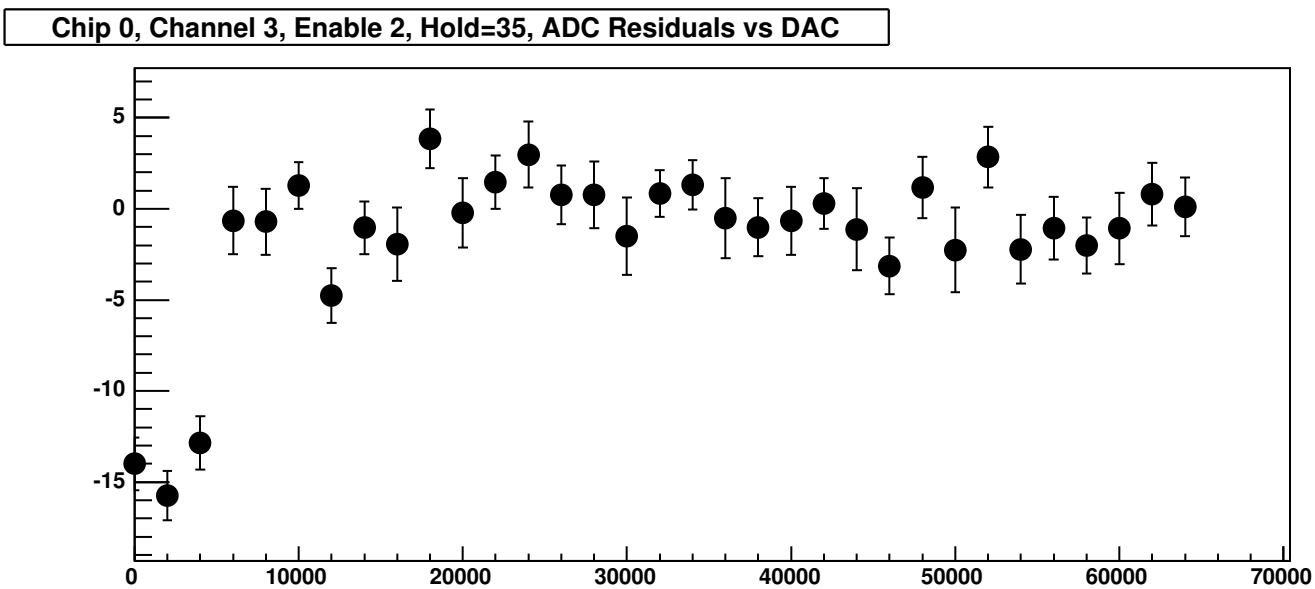

Chip 0, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 6577 / 23
p0 910.9 ± 1.034
p1 0.4933 ± 4.647e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

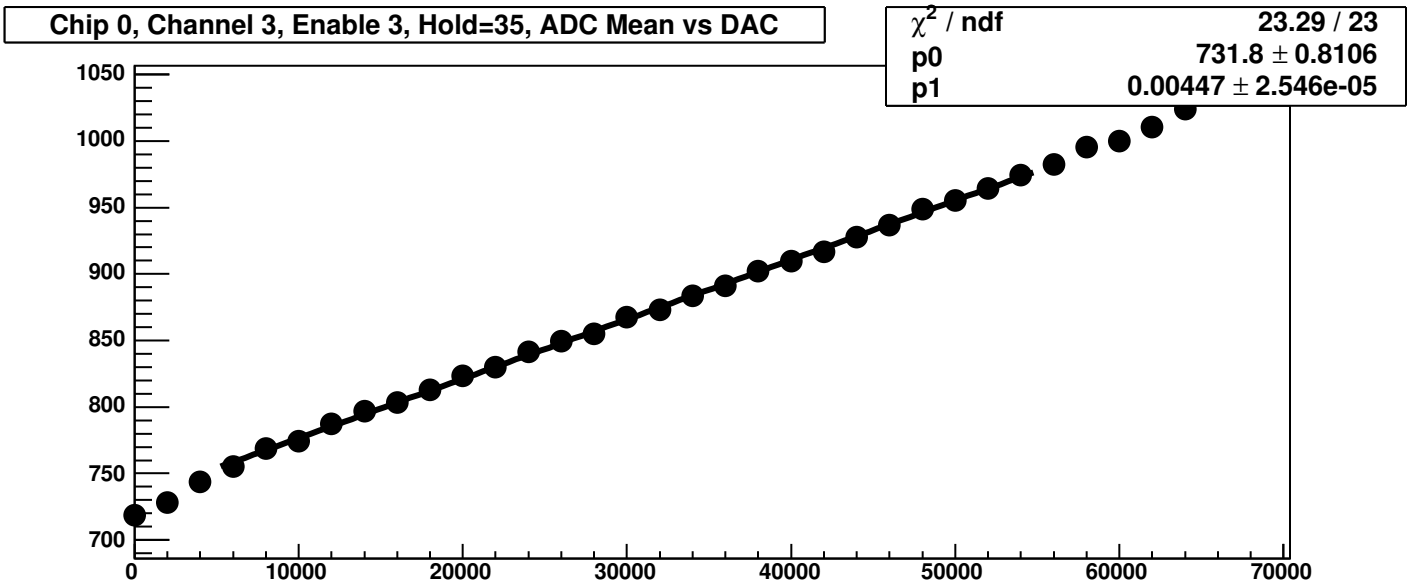

Chip 0, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000

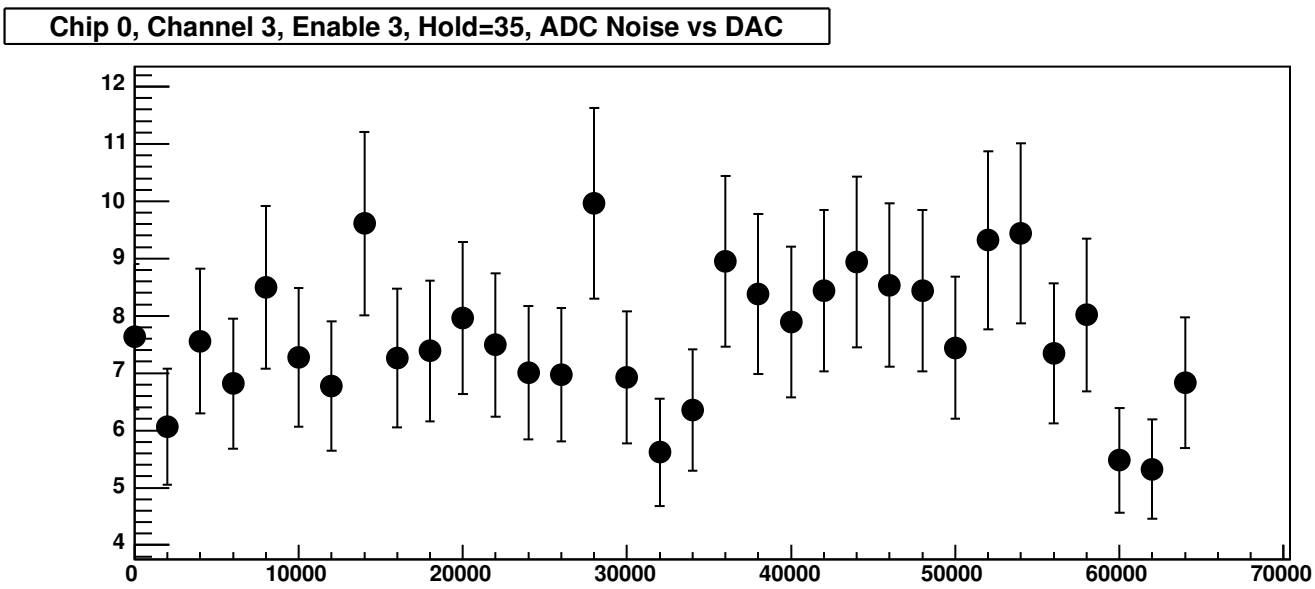

Chip 0, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000

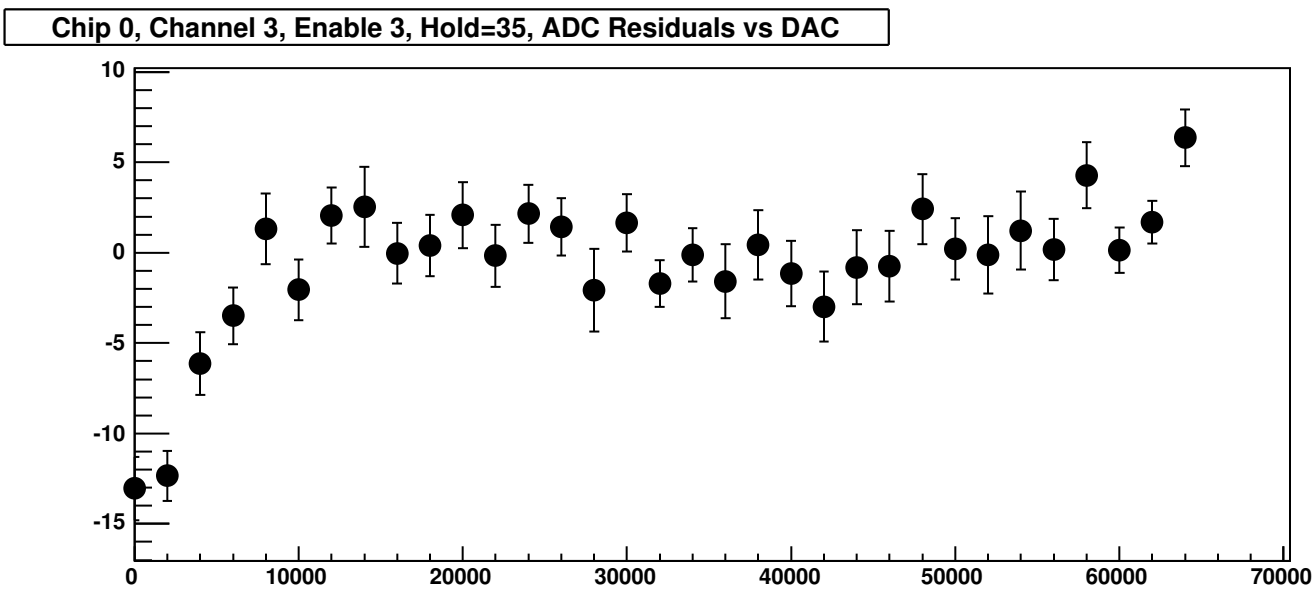

Chip 0, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 35.02 / 23
p0 731.3 ± 0.7591
p1 0.004135 ± 2.351e-05
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000
70000

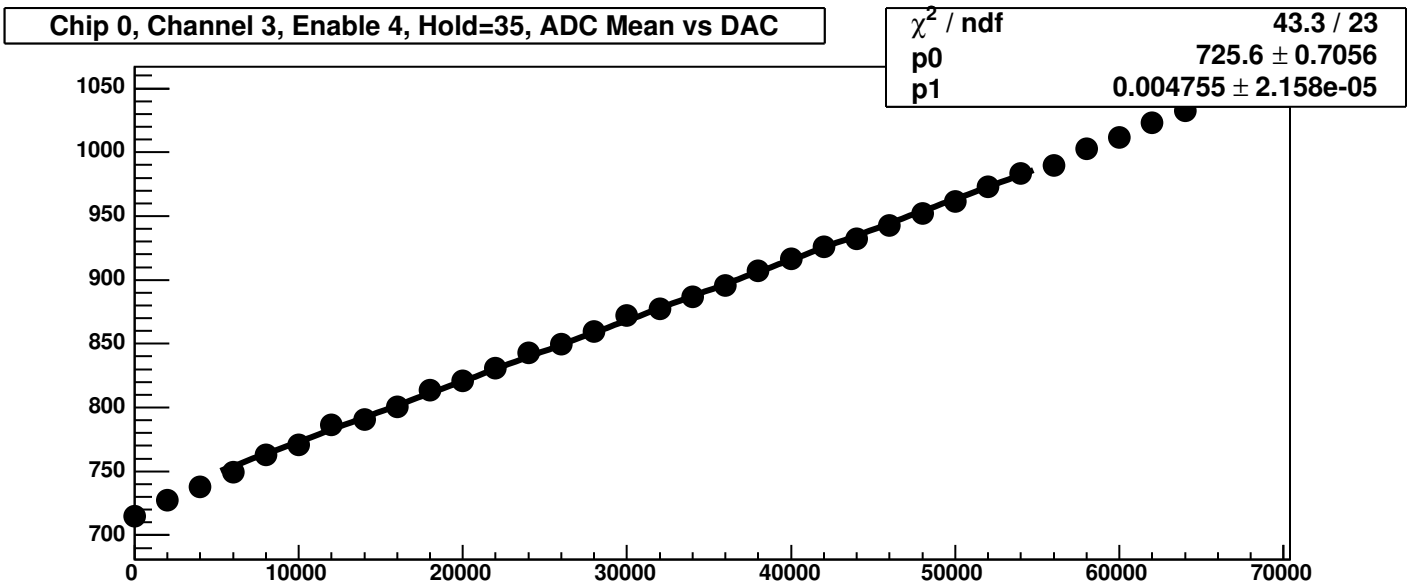

Chip 0, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

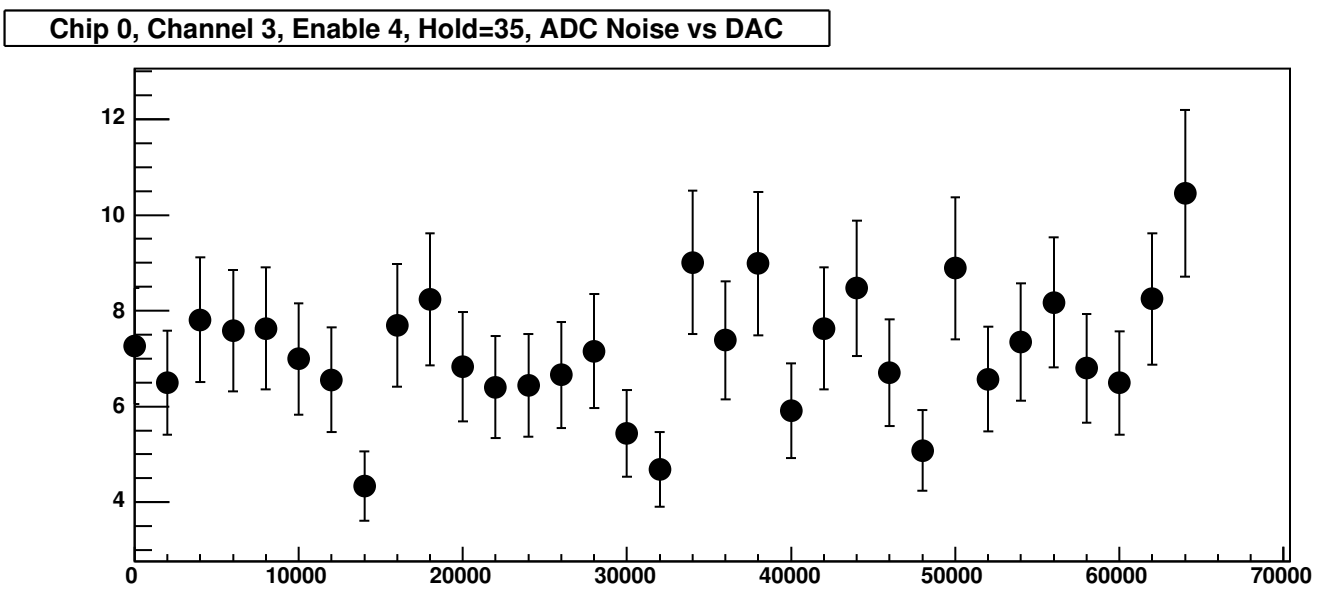

Chip 0, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

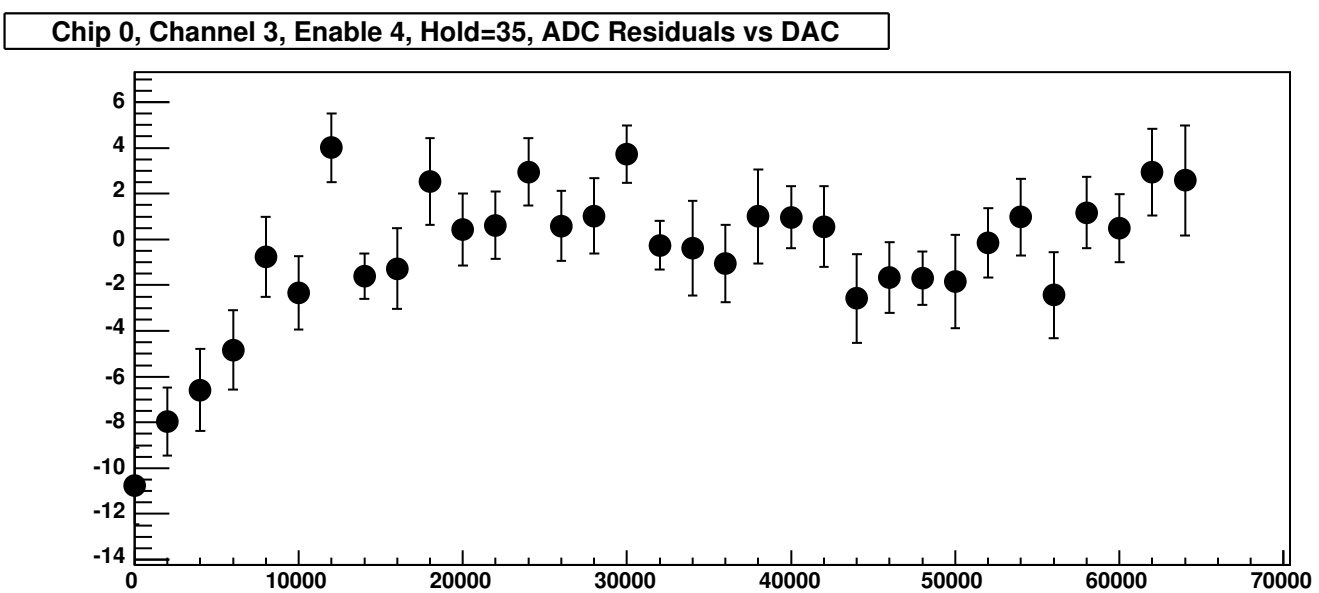

Chip 0, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.29 / 23
p0 731.8 ± 0.8106
p1 0.00447 ± 2.546e-05
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000
70000

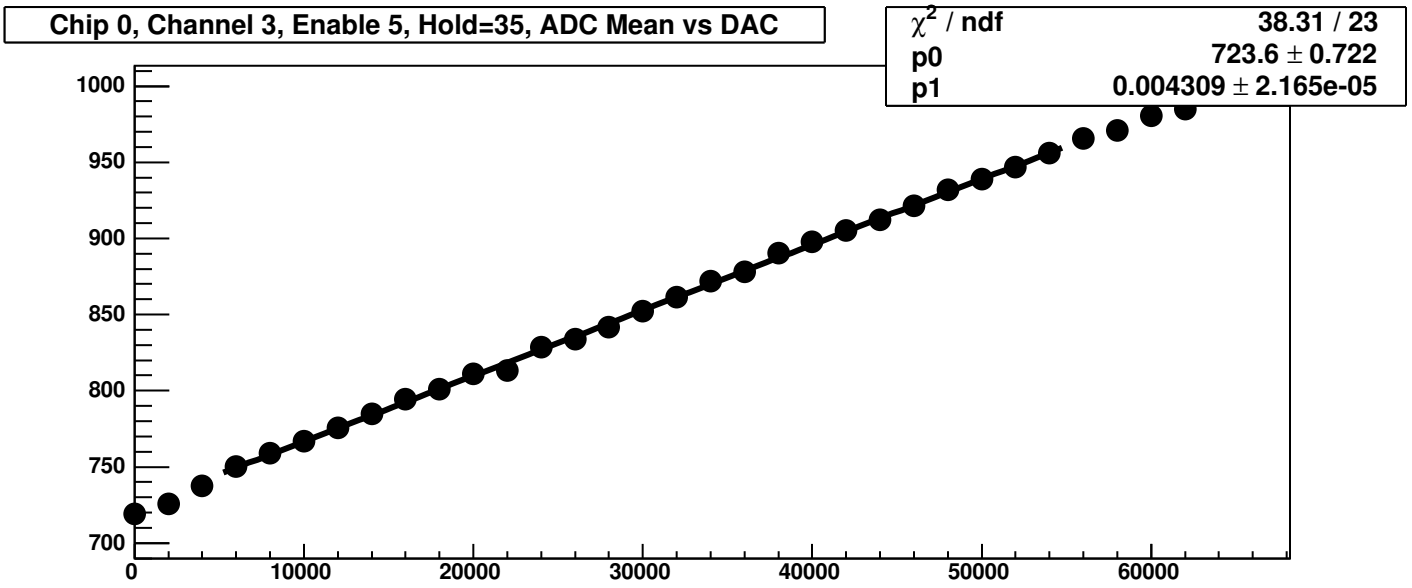

Chip 0, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

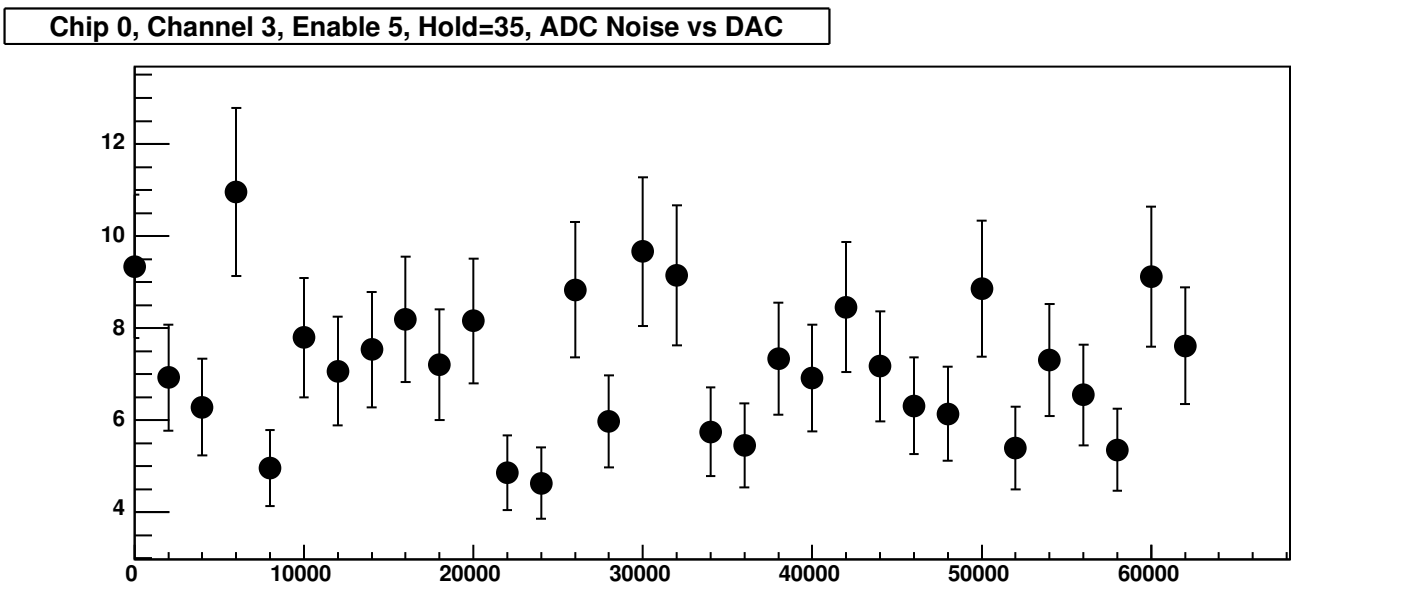

Chip 0, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

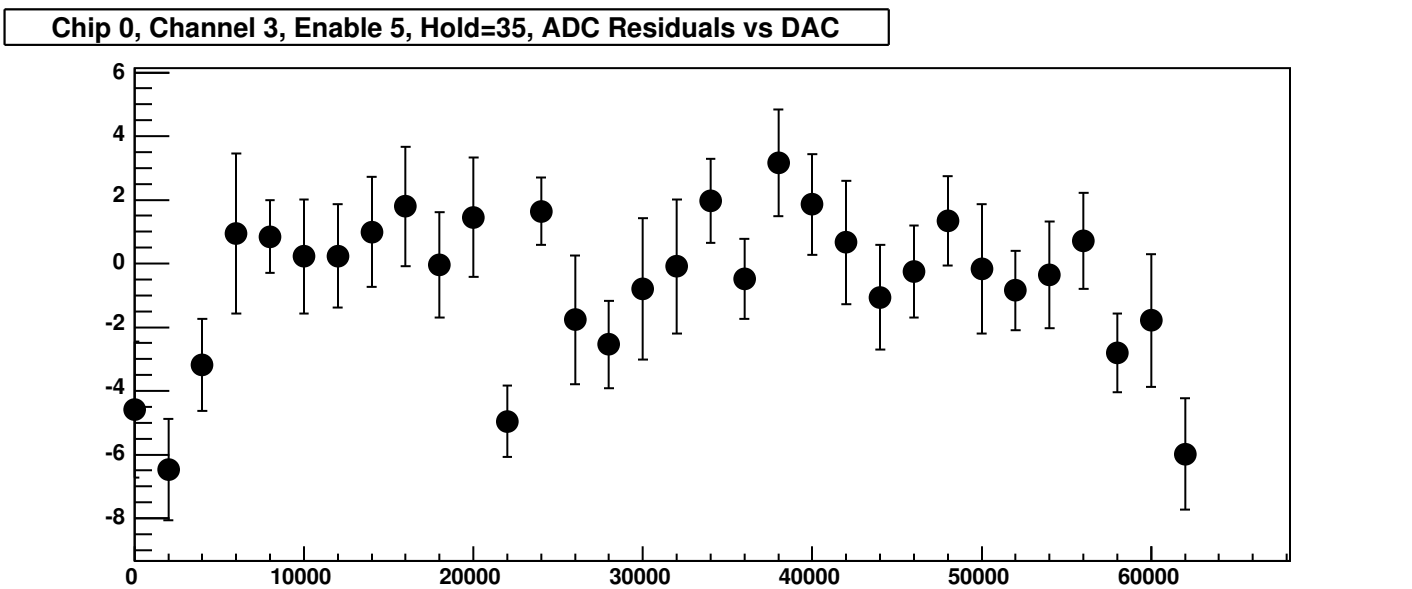

Chip 0, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 43.3 / 23
p0 725.6 ± 0.7056
p1 0.004755 ± 2.158e-05
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000
70000

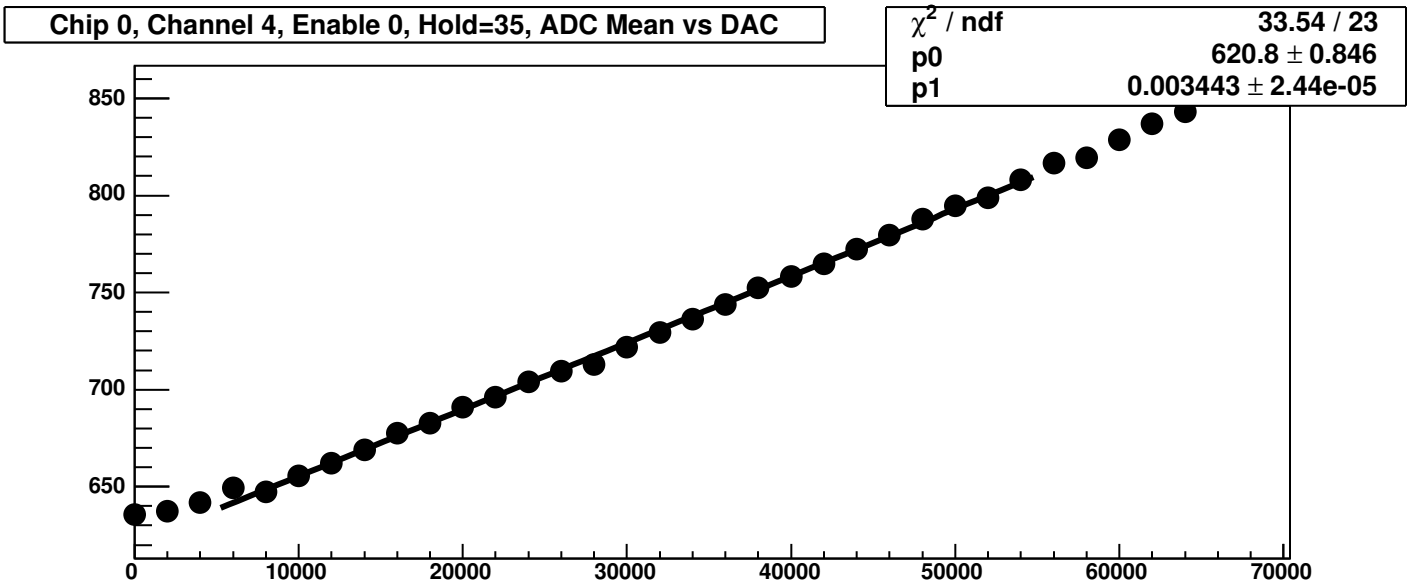

Chip 0, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

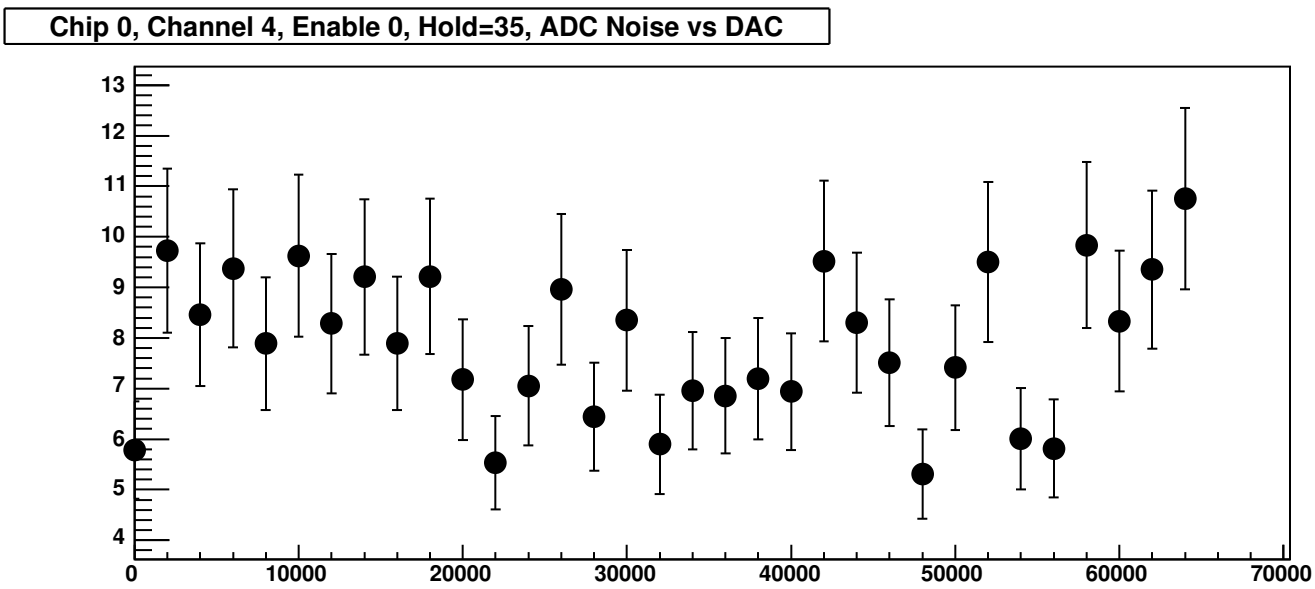

Chip 0, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
-14
0
10000
20000
30000
40000
50000
60000
70000

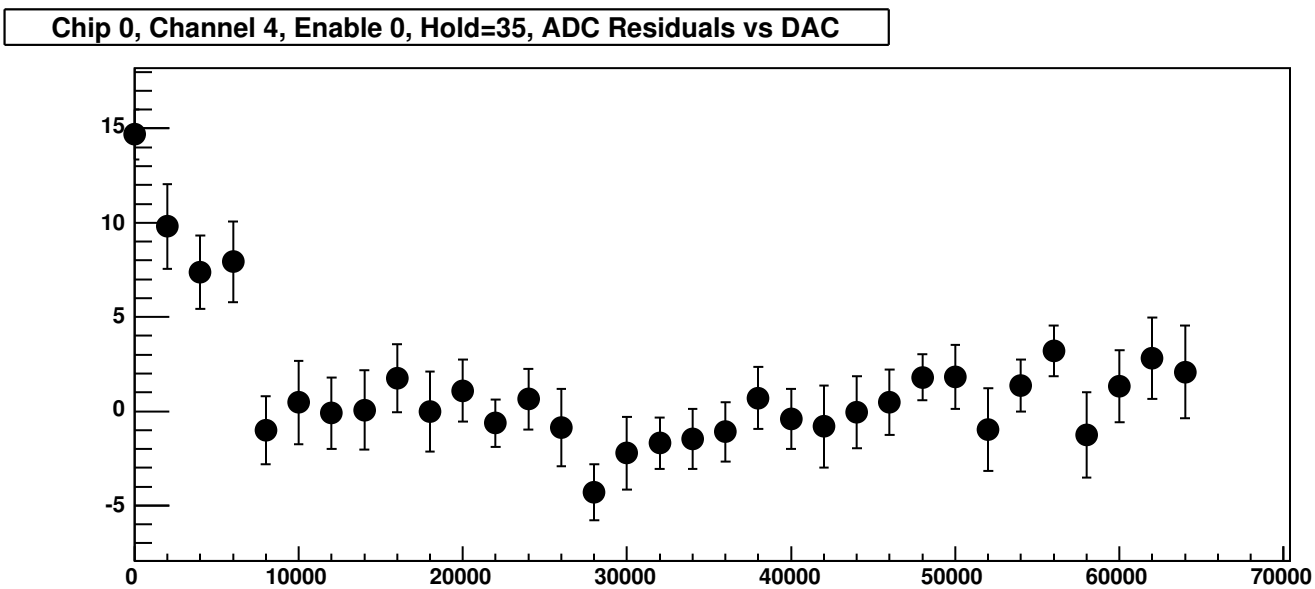

Chip 0, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 38.31 / 23
p0 723.6 ± 0.722
p1 0.004309 ± 2.165e-05
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000

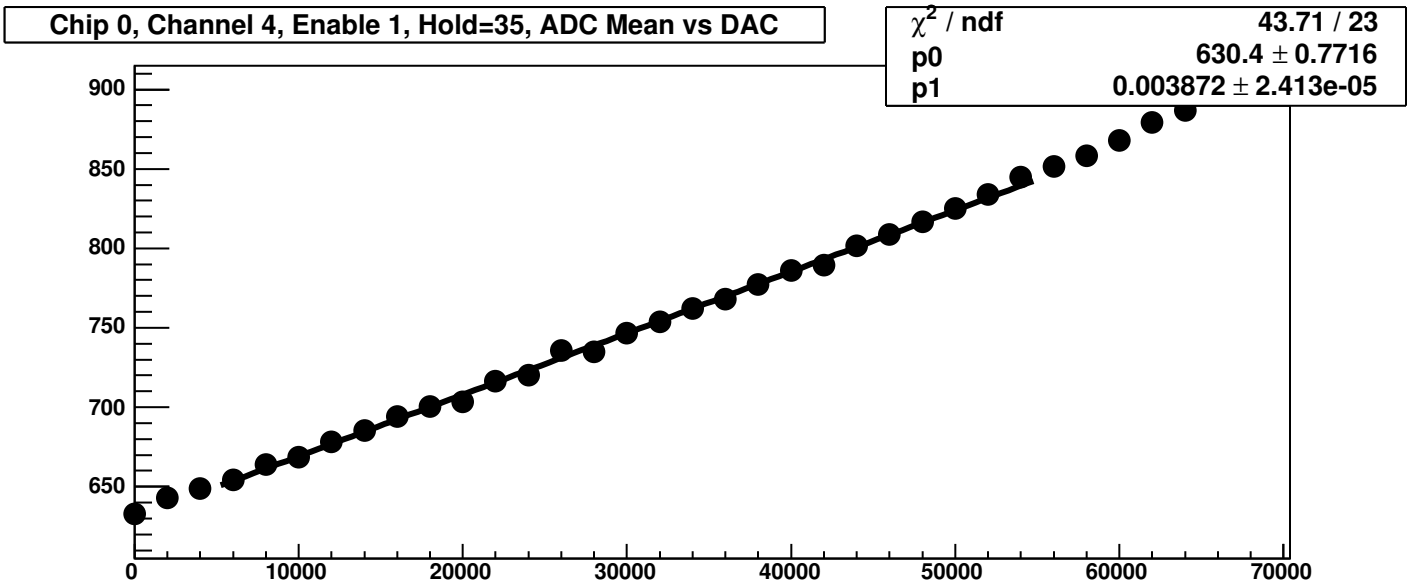

Chip 0, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000

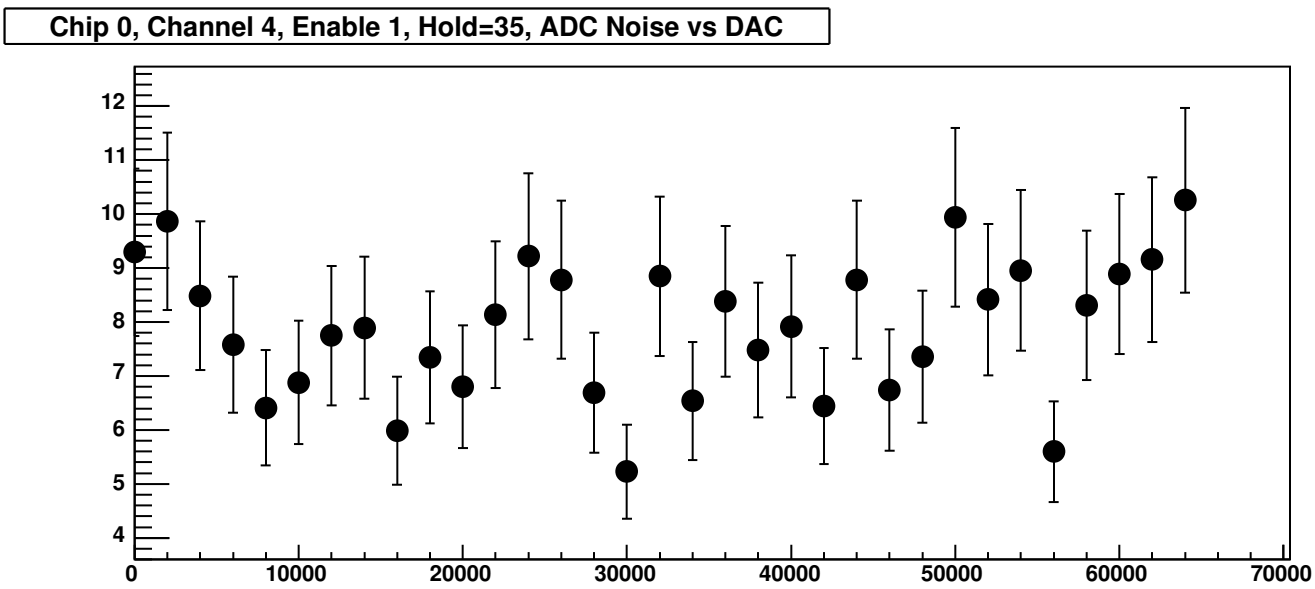

Chip 0, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000

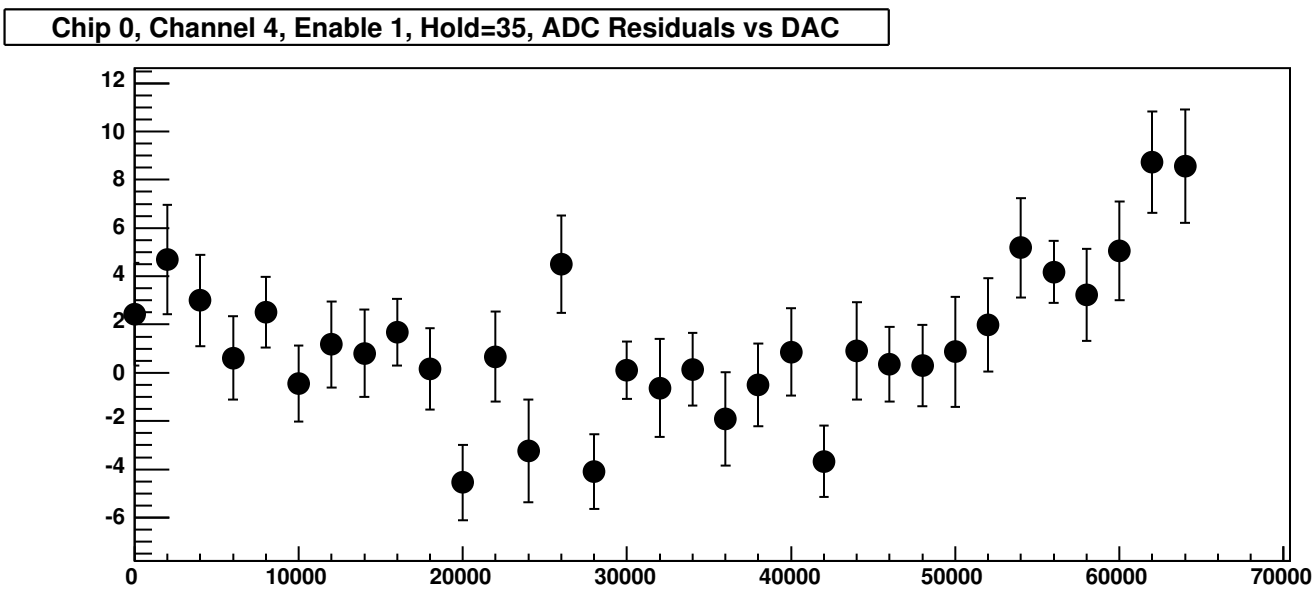

Chip 0, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 33.54 / 23
p0 620.8 ± 0.846
p1 0.003443 ± 2.44e-05
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000
70000

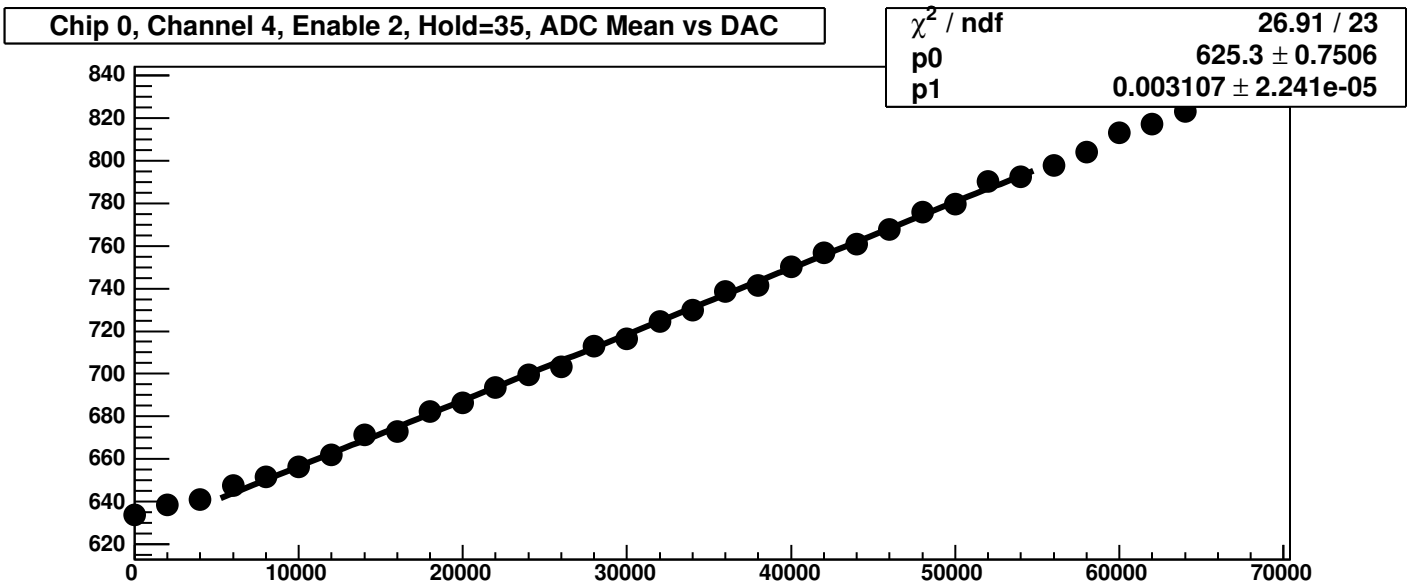

Chip 0, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

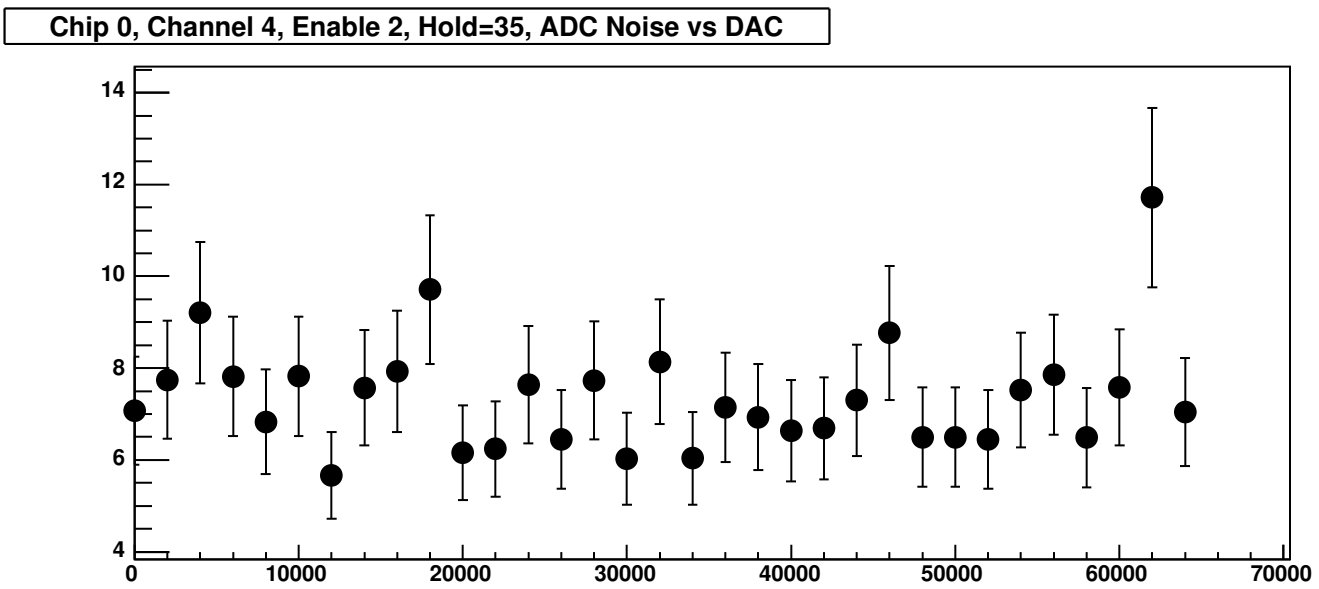

Chip 0, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

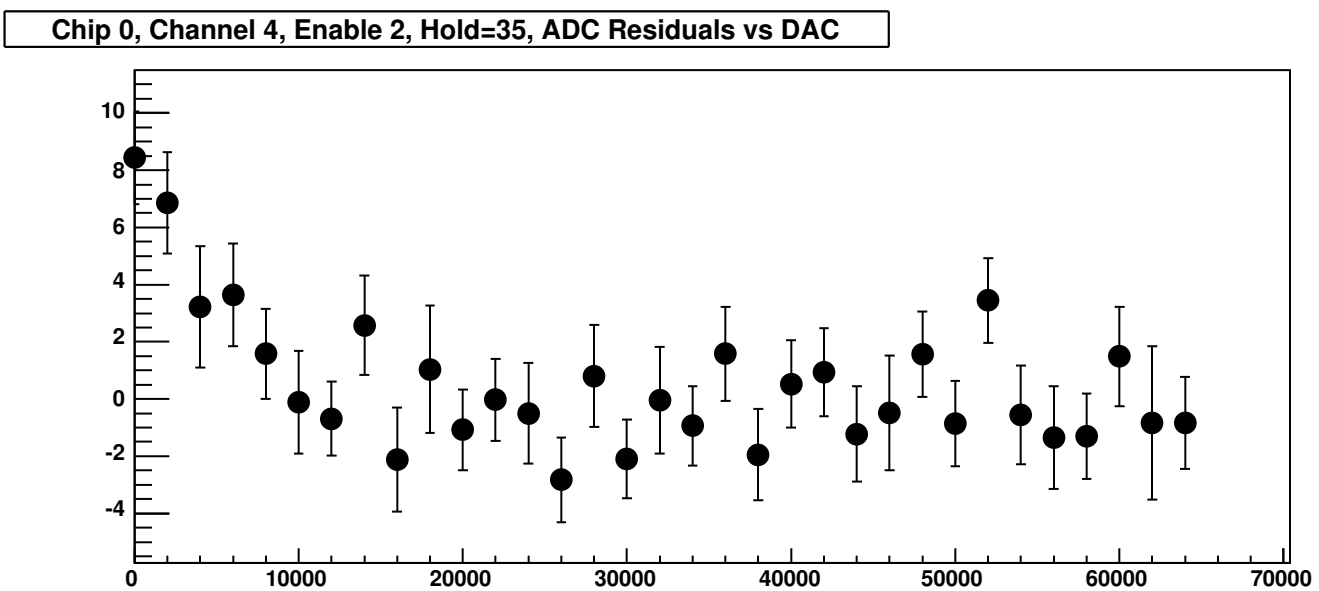

Chip 0, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 43.71 / 23
p0 630.4 ± 0.7716
p1 0.003872 ± 2.413e-05
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000
70000

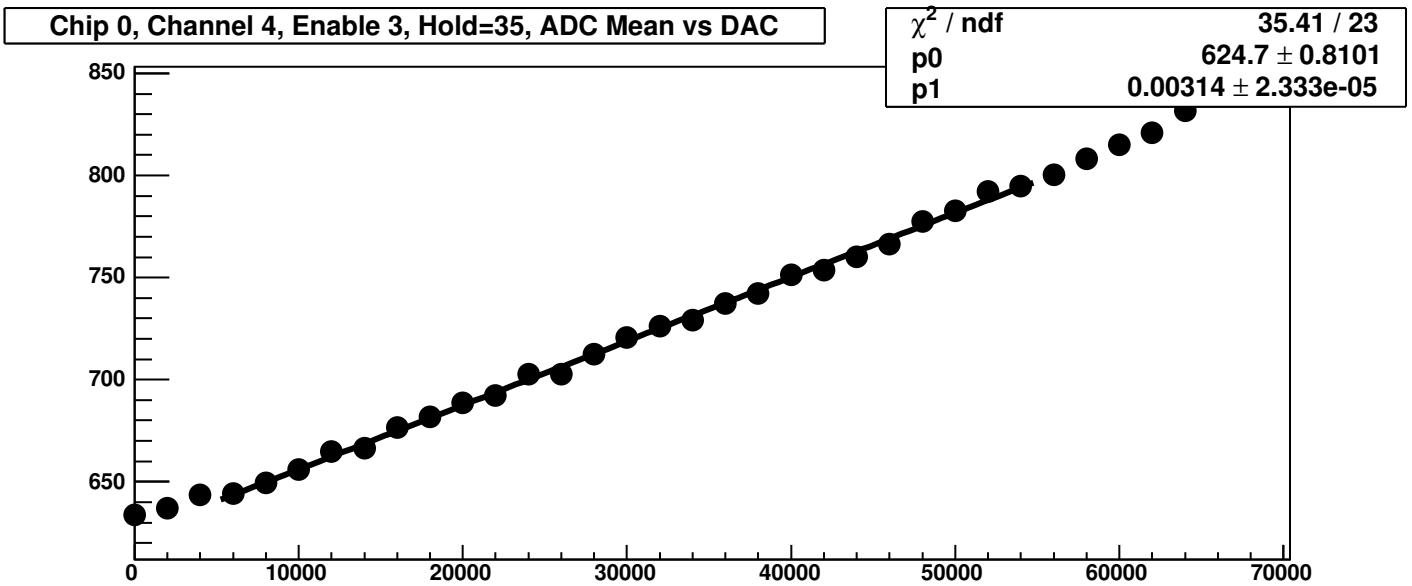

Chip 0, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

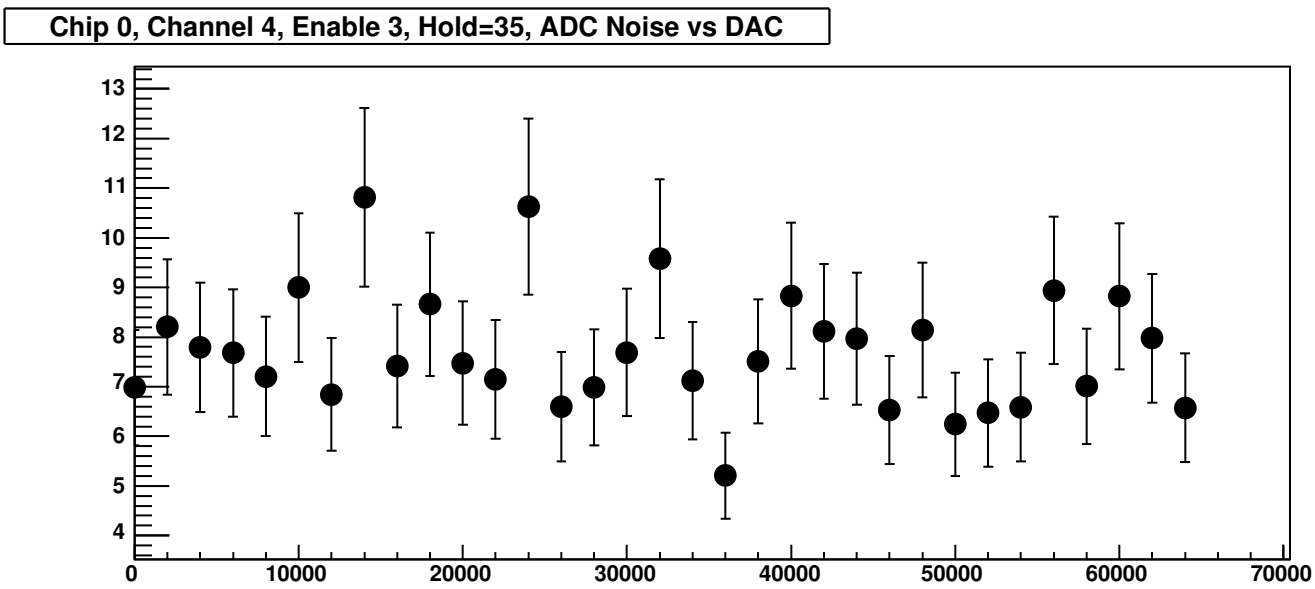

Chip 0, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

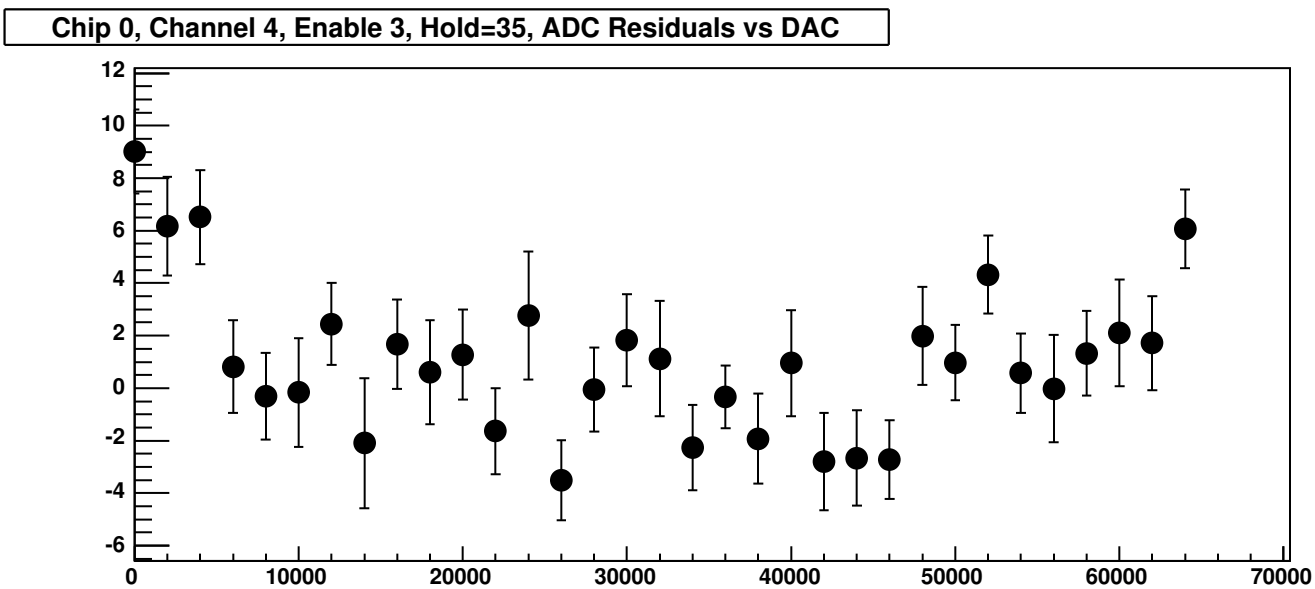

Chip 0, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 26.91 / 23
p0 625.3 ± 0.7506
p1 0.003107 ± 2.241e-05
840
820
800
780
760
740
720
700
680
660
640
620
0
10000
20000
30000
40000
50000
60000
70000

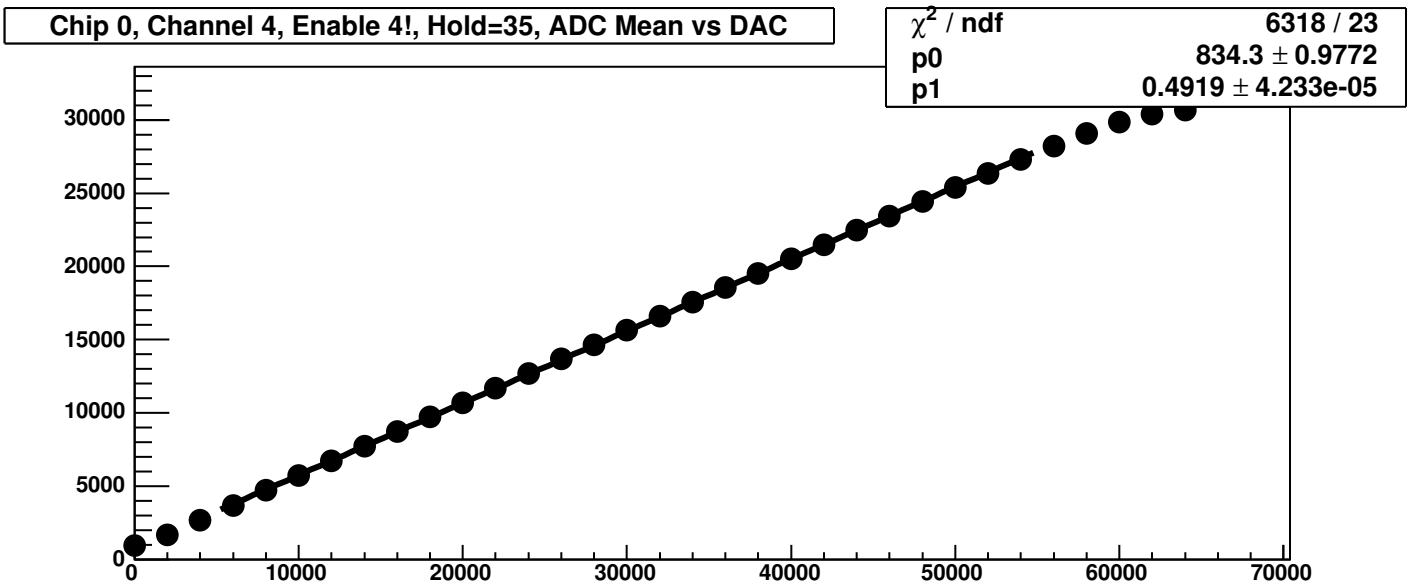

Chip 0, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

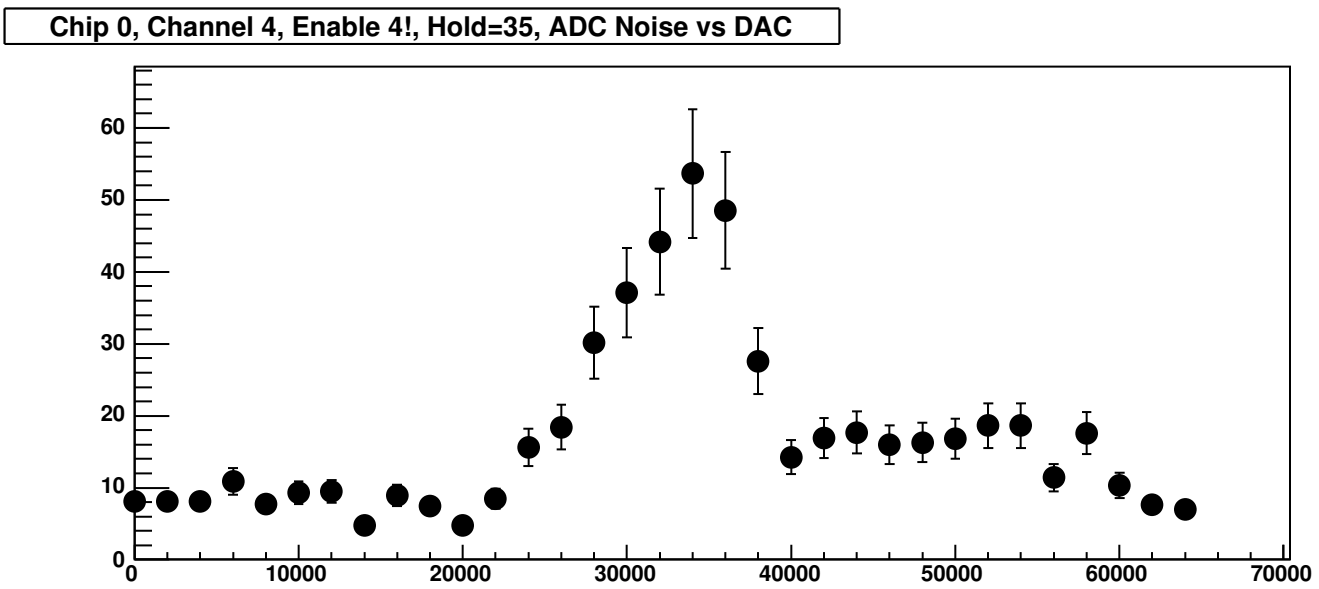

Chip 0, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
0
10000
20000
30000
40000
50000
60000
70000

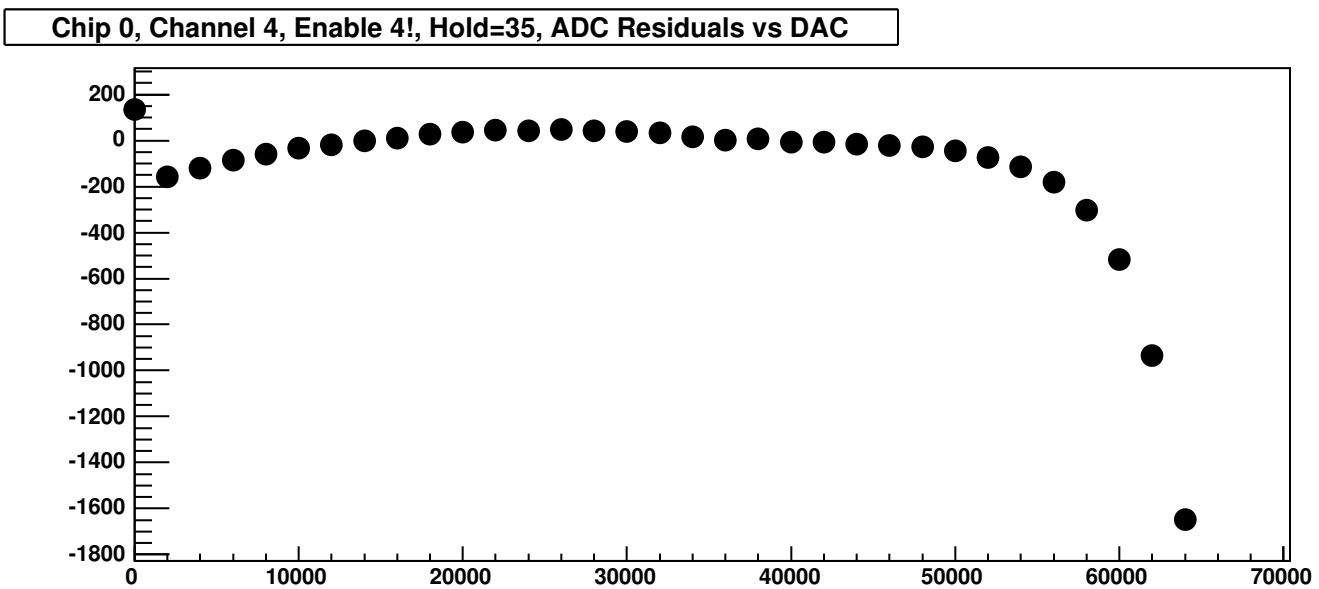

Chip 0, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 35.41 / 23
p0 624.7 ± 0.8101
p1 0.00314 ± 2.333e-05
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000
70000

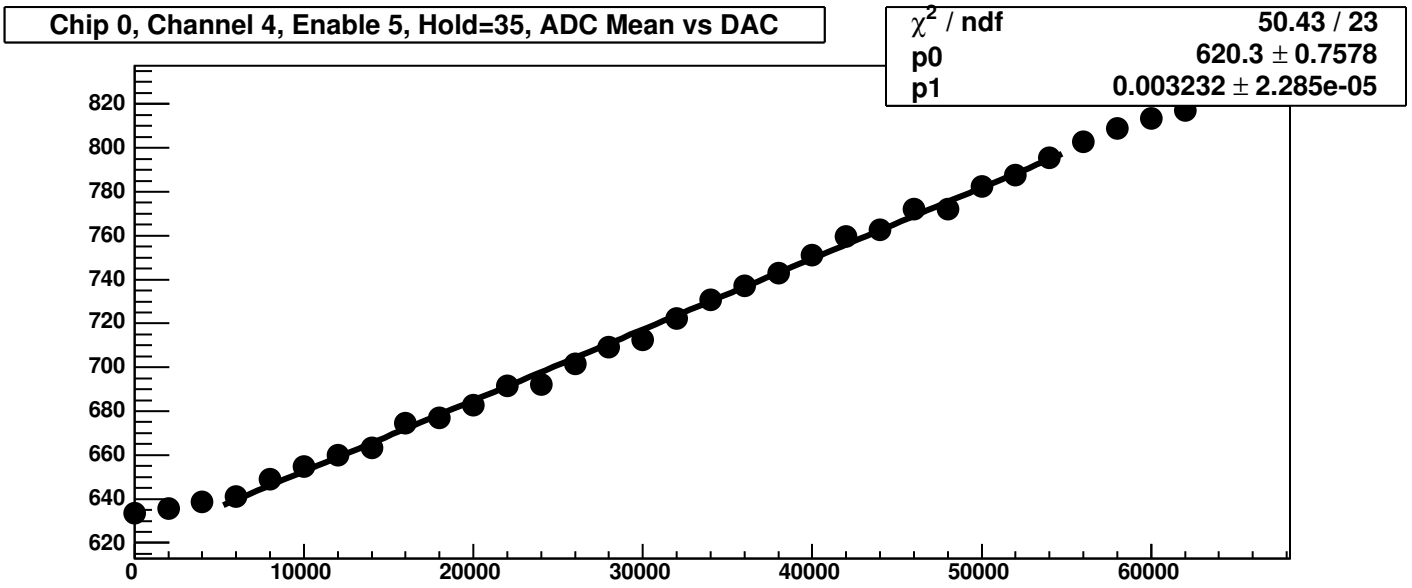

Chip 0, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

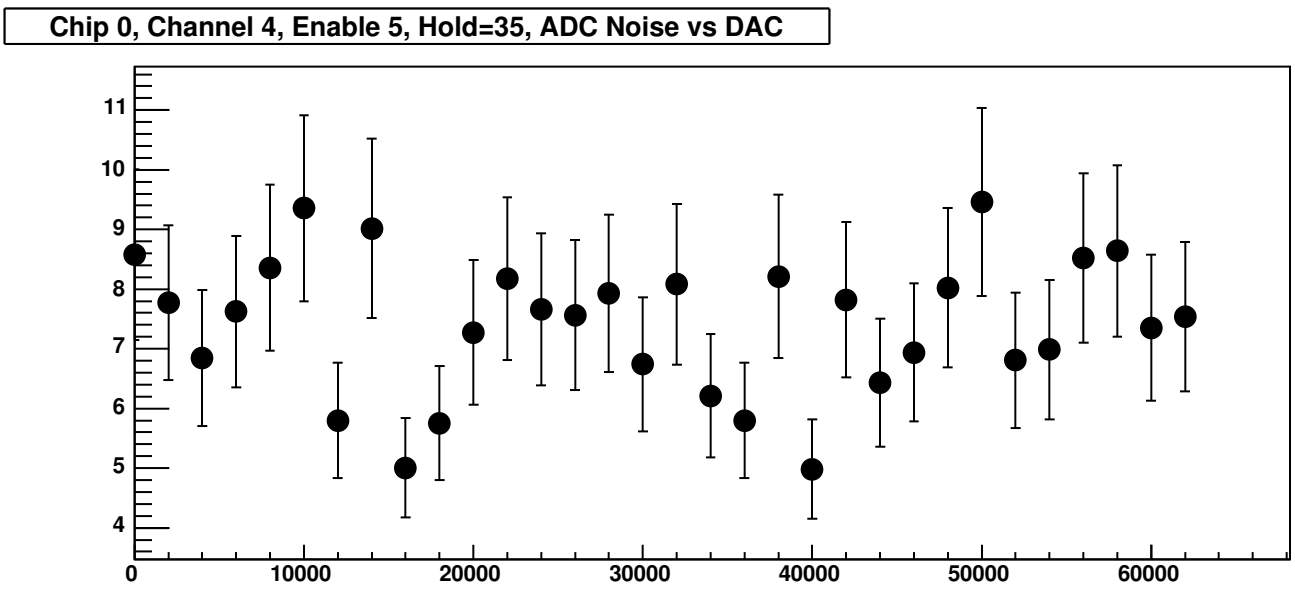

Chip 0, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

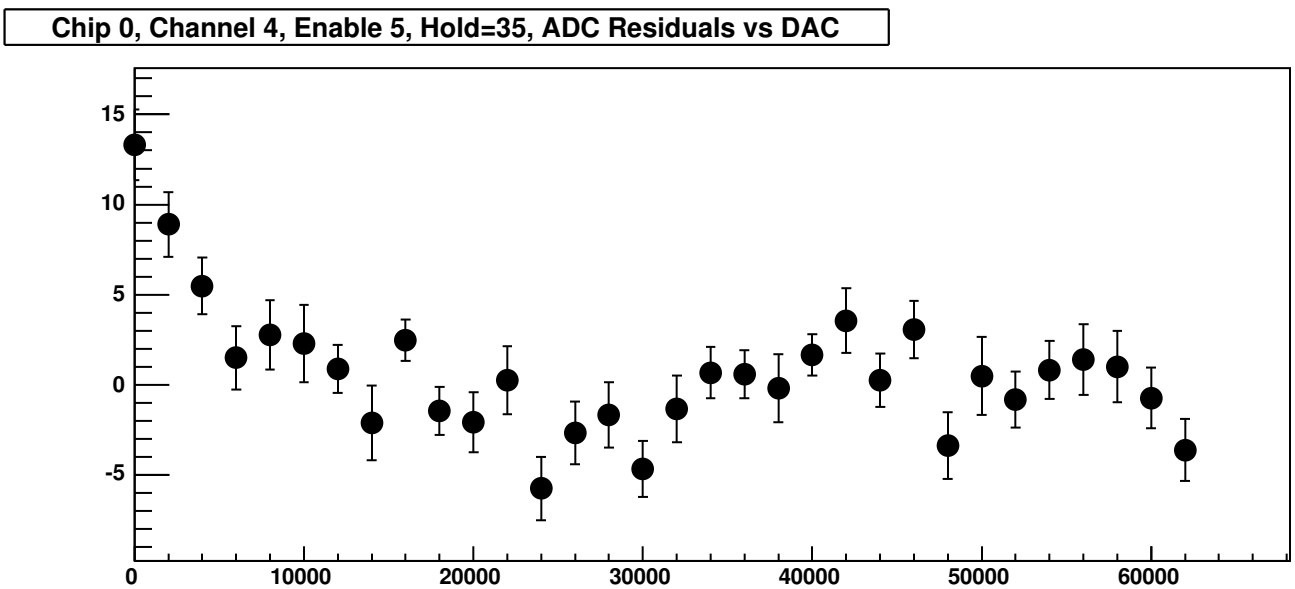

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 6318 / 23
p0 834.3 ± 0.9772
p1 0.4919 ± 4.233e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

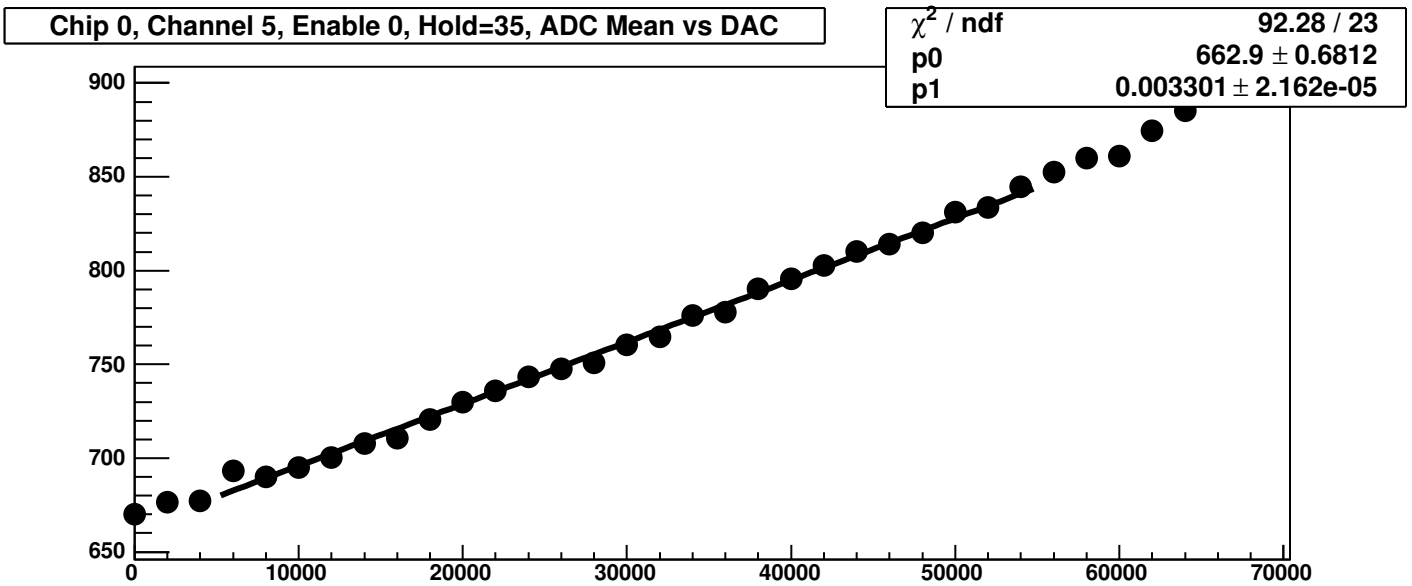

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

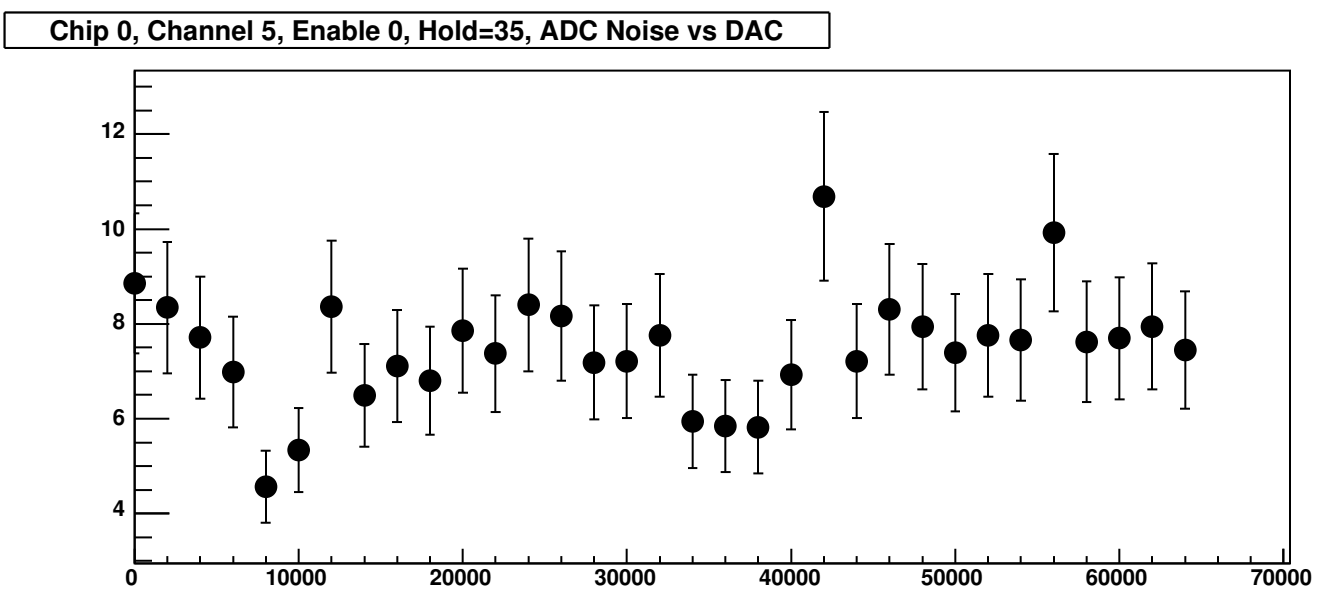

Chip 0, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
10000
20000
30000
40000
50000
60000
70000

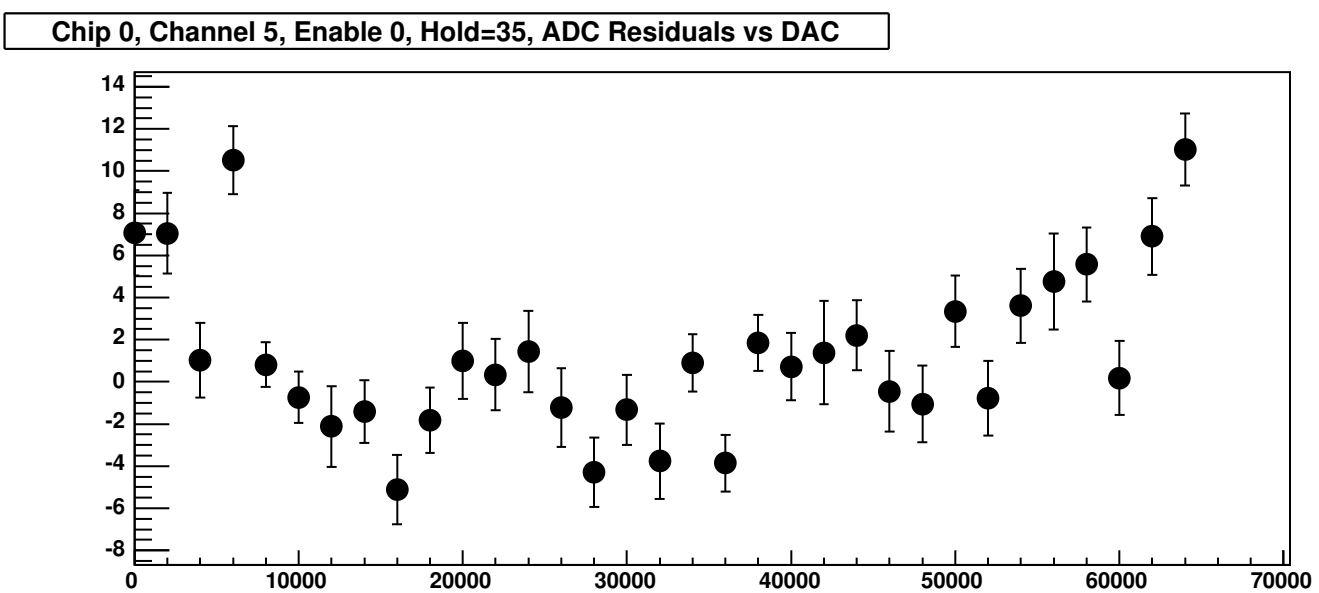

Chip 0, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 50.43 / 23
p0 620.3 ± 0.7578
p1 0.003232 ± 2.285e-05
820
800
780
760
740
720
700
680
660
640
620
0
10000
20000
30000
40000
50000
60000

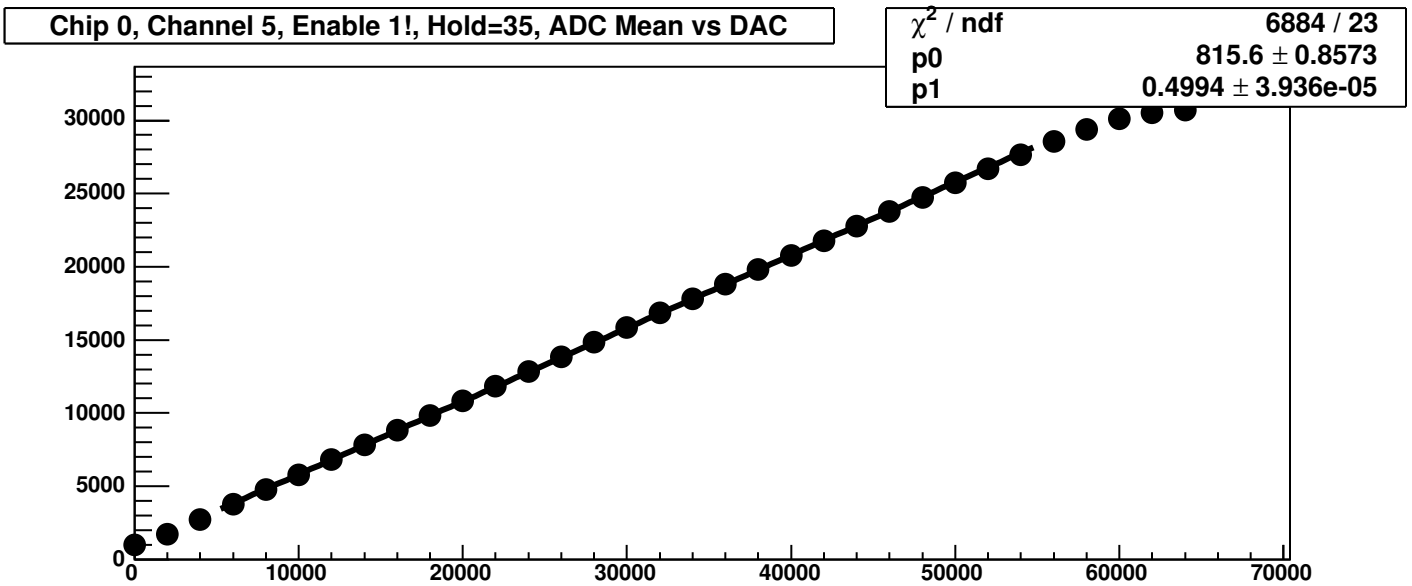

Chip 0, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

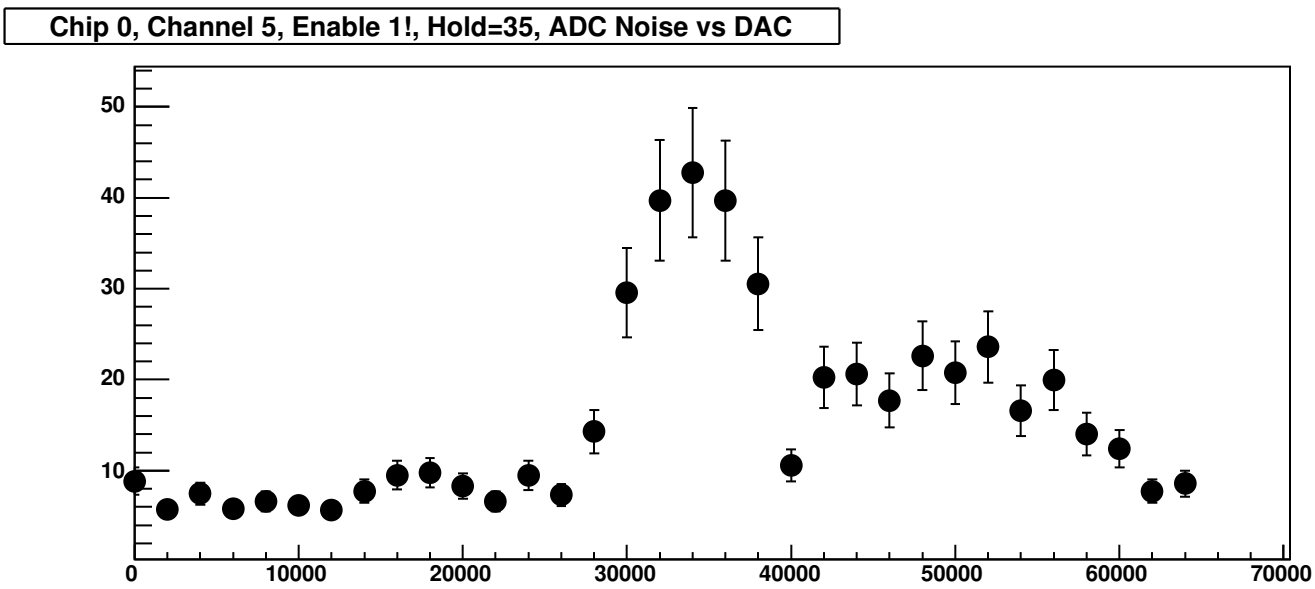

Chip 0, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000

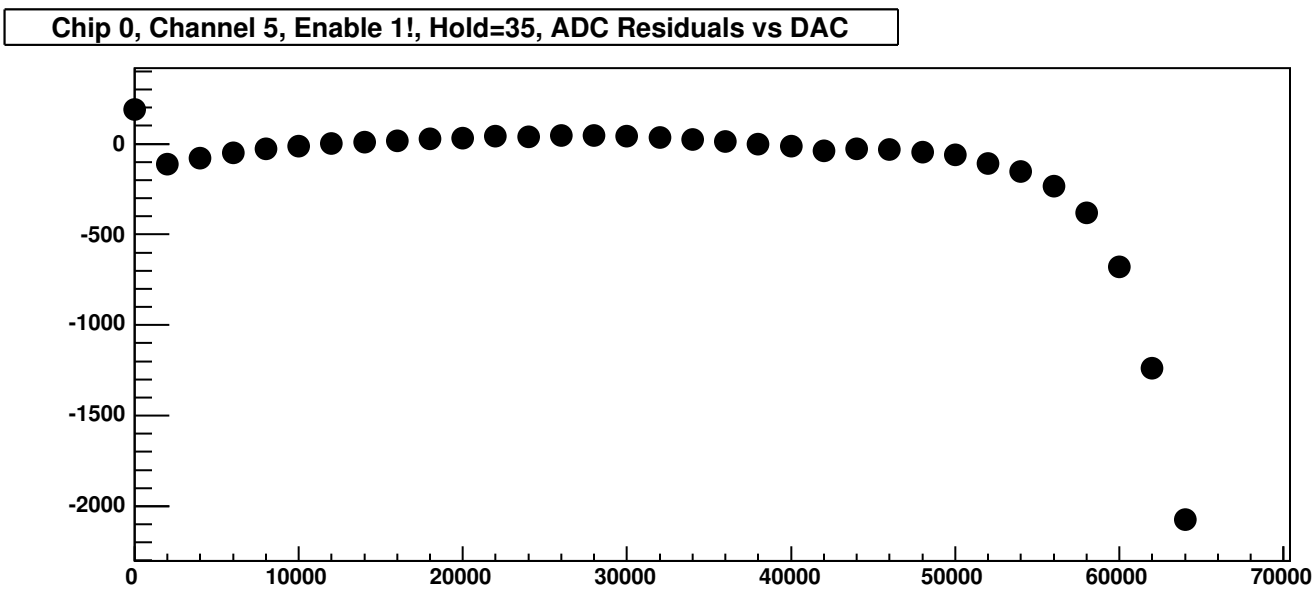

Chip 0, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 92.28 / 23
p0 662.9 ± 0.6812
p1 0.003301 ± 2.162e-05
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000
70000

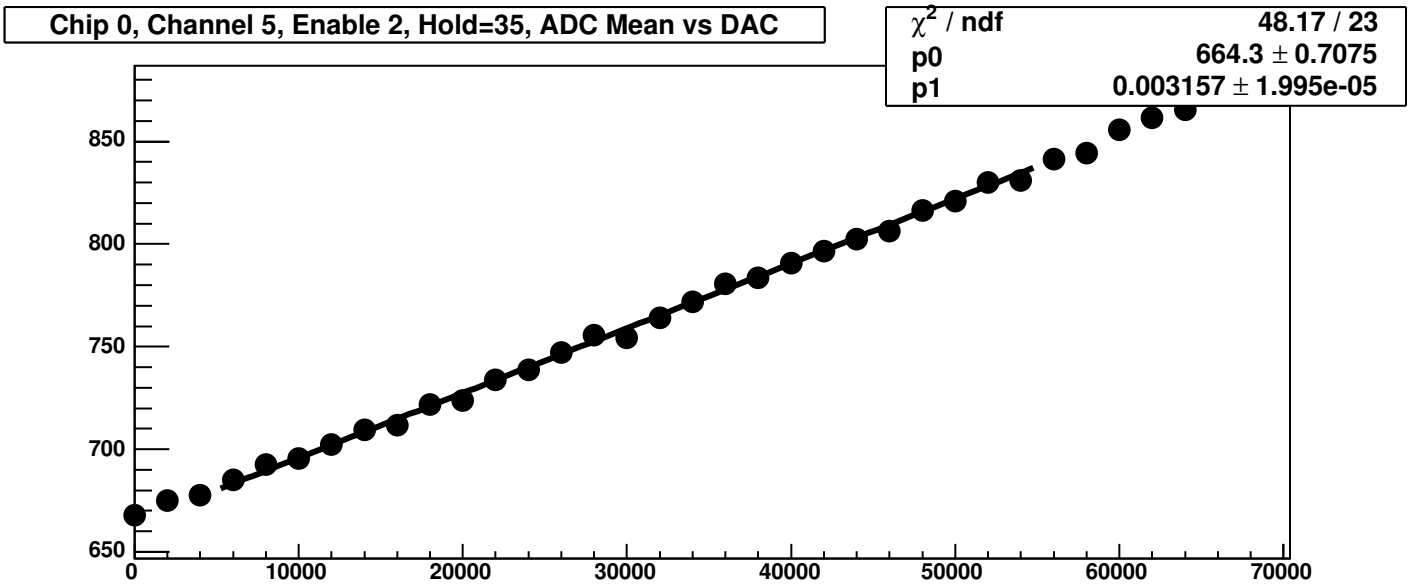

Chip 0, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

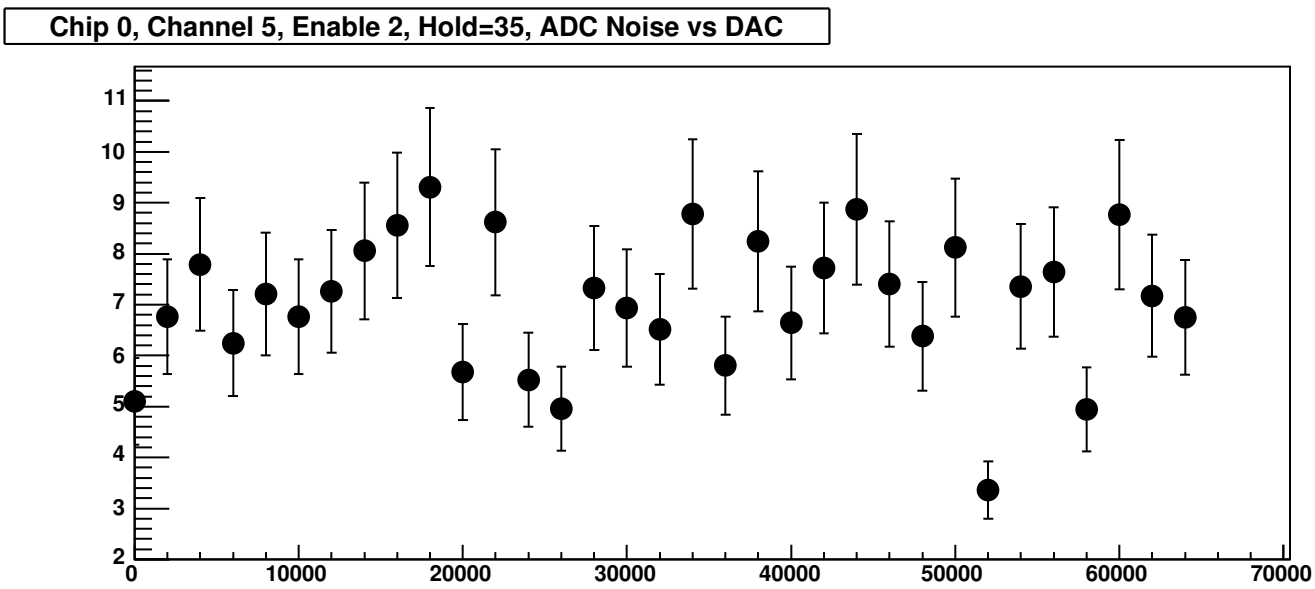

Chip 0, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

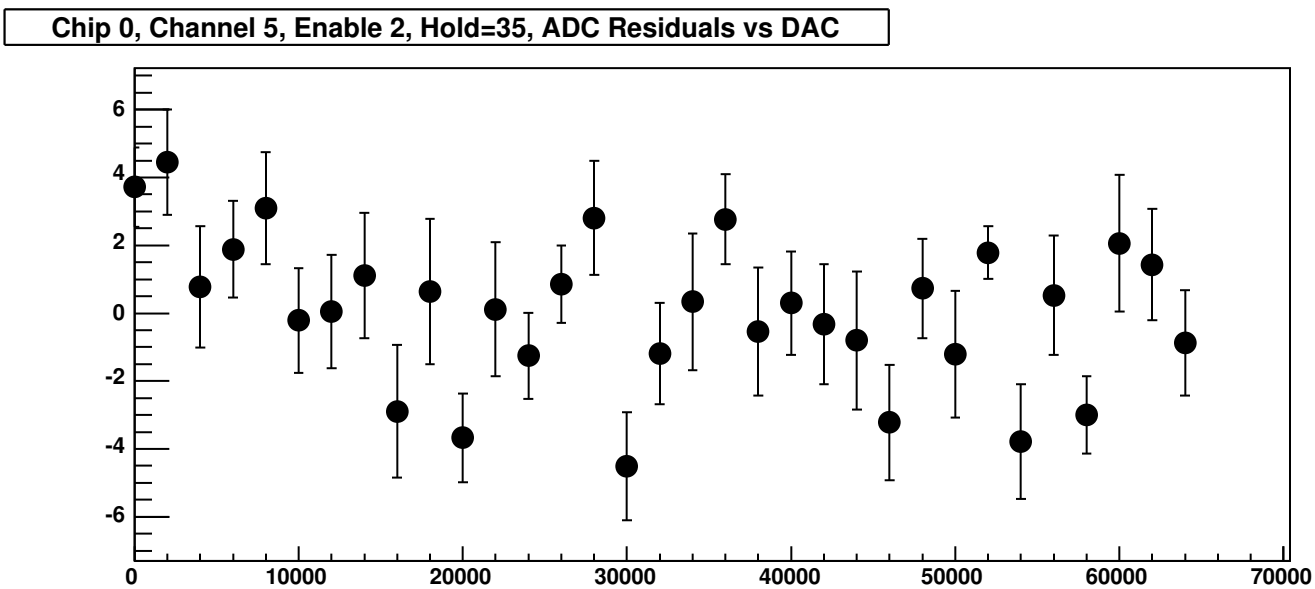

Chip 0, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 6884 / 23
p0 815.6 ± 0.8573
p1 0.4994 ± 3.936e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

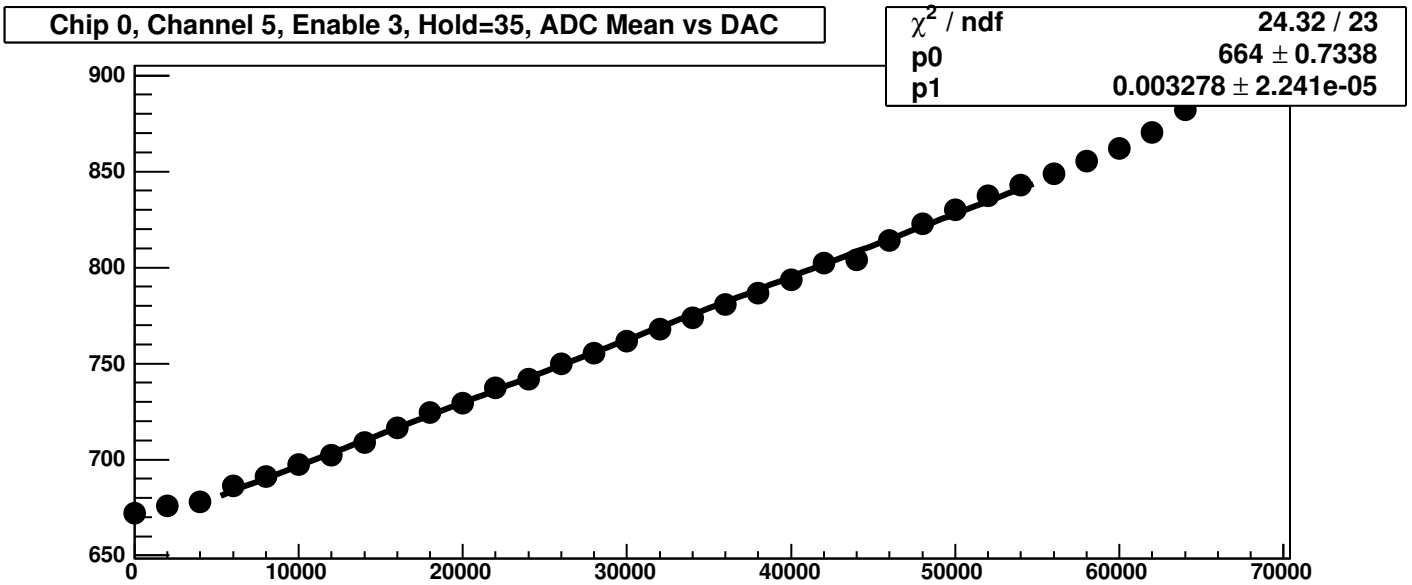

Chip 0, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000

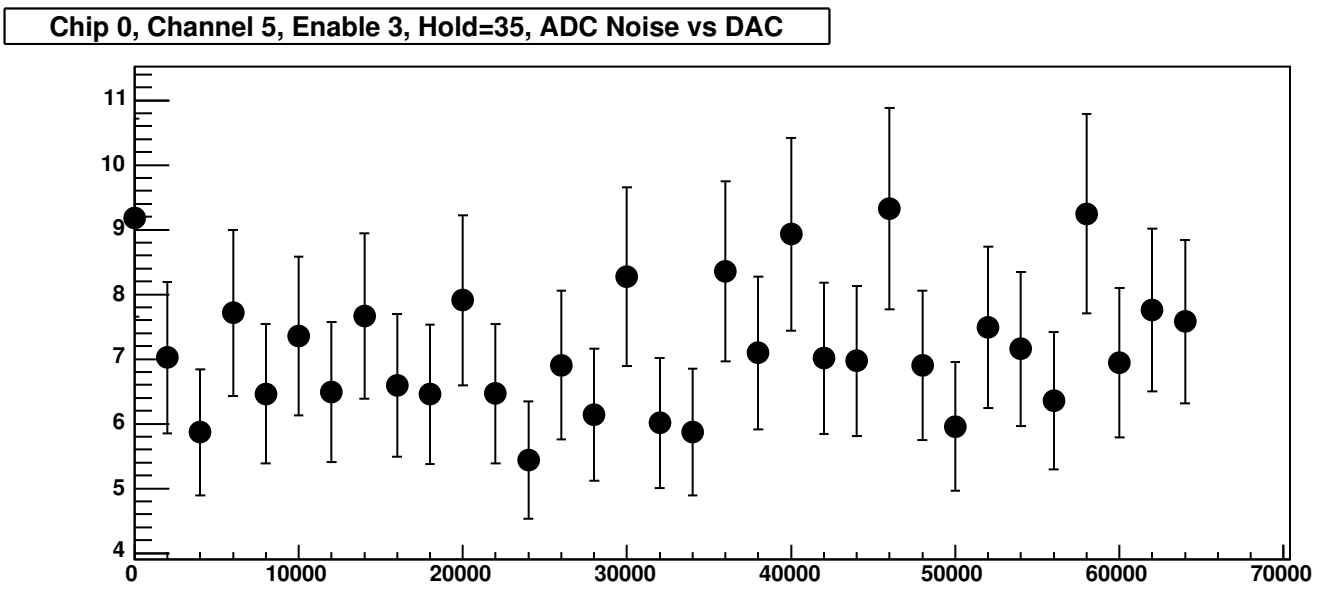

Chip 0, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000

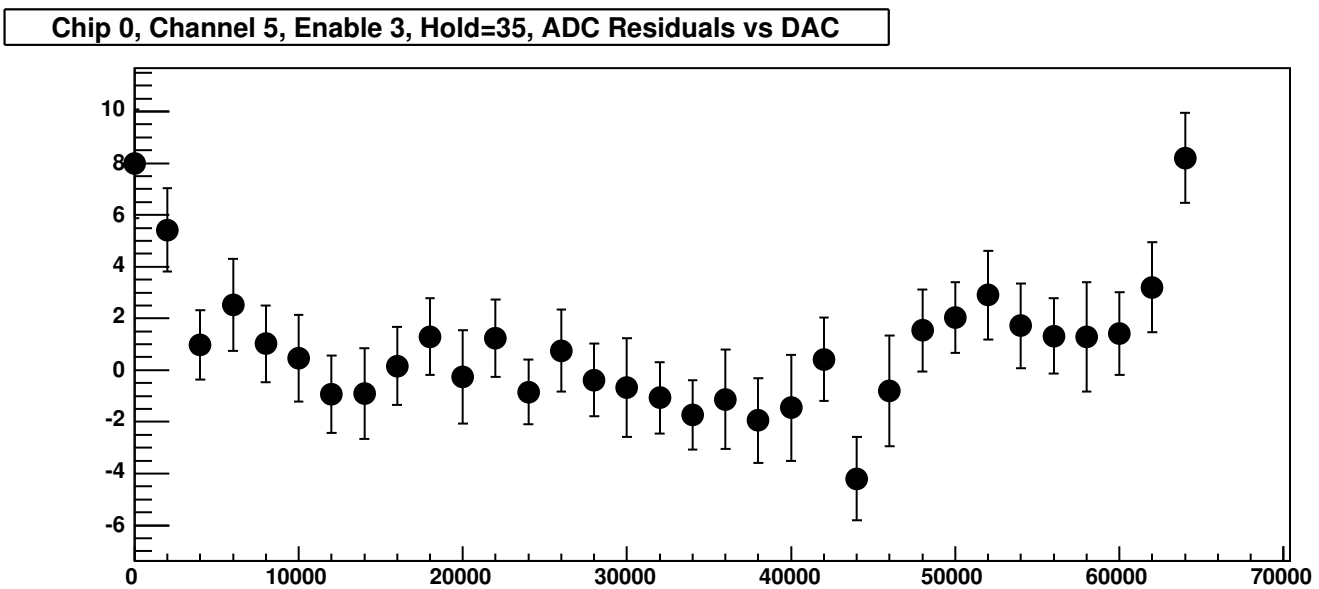

Chip 0, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 48.17 / 23
p0 664.3 ± 0.7075
p1 0.003157 ± 1.995e-05
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000
70000

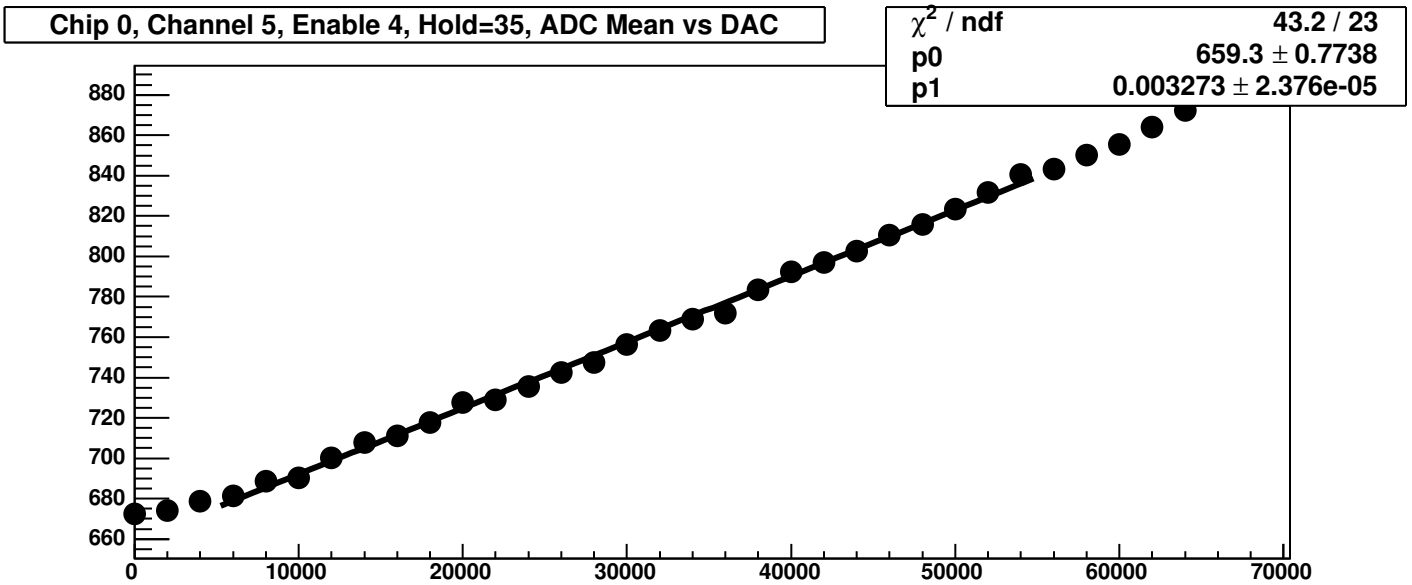

Chip 0, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
2
0
10000
20000
30000
40000
50000
60000
70000

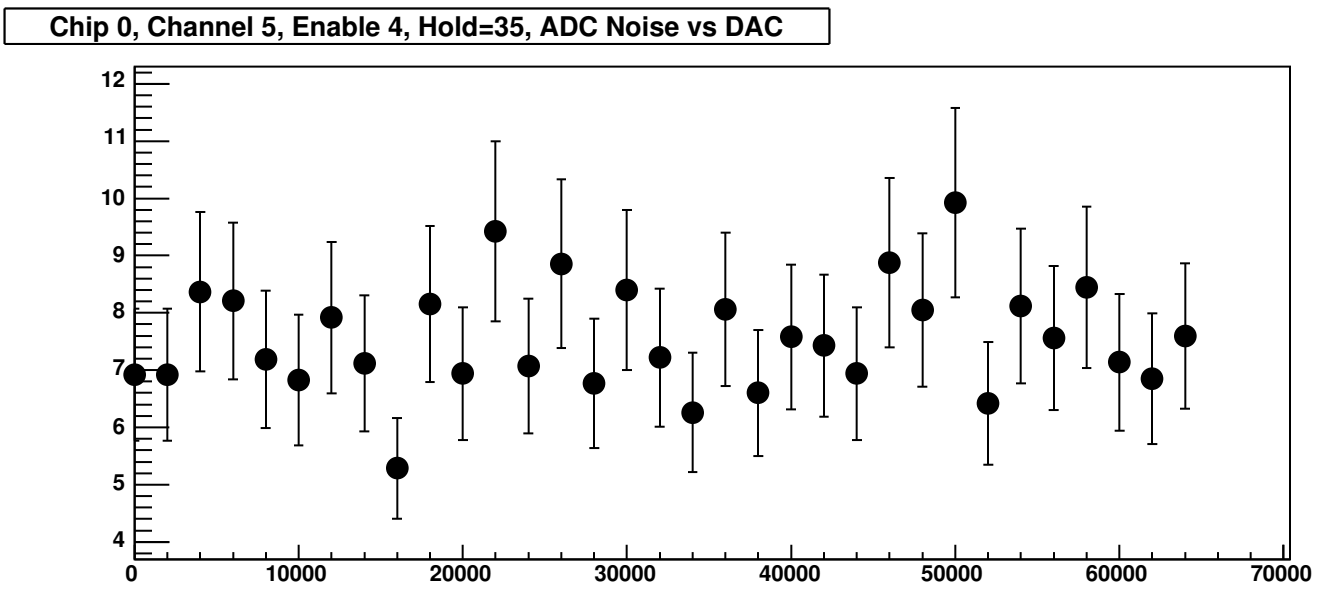

Chip 0, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

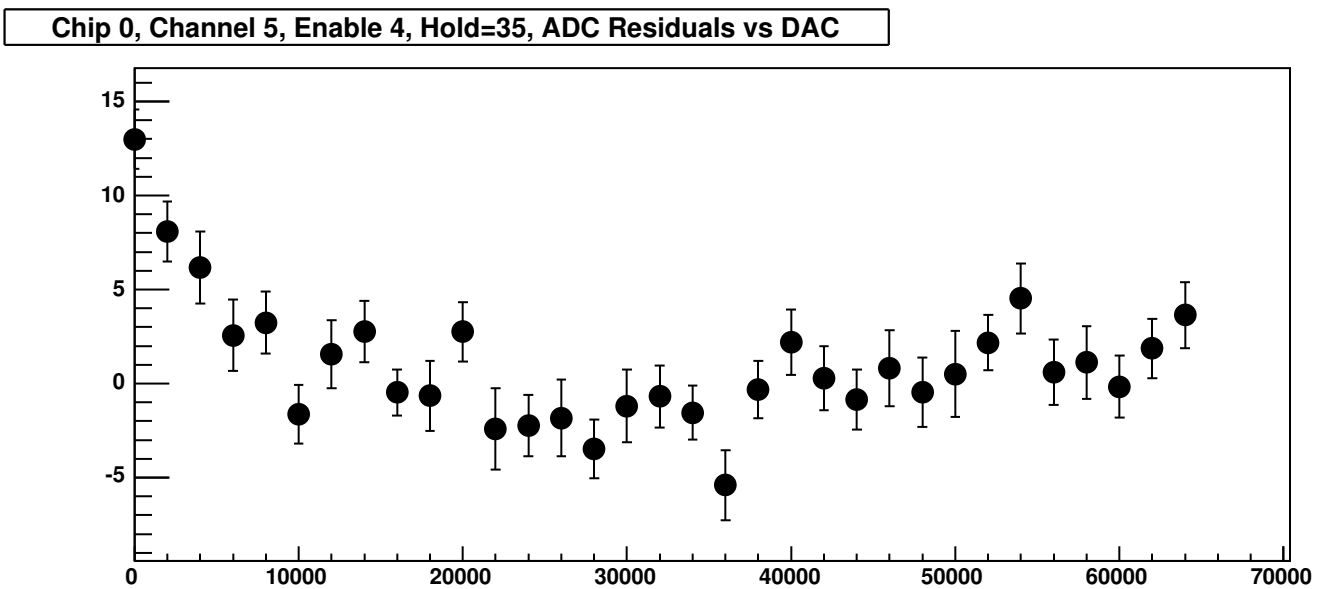

Chip 0, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.32 / 23
p0 664 ± 0.7338
p1 0.003278 ± 2.241e-05
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000
70000

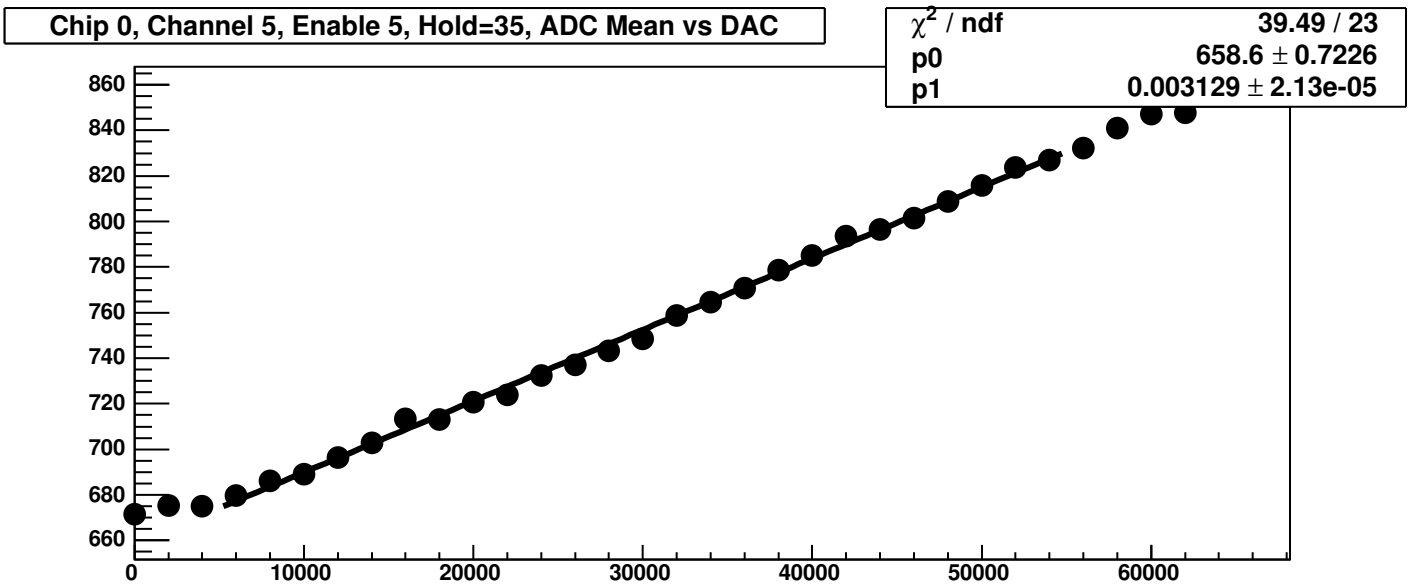

Chip 0, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

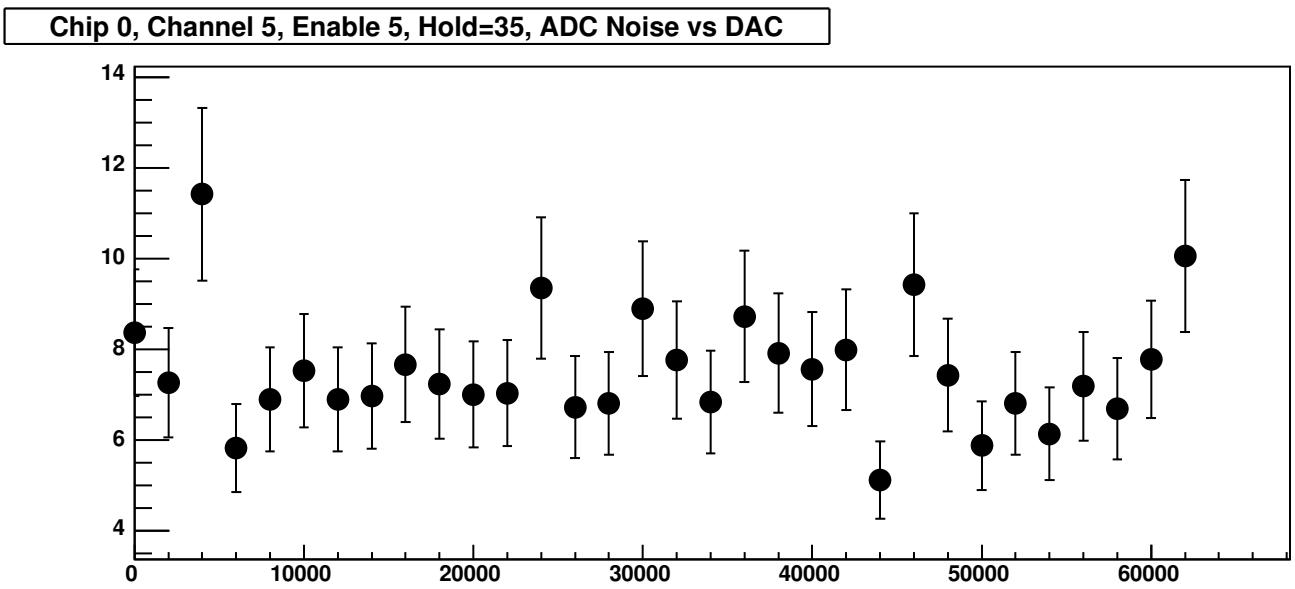

Chip 0, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

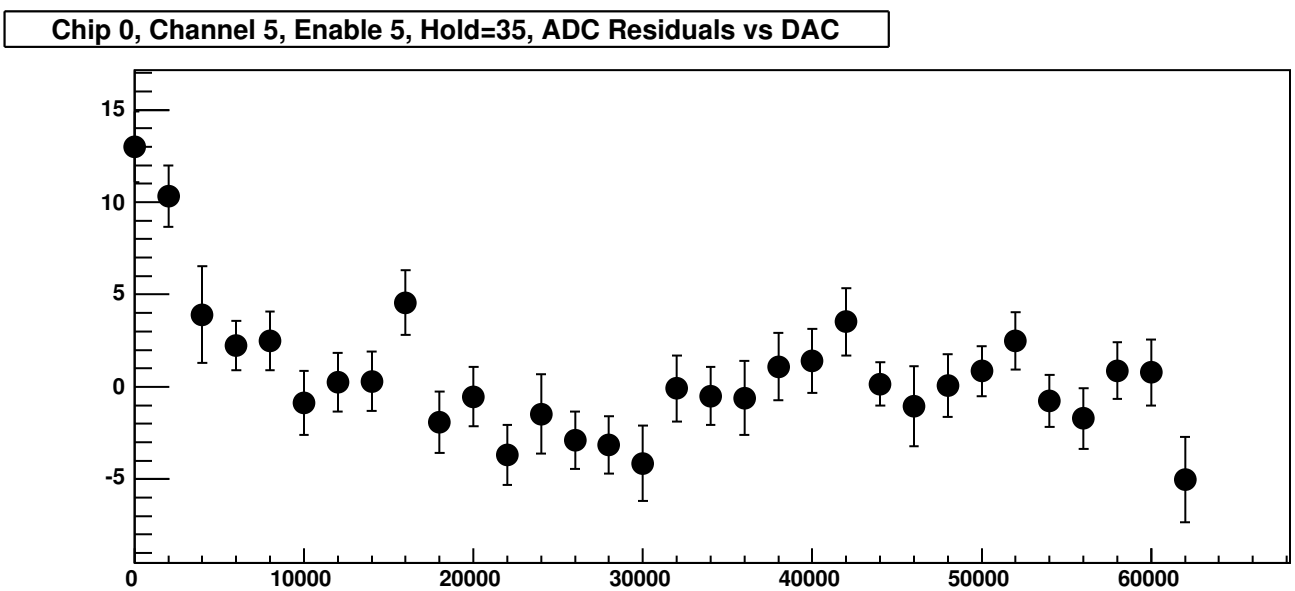

Chip 0, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 43.2 / 23
p0 659.3 ± 0.7738
p1 0.003273 ± 2.376e-05
880
860
840
820
800
780
760
740
720
700
680
660
0
10000
20000
30000
40000
50000
60000
70000

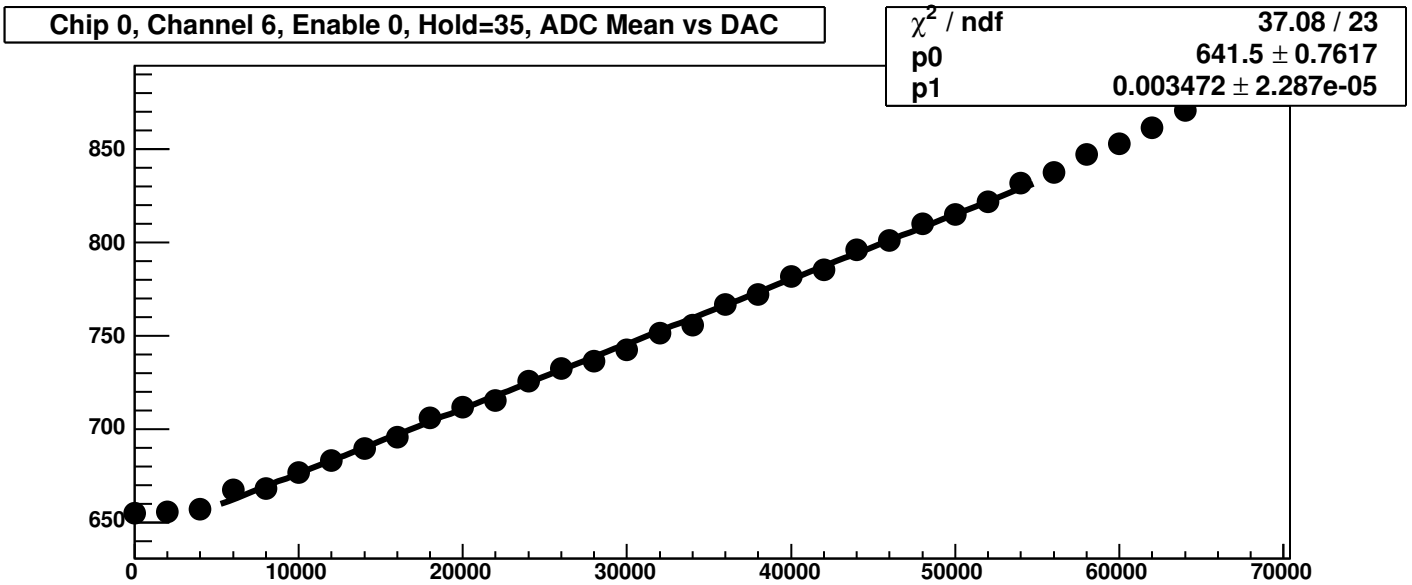

Chip 0, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

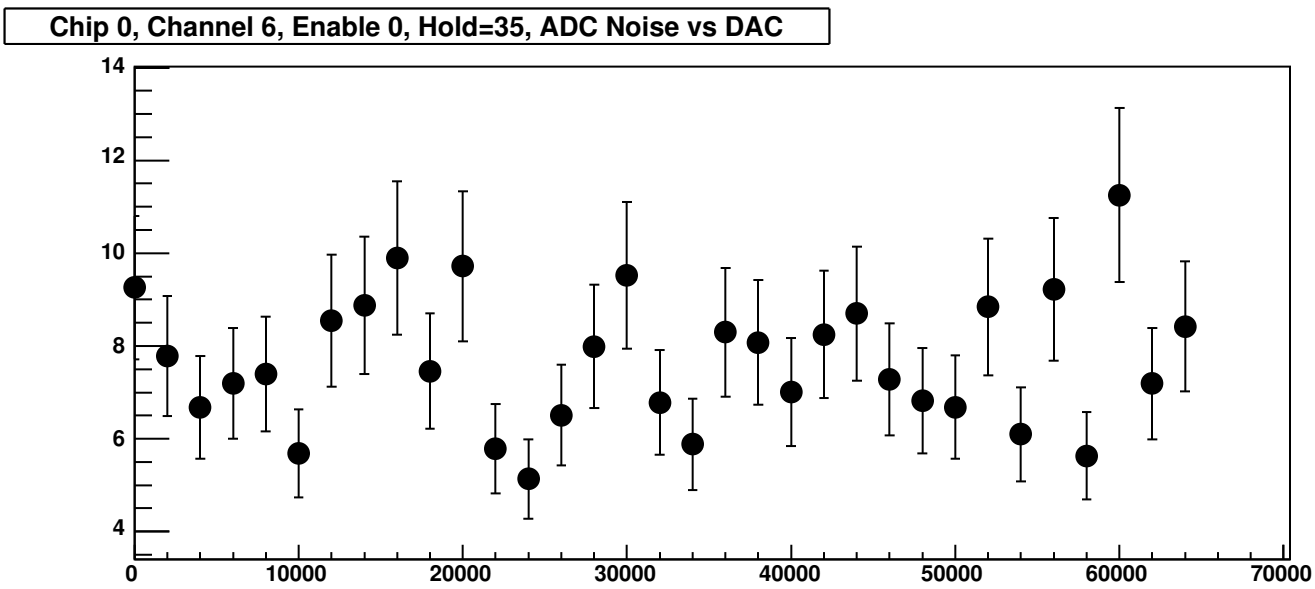

Chip 0, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

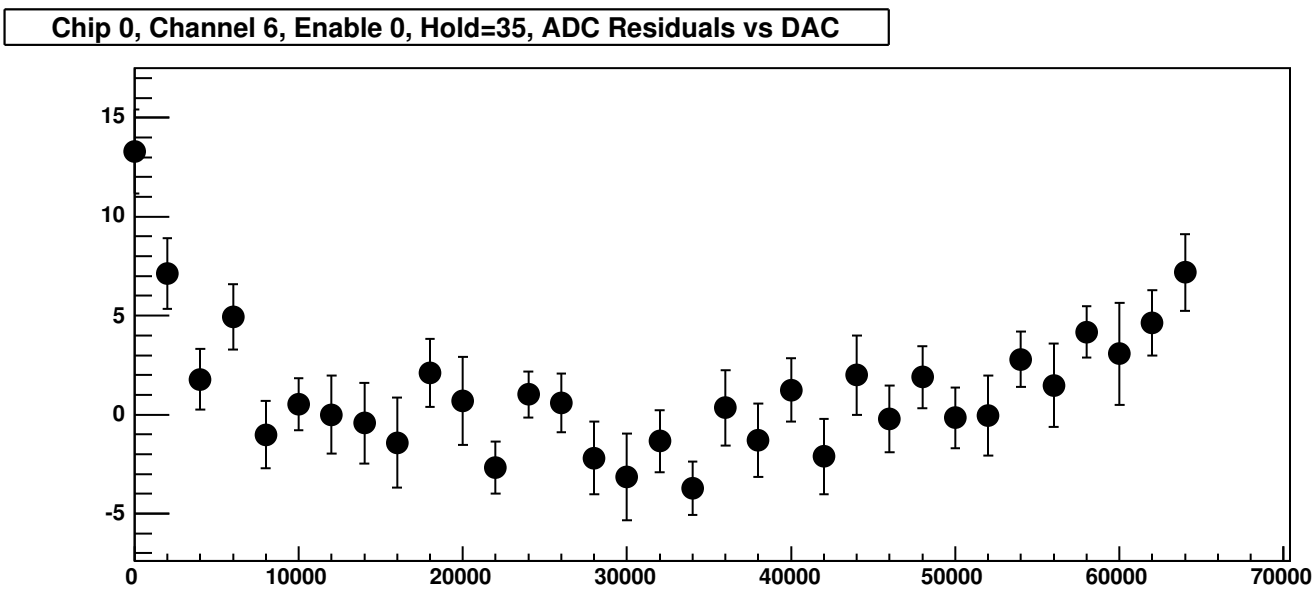

Chip 0, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 39.49 / 23
p0 658.6 ± 0.7226
p1 0.003129 ± 2.13e-05
860
840
820
800
780
760
740
720
700
680
660
0
10000
20000
30000
40000
50000
60000

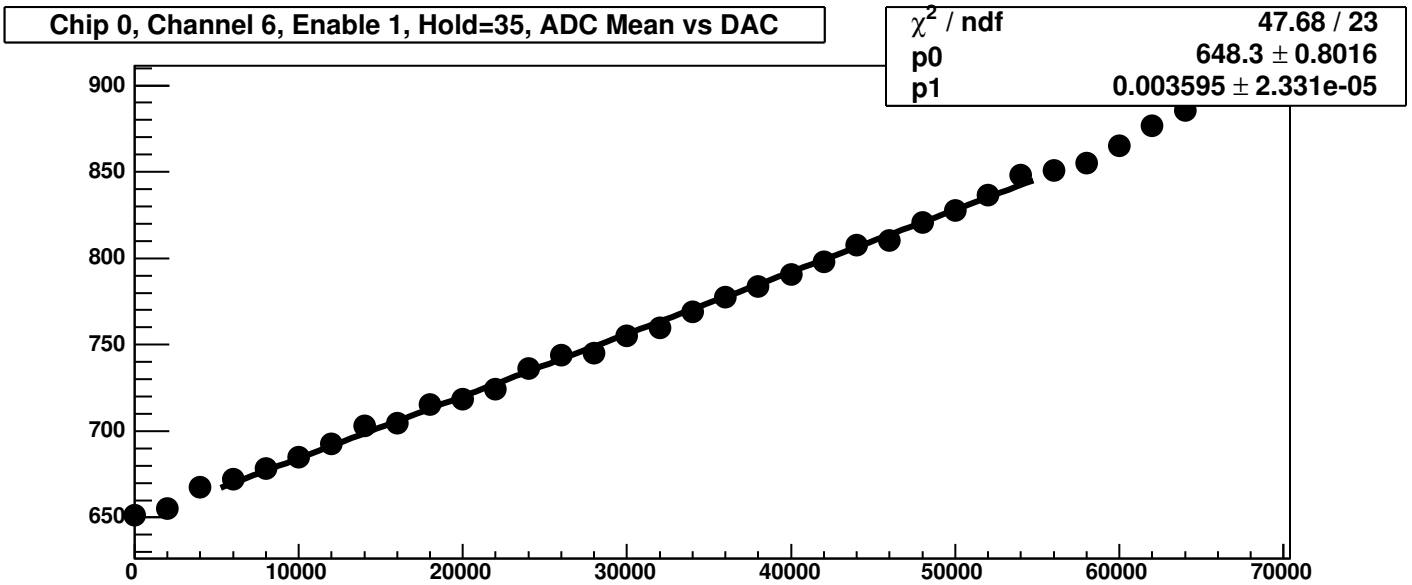

Chip 0, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000

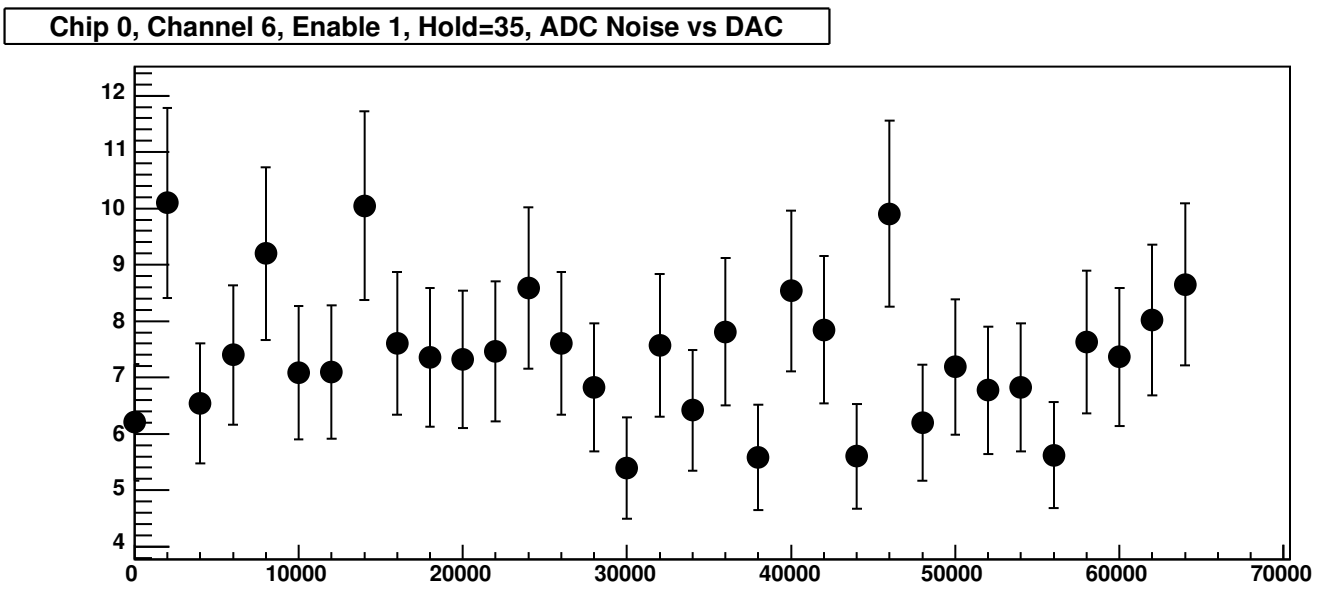

Chip 0, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000

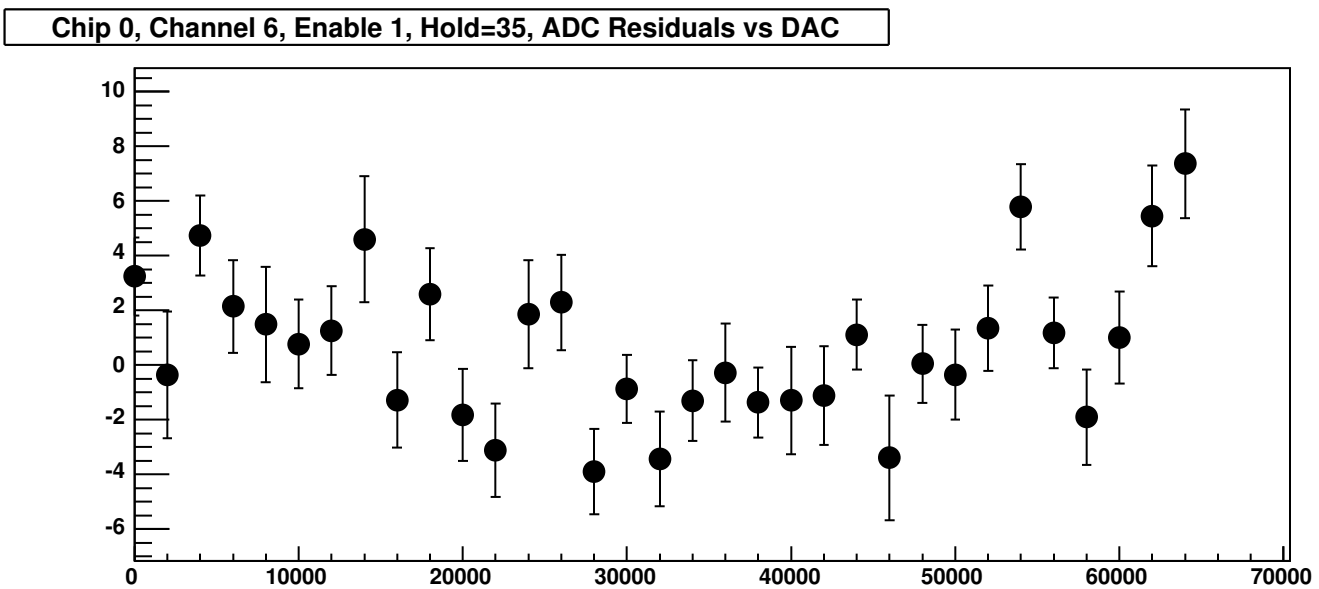

Chip 0, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 37.08 / 23
p0 641.5 ± 0.7617
p1 0.003472 ± 2.287e-05
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000
70000

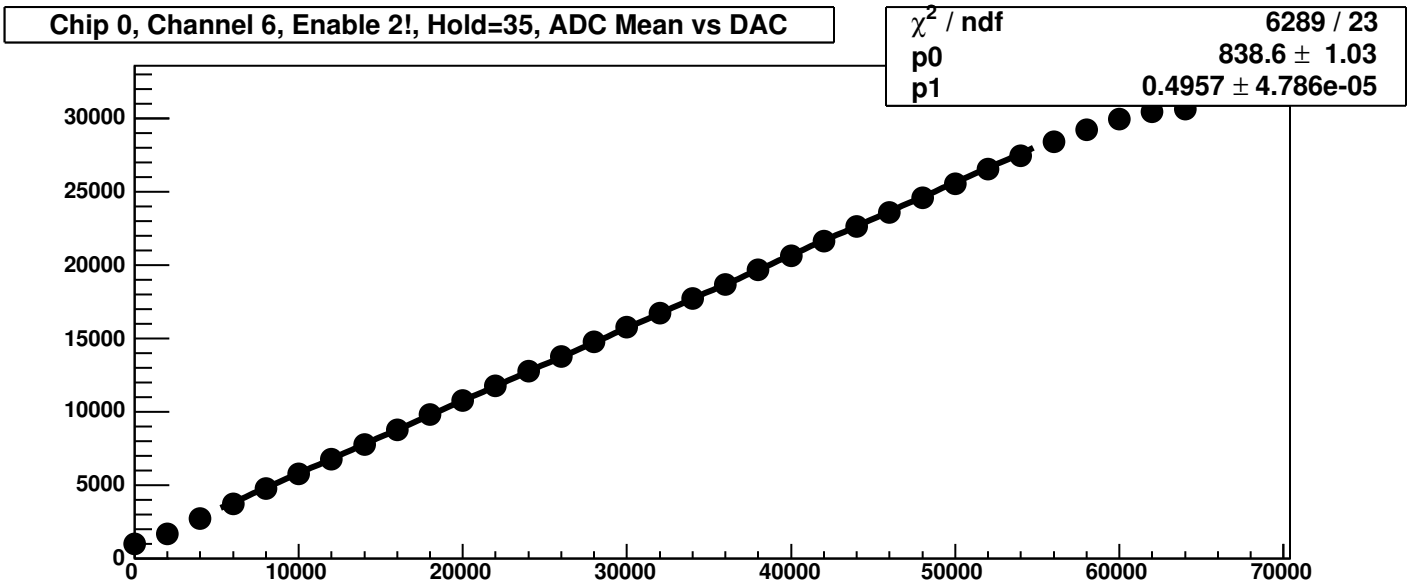

Chip 0, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

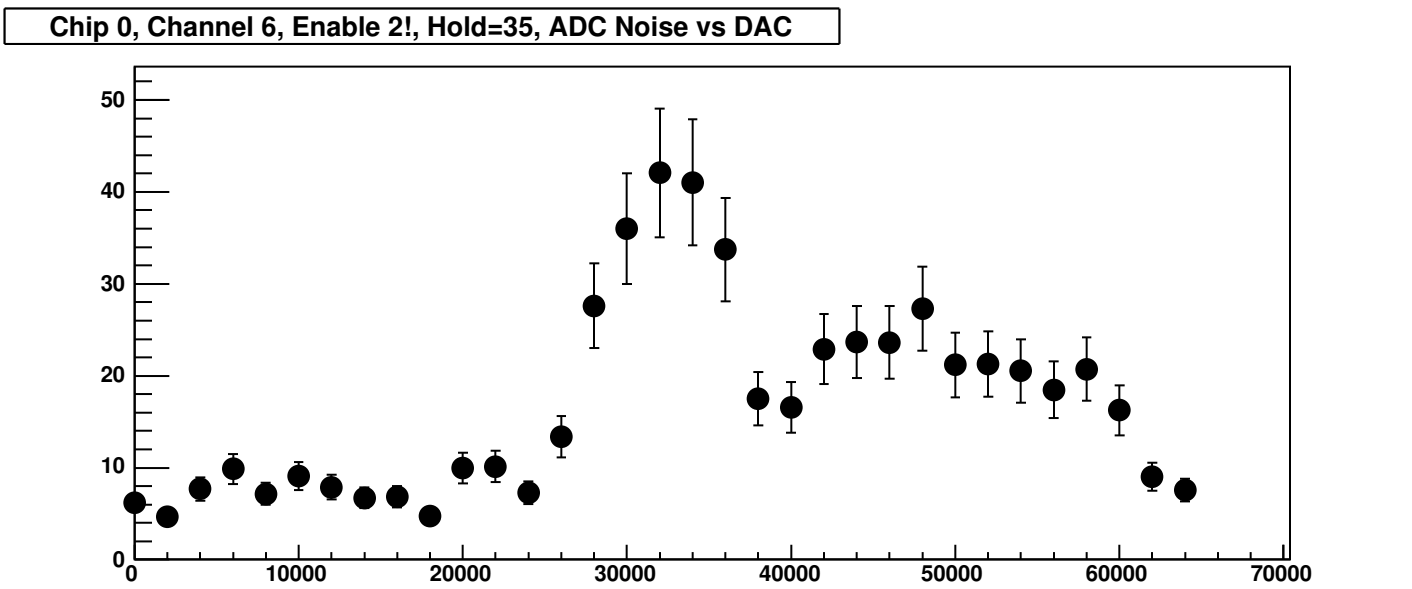

Chip 0, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

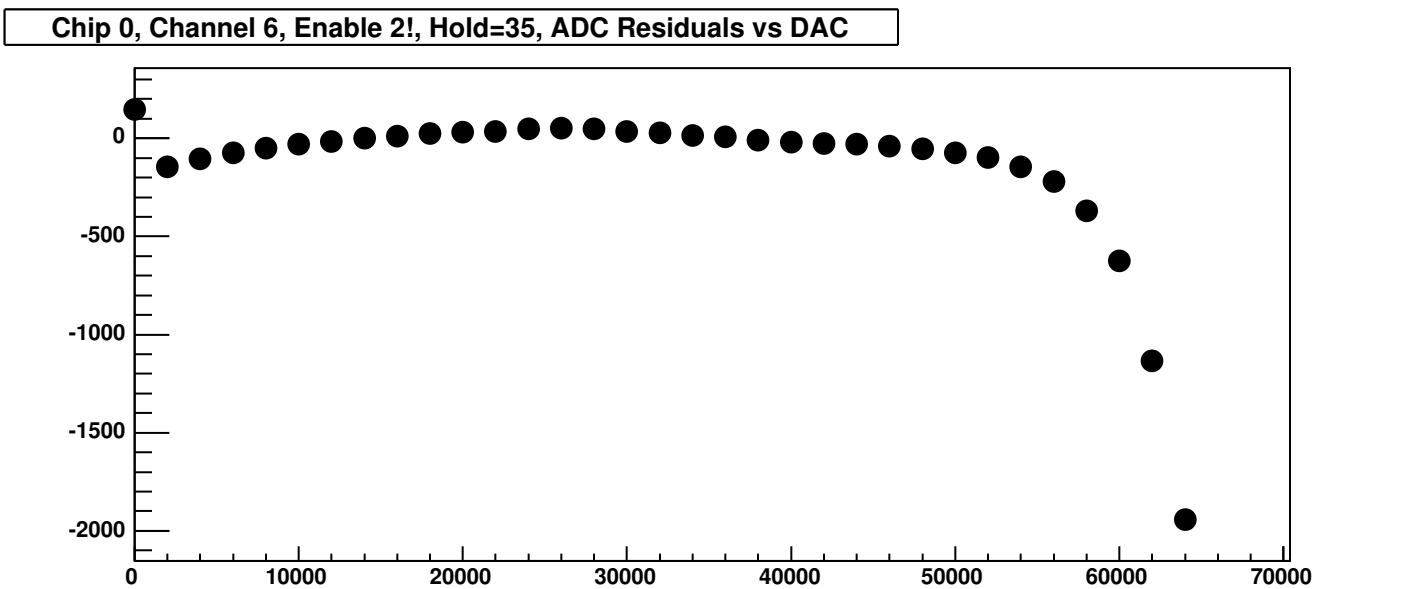

Chip 0, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 47.68 / 23
p0 648.3 ± 0.8016
p1 0.003595 ± 2.331e-05
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000
70000

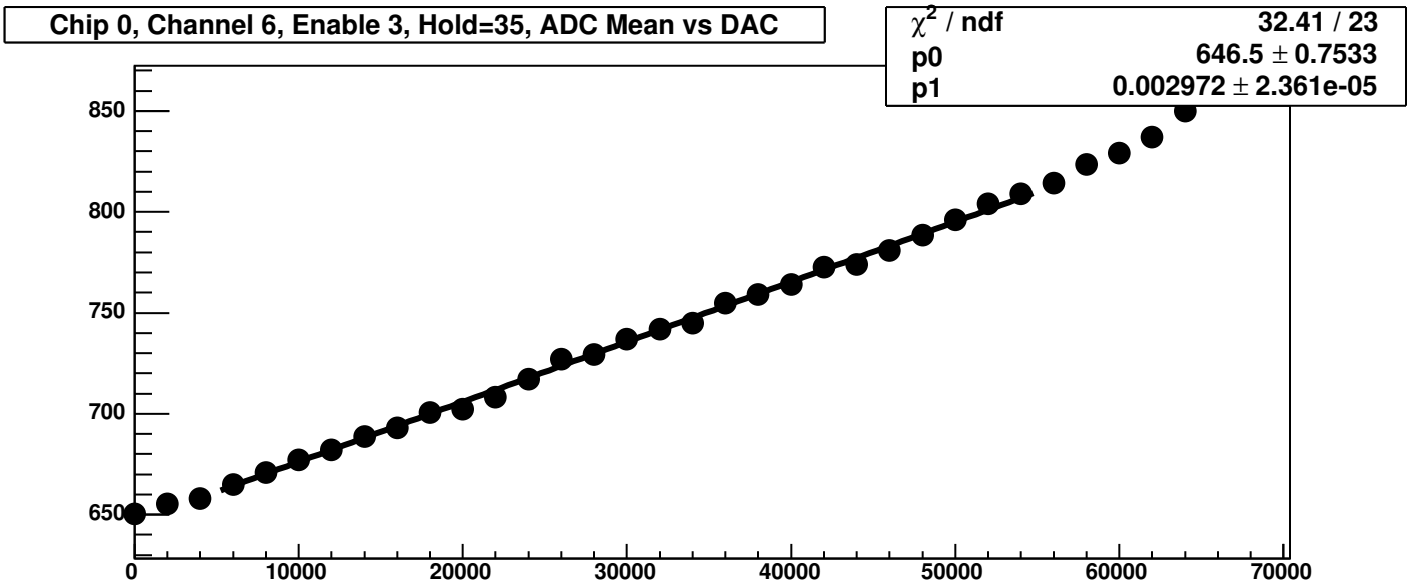

Chip 0, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

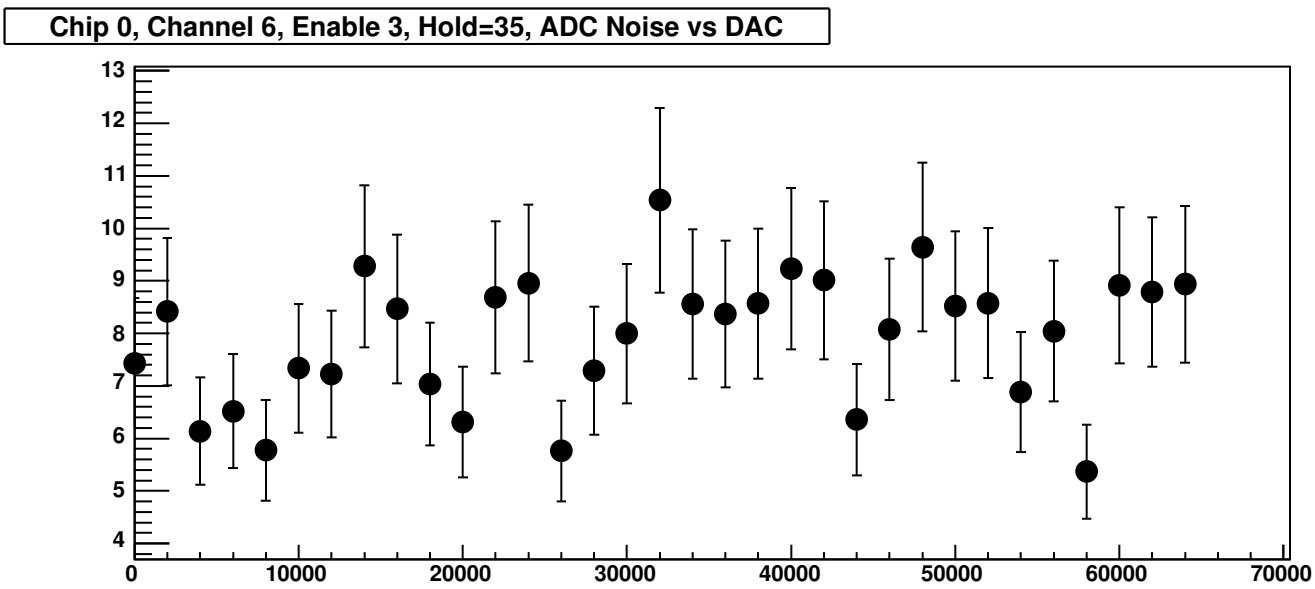

Chip 0, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

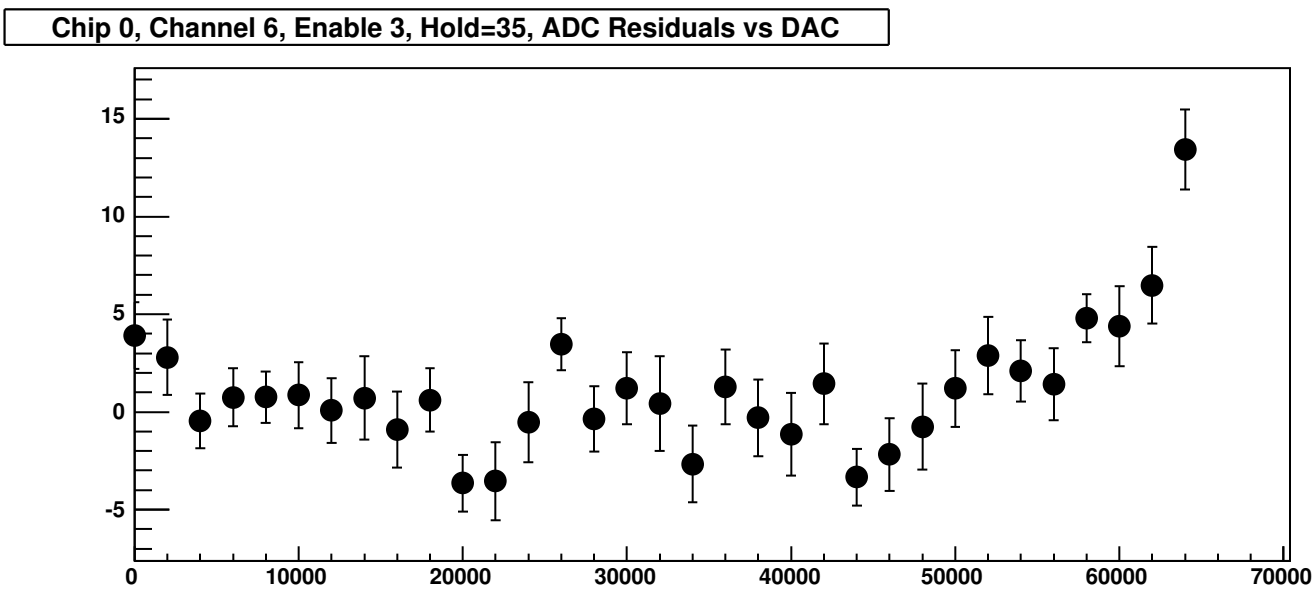

Chip 0, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 6289 / 23
p0 838.6 ± 1.03
p1 0.4957 ± 4.786e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

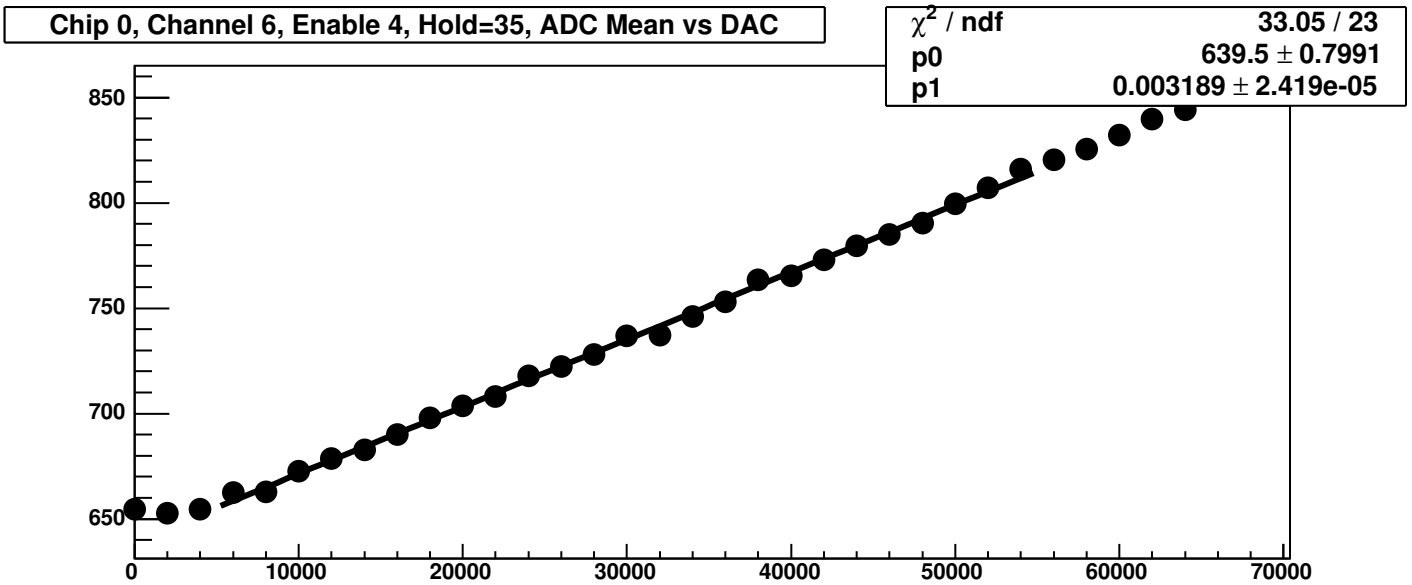

Chip 0, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

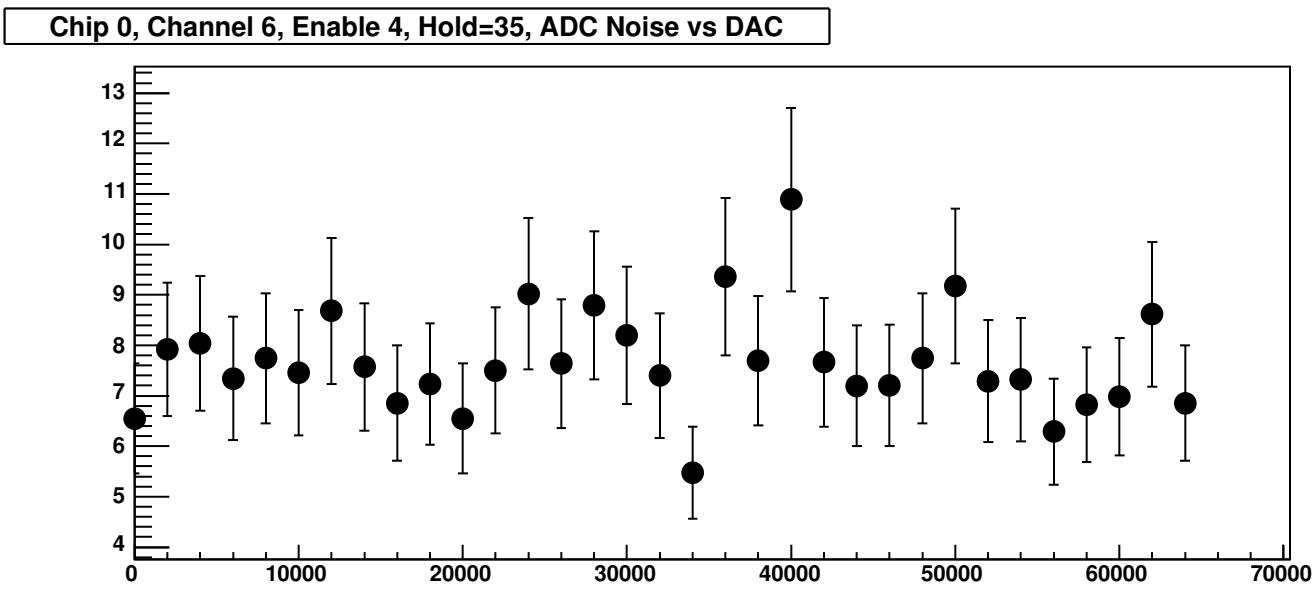

Chip 0, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000

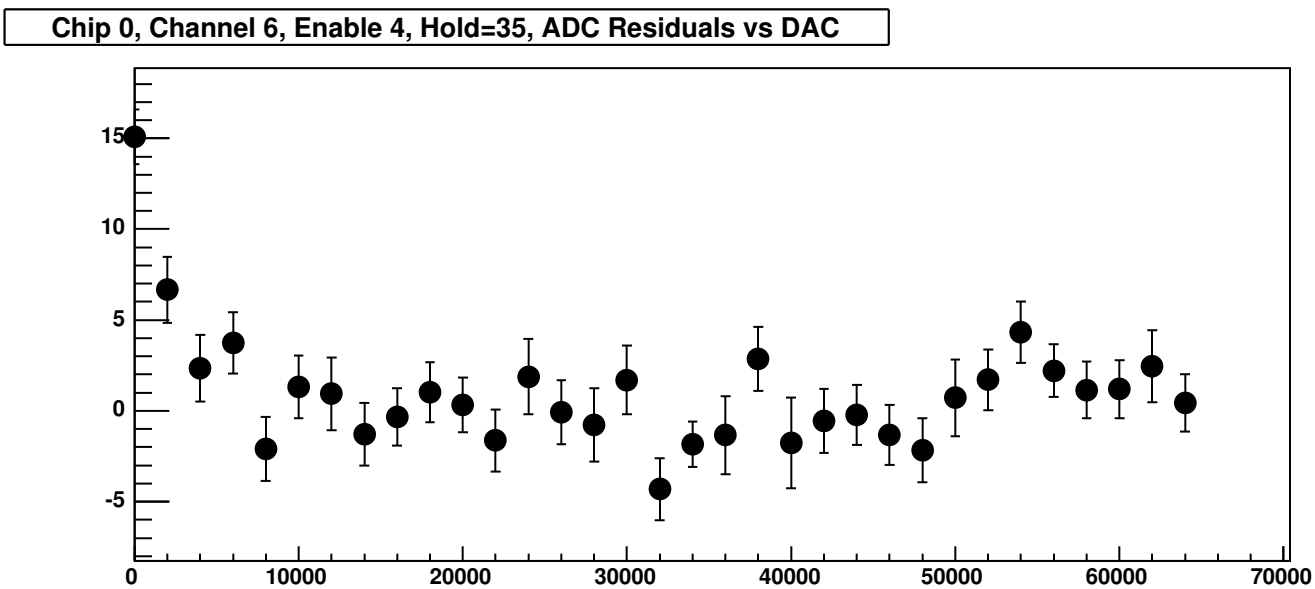

Chip 0, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 32.41 / 23
p0 646.5 ± 0.7533
p1 0.002972 ± 2.361e-05
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000
70000

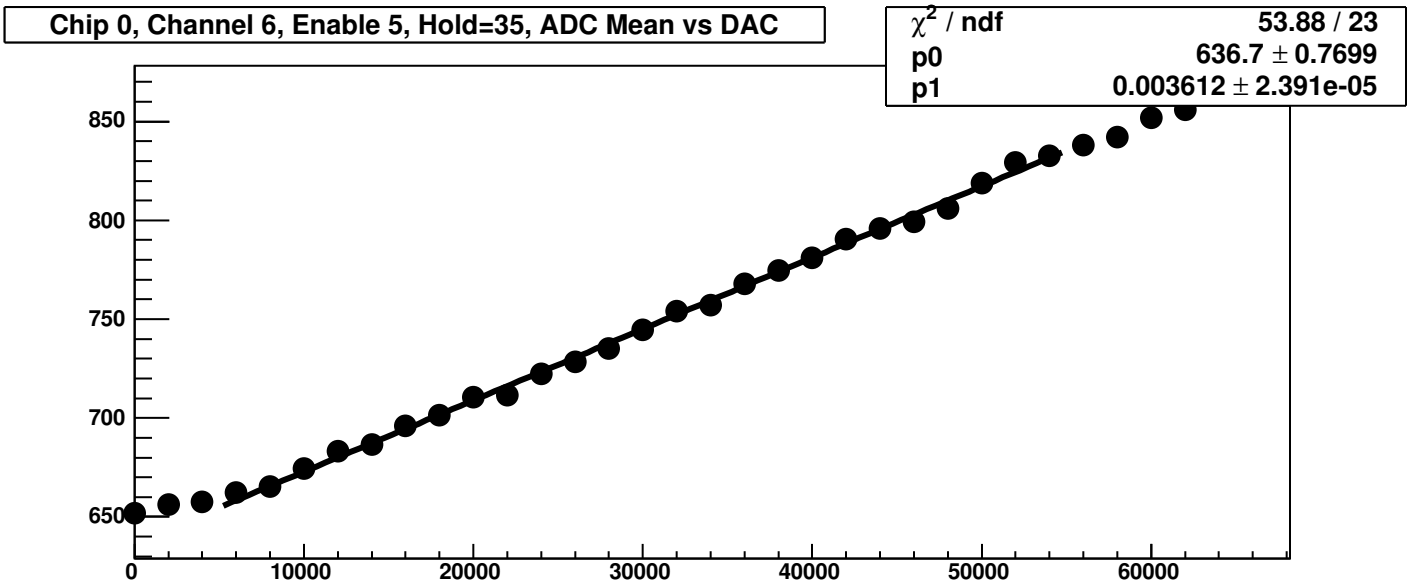

Chip 0, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

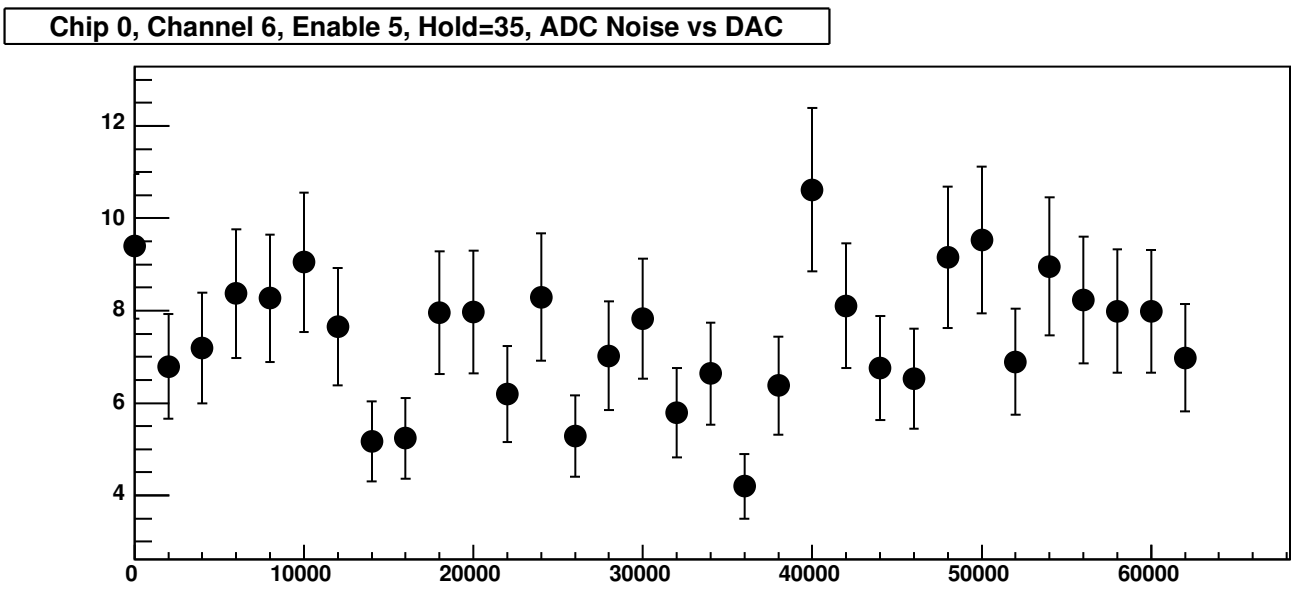

Chip 0, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

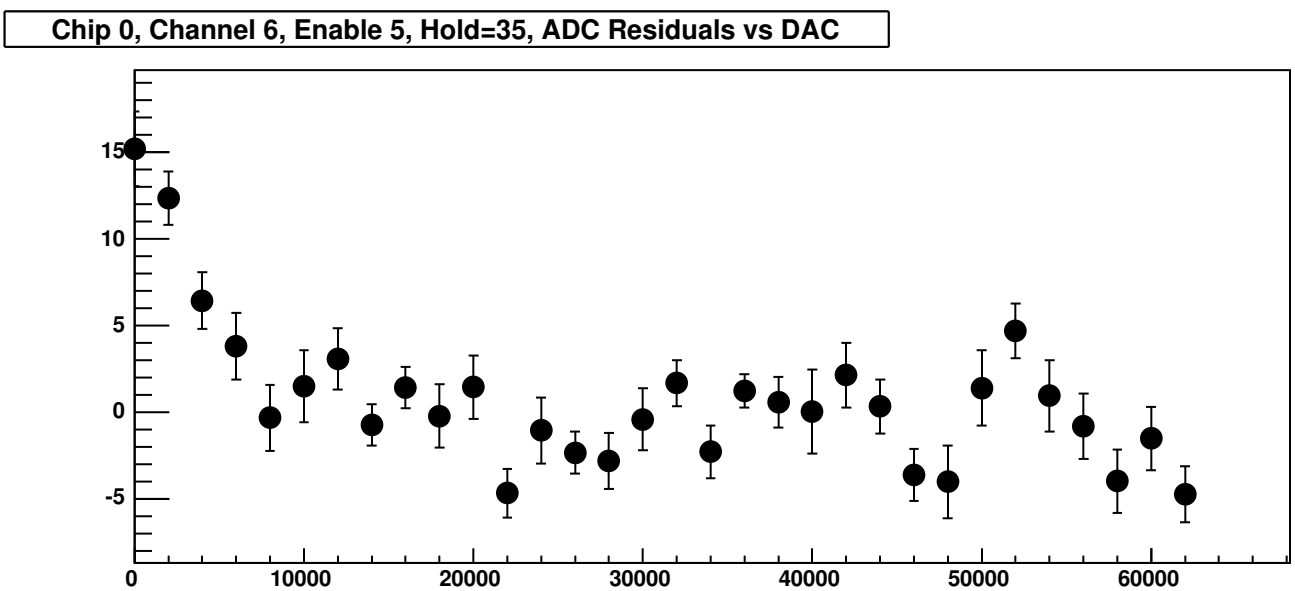

Chip 0, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 33.05 / 23
p0 639.5 ± 0.7991
p1 0.003189 ± 2.419e-05
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000
70000

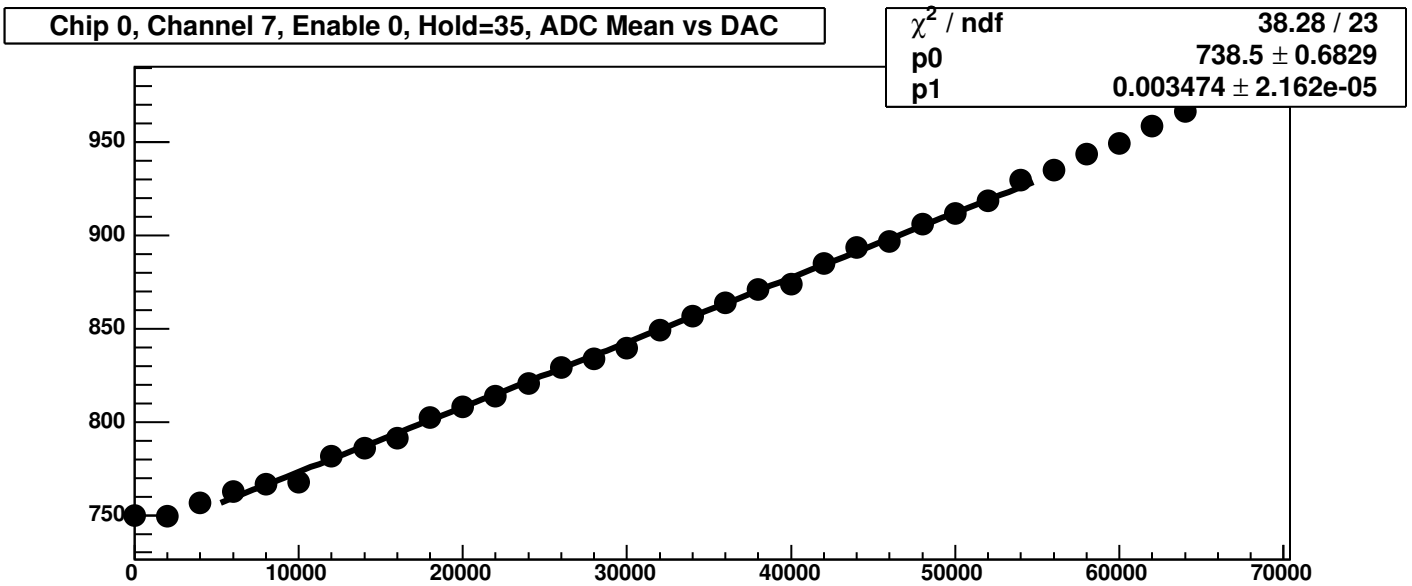

Chip 0, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

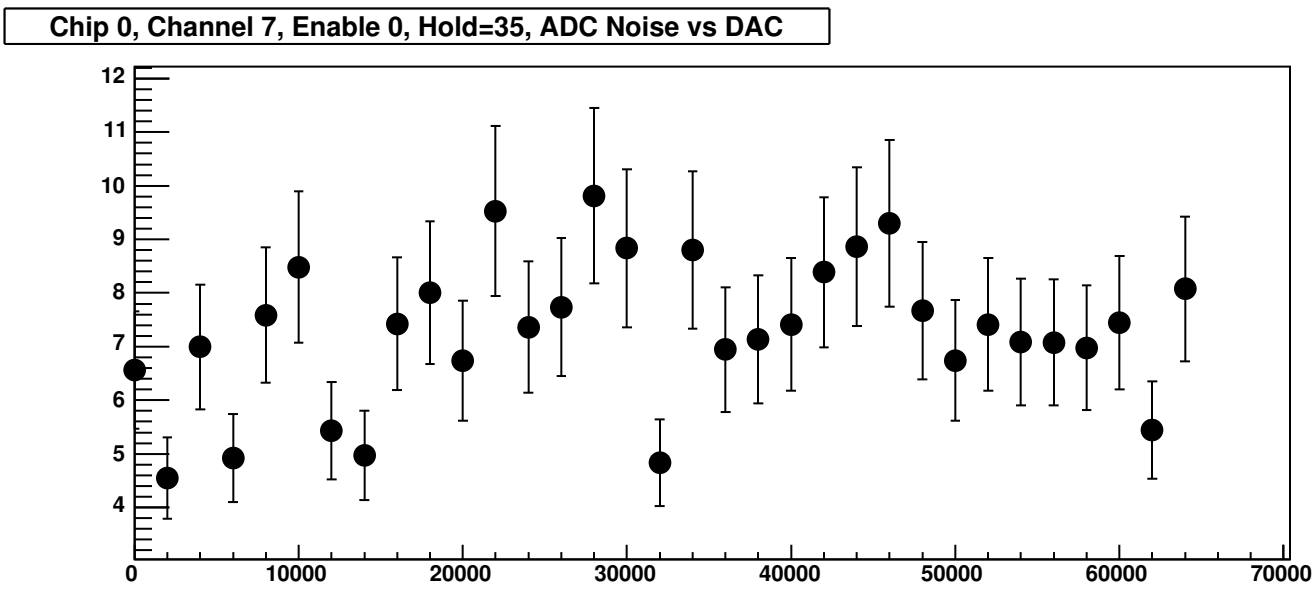

Chip 0, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

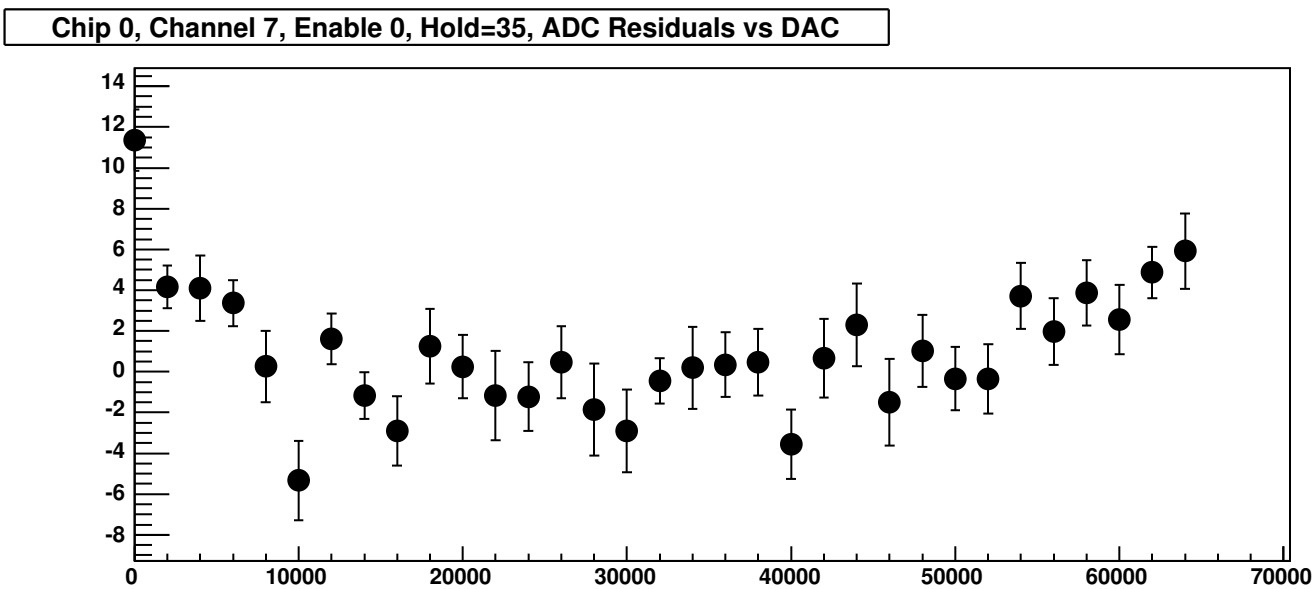

Chip 0, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 53.88 / 23
p0 636.7 ± 0.7699
p1 0.003612 ± 2.391e-05
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

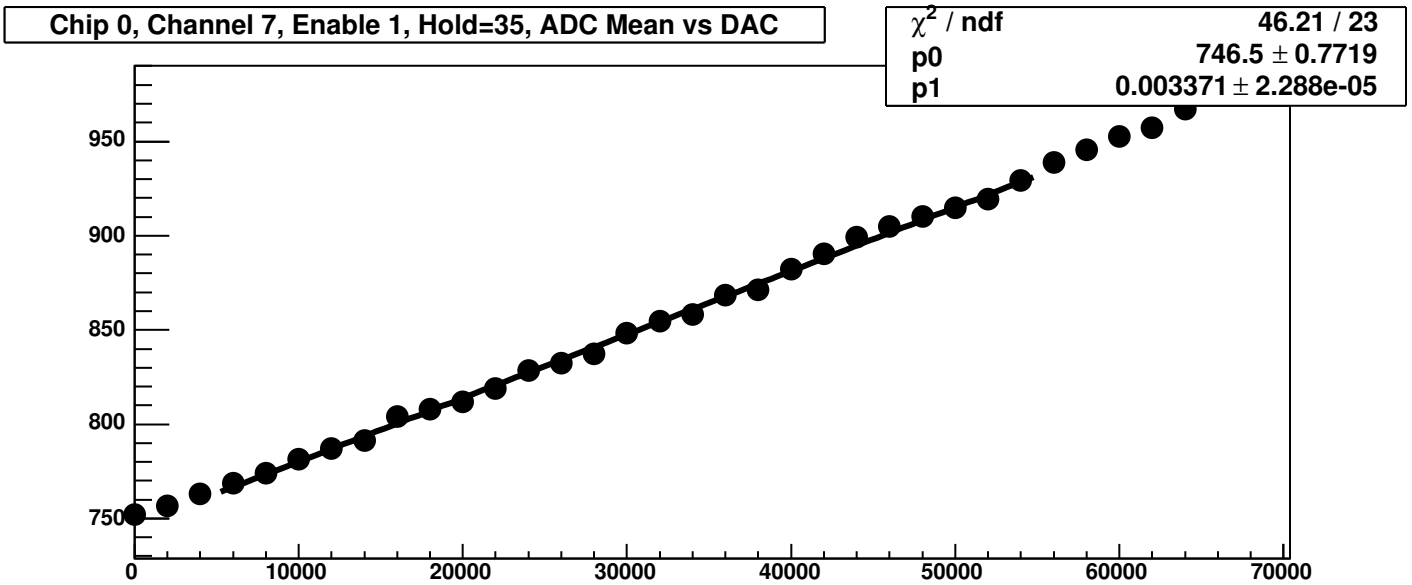

Chip 0, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000

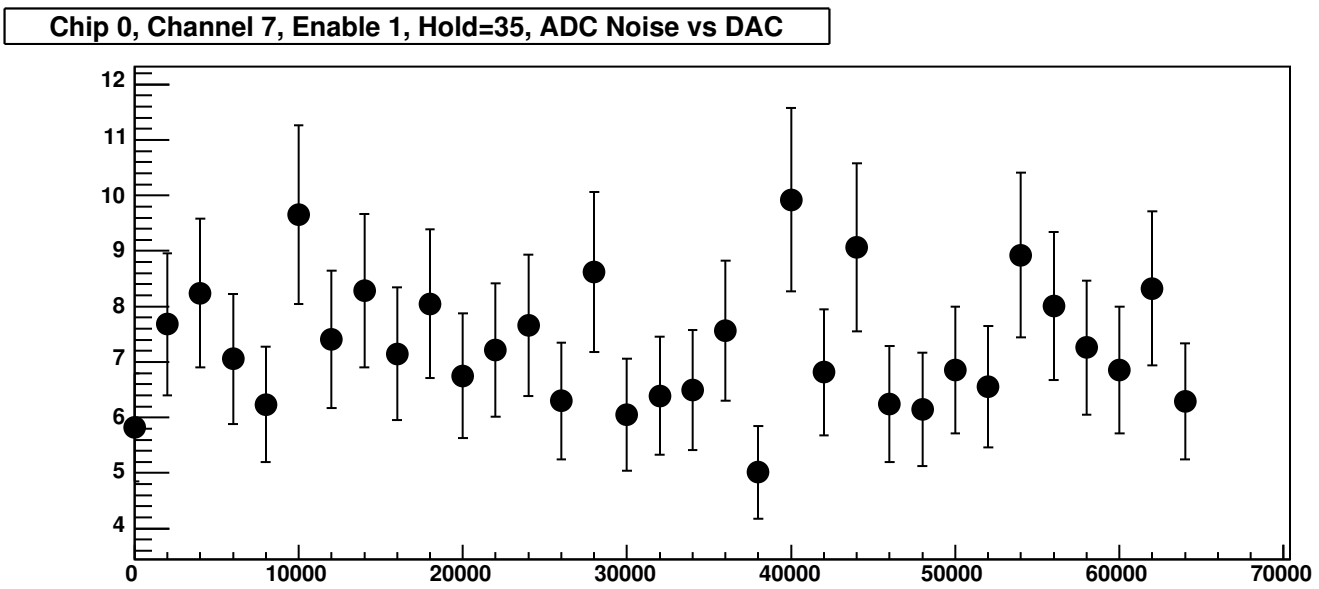

Chip 0, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000

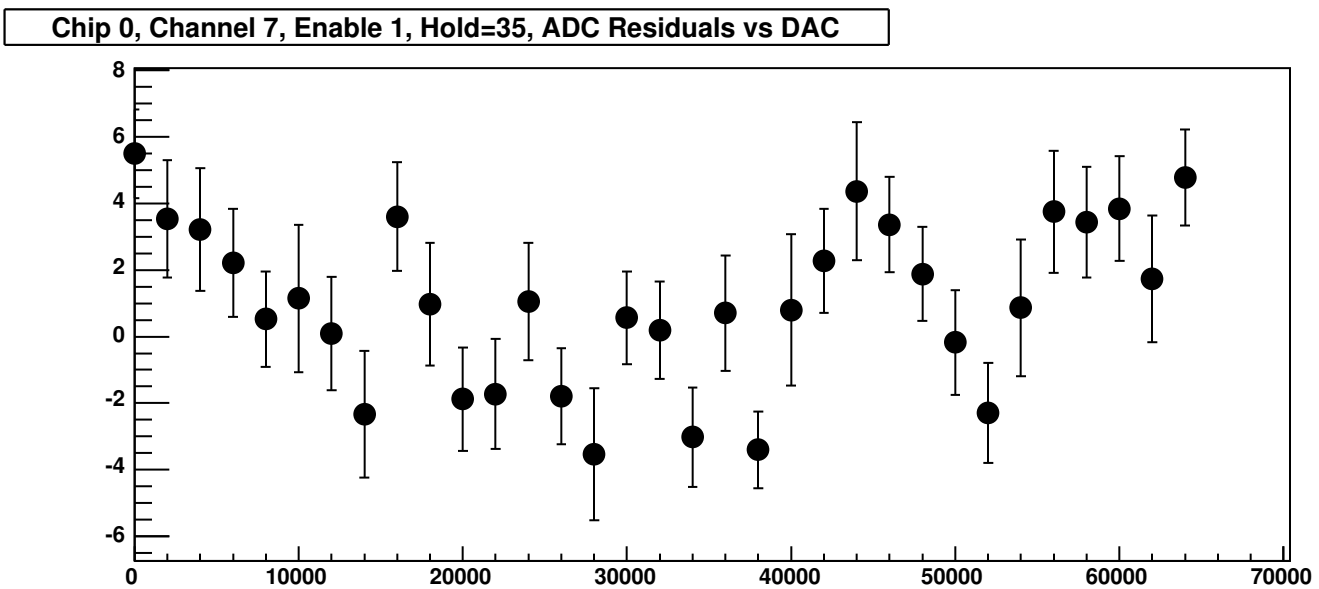

Chip 0, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 38.28 / 23
p0 738.5 ± 0.6829
p1 0.003474 ± 2.162e-05
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000
70000

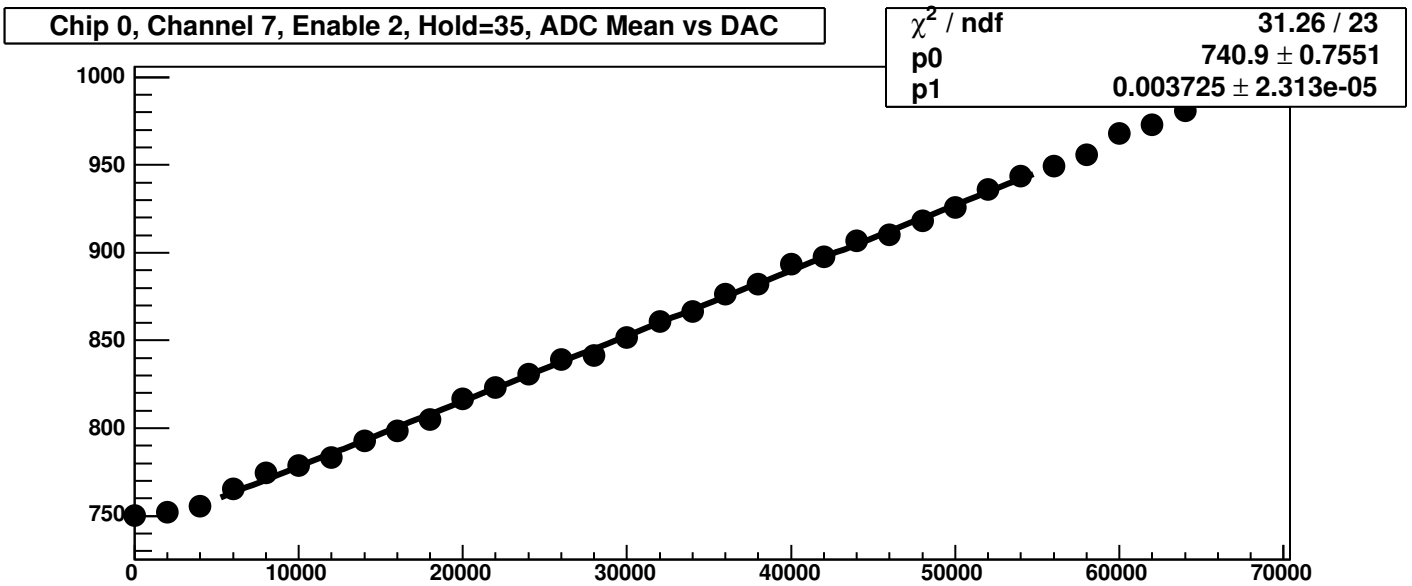

Chip 0, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

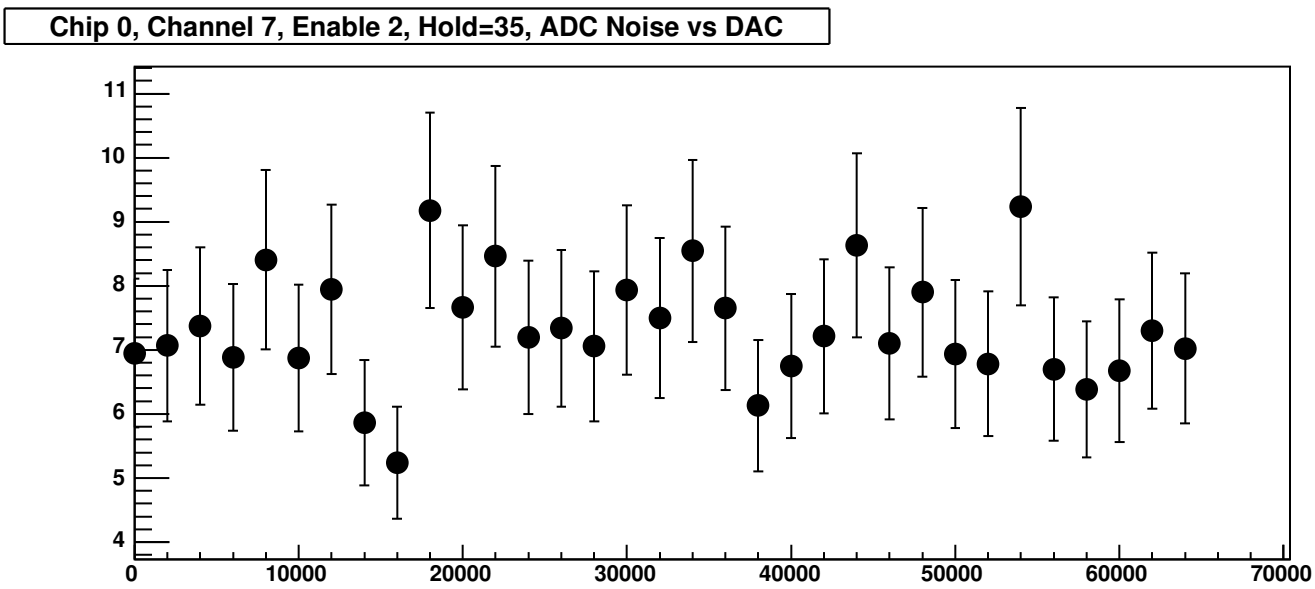

Chip 0, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

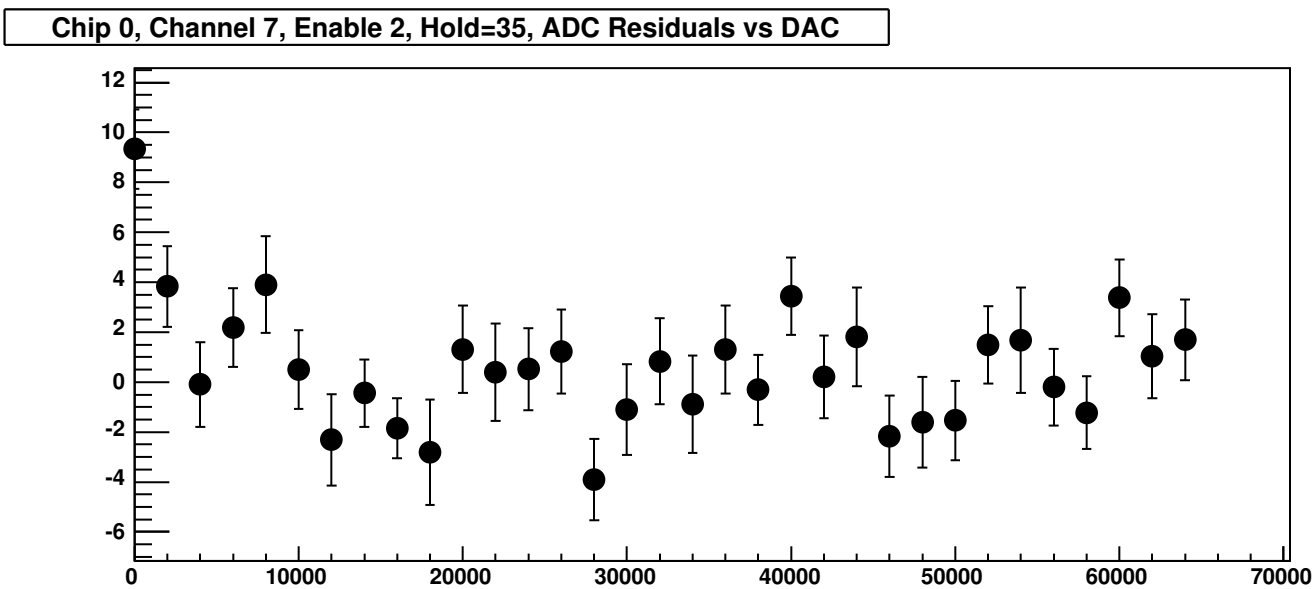

Chip 0, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 46.21 / 23
p0 746.5 ± 0.7719
p1 0.003371 ± 2.288e-05
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000
70000

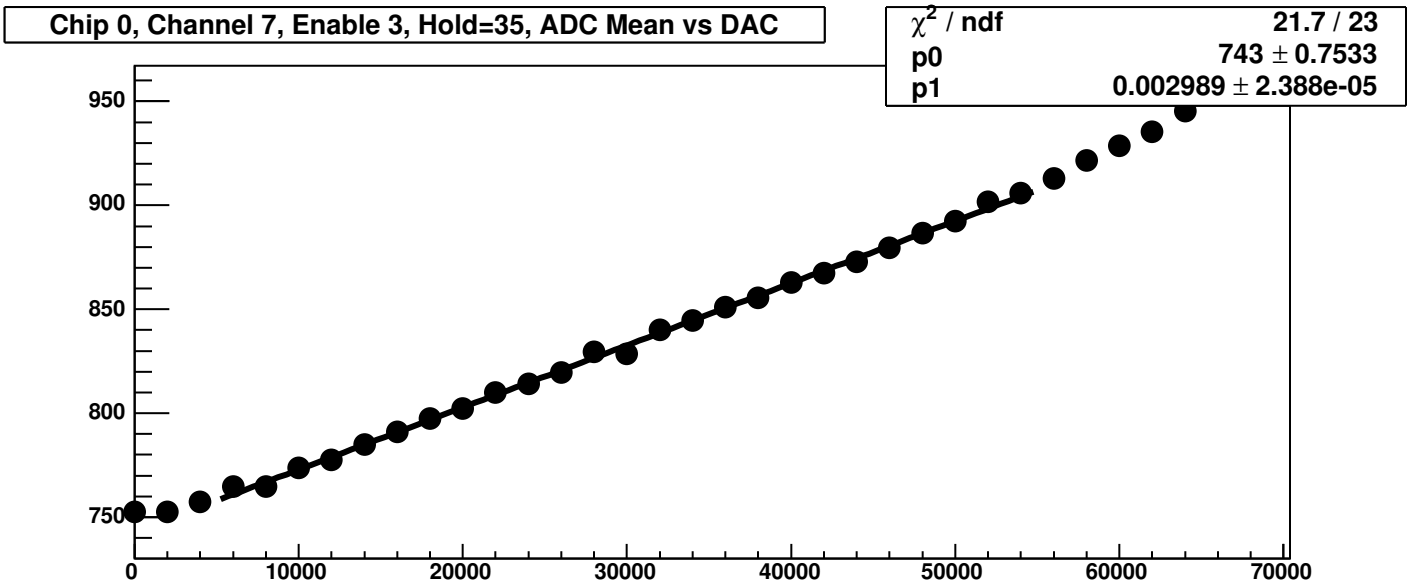

Chip 0, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

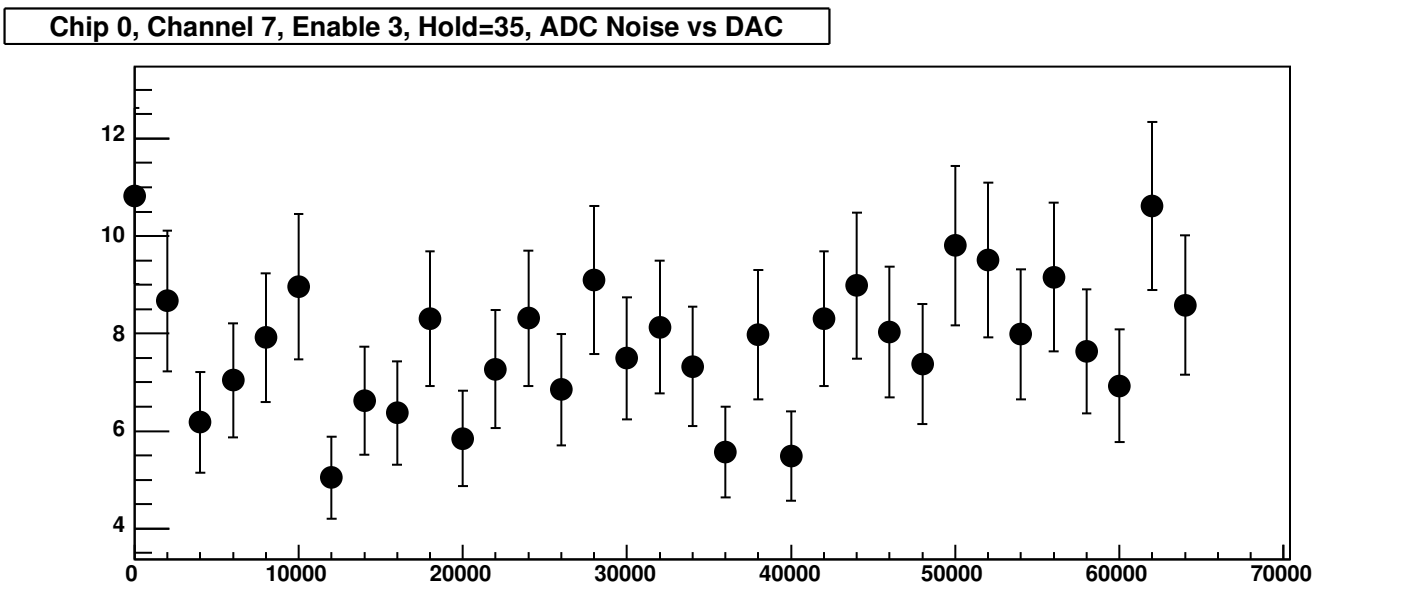

Chip 0, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

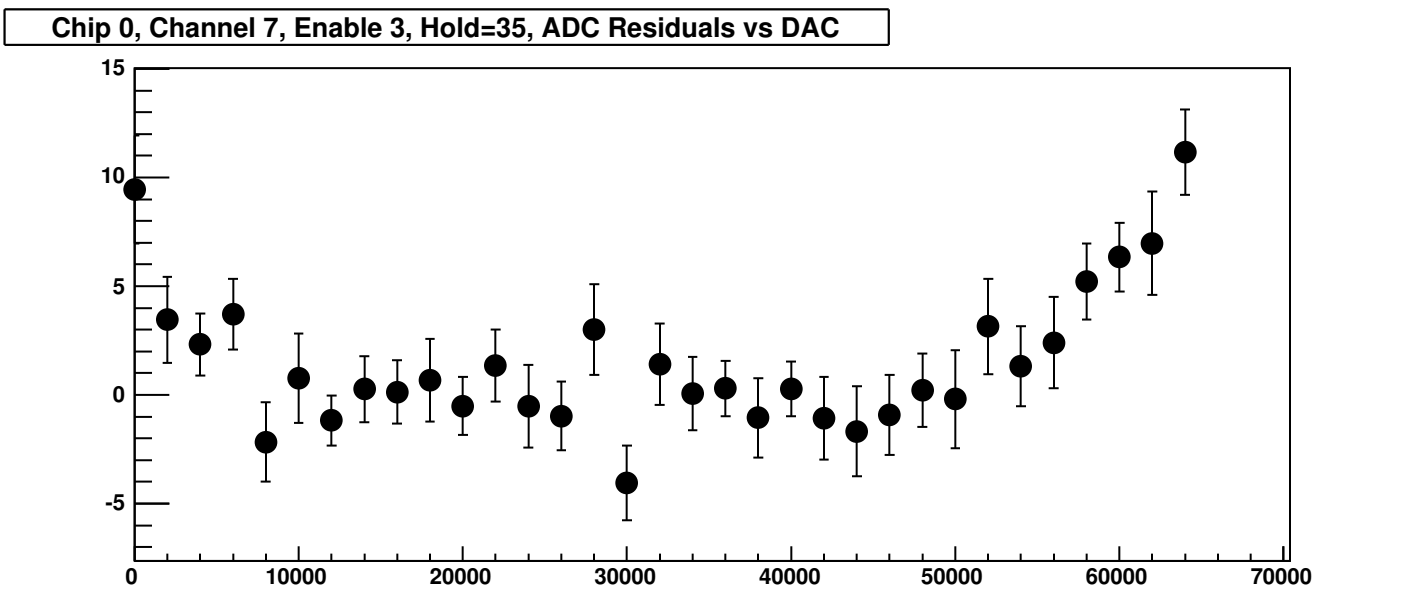

Chip 0, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 31.26 / 23
p0 740.9 ± 0.7551
p1 0.003725 ± 2.313e-05
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000
70000

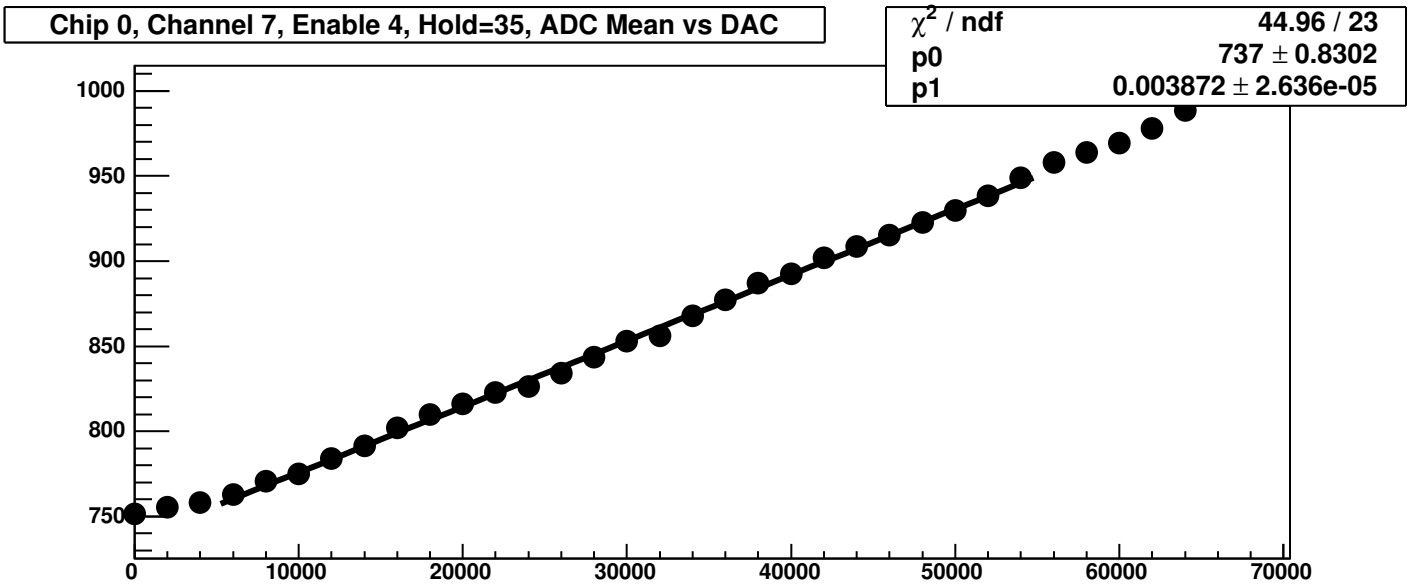

Chip 0, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

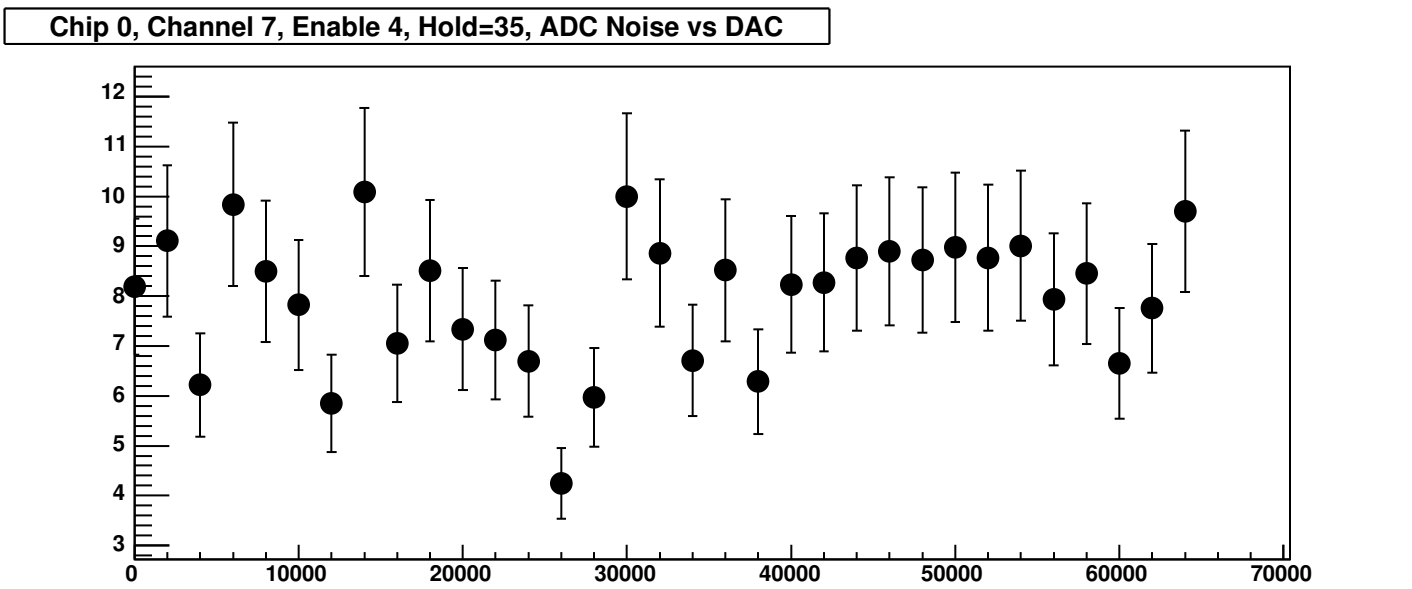

Chip 0, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

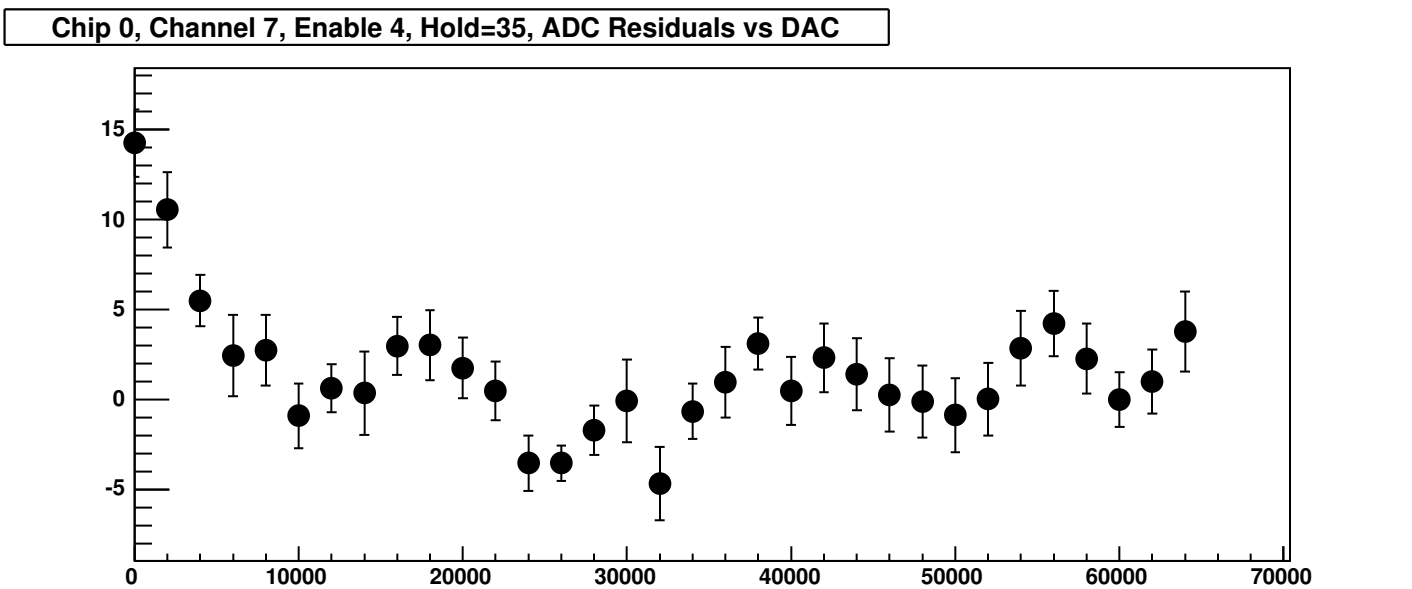

Chip 0, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.7 / 23
p0 743 ± 0.7533
p1 0.002989 ± 2.388e-05
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000
70000

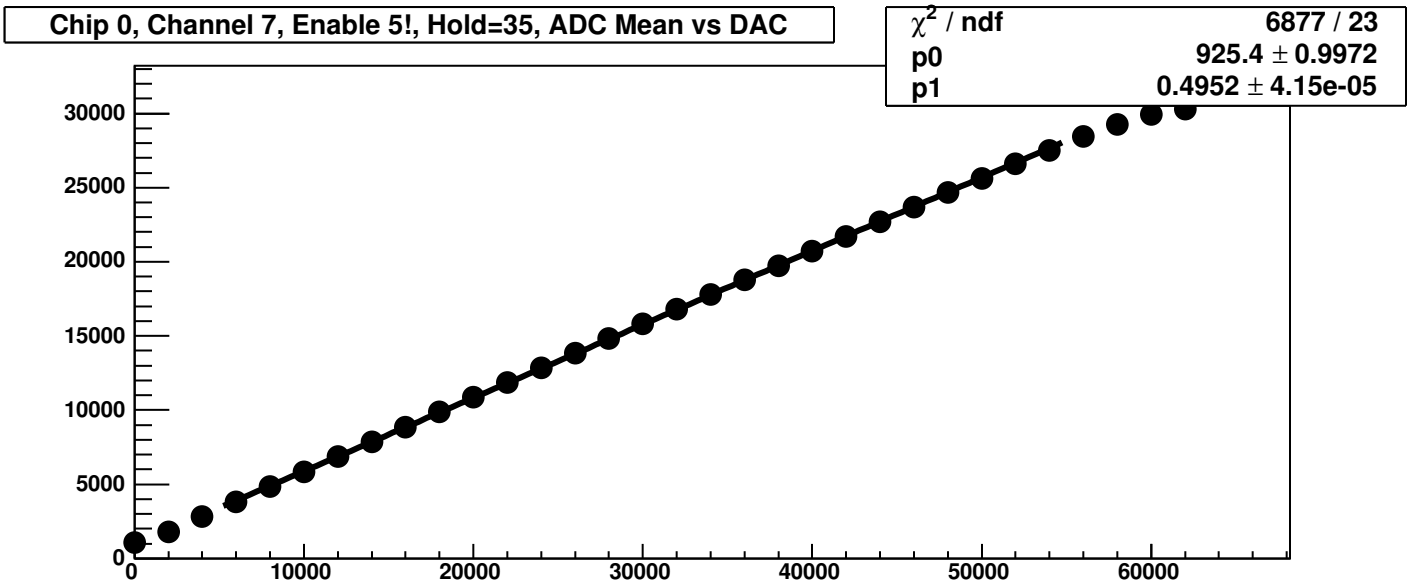

Chip 0, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

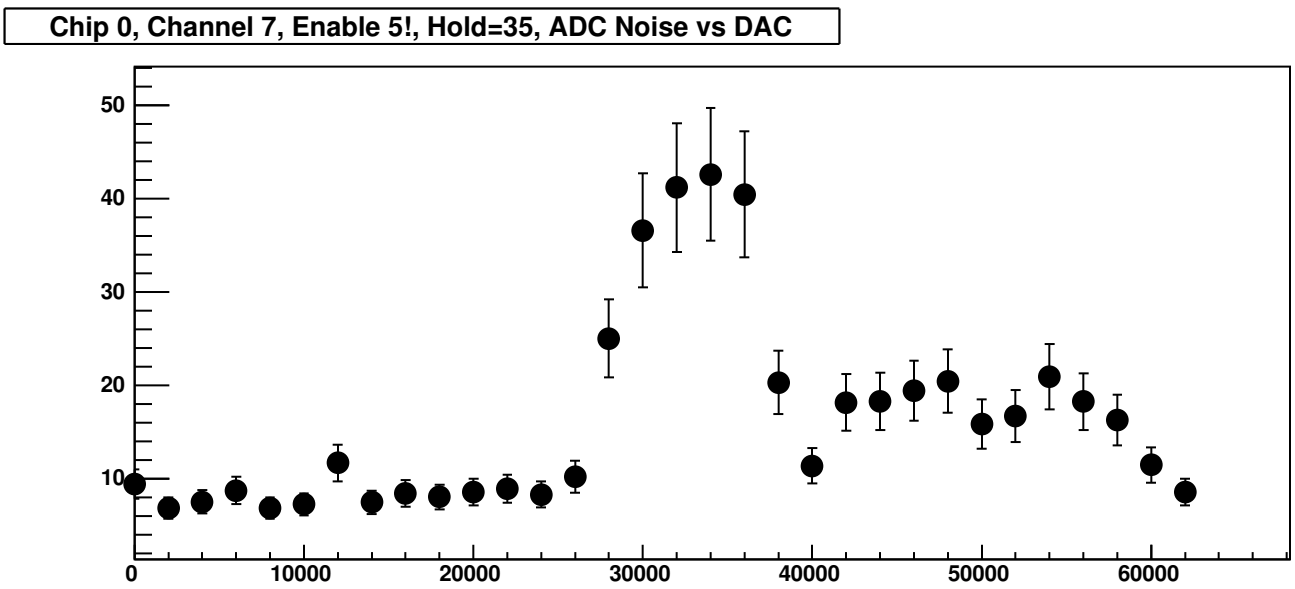

Chip 0, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

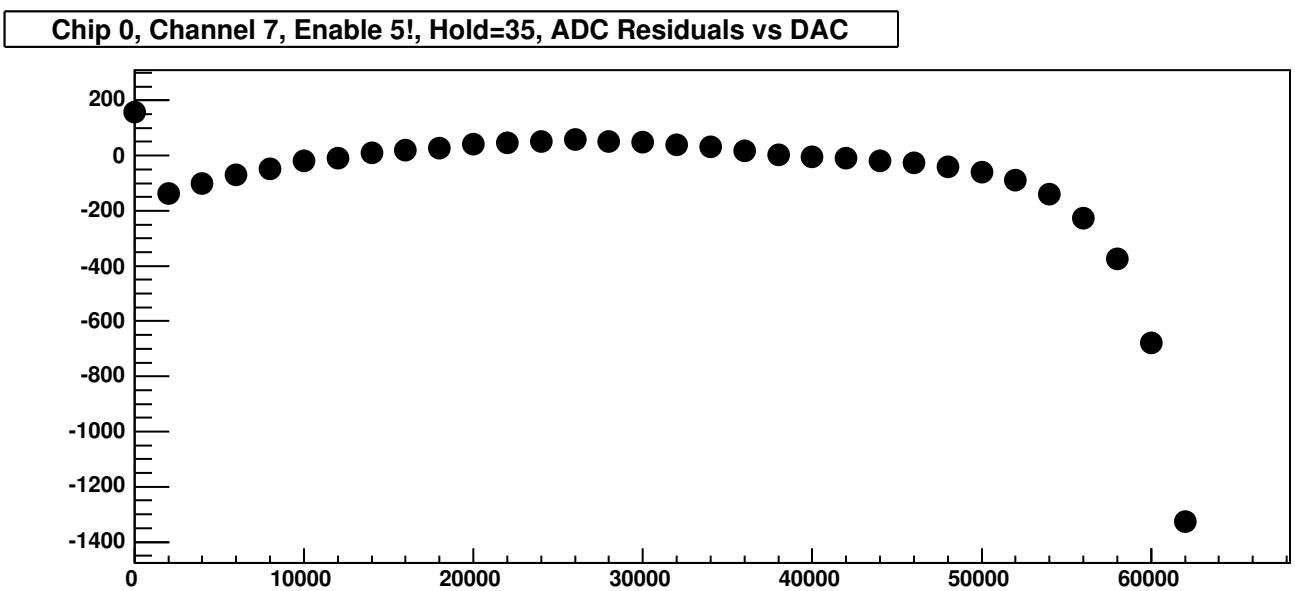

Chip 0, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 44.96 / 23
p0 737 ± 0.8302
p1 0.003872 ± 2.636e-05
1000
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000
70000

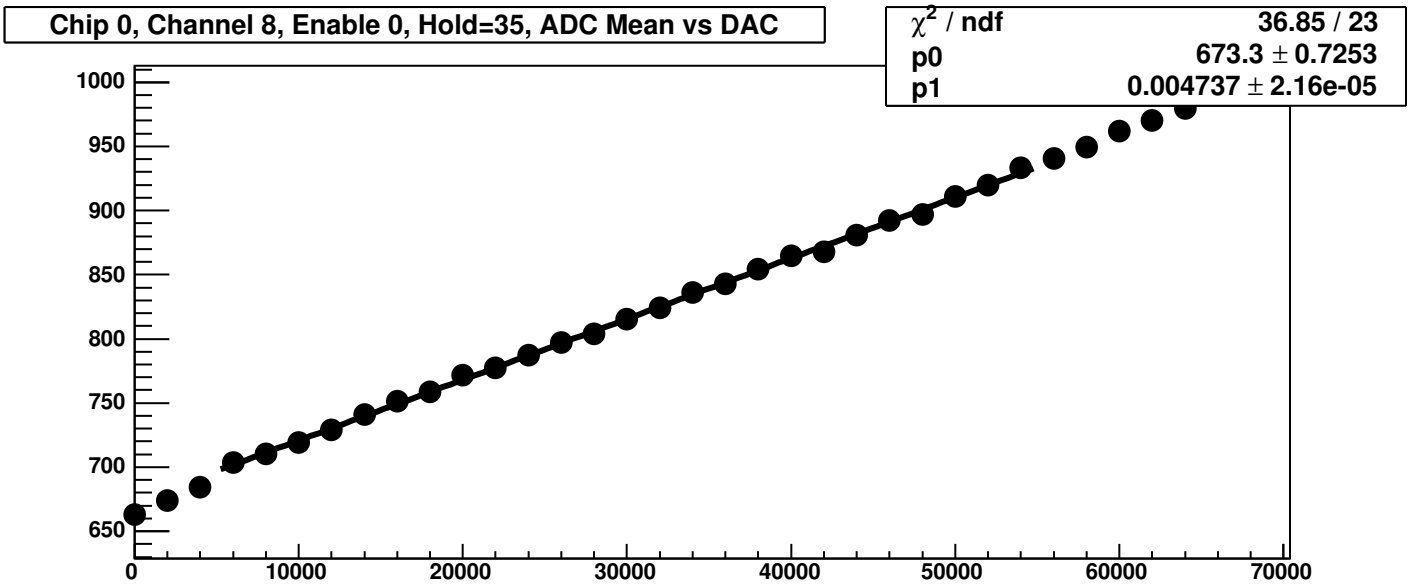

Chip 0, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

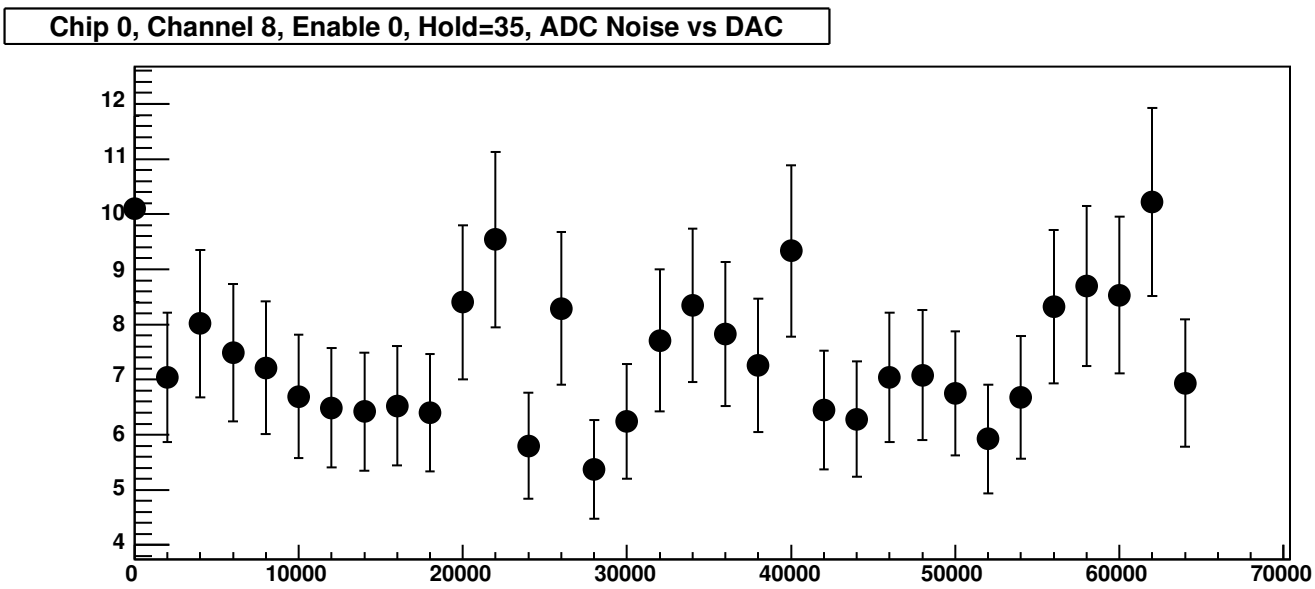

Chip 0, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

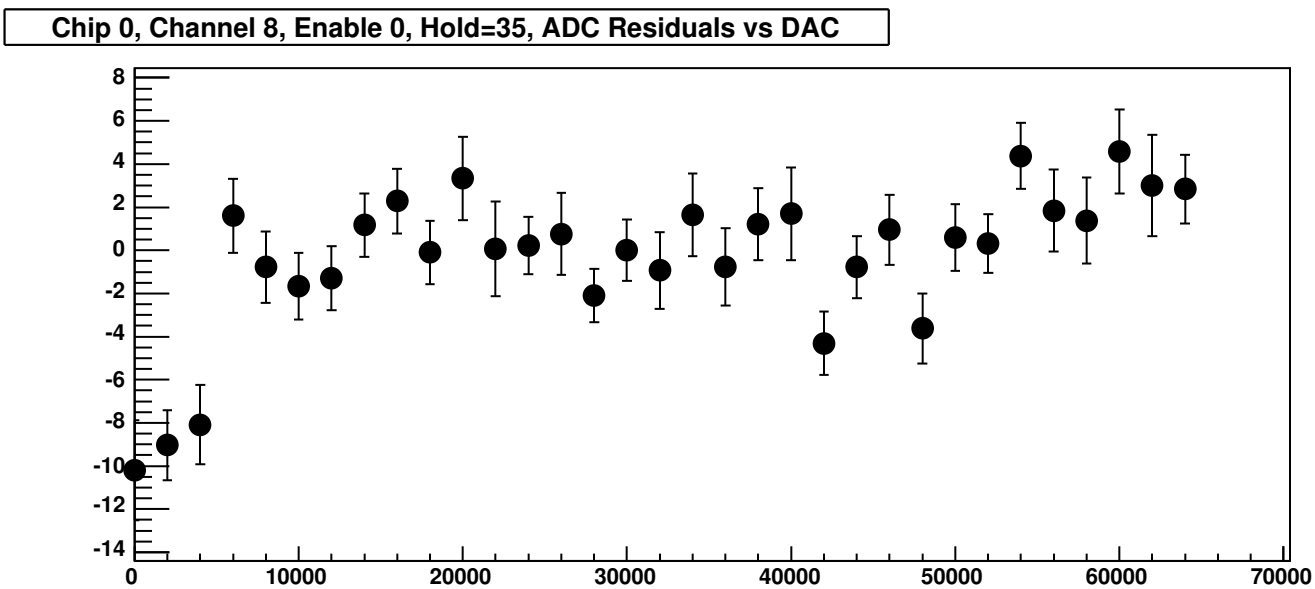

Chip 0, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 6877 / 23
p0 925.4 ± 0.9972
p1 0.4952 ± 4.15e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

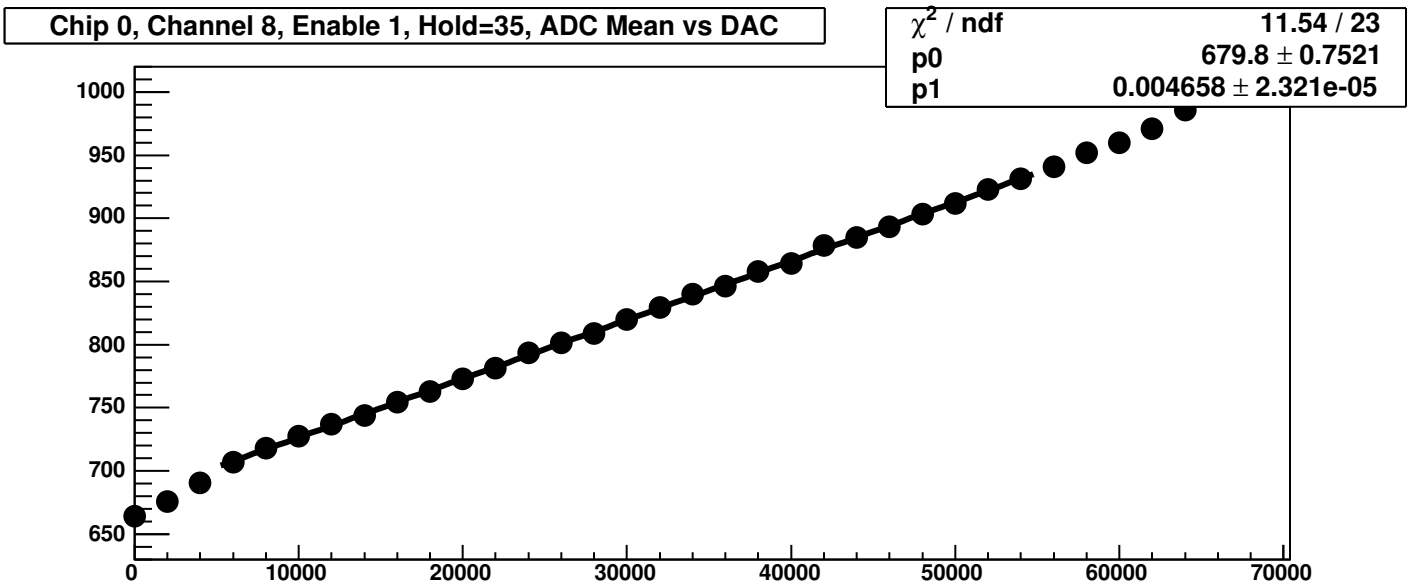

Chip 0, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000

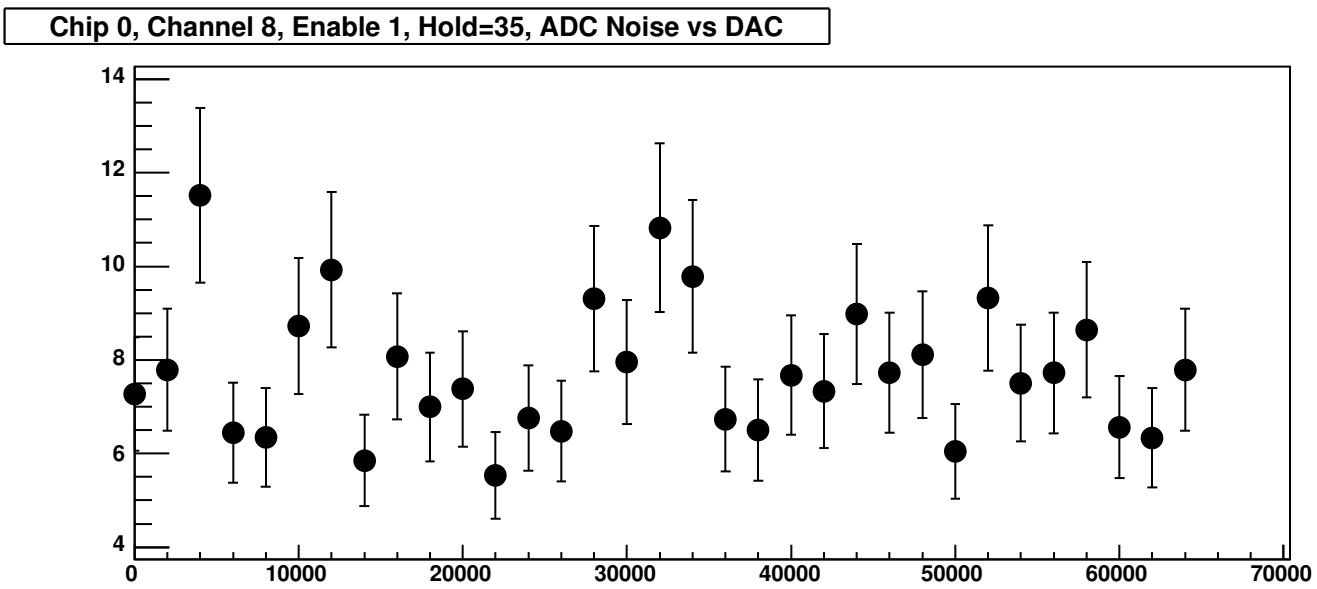

Chip 0, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
10000
20000
30000
40000
50000
60000

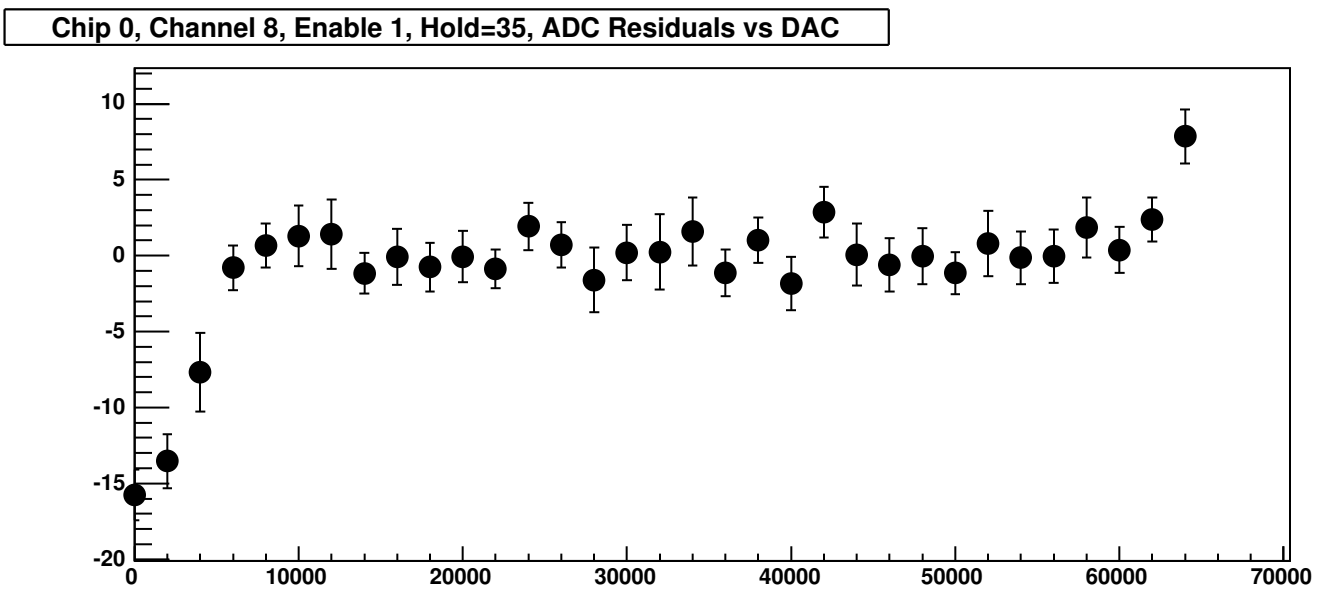

Chip 0, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 36.85 / 23
p0 673.3 ± 0.7253
p1 0.004737 ± 2.16e-05
1000
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

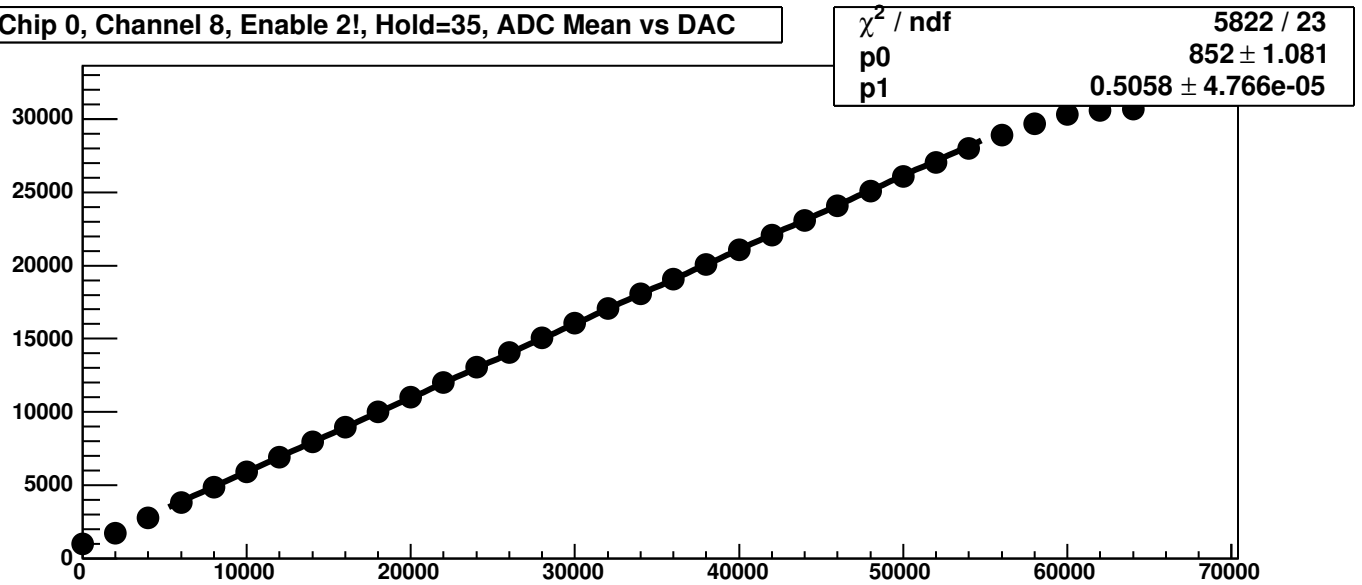

Chip 0, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
-14
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 11.54 / 23
p0 679.8 ± 0.7521
p1 0.004658 ± 2.321e-05
1000
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000
70000

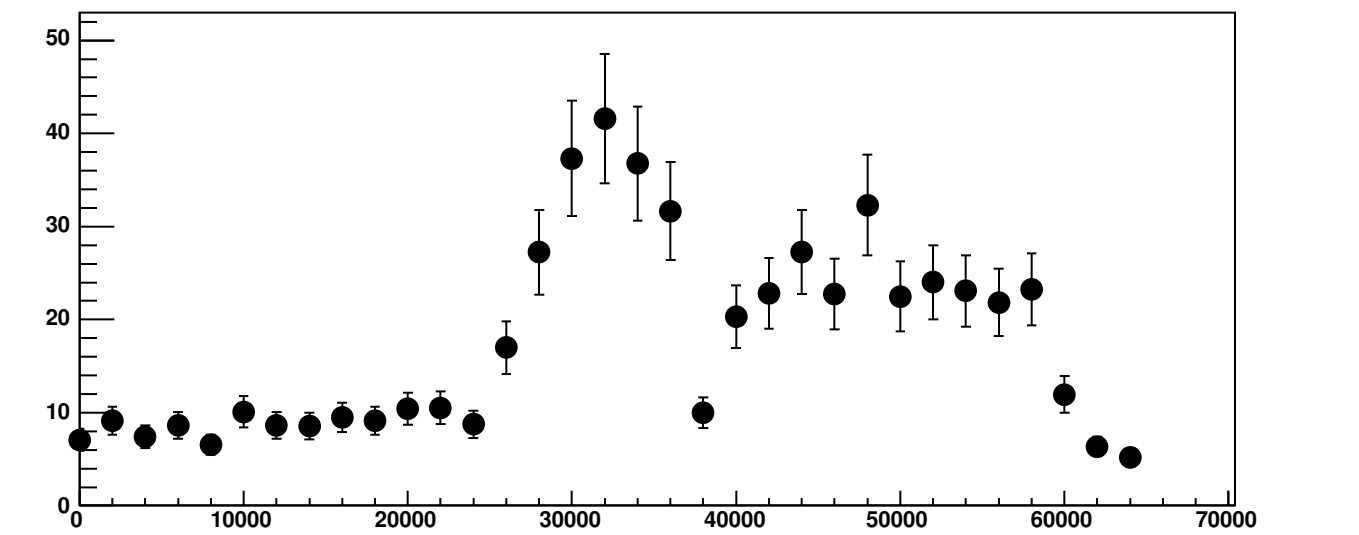

Chip 0, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
0
10000
20000
30000
40000
50000
60000
70000

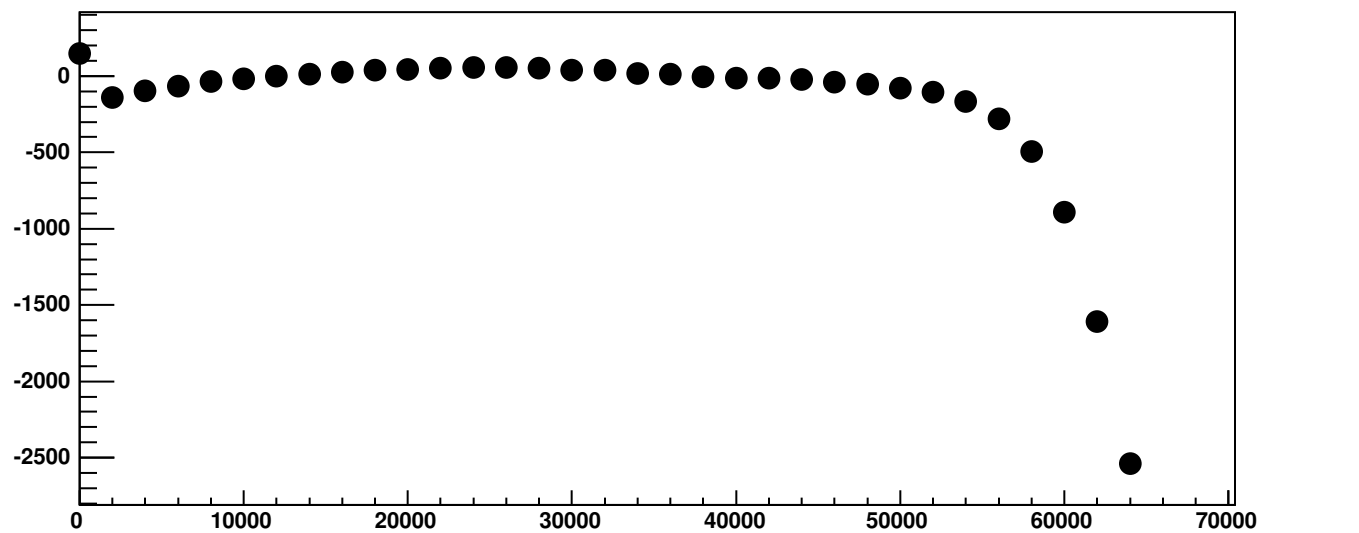
Chip 0, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC

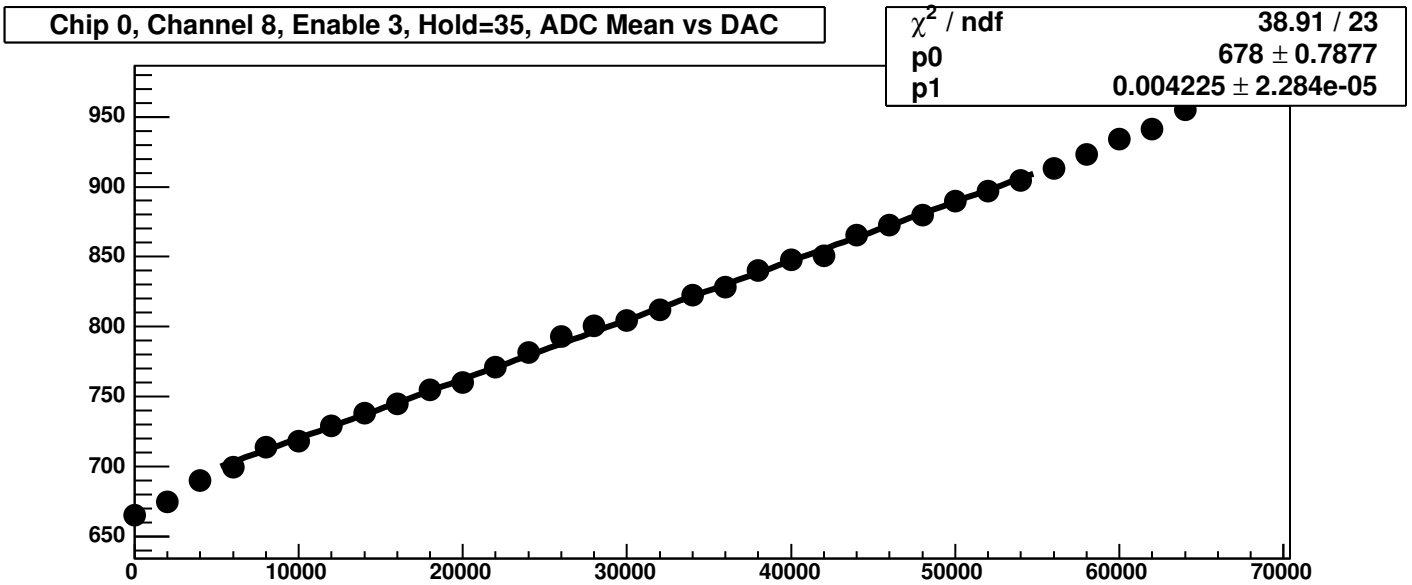


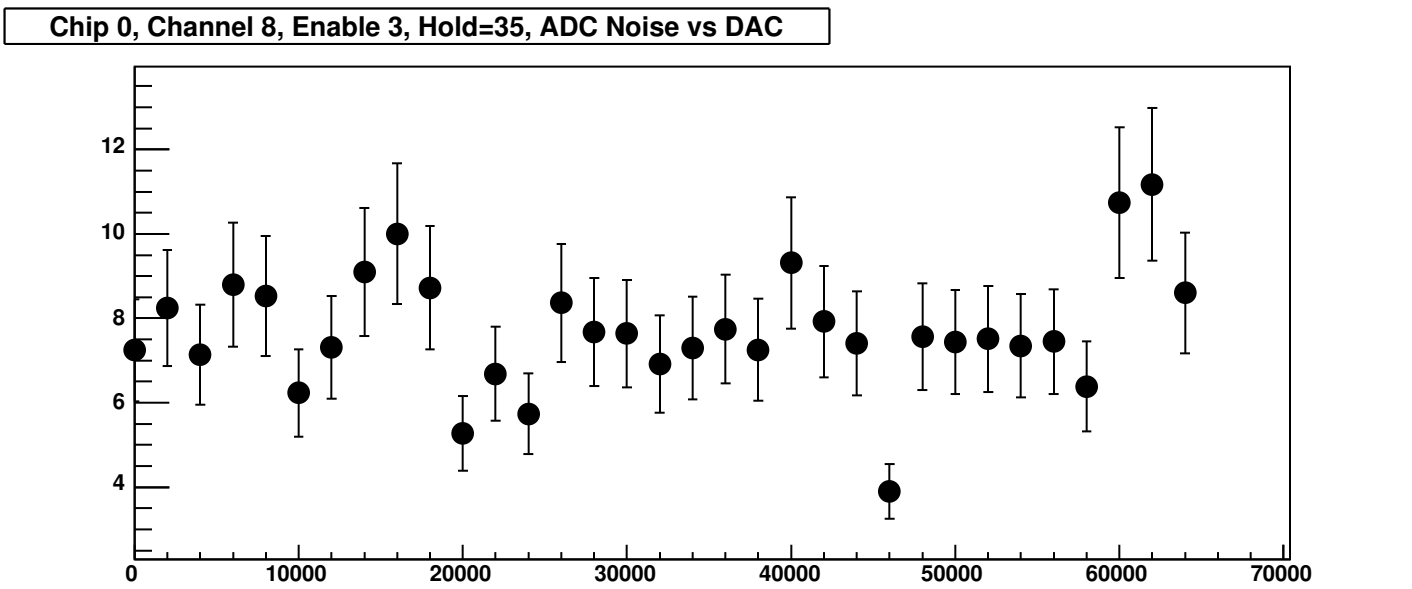
Chip 0, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC

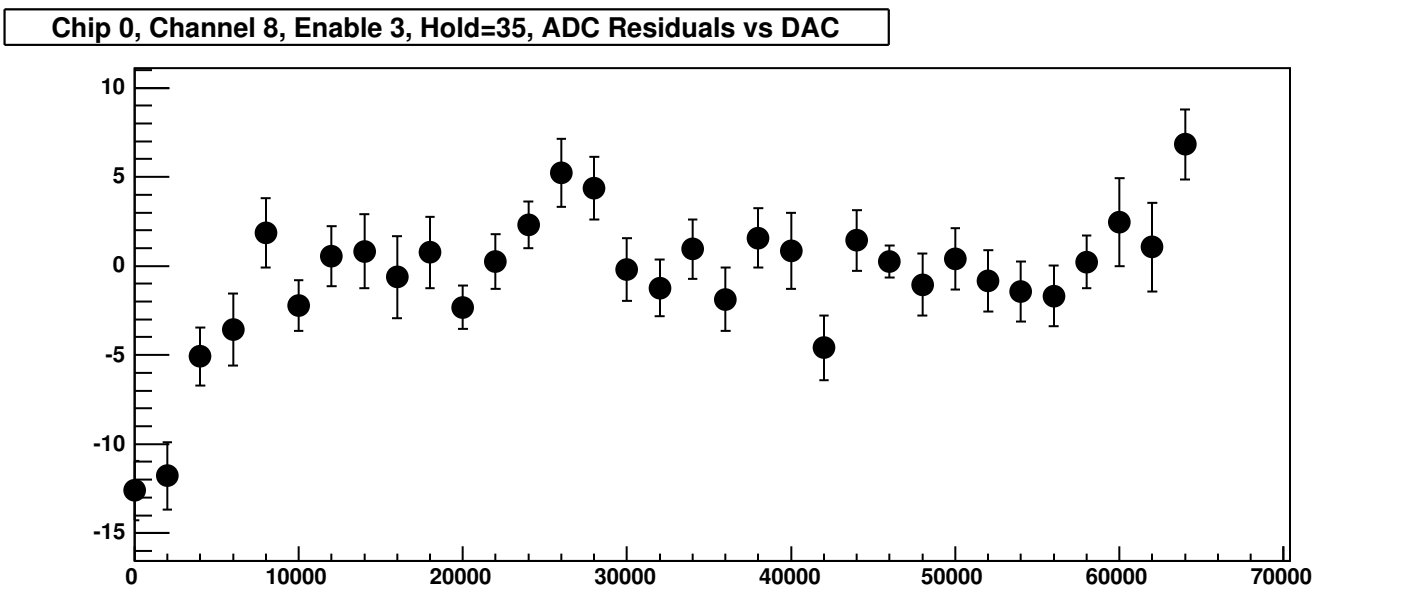


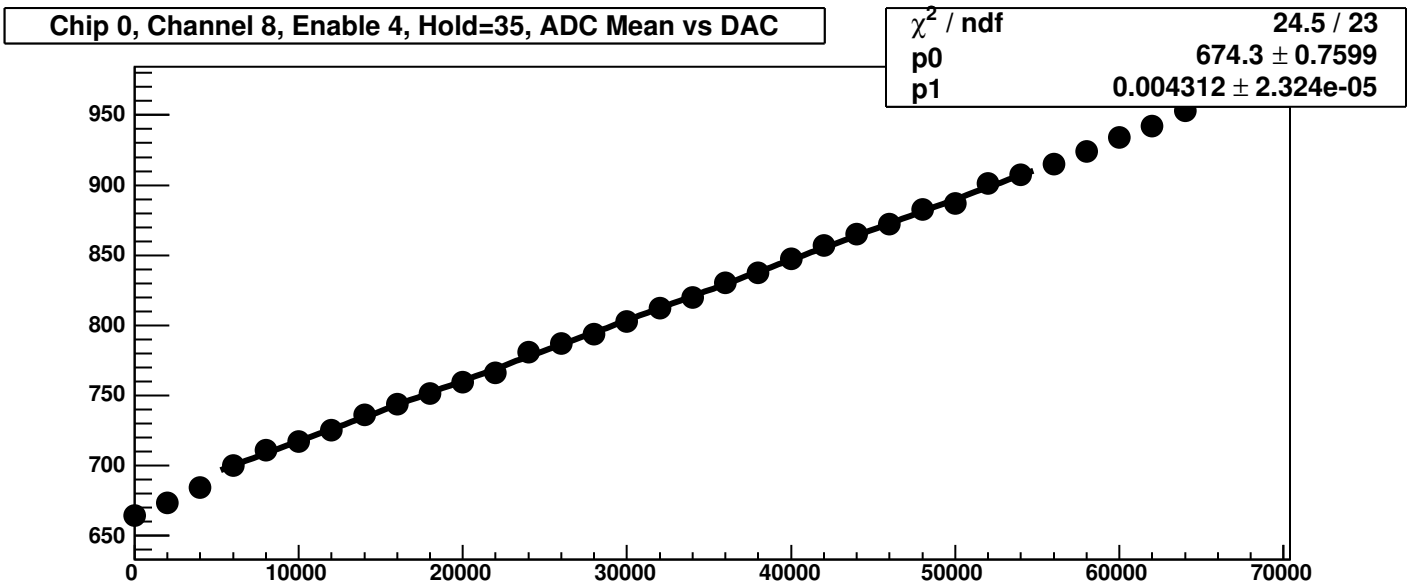
Chip 0, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC

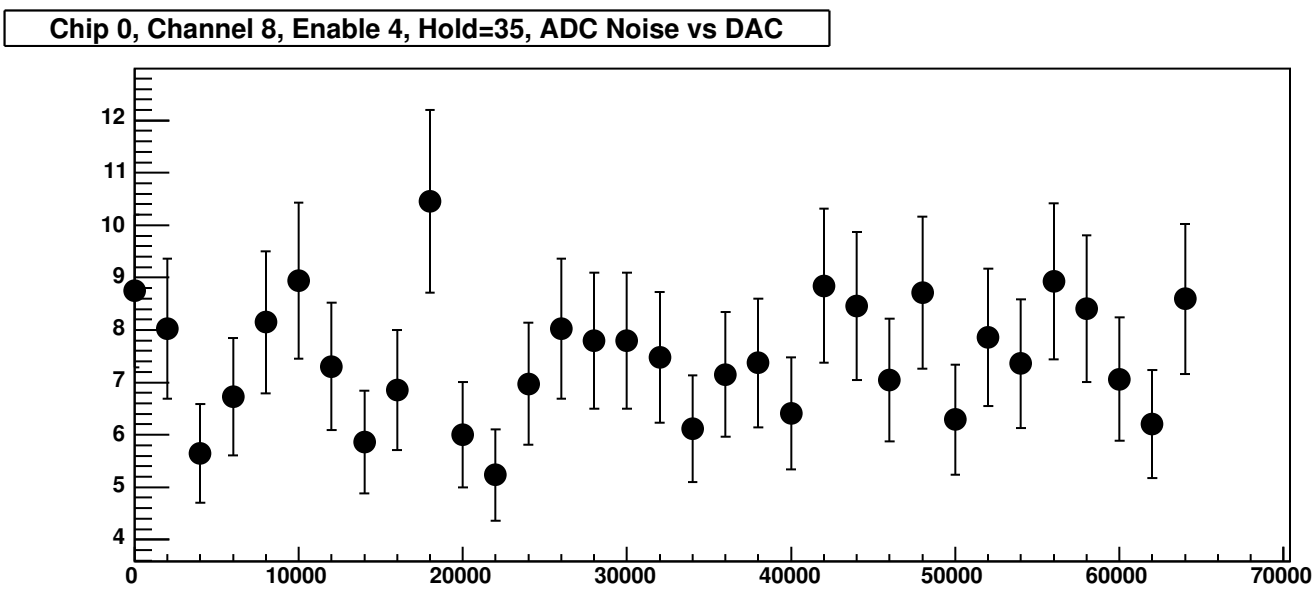


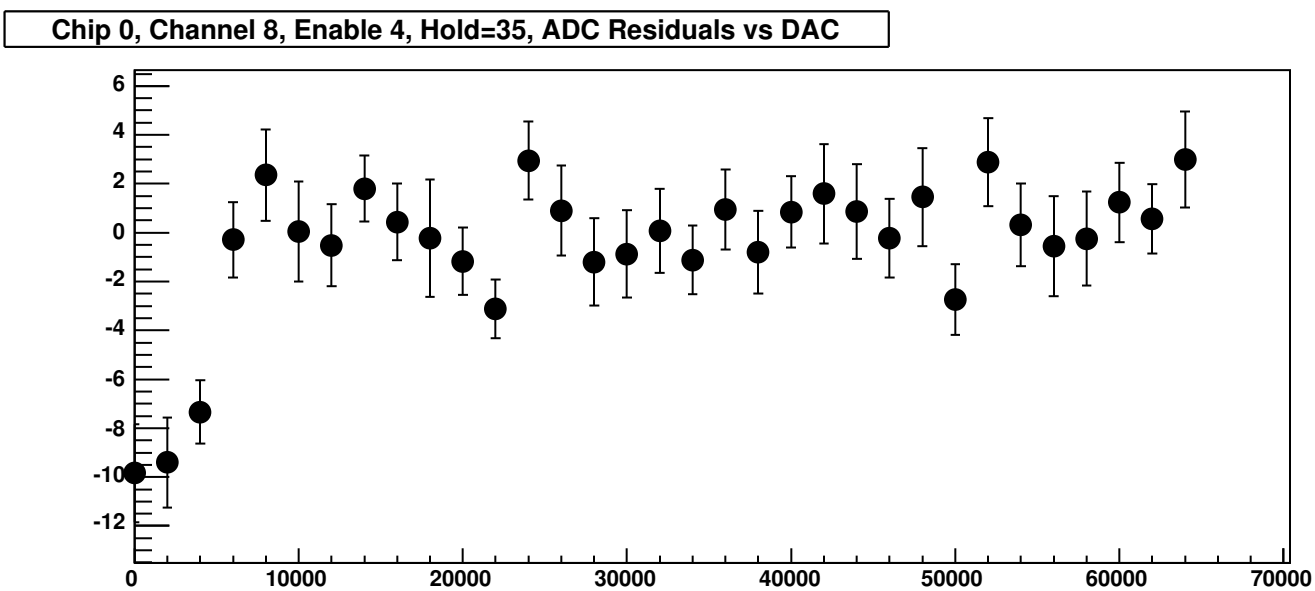

Chip 0, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.91 / 23
p0 678 ± 0.7877
p1 0.004225 ± 2.284e-05
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000
70000

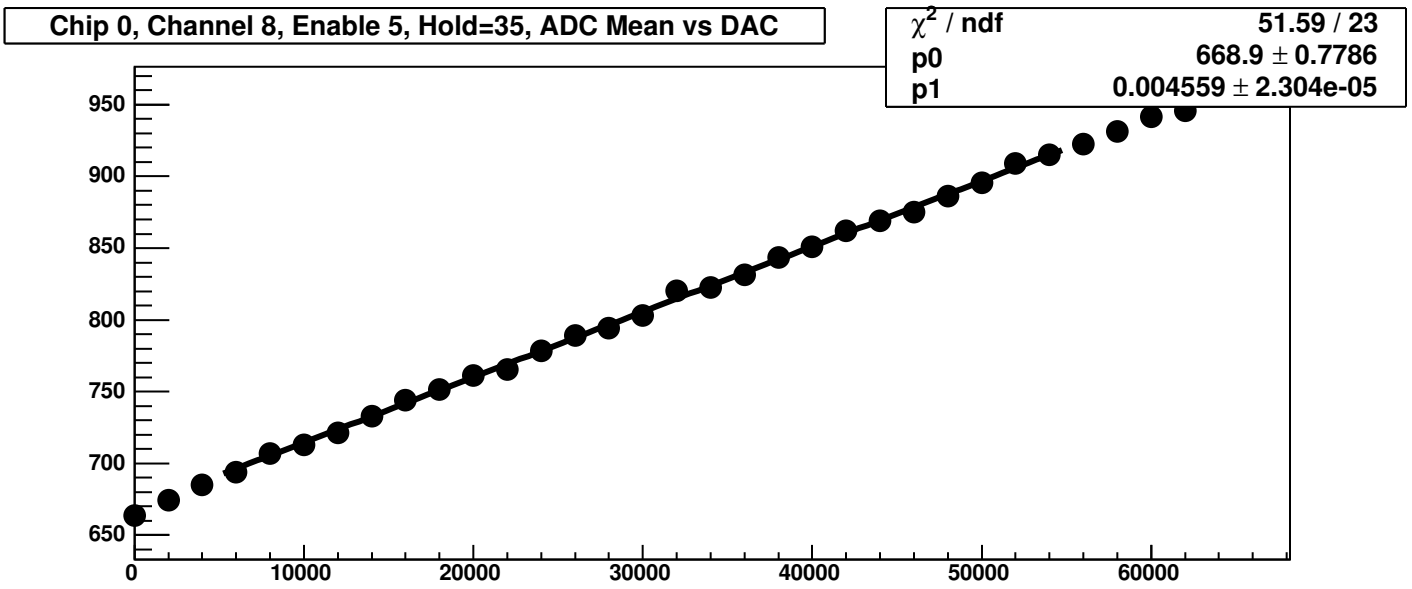

Chip 0, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

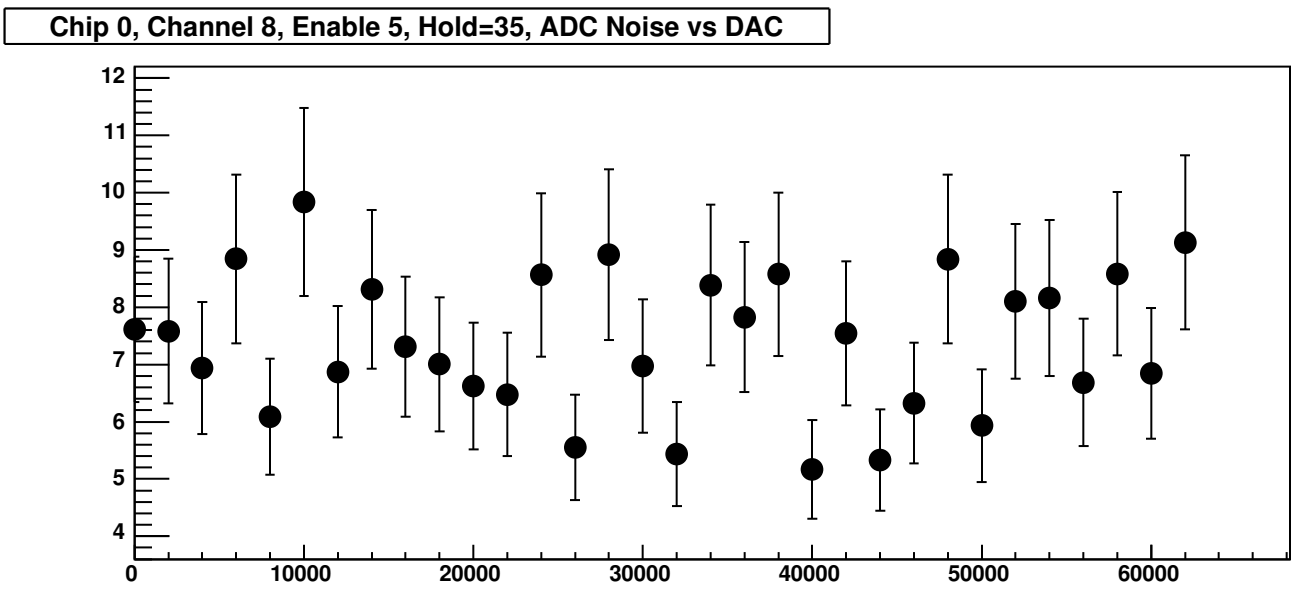

Chip 0, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

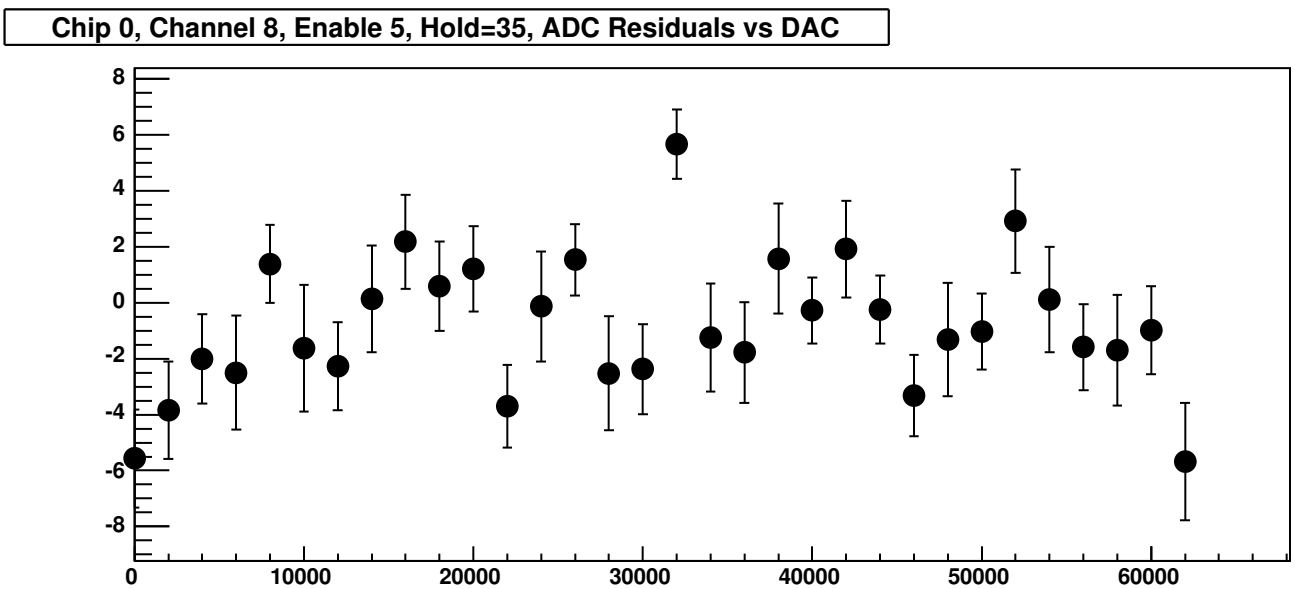

Chip 0, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.5 / 23
p0 674.3 ± 0.7599
p1 0.004312 ± 2.324e-05
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000
70000

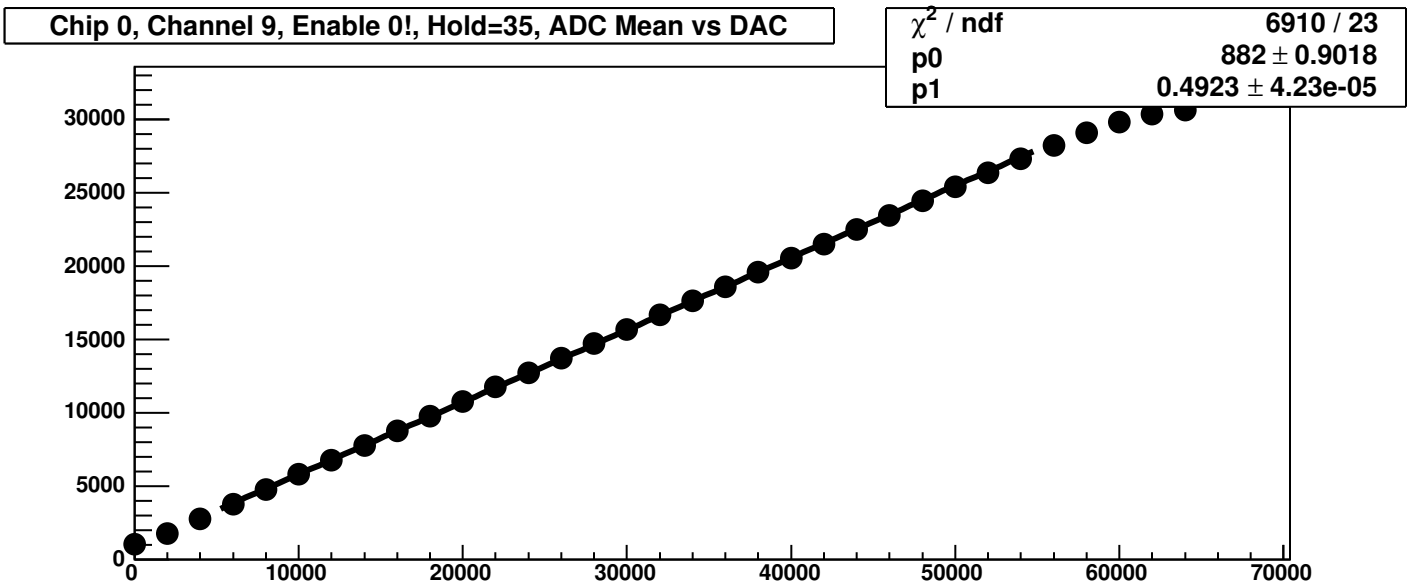

Chip 0, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

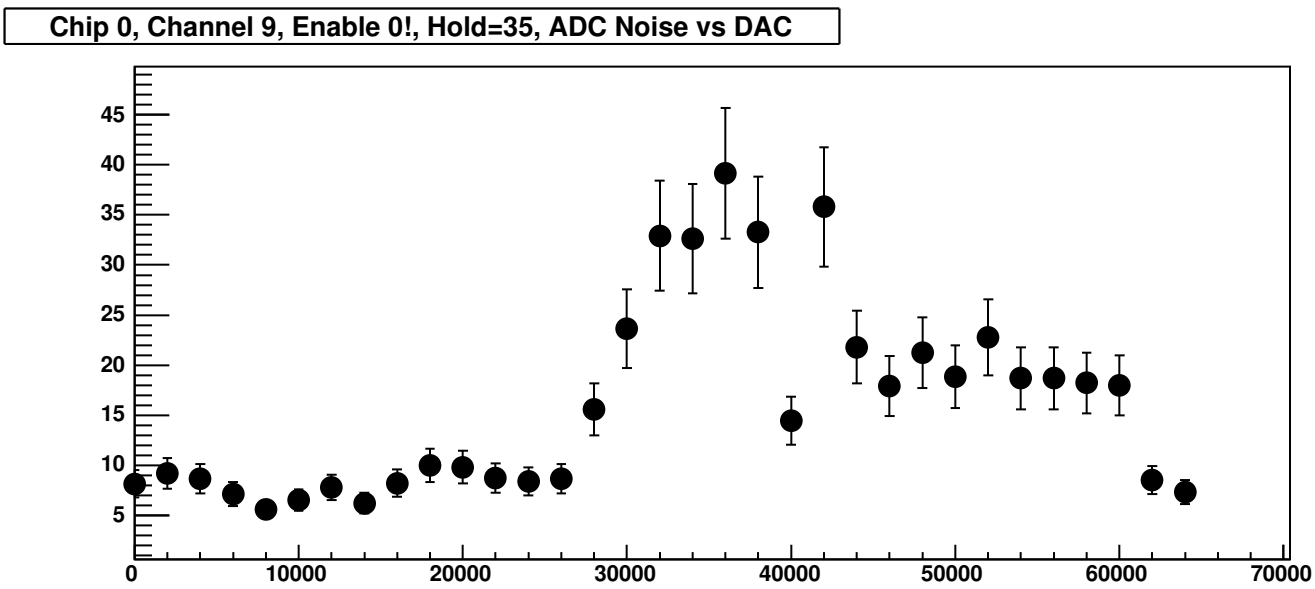

Chip 0, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
0
10000
20000
30000
40000
50000
60000
70000

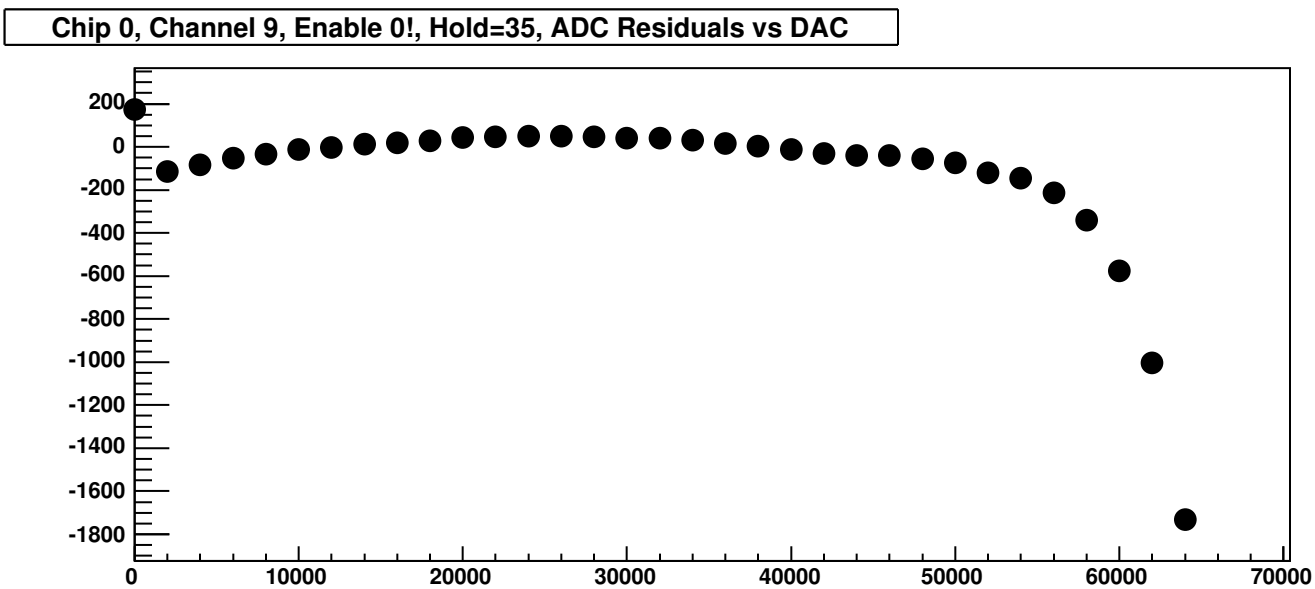

Chip 0, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 51.59 / 23
p0 668.9 ± 0.7786
p1 0.004559 ± 2.304e-05
950
900
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

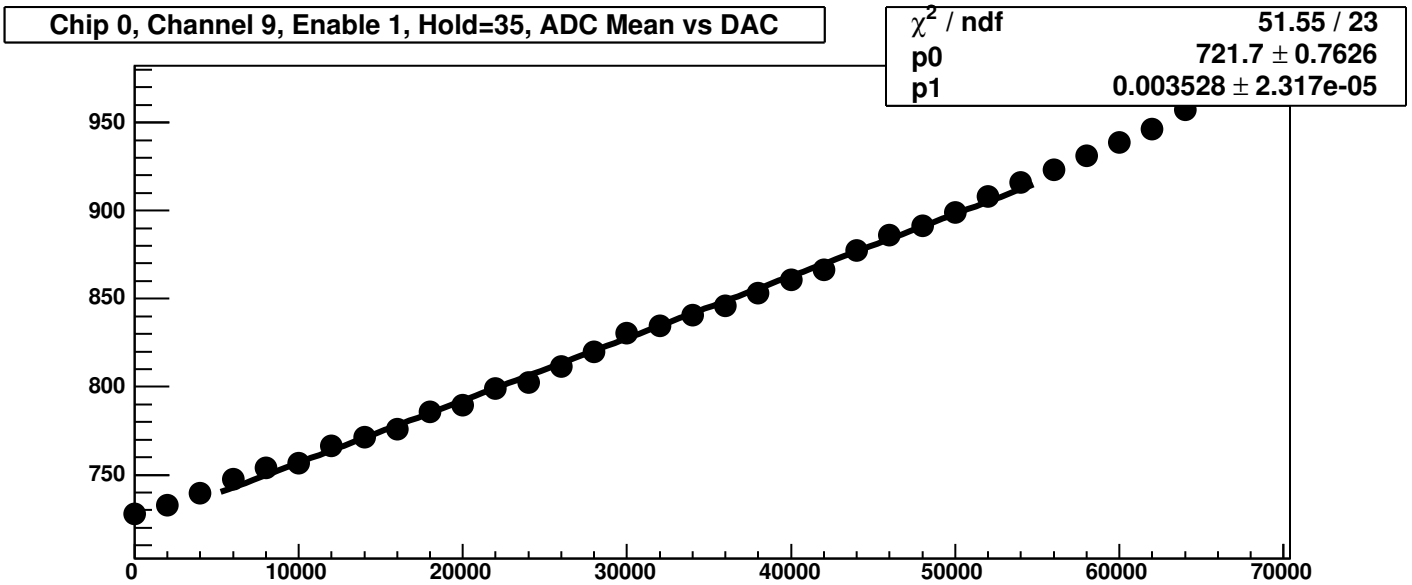

Chip 0, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

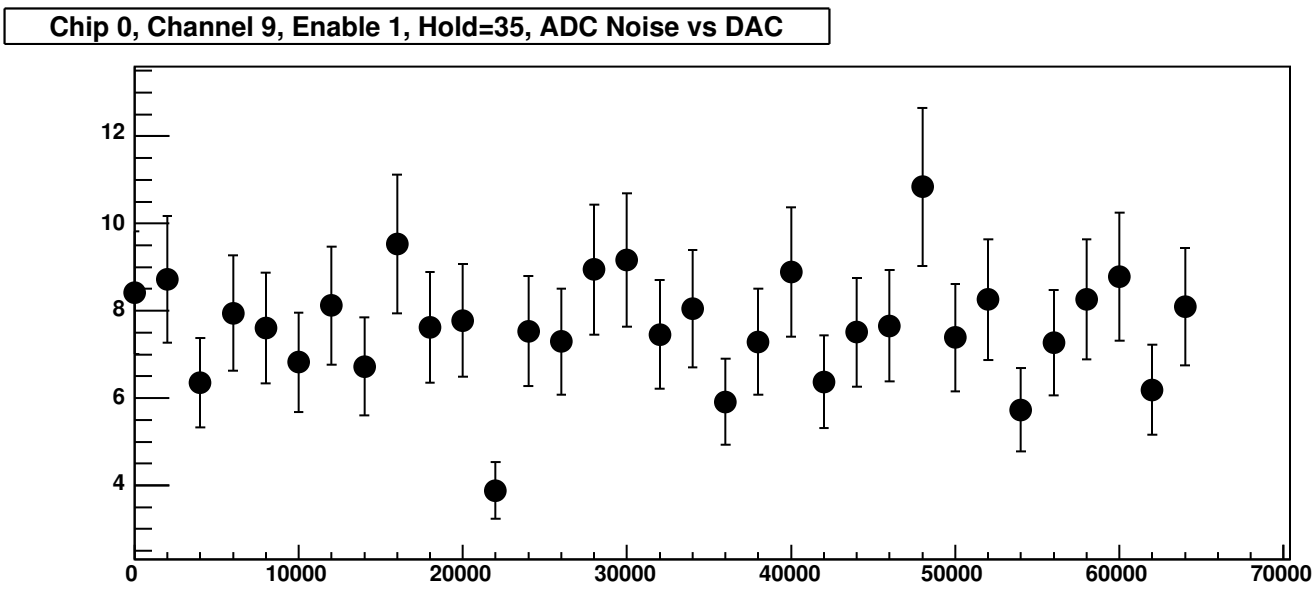

Chip 0, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000

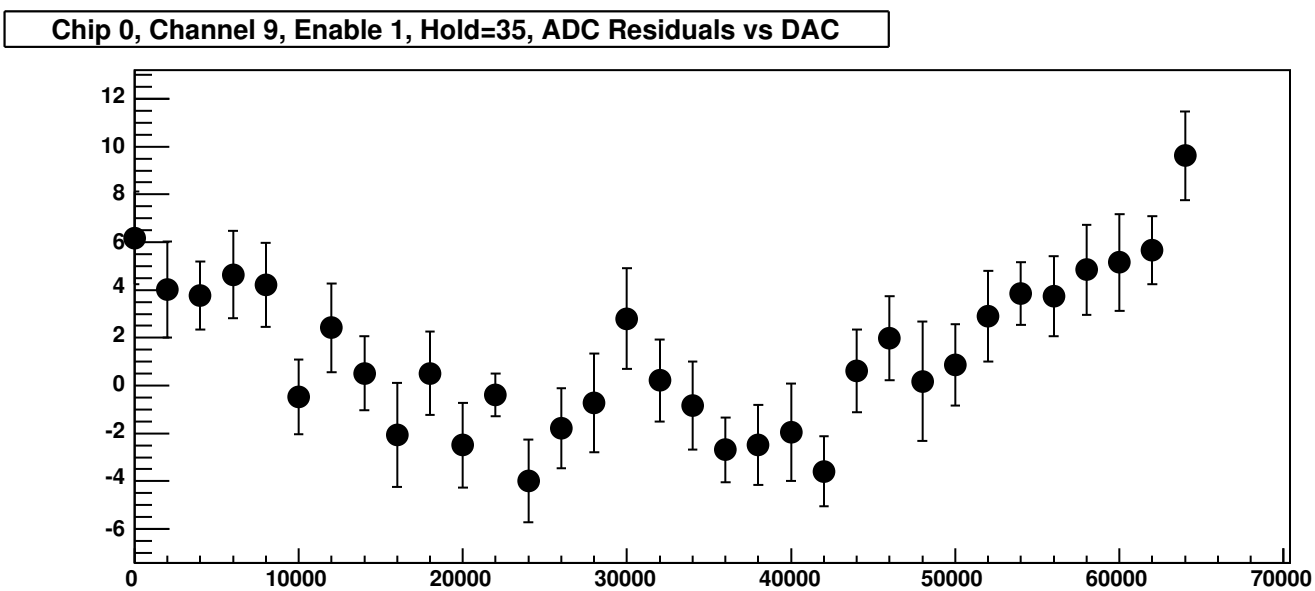

Chip 0, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 6910 / 23
p0 882 ± 0.9018
p1 0.4923 ± 4.23e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

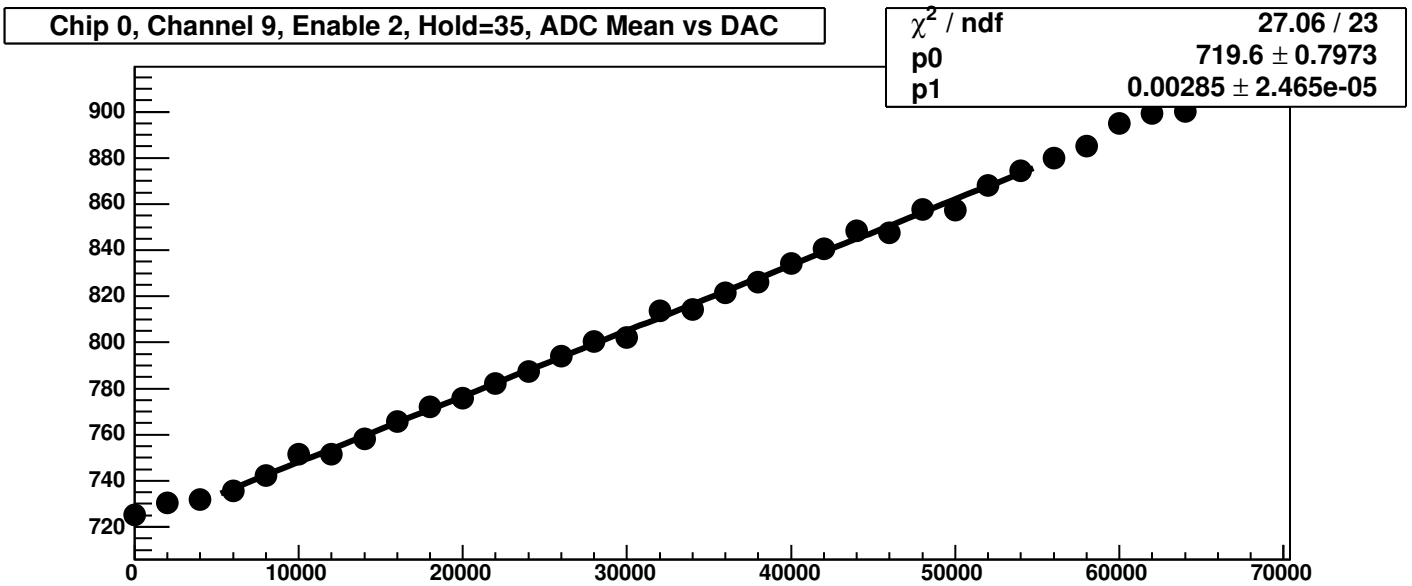

Chip 0, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000

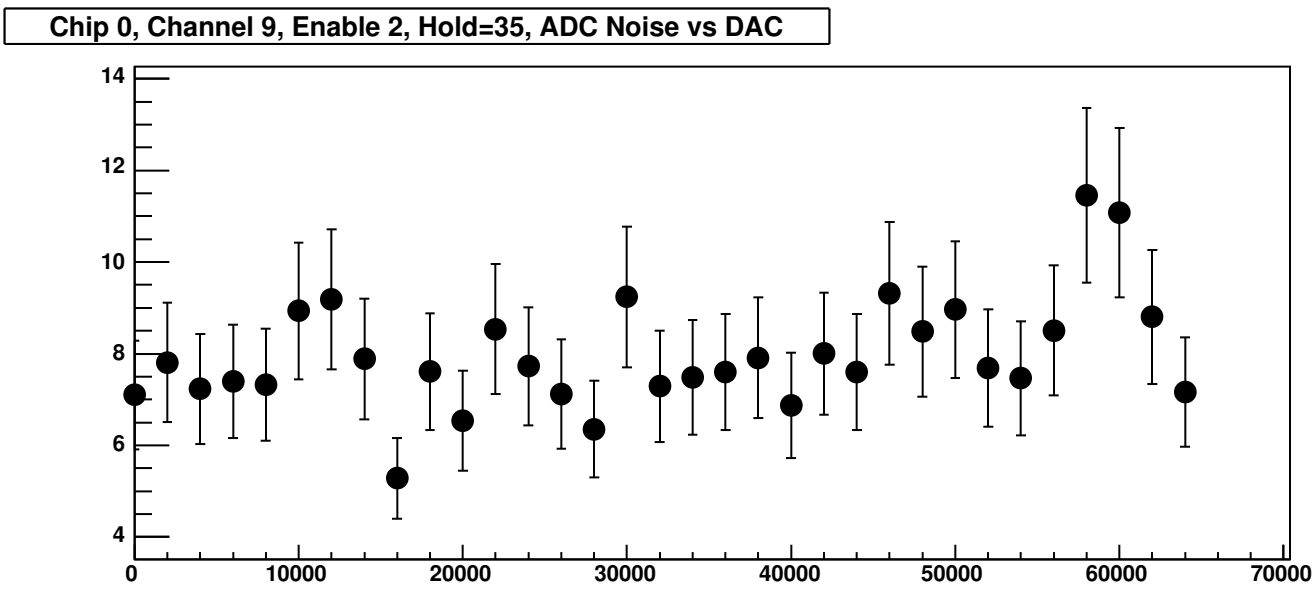

Chip 0, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
10000
20000
30000
40000
50000
60000
70000

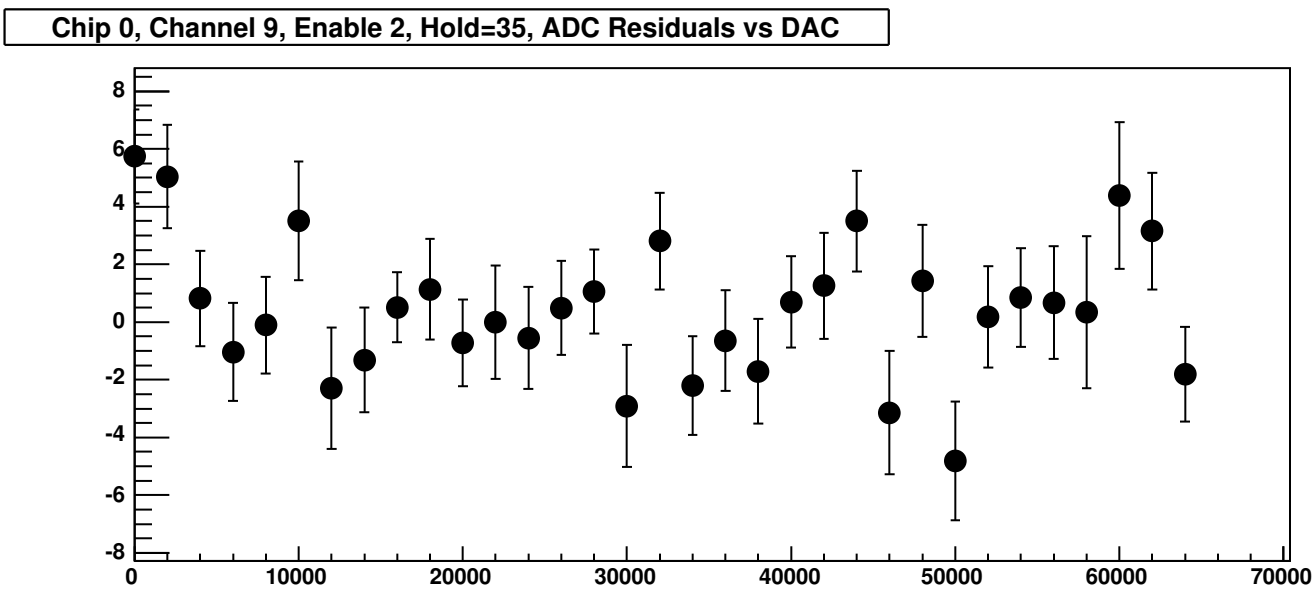

Chip 0, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 51.55 / 23
p0 721.7 ± 0.7626
p1 0.003528 ± 2.317e-05
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000
70000

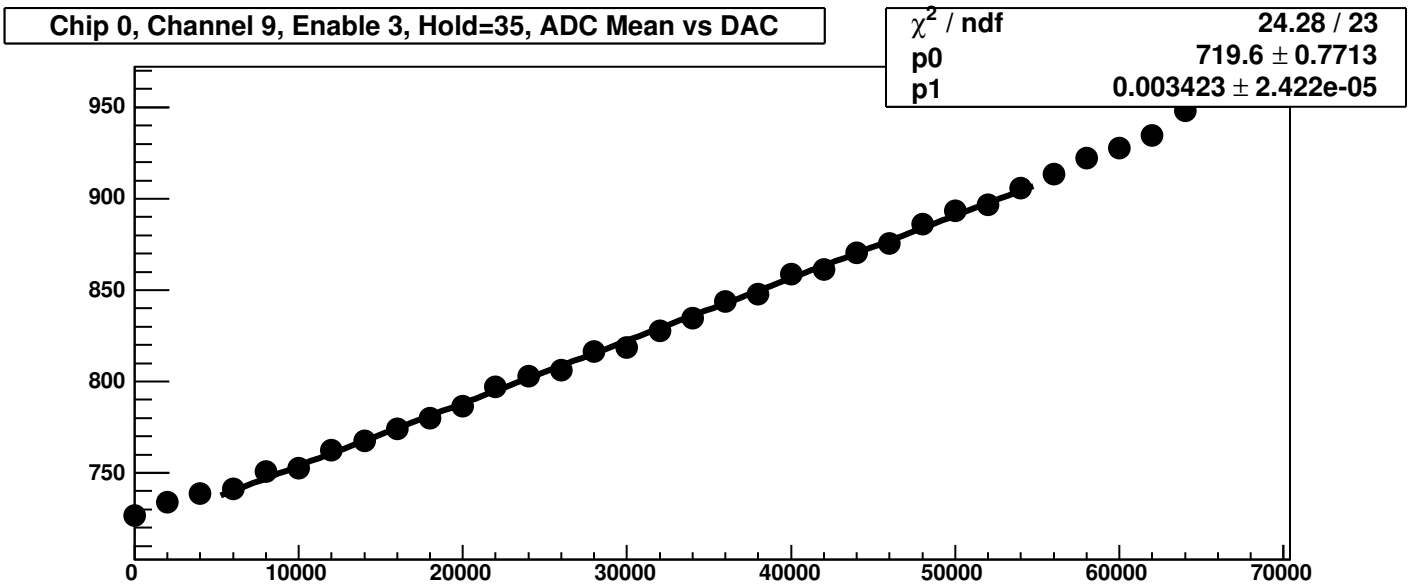

Chip 0, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

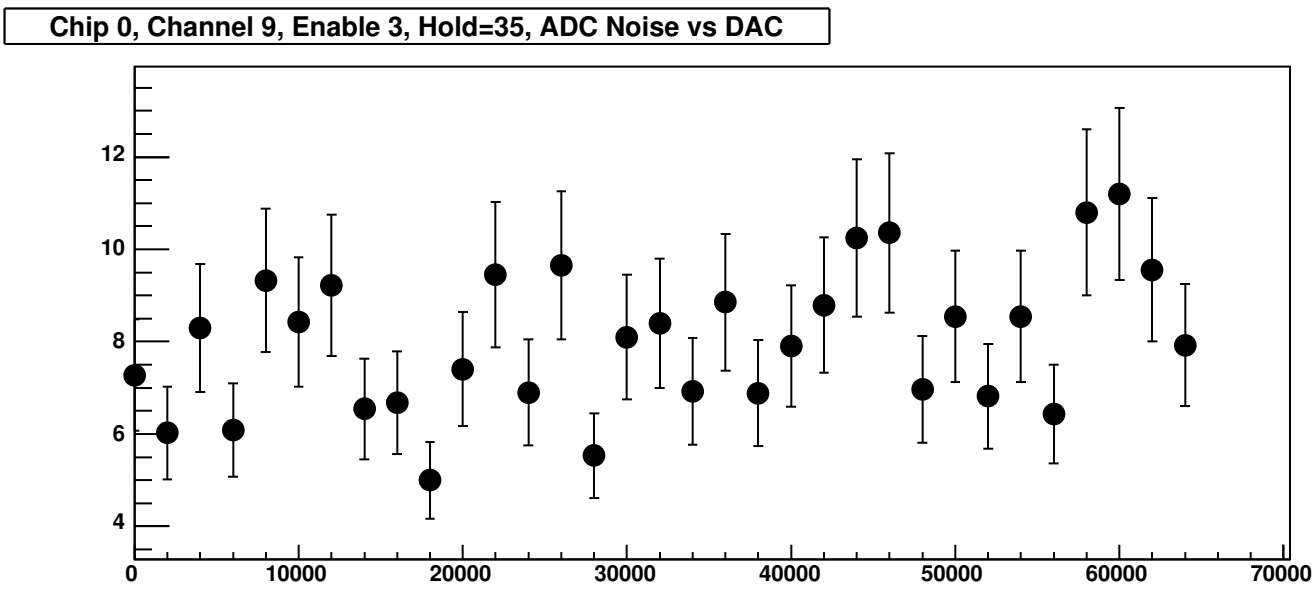

Chip 0, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

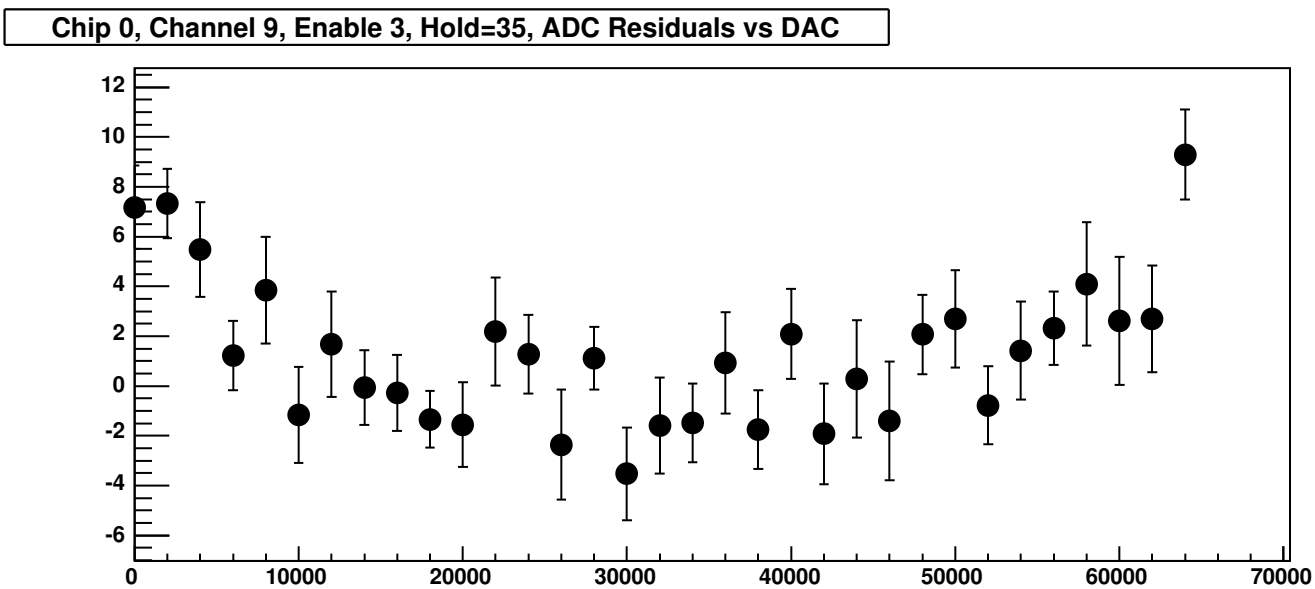

Chip 0, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 27.06 / 23
p0 719.6 ± 0.7973
p1 0.00285 ± 2.465e-05
900
880
860
840
820
800
780
760
740
720
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

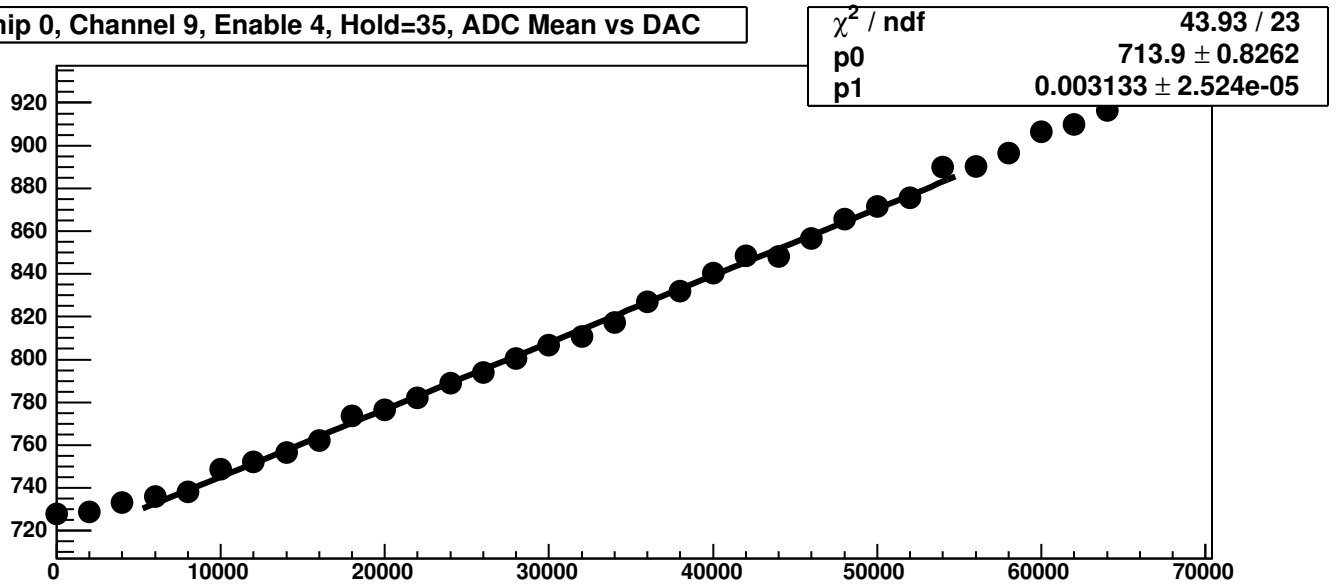

Chip 0, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.28 / 23
p0 719.6 ± 0.7713
p1 0.003423 ± 2.422e-05
950
900
850
800
750
0
10000
20000
30000
40000
50000
60000
70000

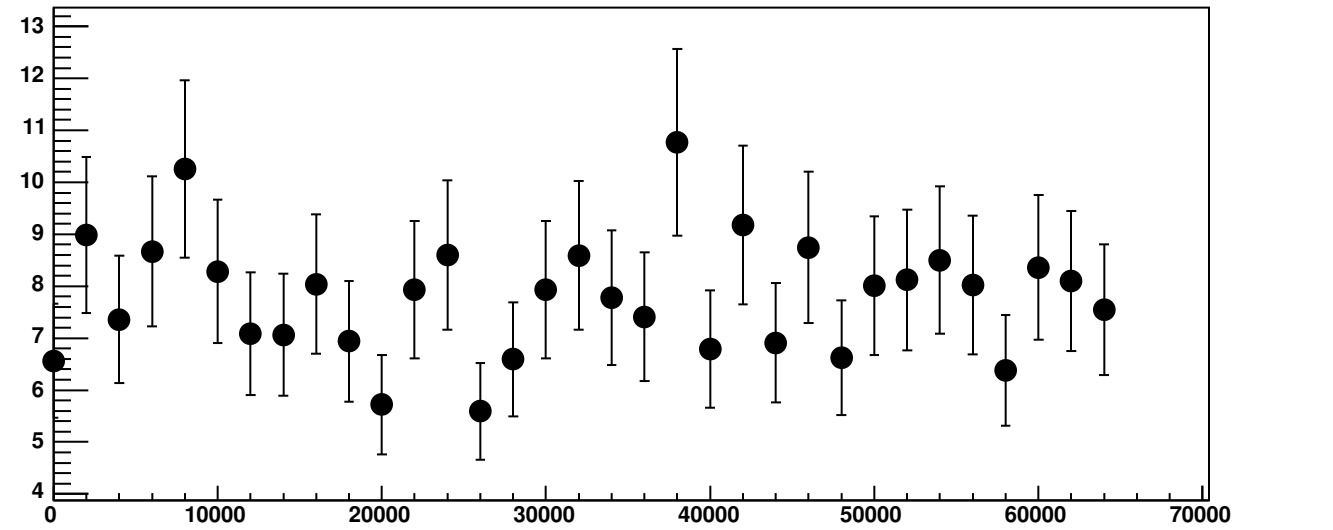

Chip 0, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

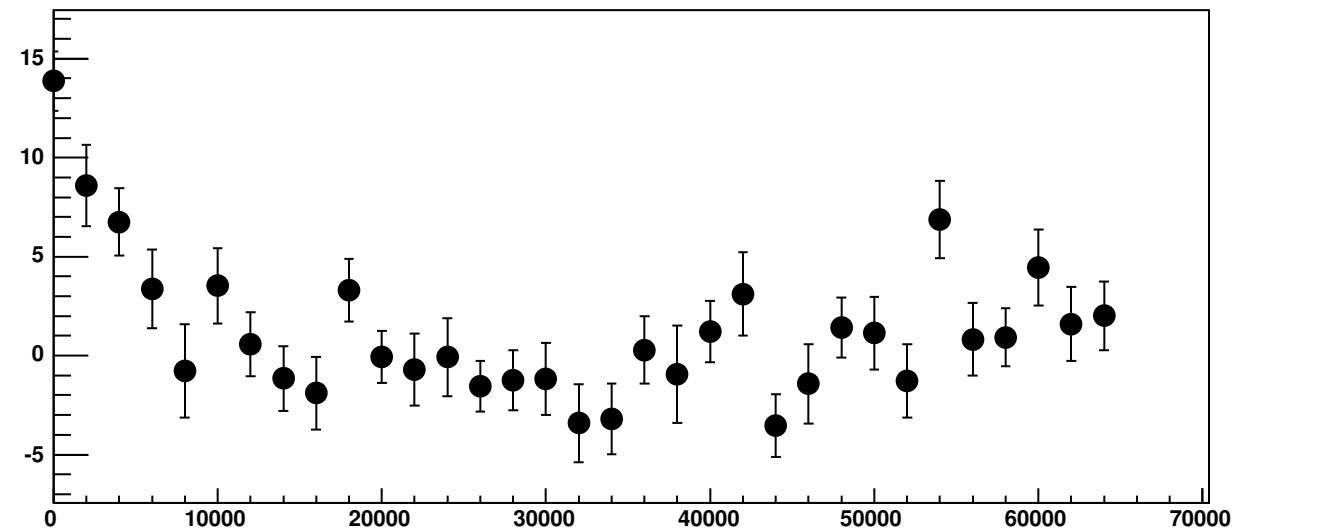
Chip 0, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

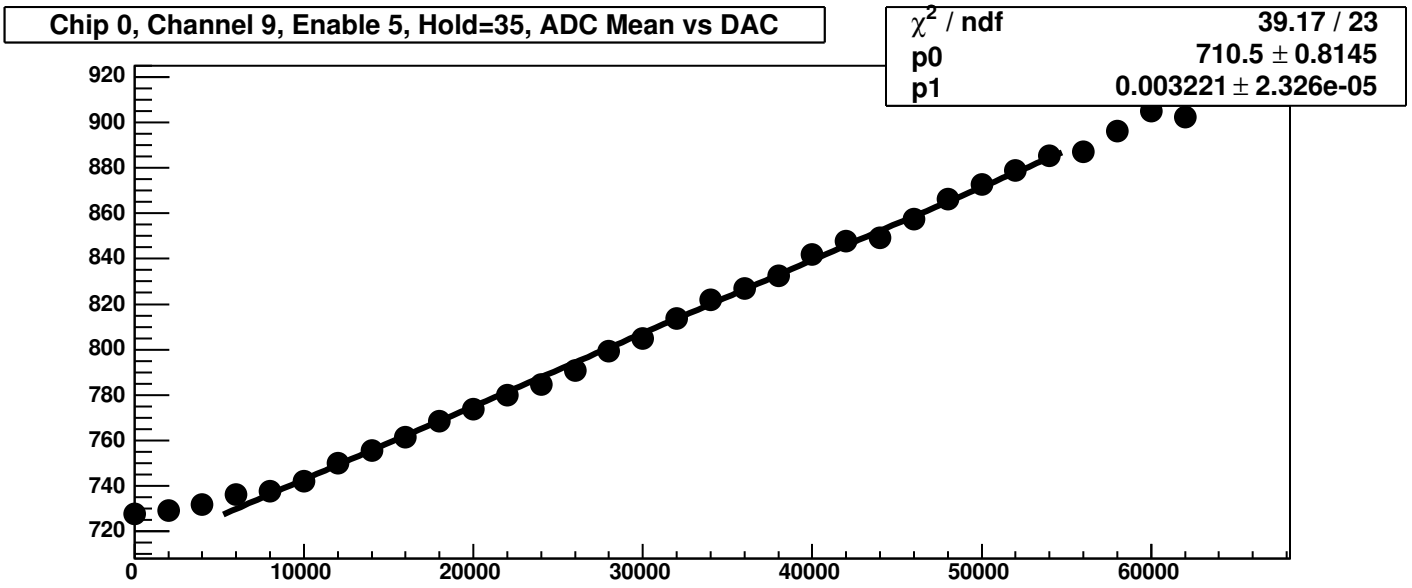


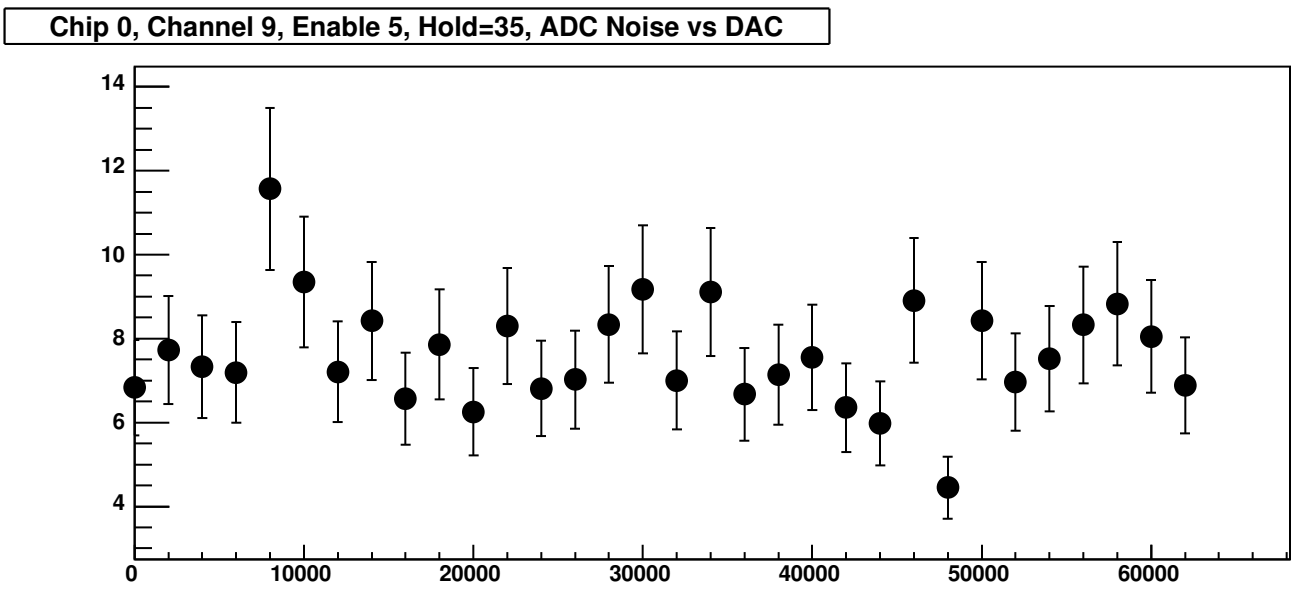
Chip 0, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC

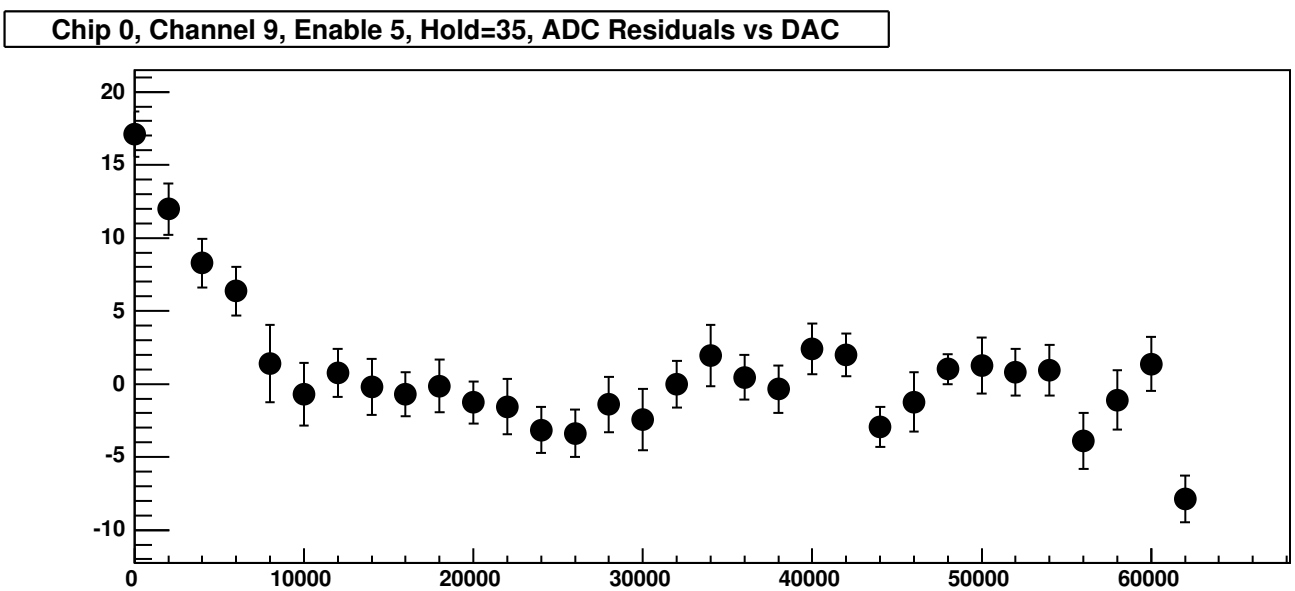


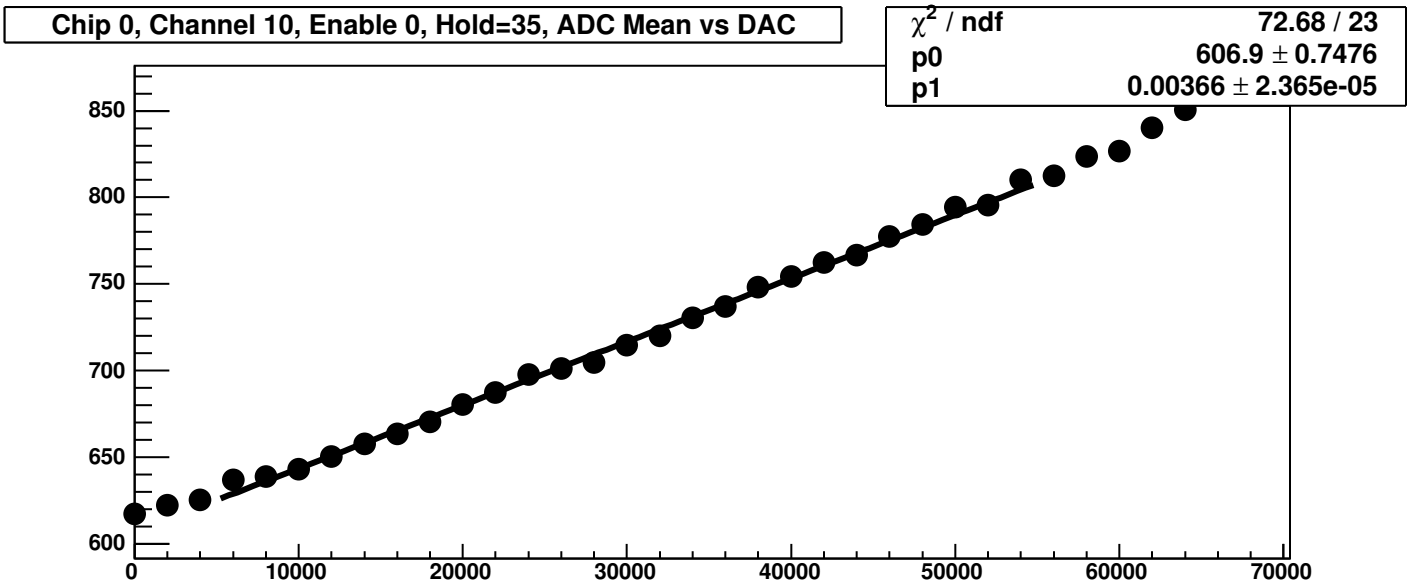
Chip 0, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

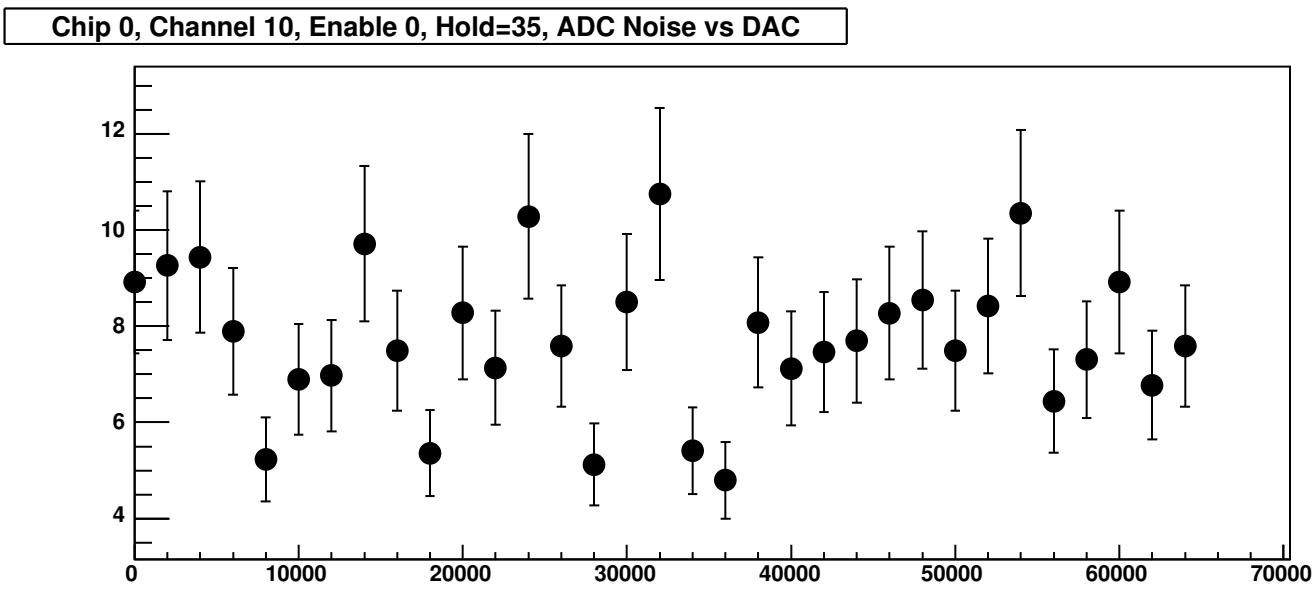


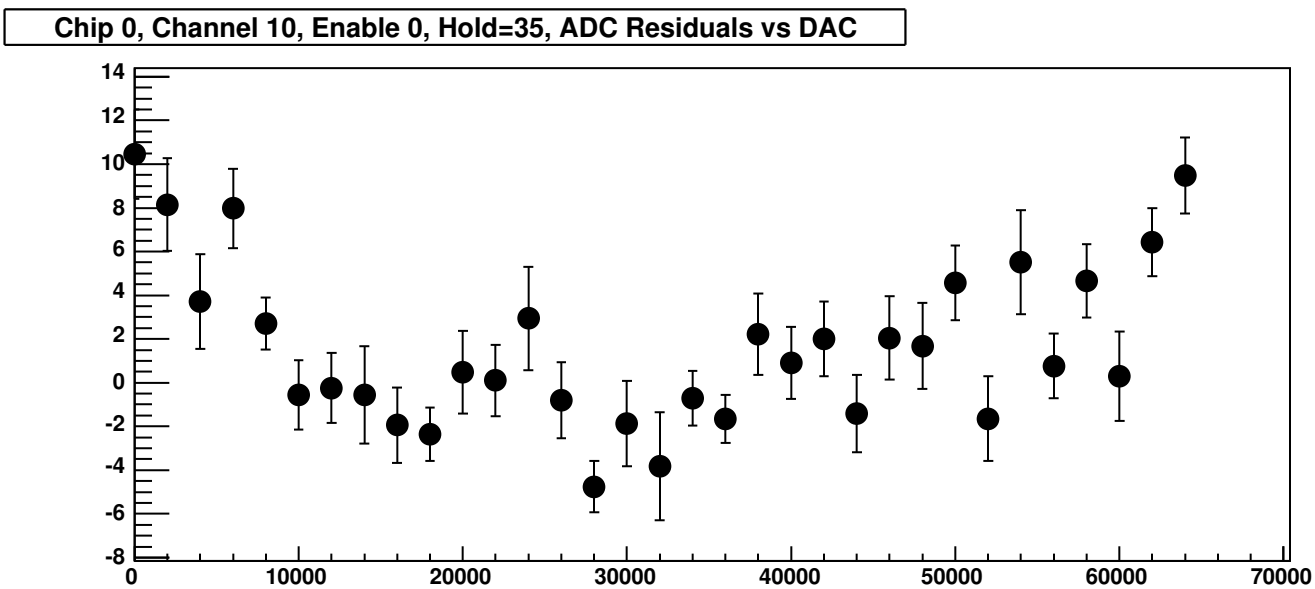

Chip 0, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 39.17 / 23
p0 710.5 ± 0.8145
p1 0.003221 ± 2.326e-05
920
900
880
860
840
820
800
780
760
740
720
0
10000
20000
30000
40000
50000
60000

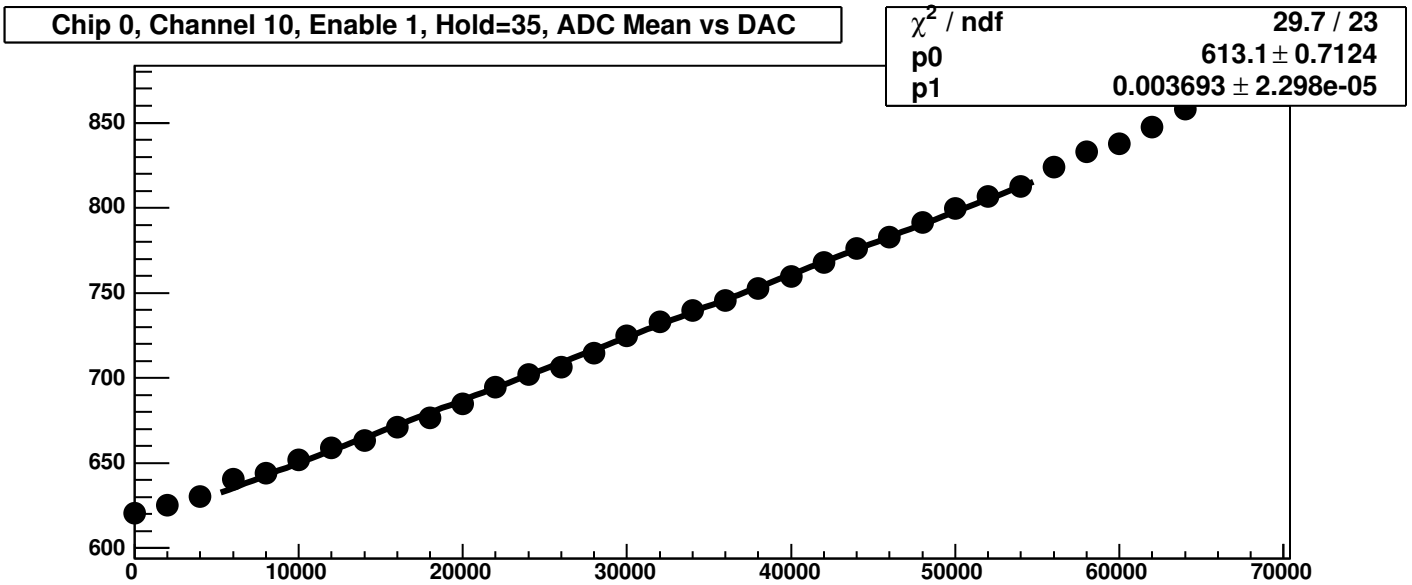

Chip 0, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000

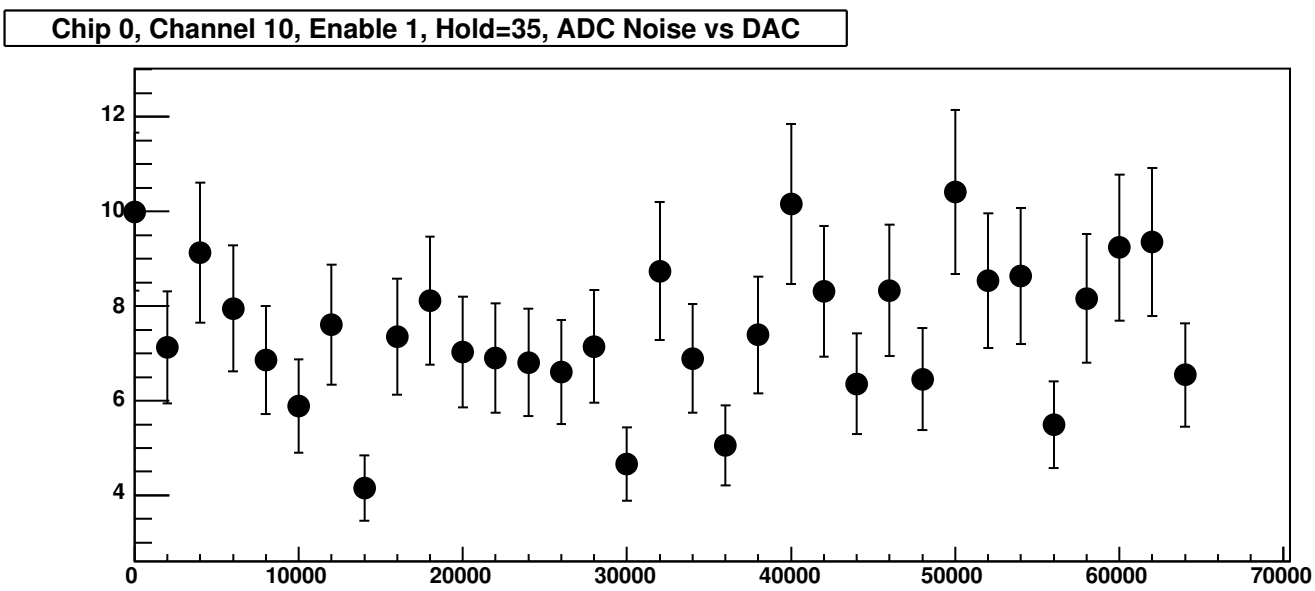

Chip 0, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000

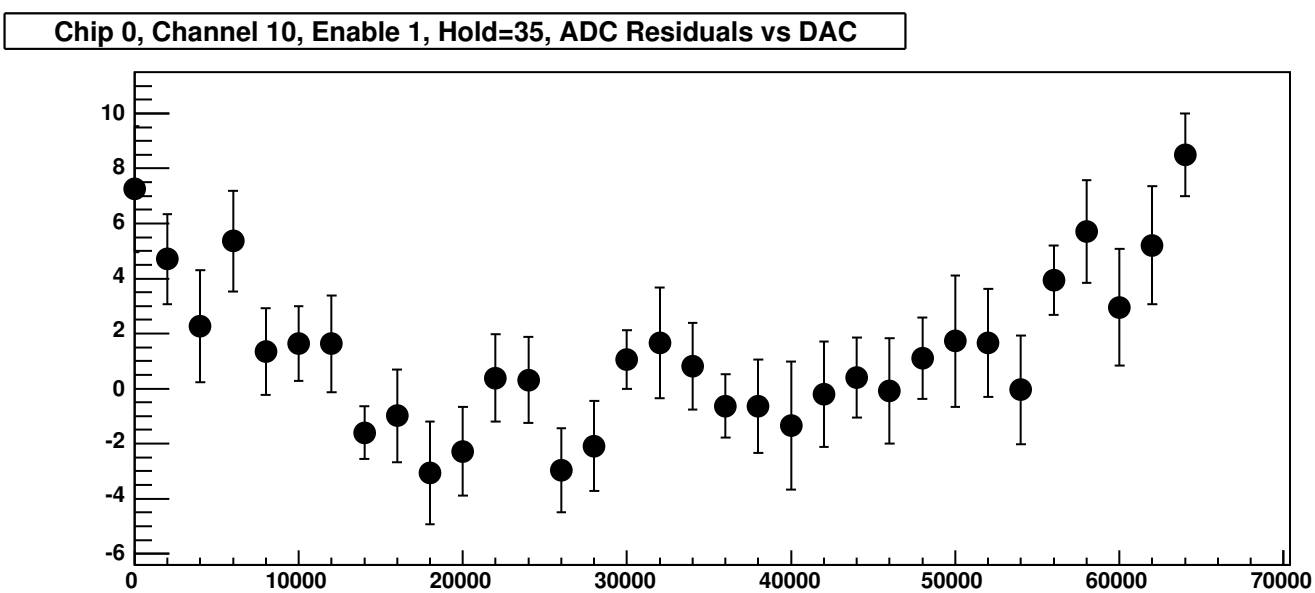

Chip 0, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 72.68 / 23
p0 606.9 ± 0.7476
p1 0.00366 ± 2.365e-05
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000
70000

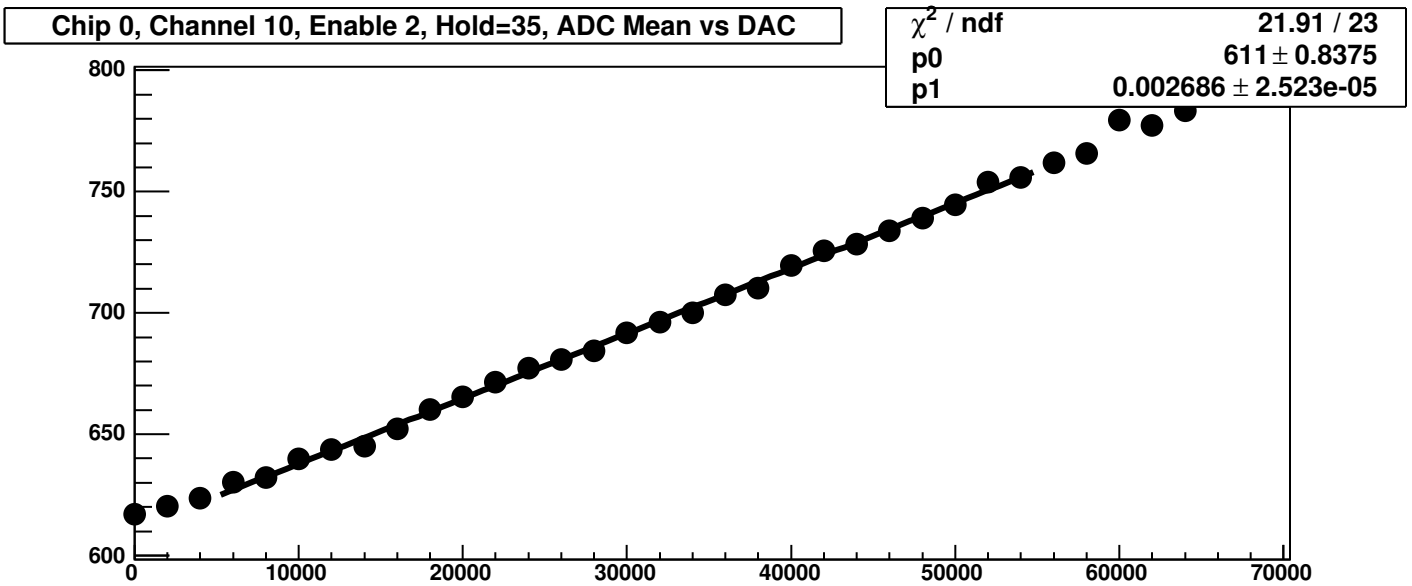

Chip 0, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

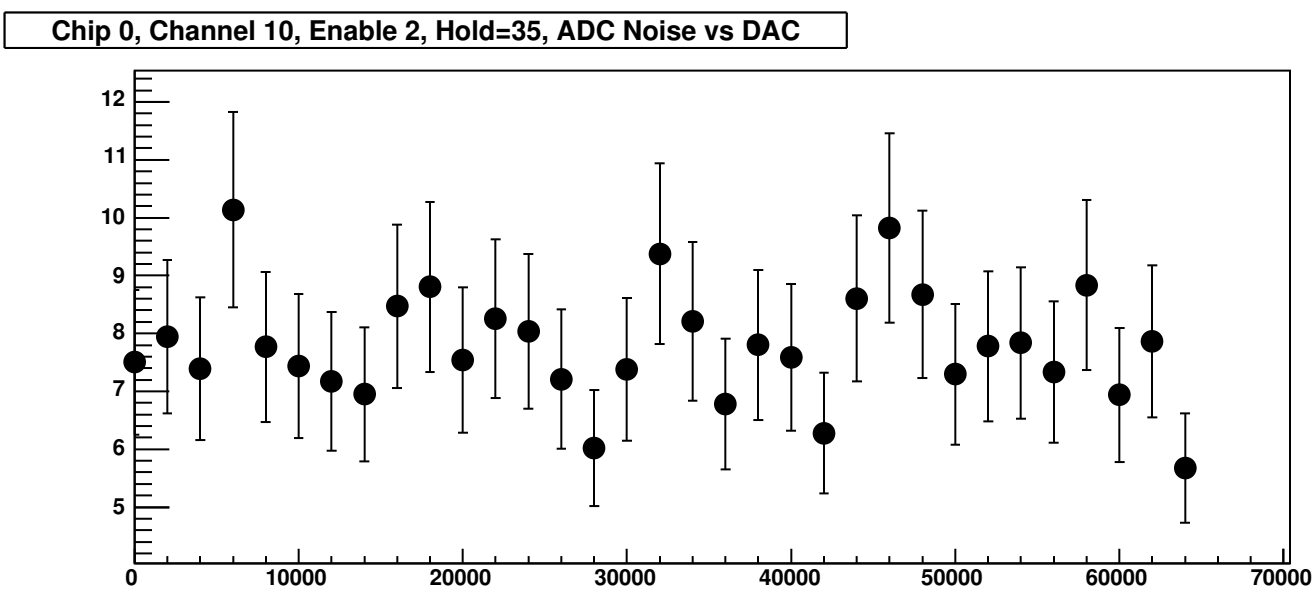

Chip 0, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

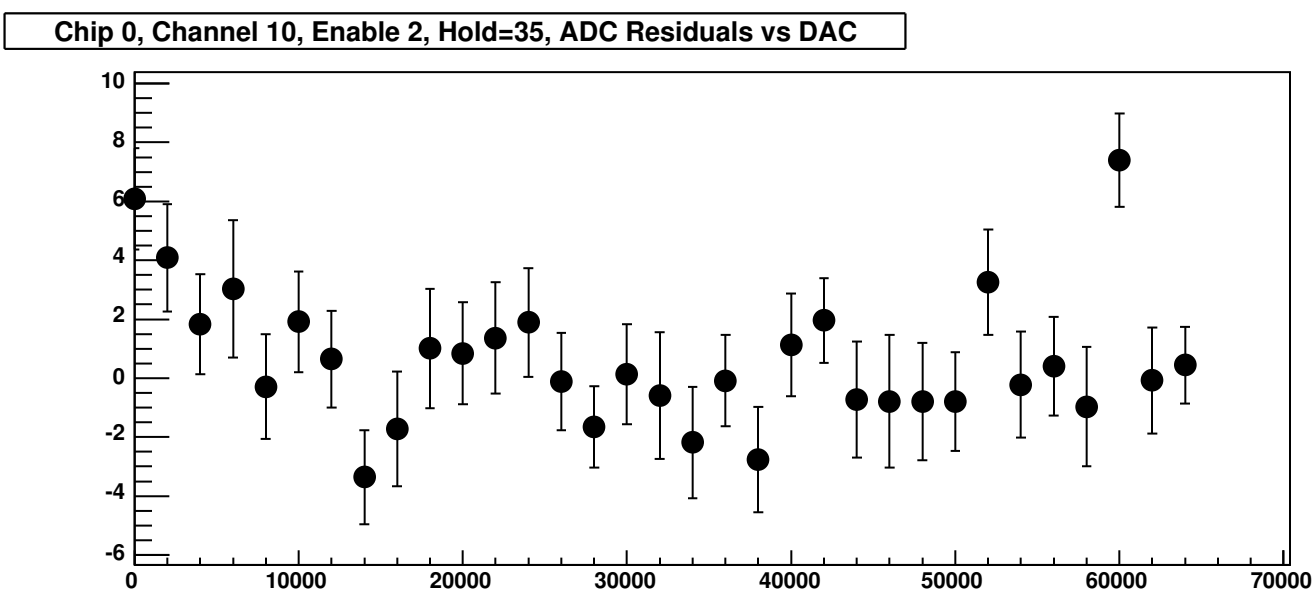

Chip 0, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 29.7 / 23
p0 613.1 ± 0.7124
p1 0.003693 ± 2.298e-05
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000
70000

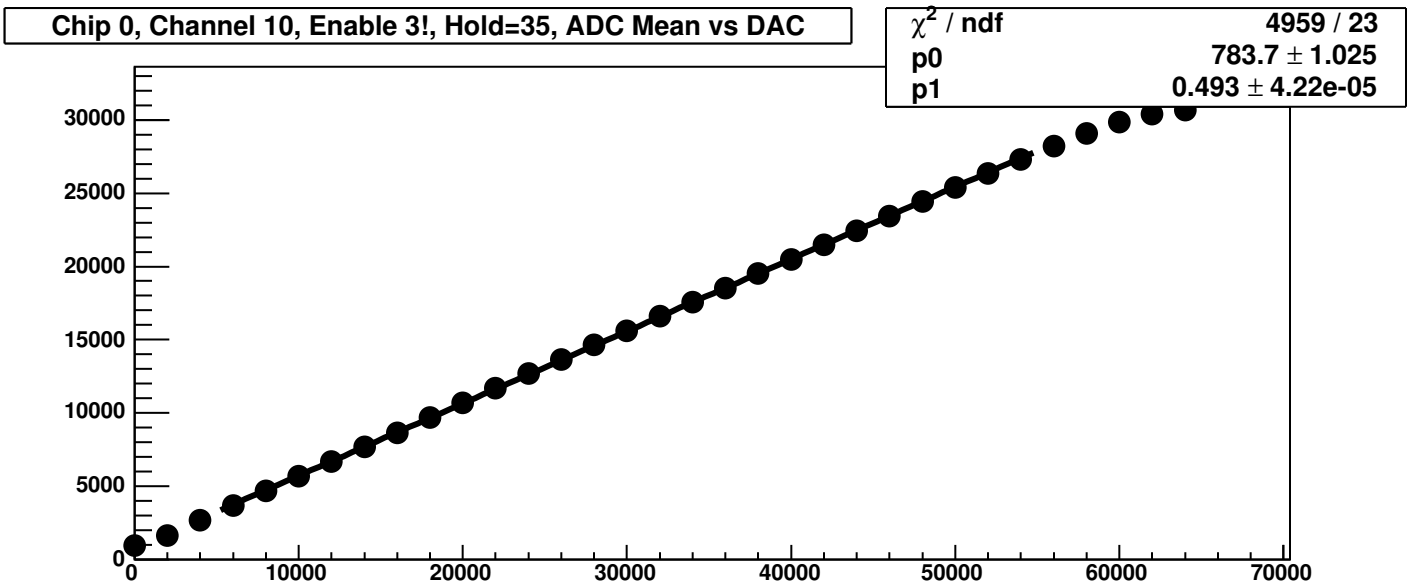

Chip 0, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

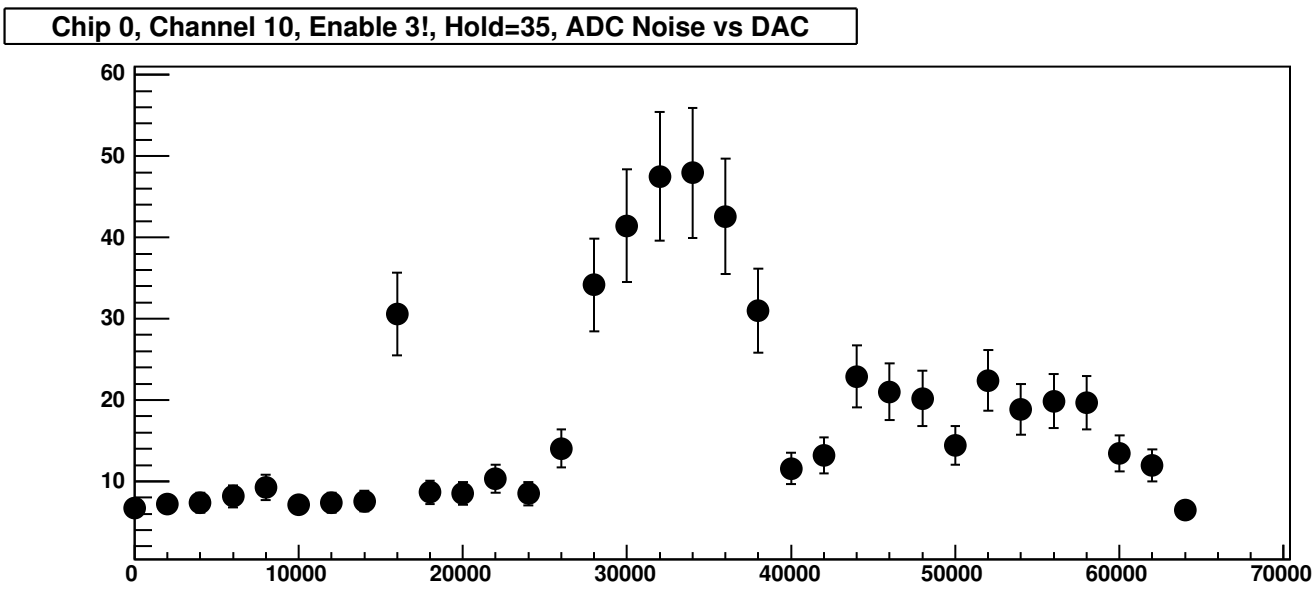

Chip 0, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

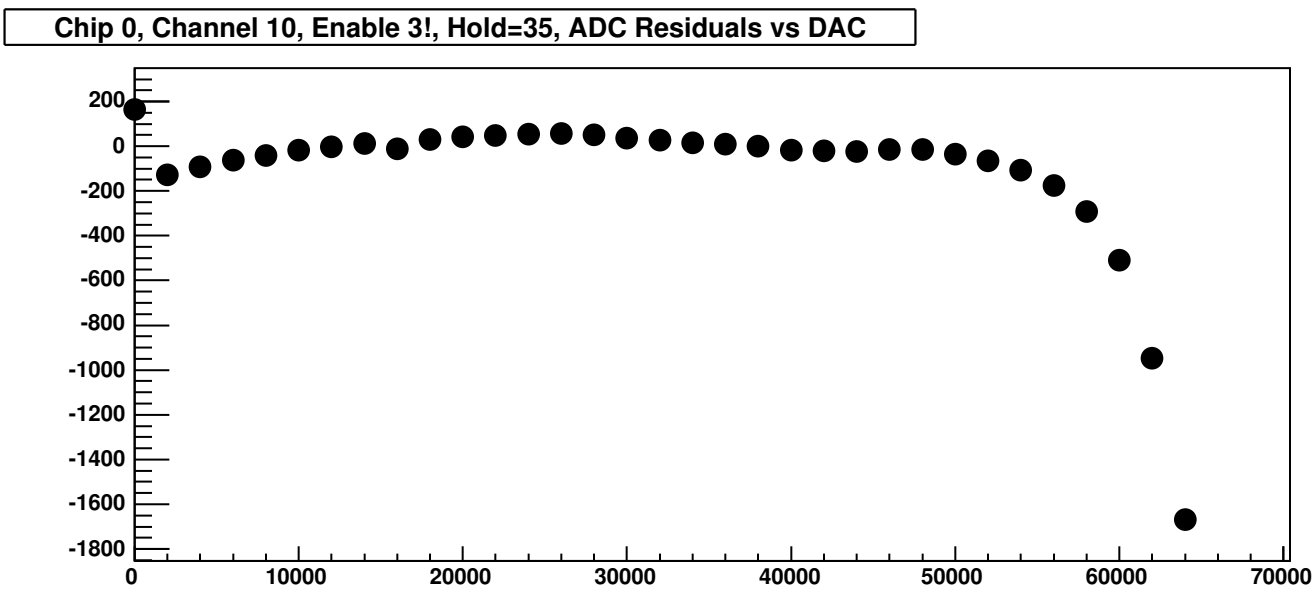

Chip 0, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 21.91 / 23
p0 611 ± 0.8375
p1 0.002686 ± 2.523e-05
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000
70000

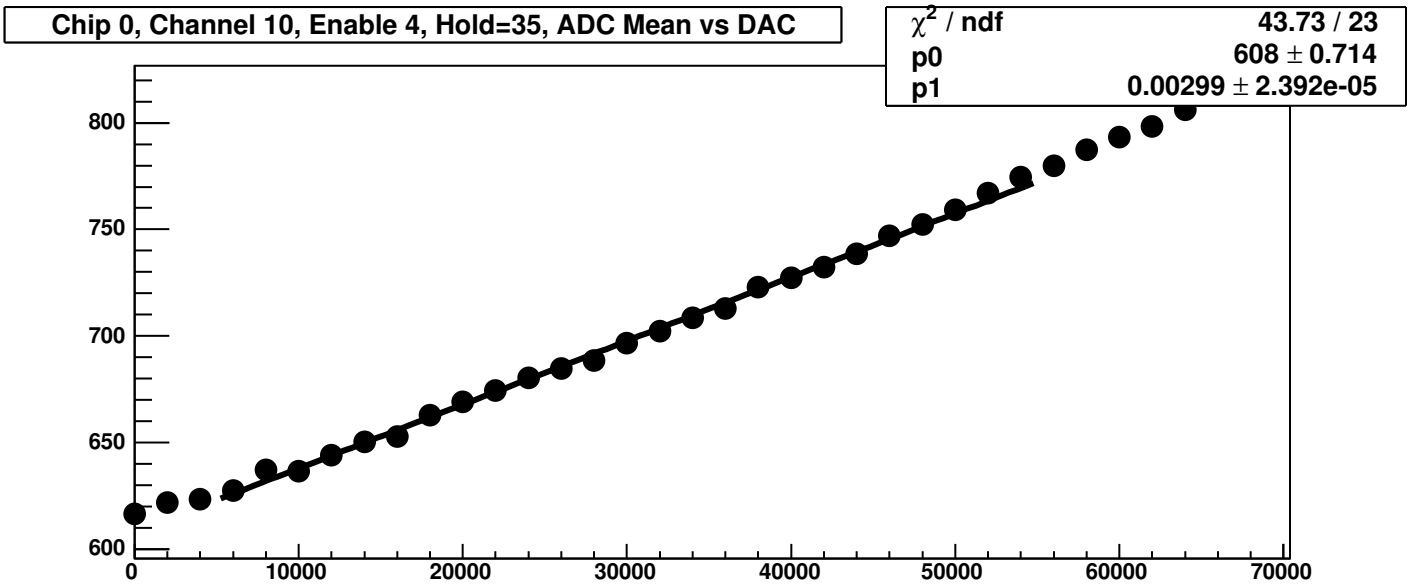

Chip 0, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000

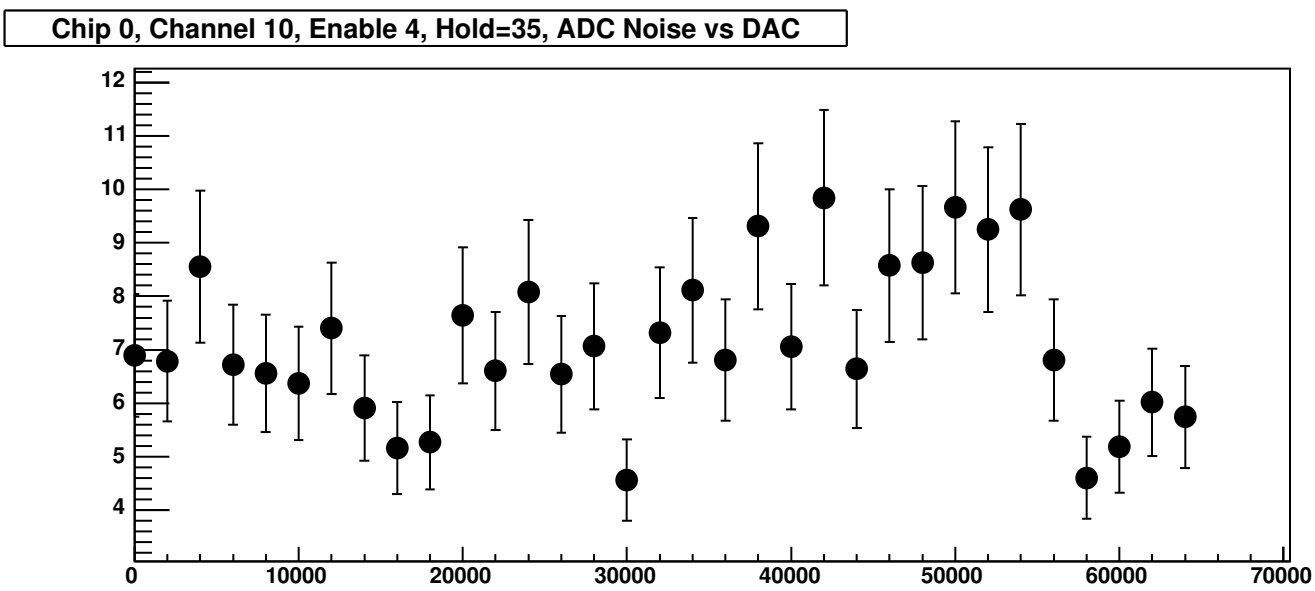

Chip 0, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

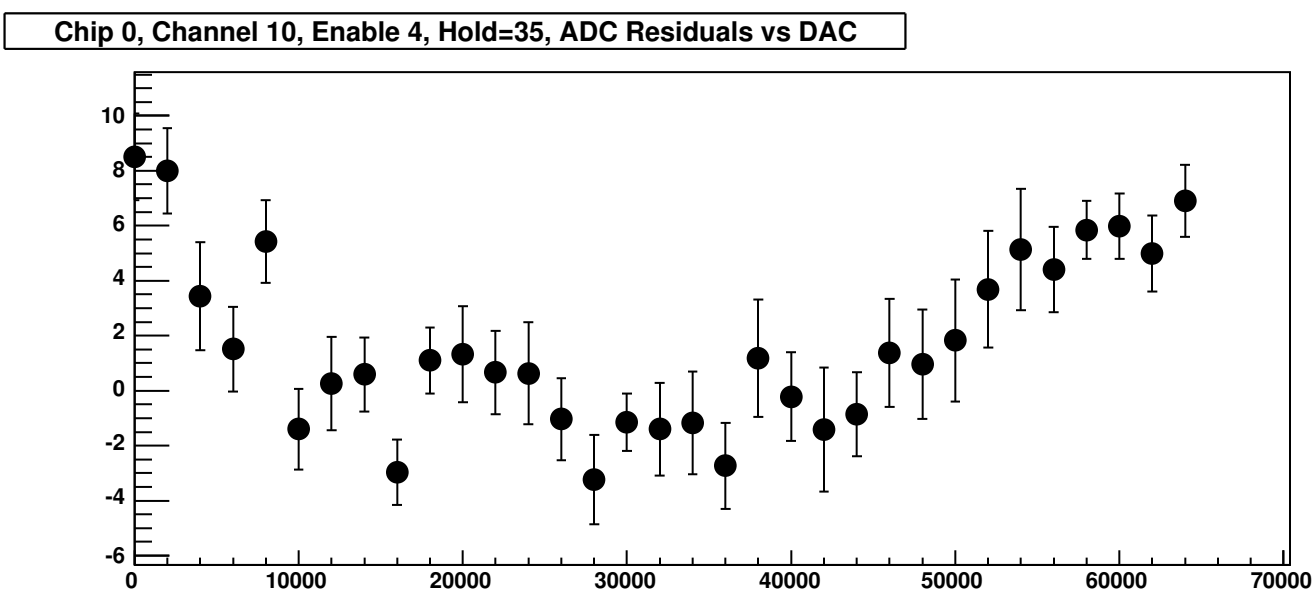

Chip 0, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 4959 / 23
p0 783.7 ± 1.025
p1 0.493 ± 4.22e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000

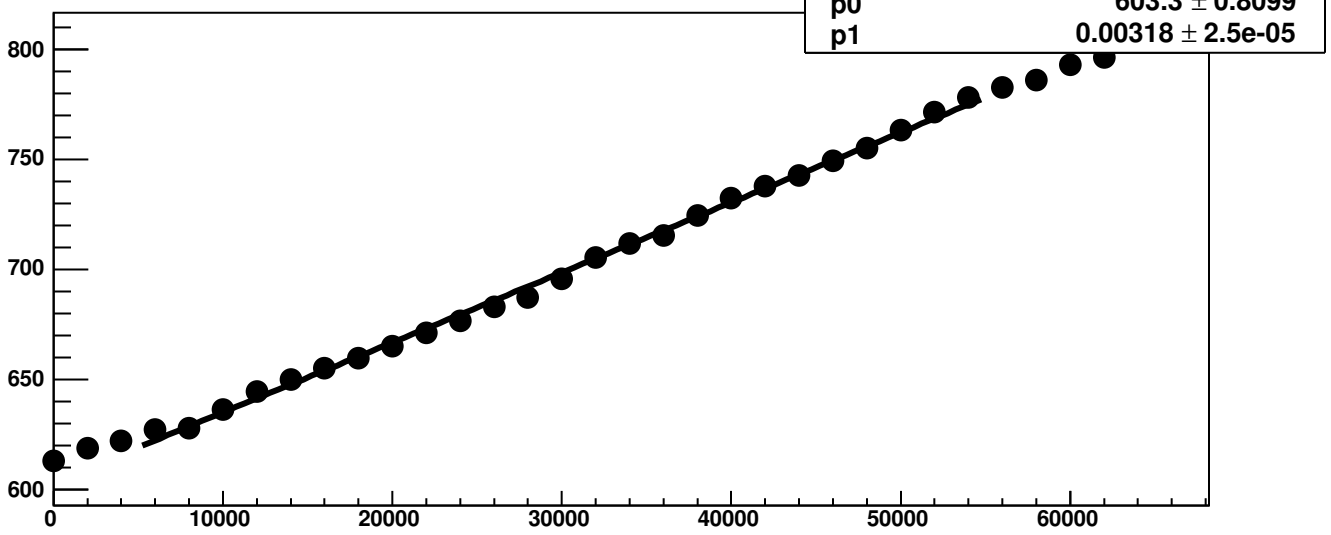

Chip 0, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 43.73 / 23
p0 608 ± 0.714
p1 0.00299 ± 2.392e-05
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000
70000

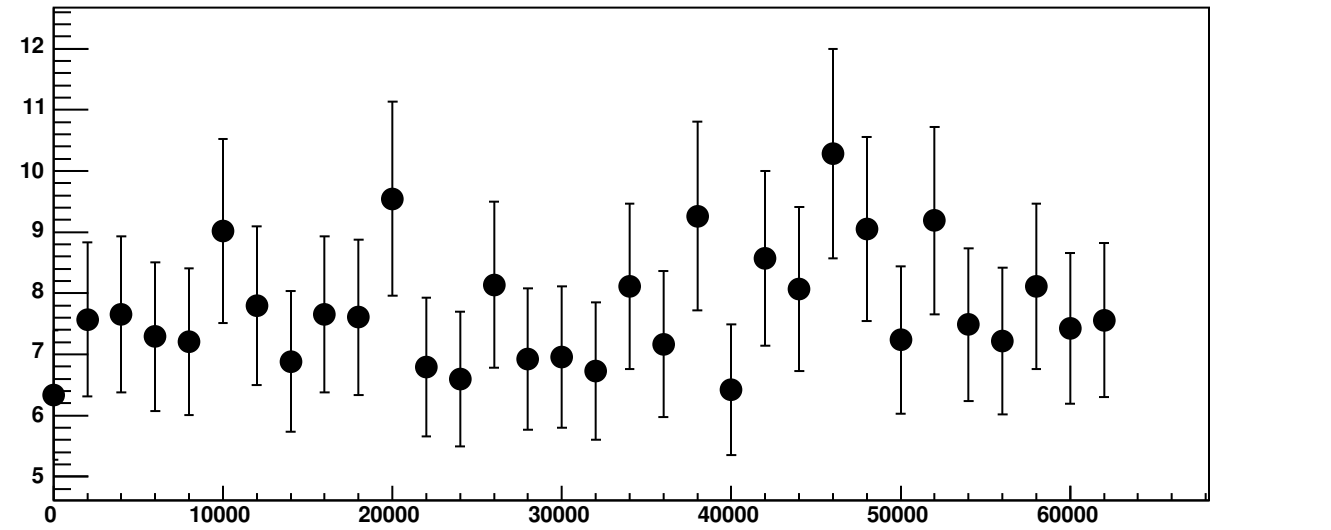

Chip 0, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

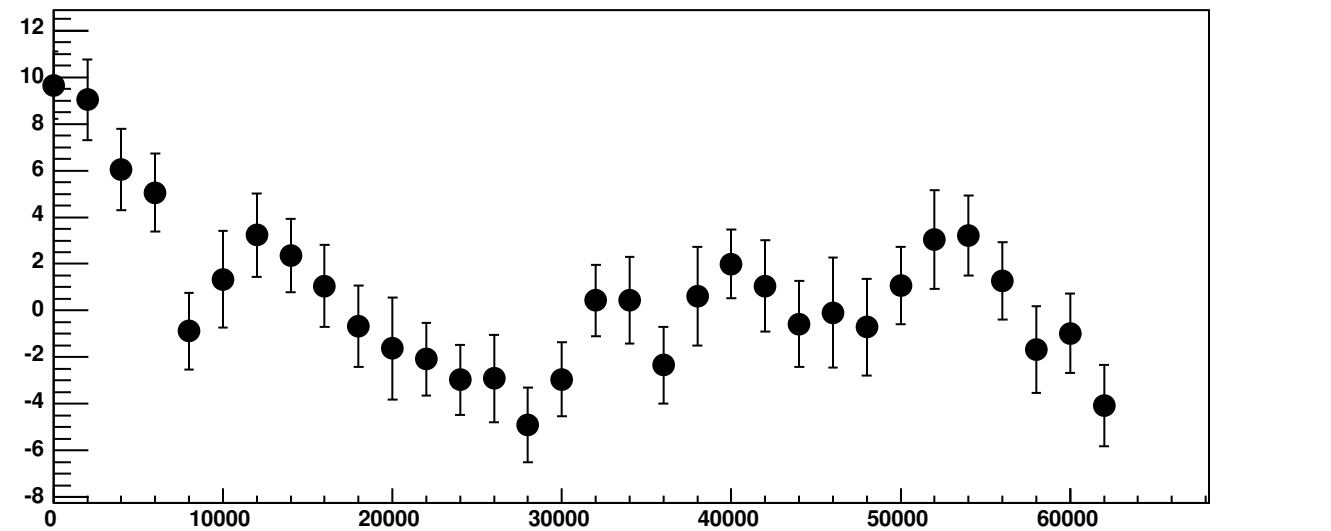
Chip 0, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

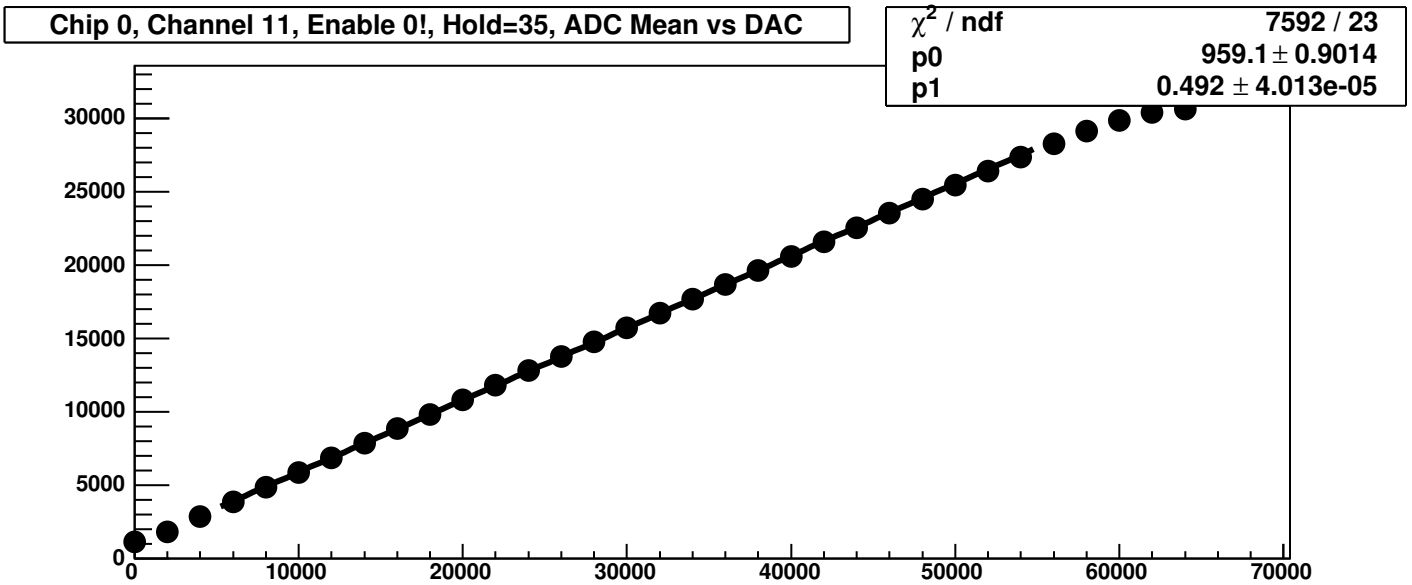


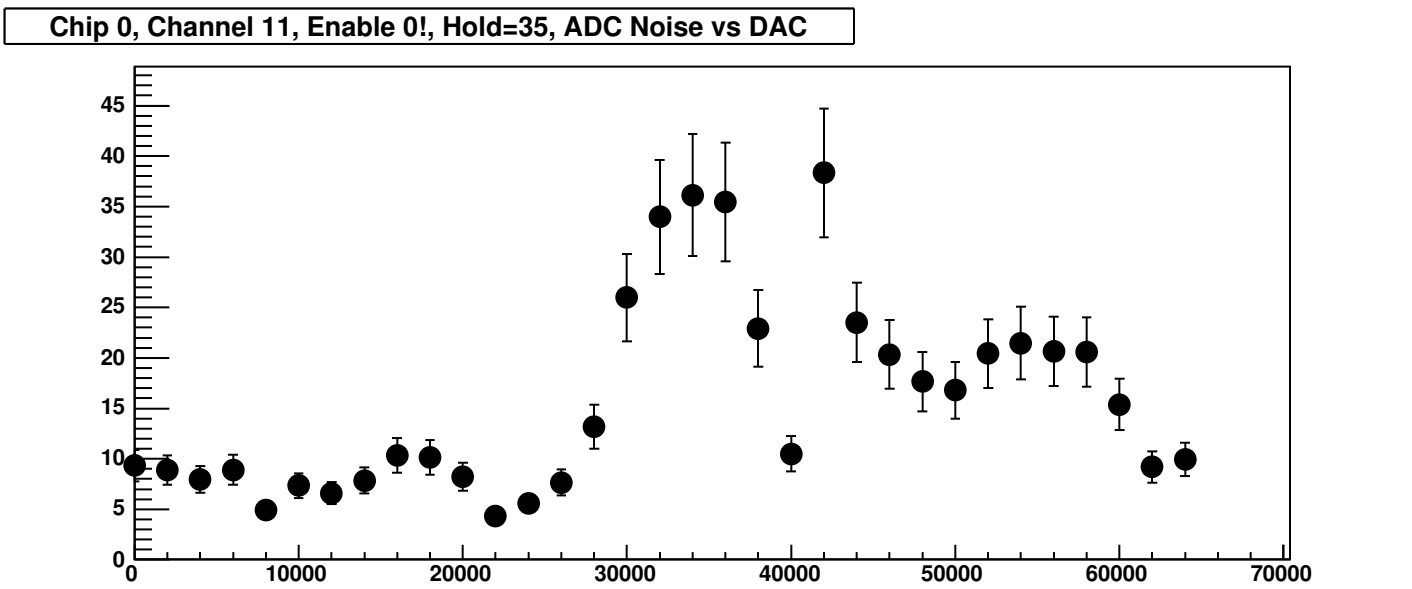
Chip 0, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

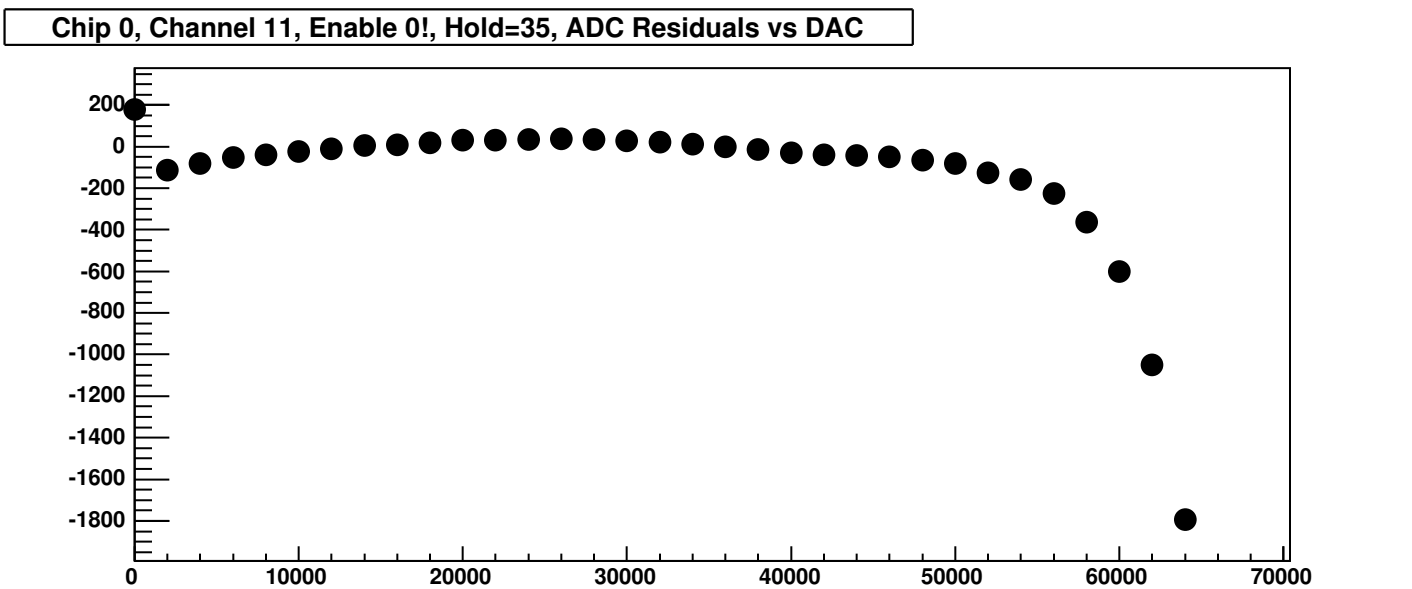


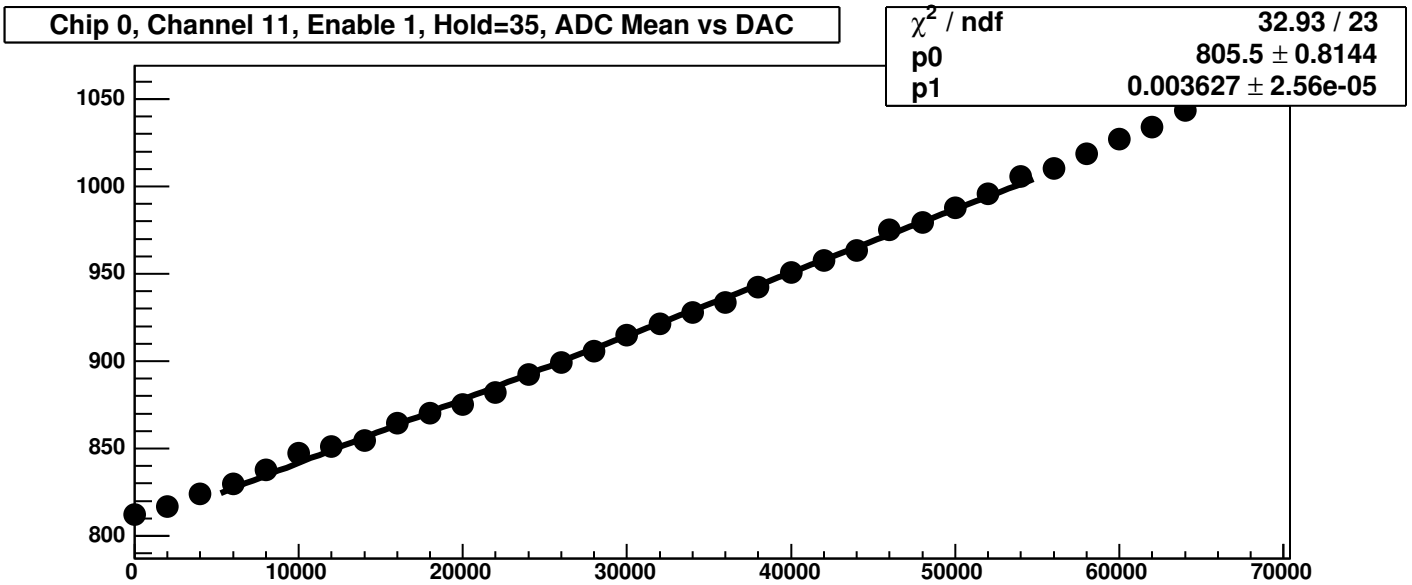
Chip 0, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

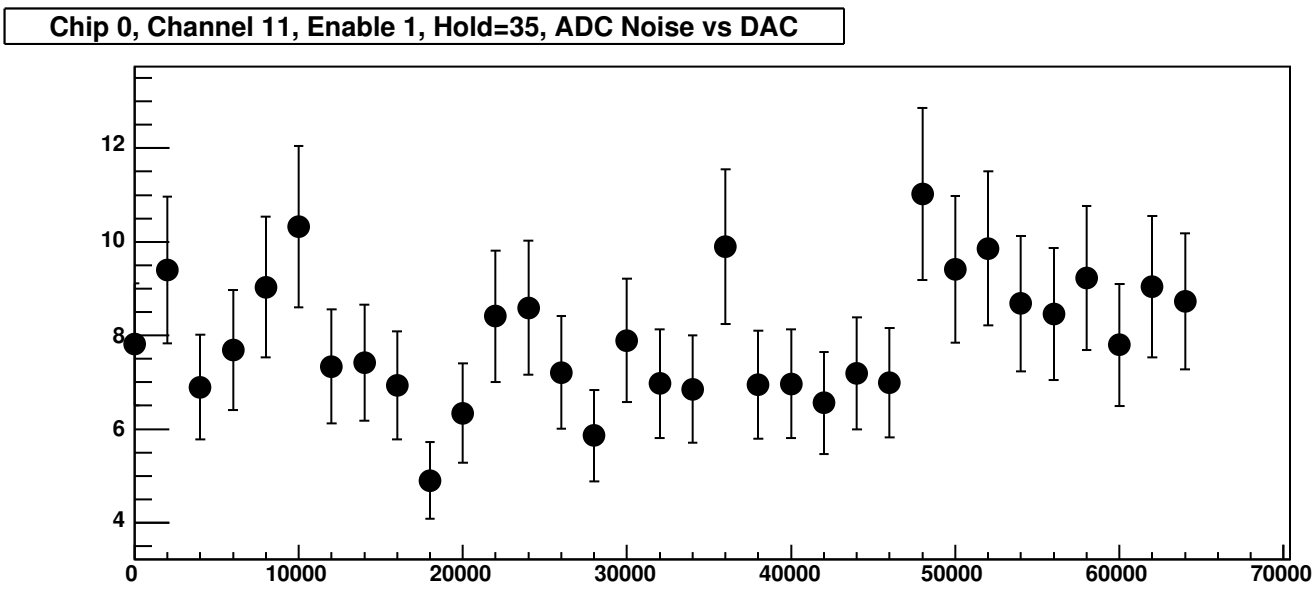


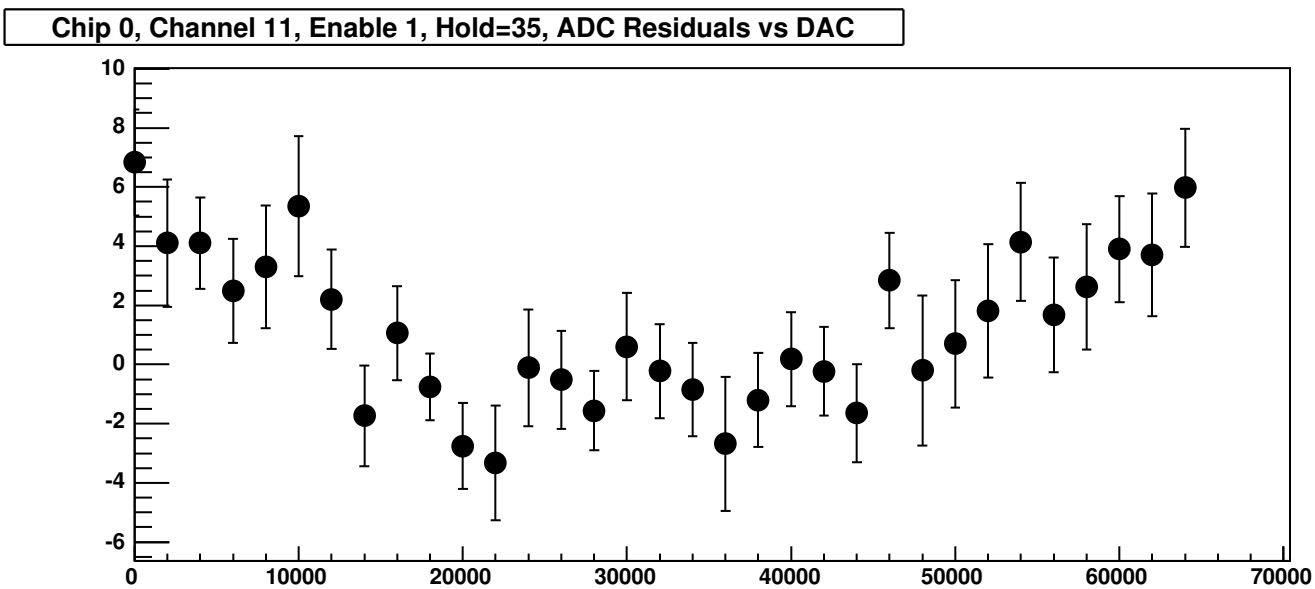

Chip 0, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 7592 / 23
p0 959.1 ± 0.9014
p1 0.492 ± 4.013e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

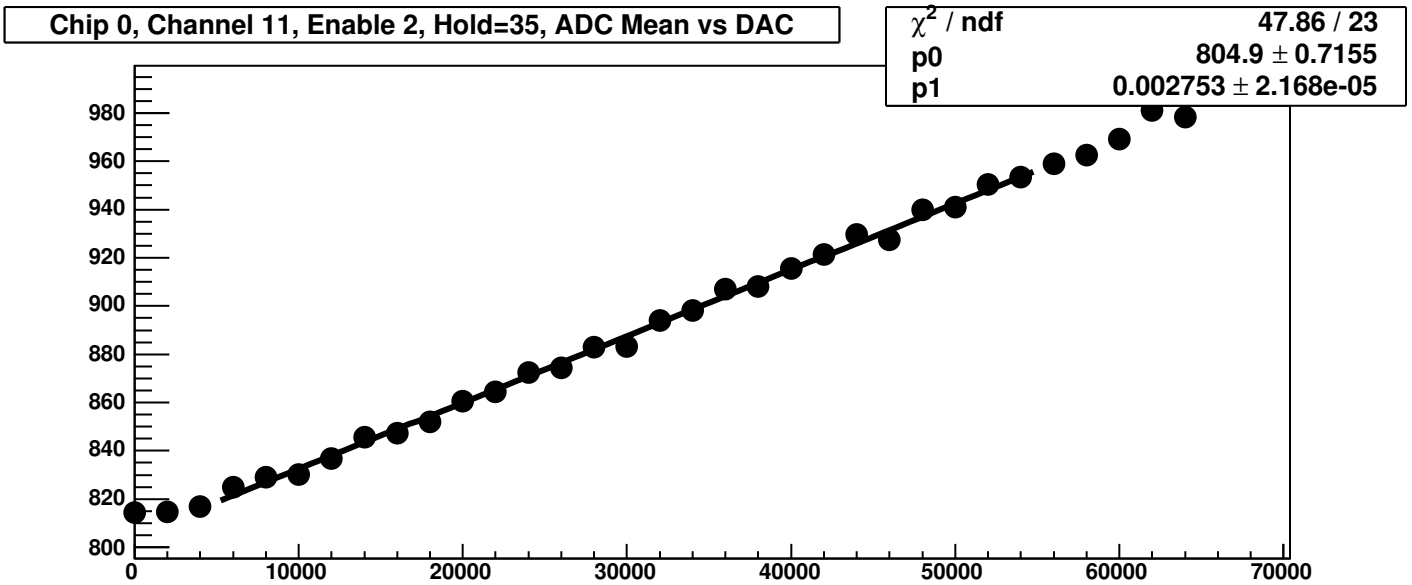

Chip 0, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
0
10000
20000
30000
40000
50000
60000
70000

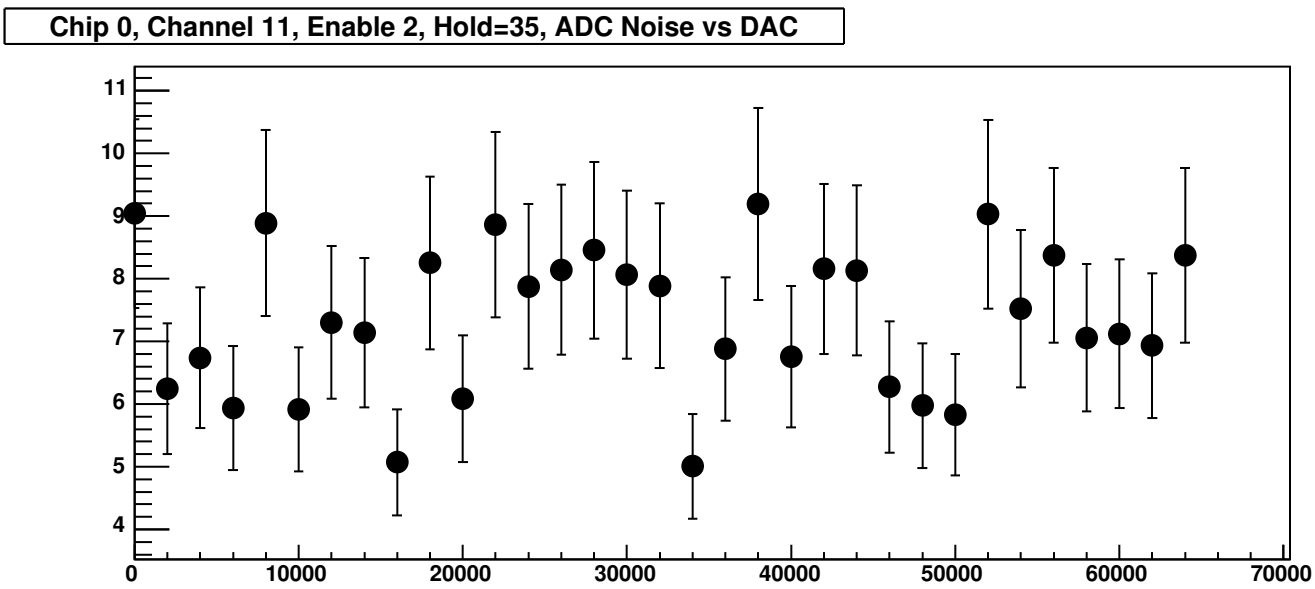

Chip 0, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
10000
20000
30000
40000
50000
60000
70000

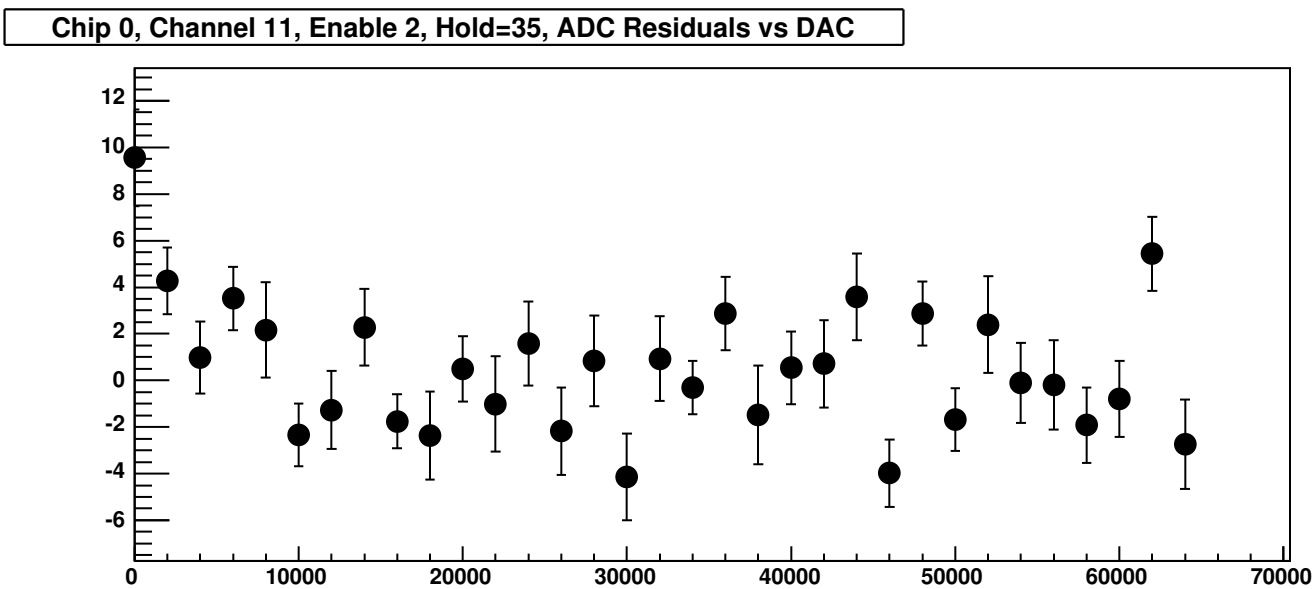

Chip 0, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.93 / 23
p0 805.5 ± 0.8144
p1 0.003627 ± 2.56e-05
1050
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000
70000

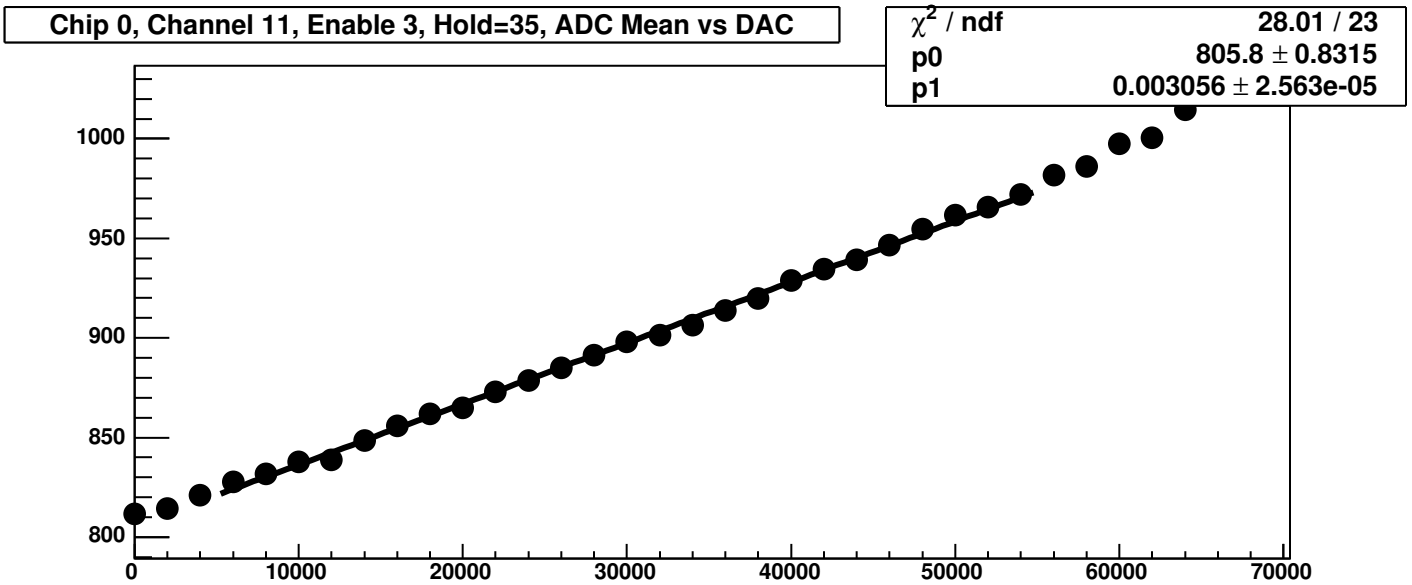

Chip 0, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

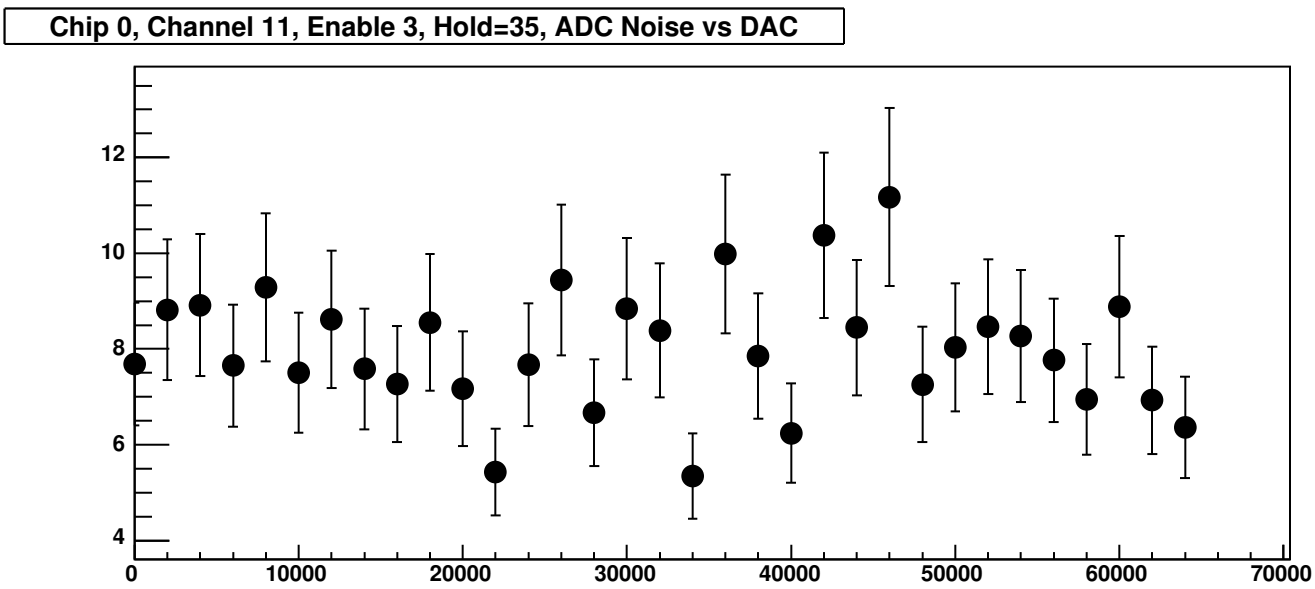

Chip 0, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

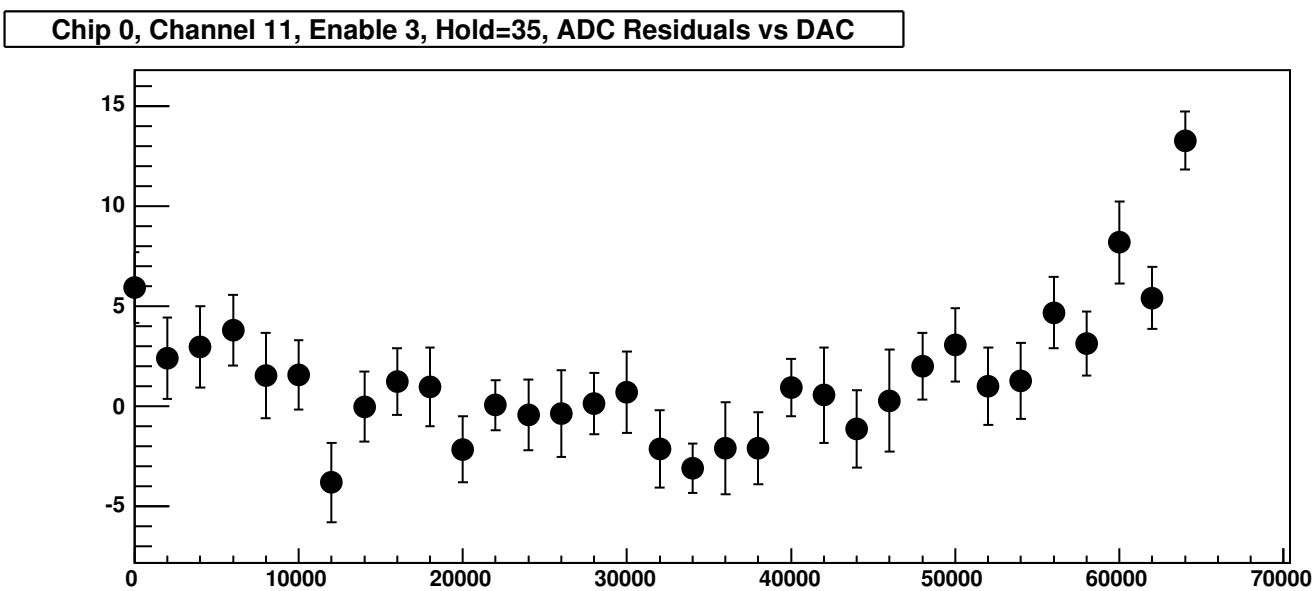

Chip 0, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 47.86 / 23
p0 804.9 ± 0.7155
p1 0.002753 ± 2.168e-05
980
960
940
920
900
880
860
840
820
800
0
10000
20000
30000
40000
50000
60000
70000

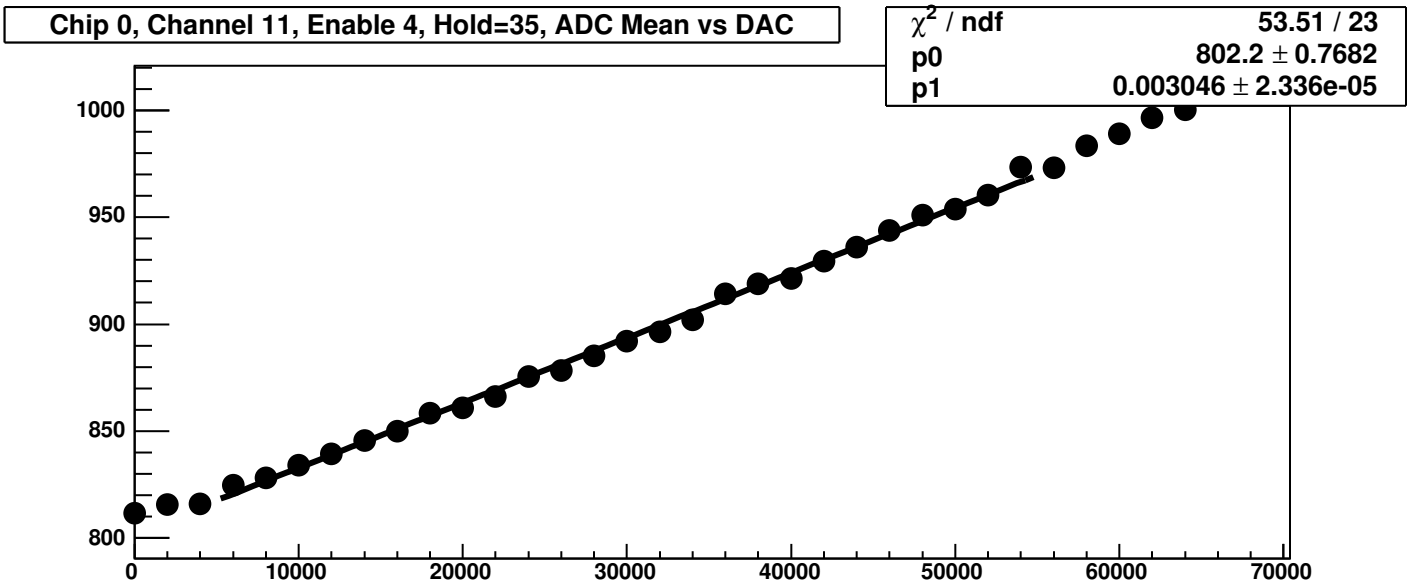

Chip 0, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

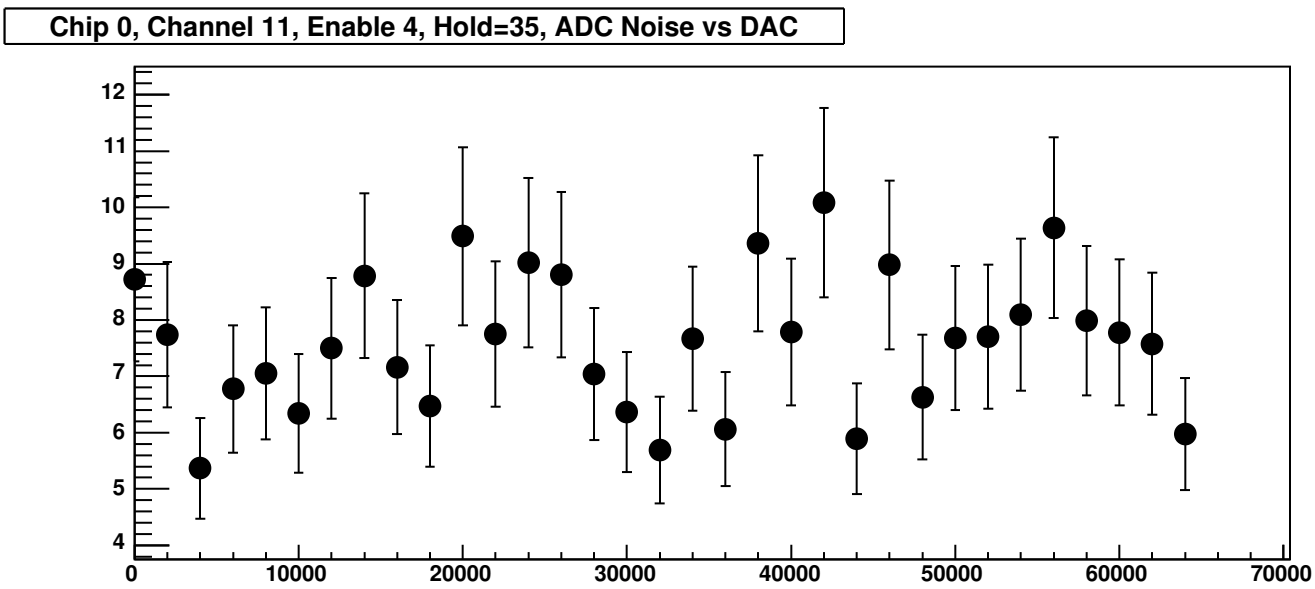

Chip 0, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

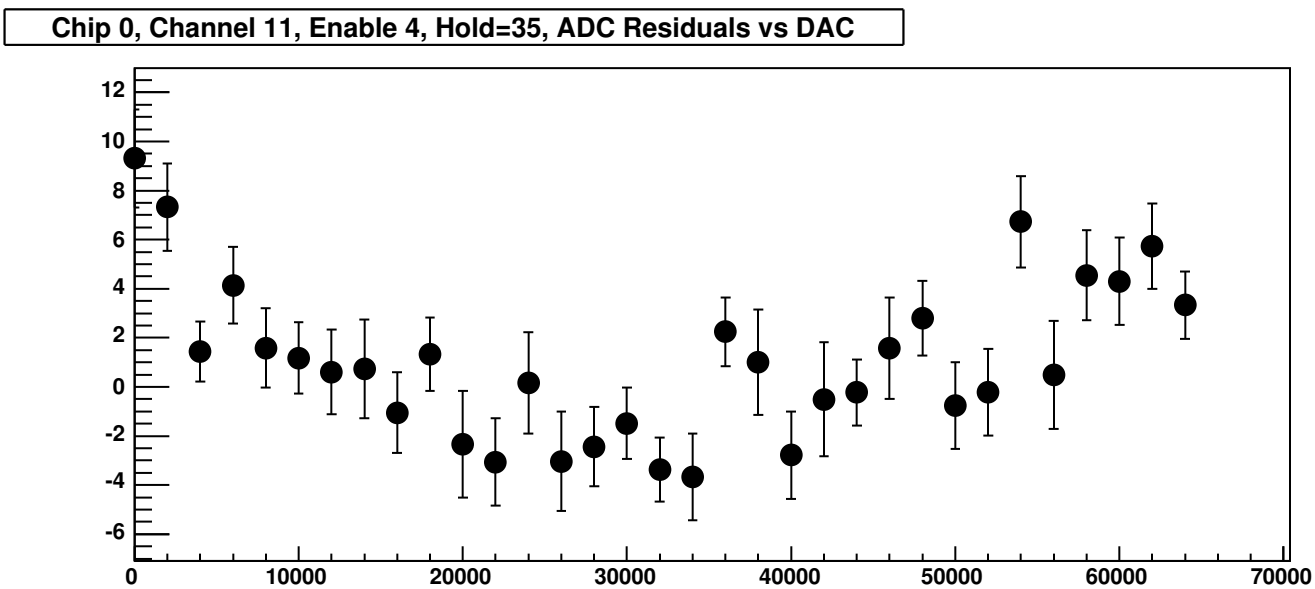

Chip 0, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.01 / 23
p0 805.8 ± 0.8315
p1 0.003056 ± 2.563e-05
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000
70000

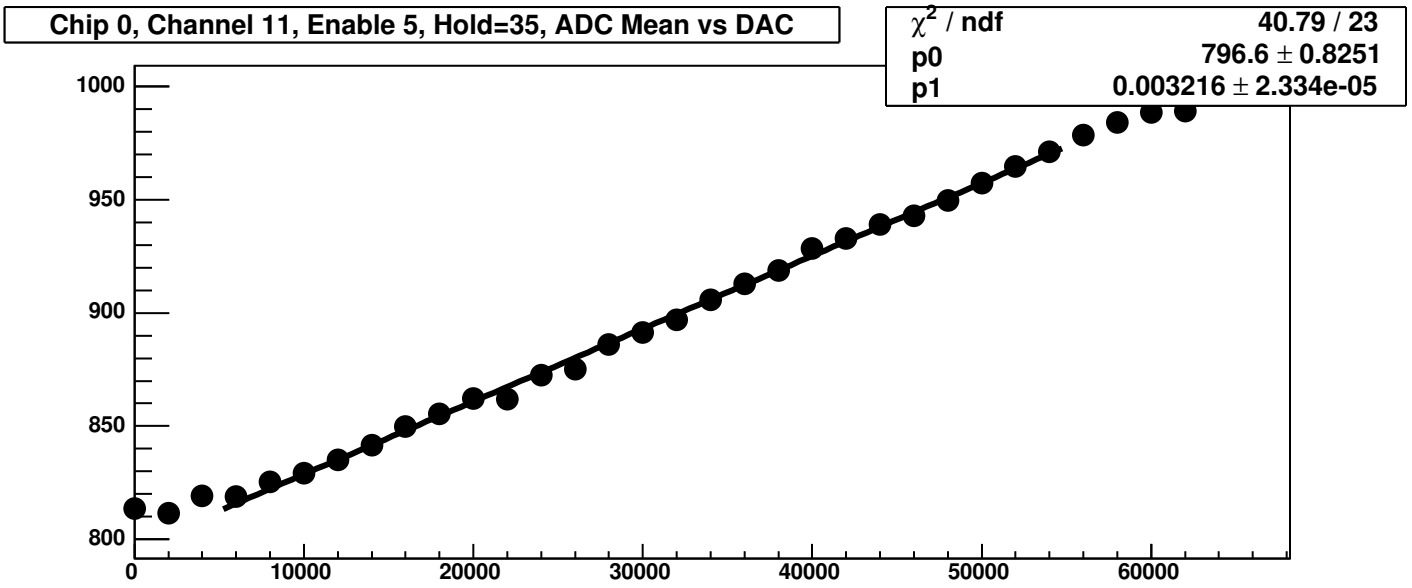

Chip 0, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

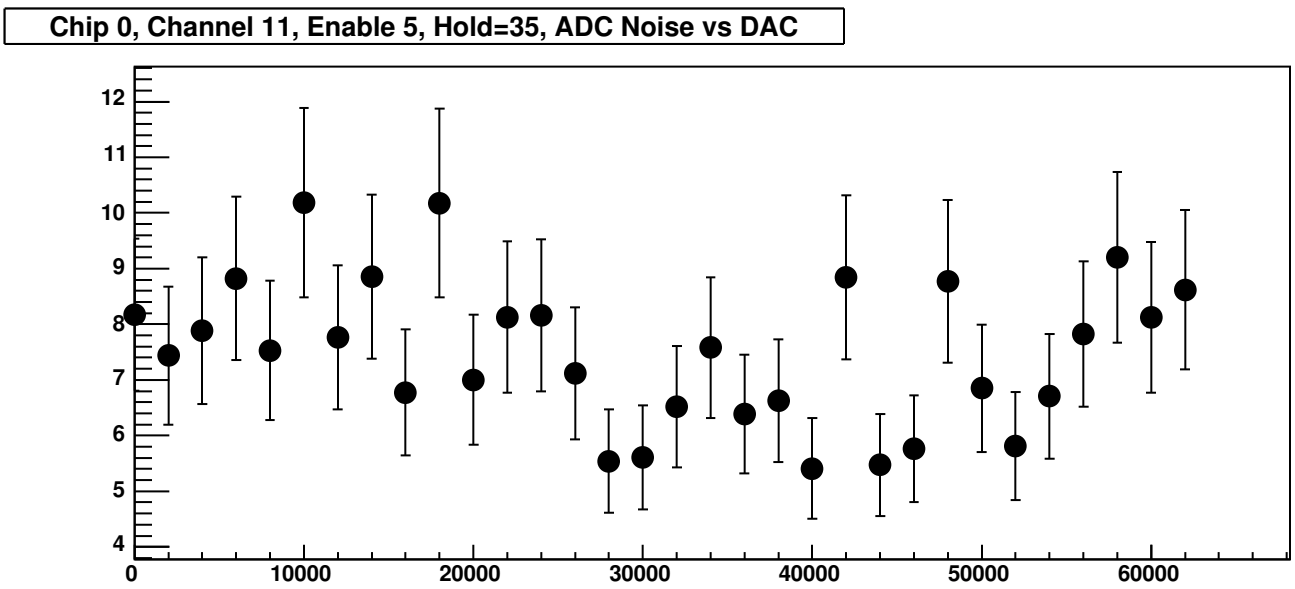

Chip 0, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

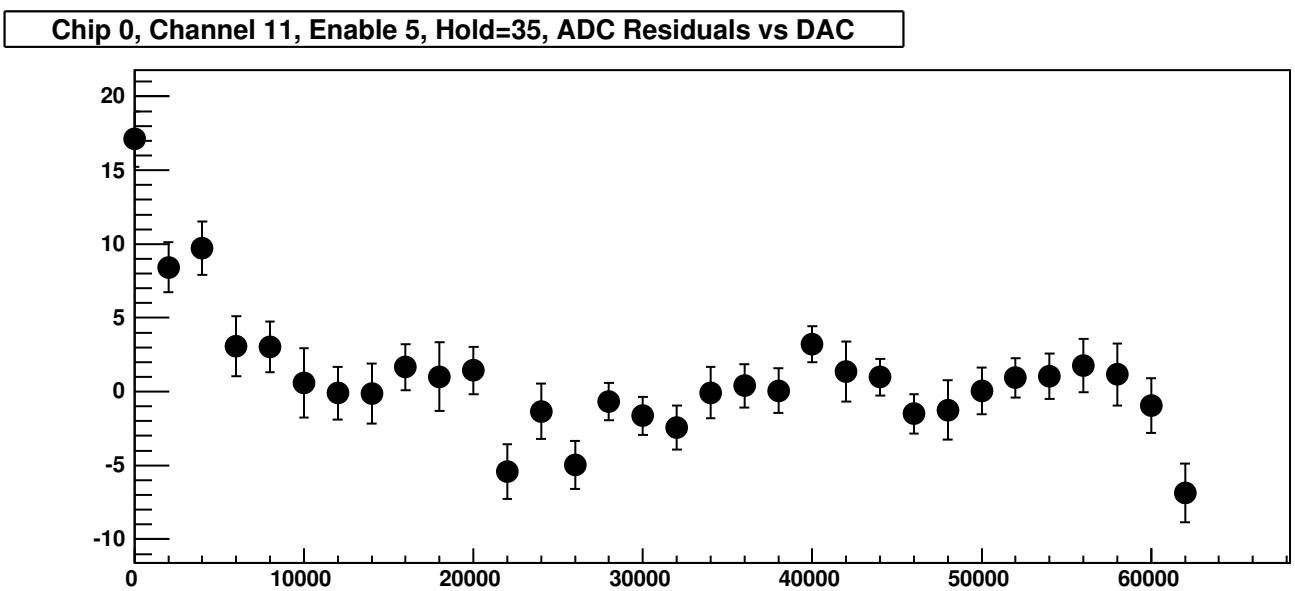

Chip 0, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 53.51 / 23
p0 802.2 ± 0.7682
p1 0.003046 ± 2.336e-05
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000
70000

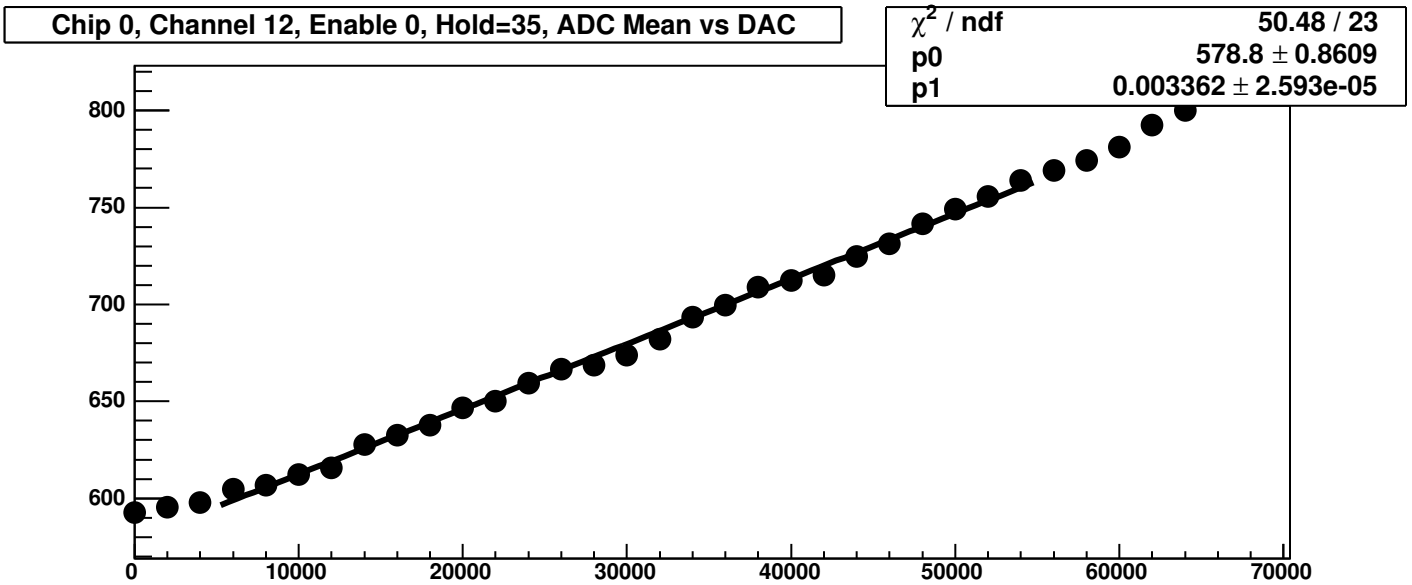

Chip 0, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

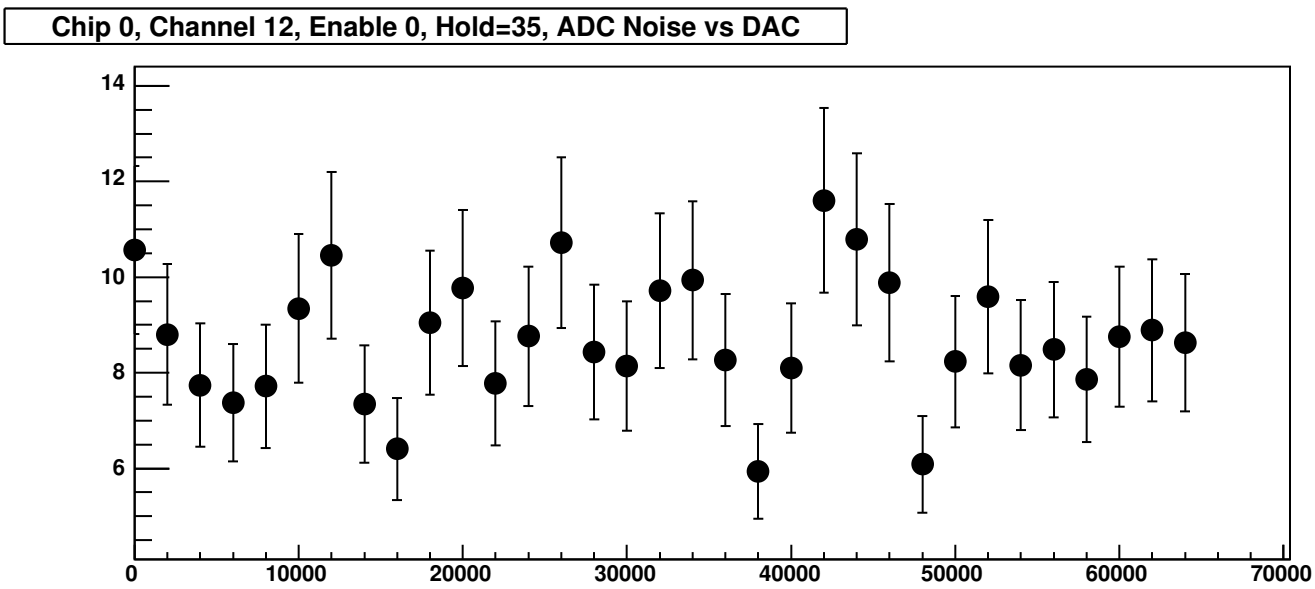

Chip 0, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

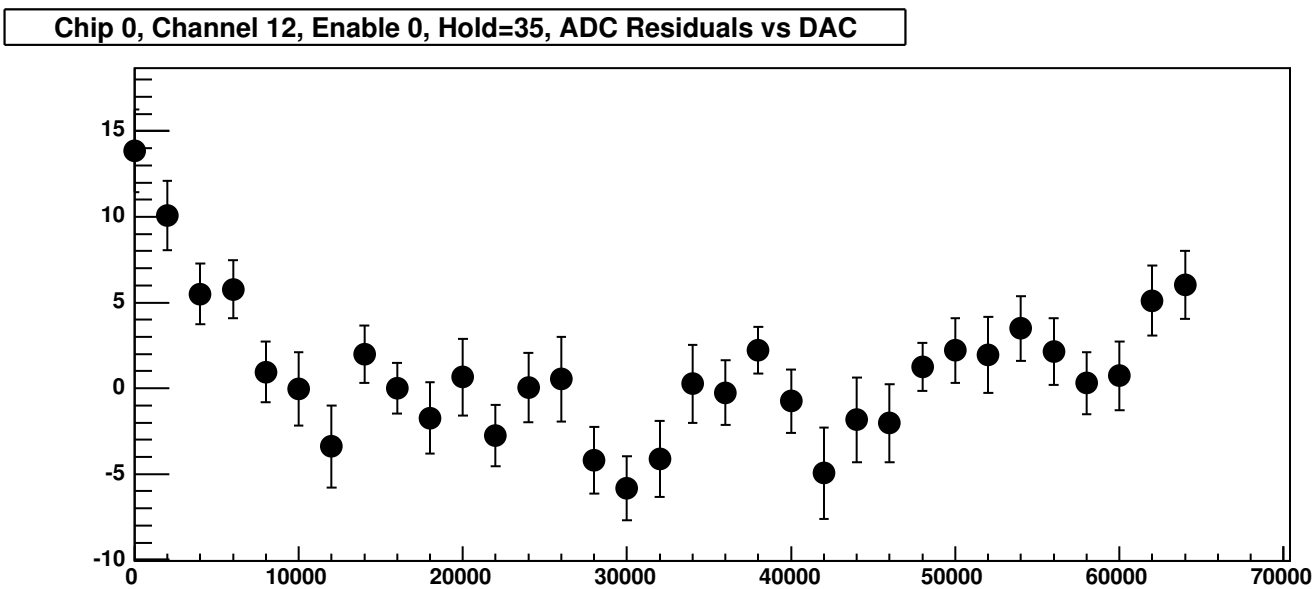

Chip 0, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 40.79 / 23
p0 796.6 ± 0.8251
p1 0.003216 ± 2.334e-05
1000
950
900
850
800
0
10000
20000
30000
40000
50000
60000

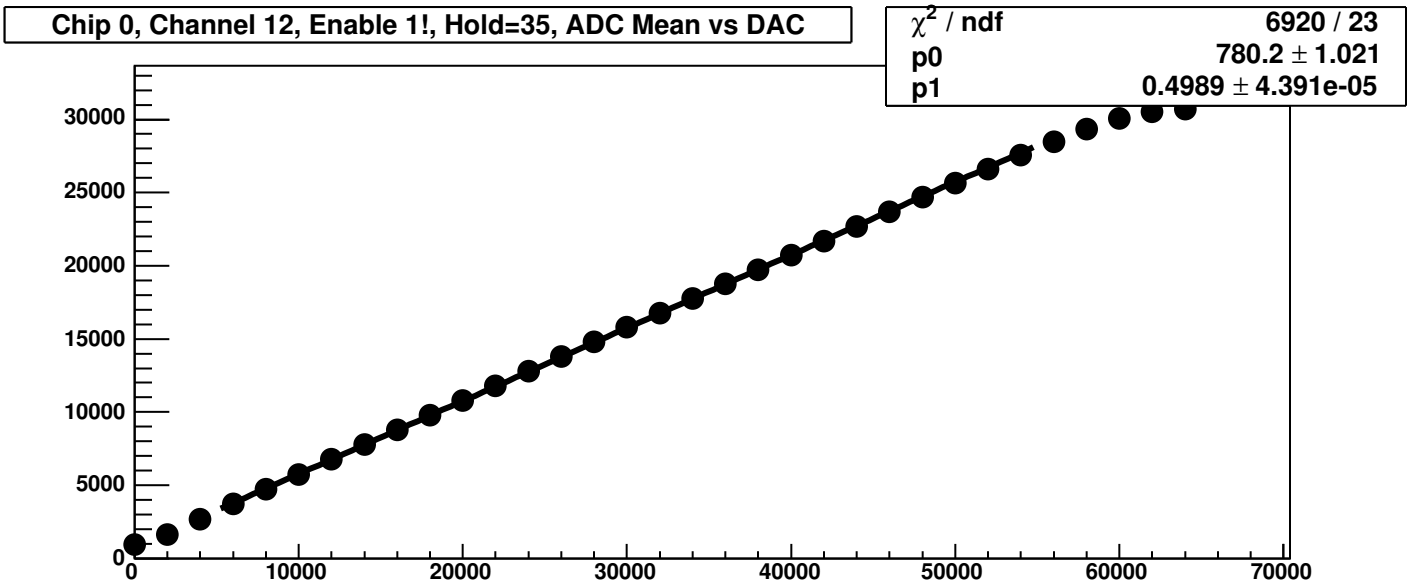

Chip 0, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

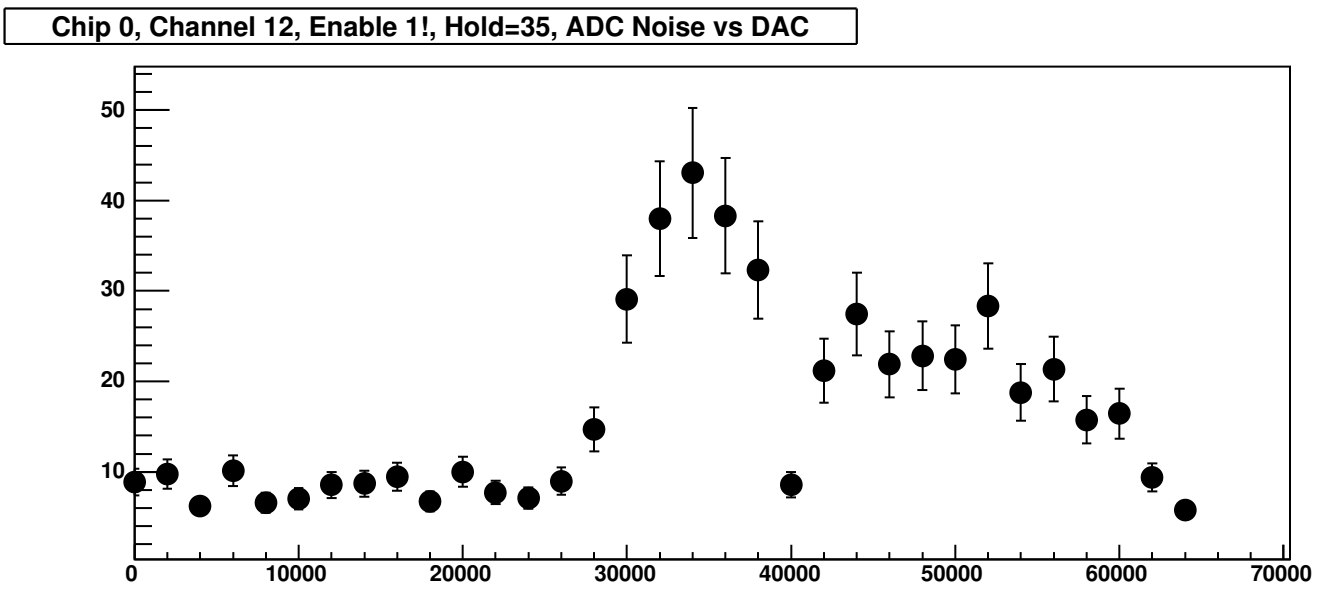

Chip 0, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000

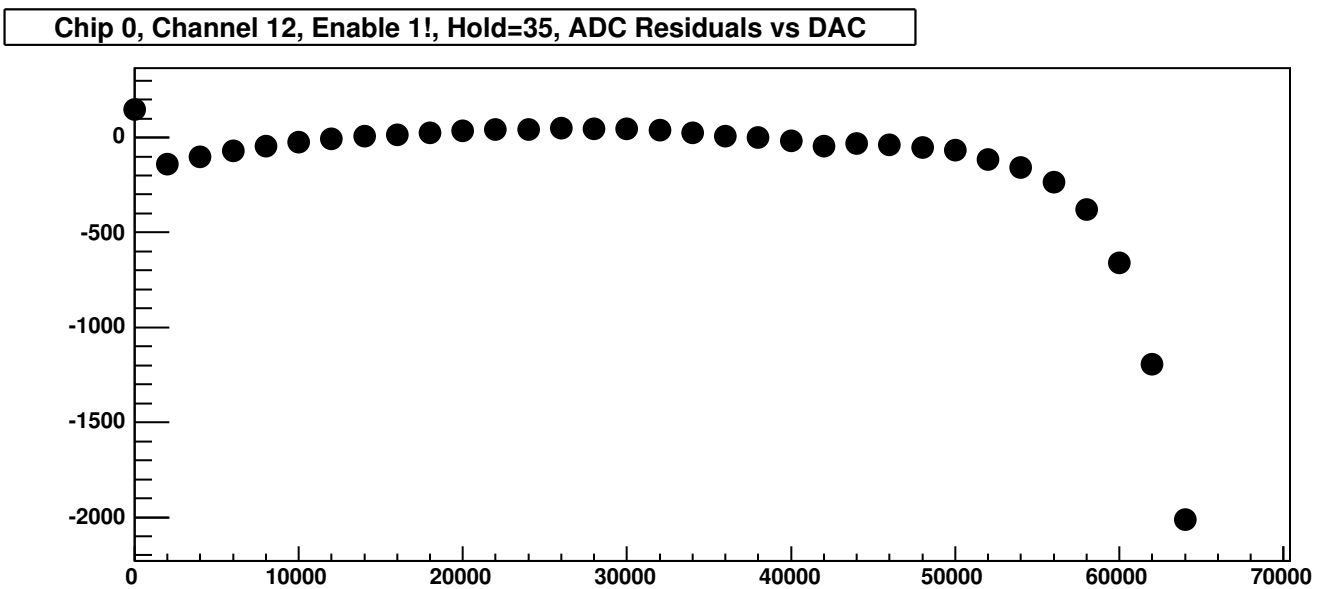

Chip 0, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 50.48 / 23
p0 578.8 ± 0.8609
p1 0.003362 ± 2.593e-05
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000
70000

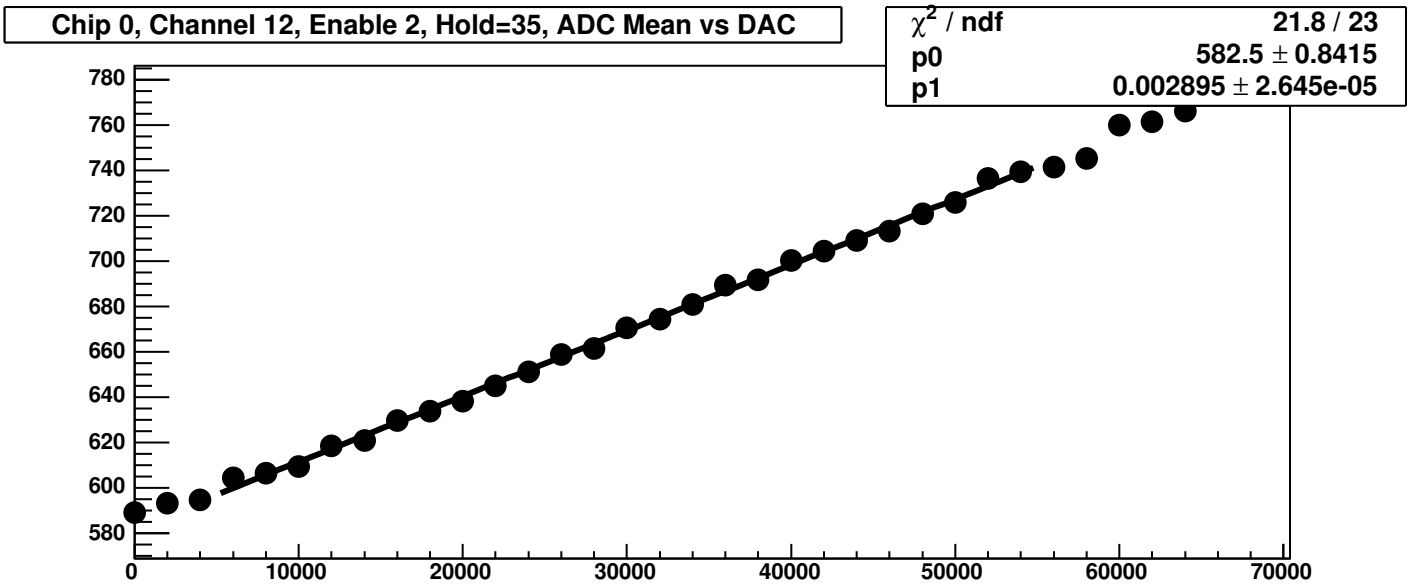

Chip 0, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
10000
20000
30000
40000
50000
60000
70000

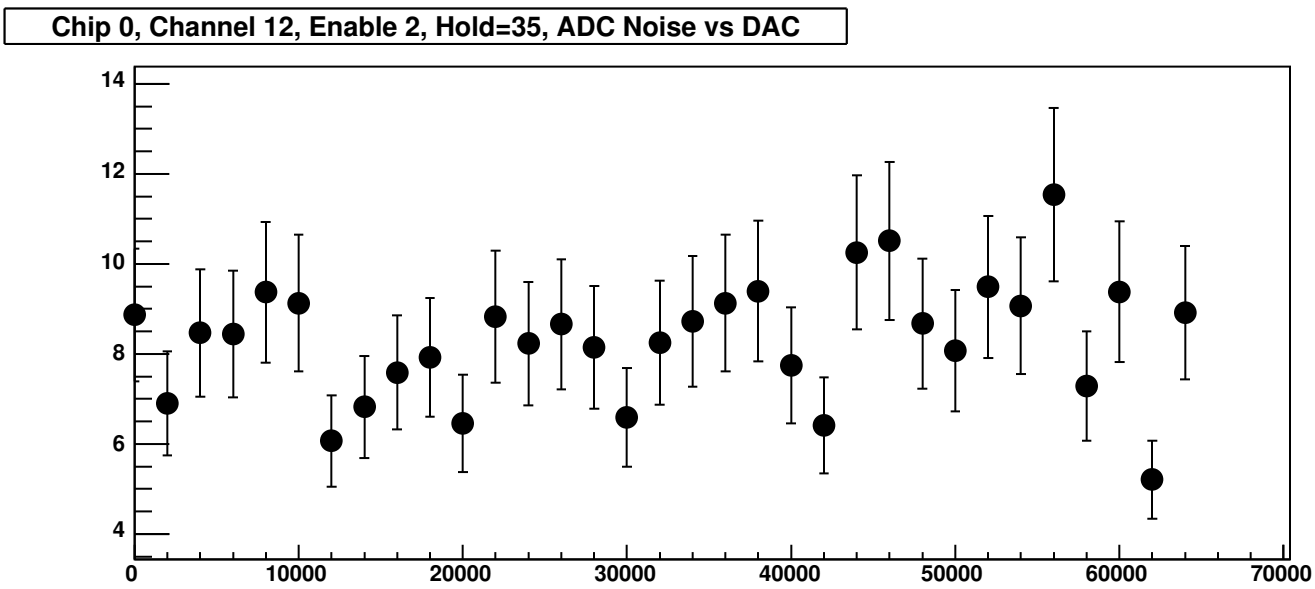

Chip 0, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

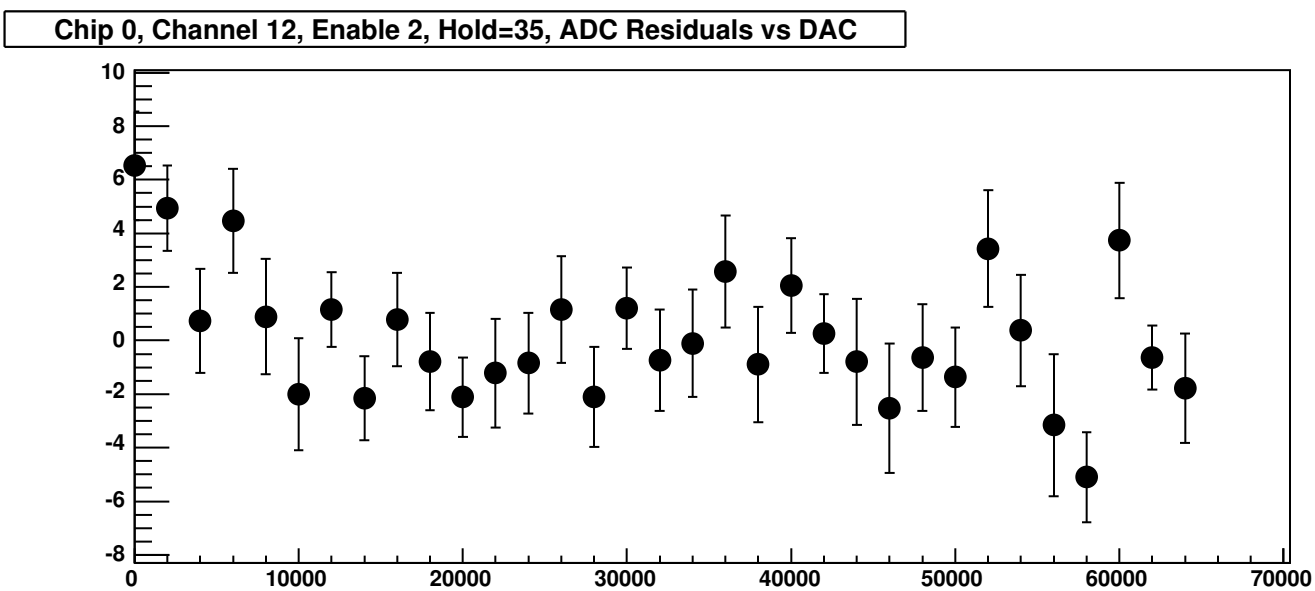

Chip 0, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 6920 / 23
p0 780.2 ± 1.021
p1 0.4989 ± 4.391e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

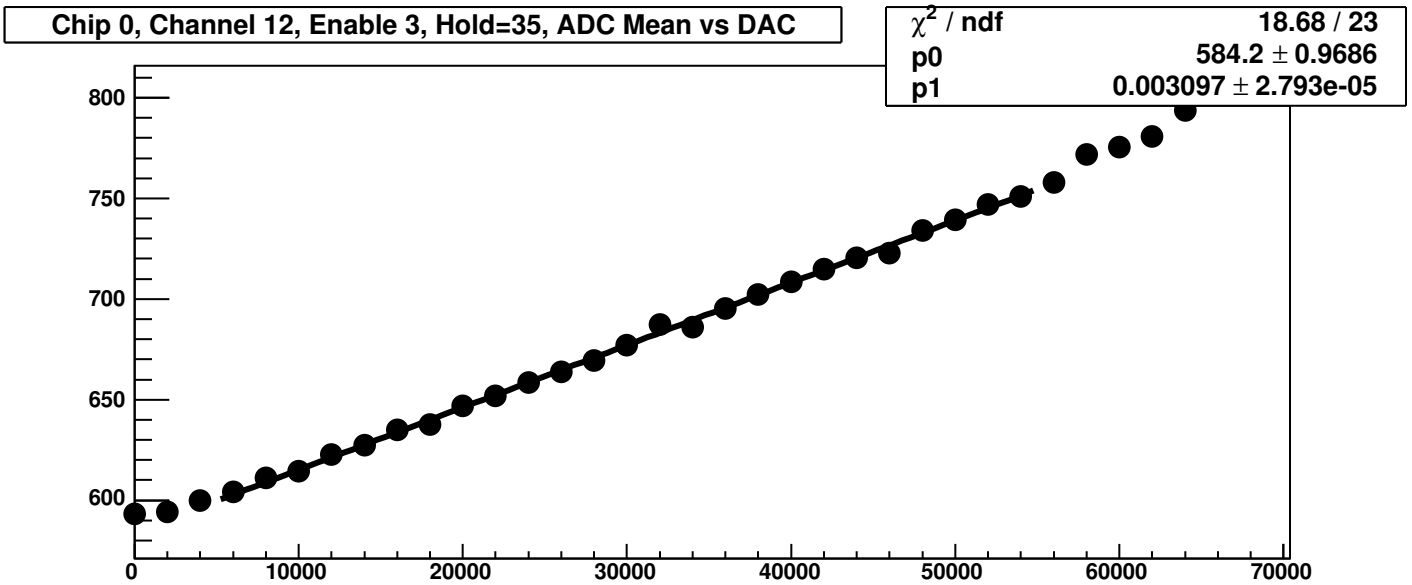

Chip 0, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000

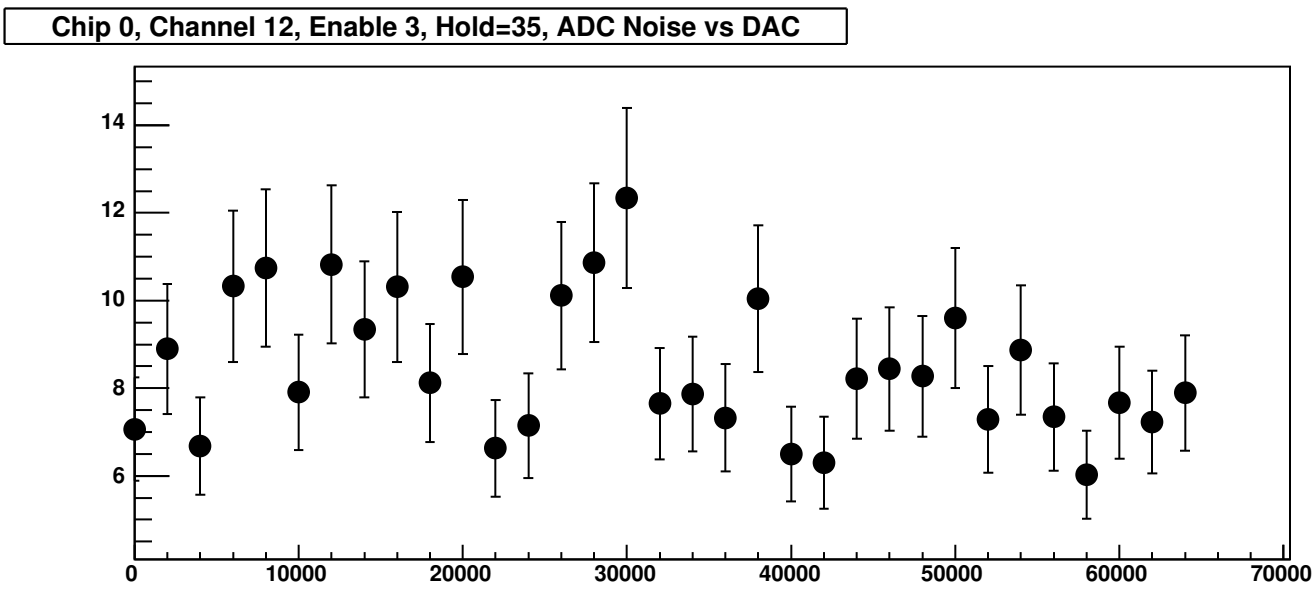

Chip 0, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000

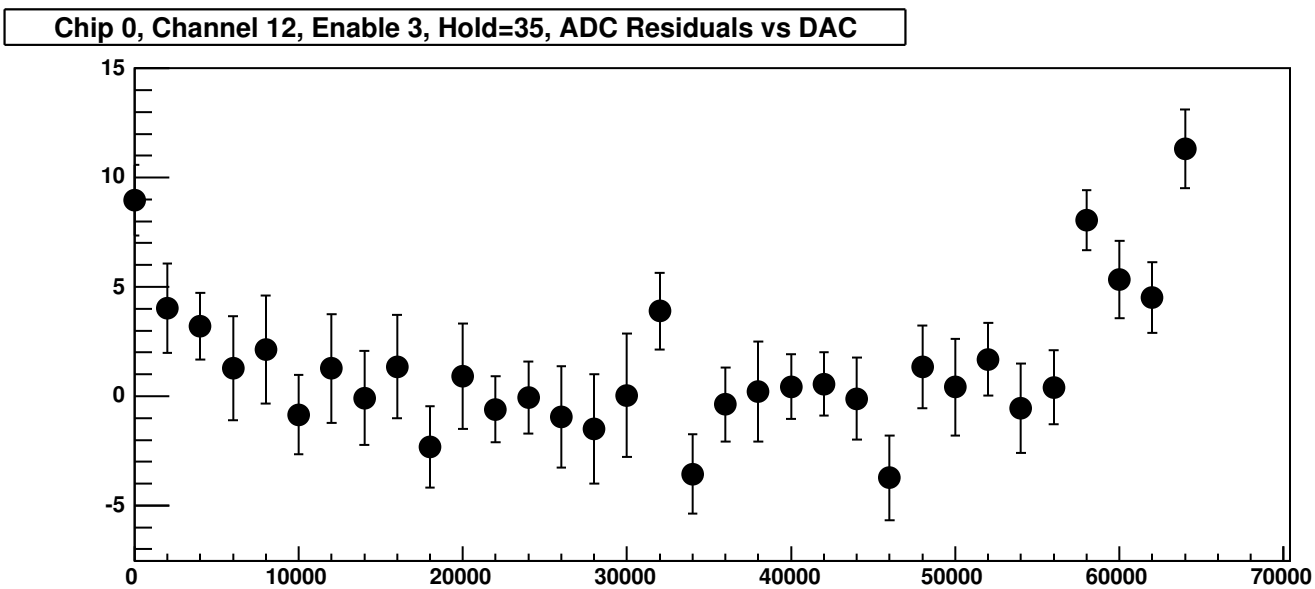

Chip 0, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 21.8 / 23
p0 582.5 ± 0.8415
p1 0.002895 ± 2.645e-05
780
760
740
720
700
680
660
640
620
600
580
0
10000
20000
30000
40000
50000
60000
70000

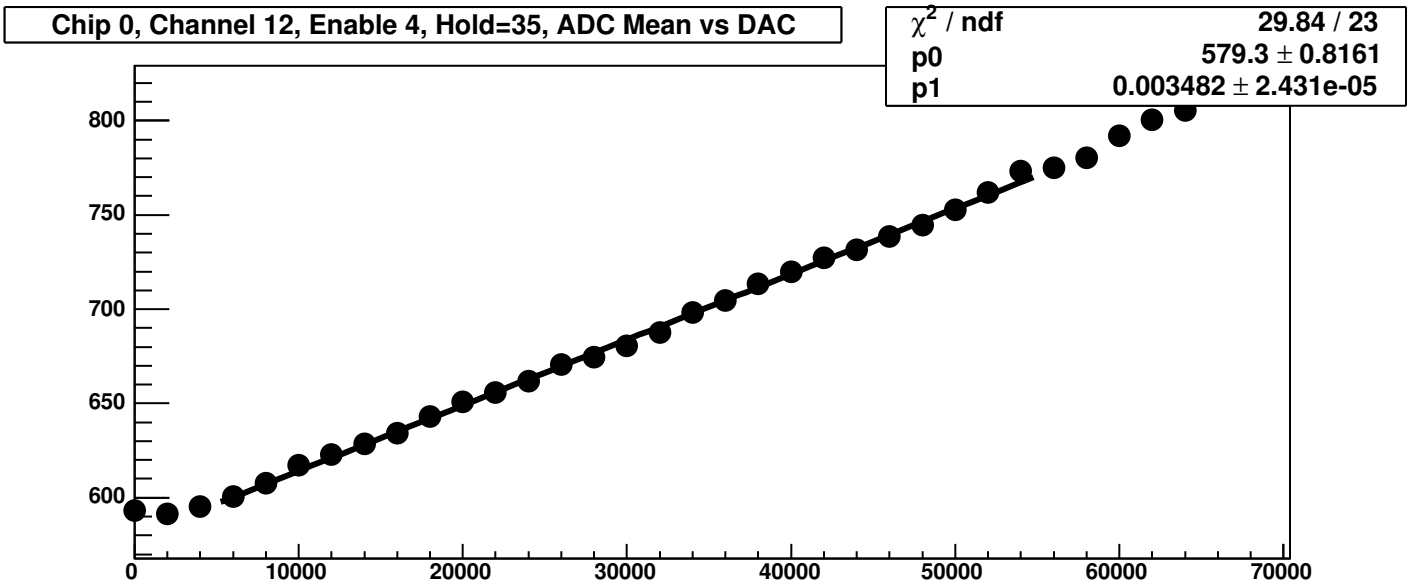

Chip 0, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

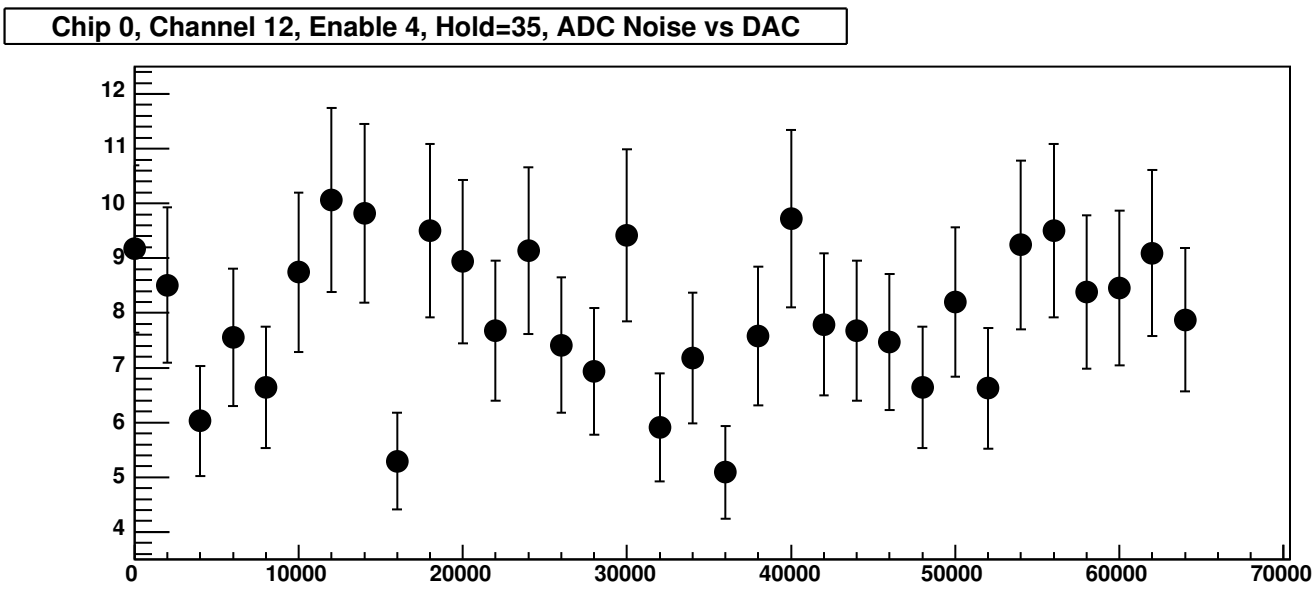

Chip 0, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

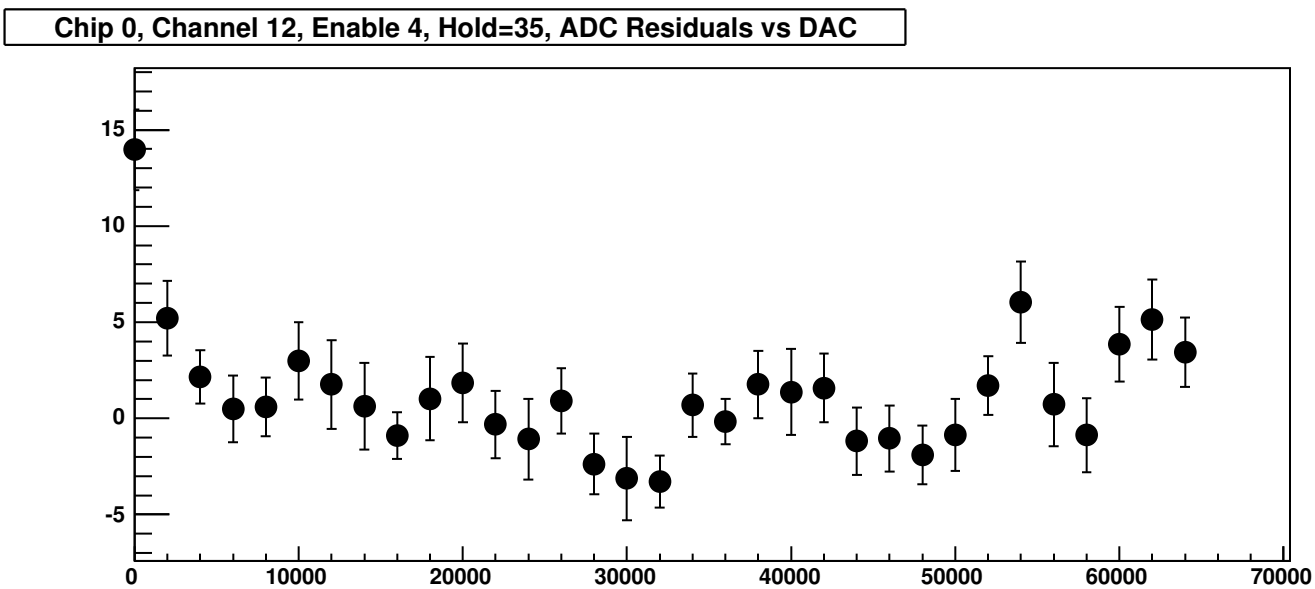

Chip 0, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 18.68 / 23
p0 584.2 ± 0.9686
p1 0.003097 ± 2.793e-05
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000
70000

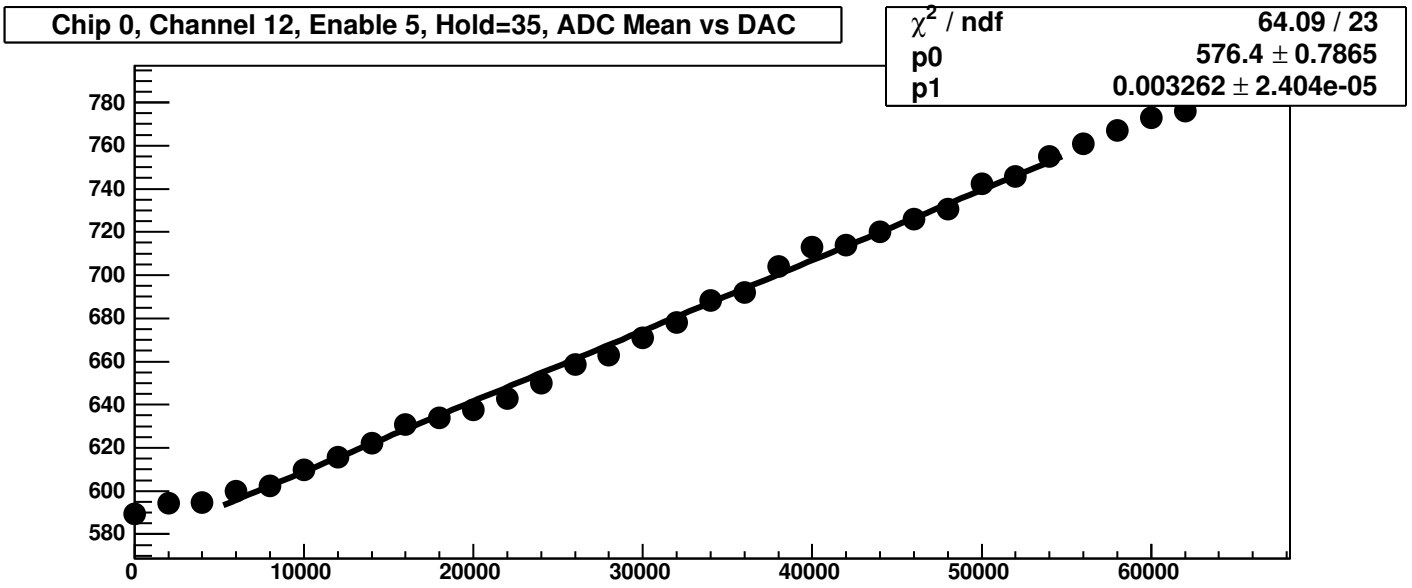

Chip 0, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
10000
20000
30000
40000
50000
60000
70000

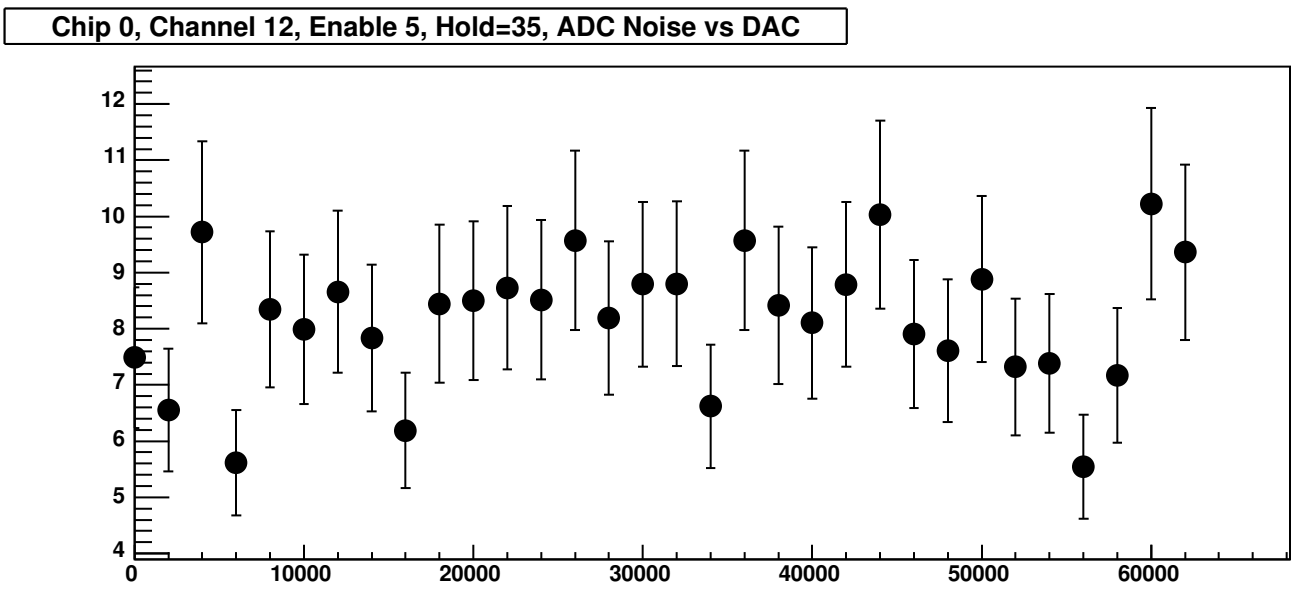

Chip 0, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

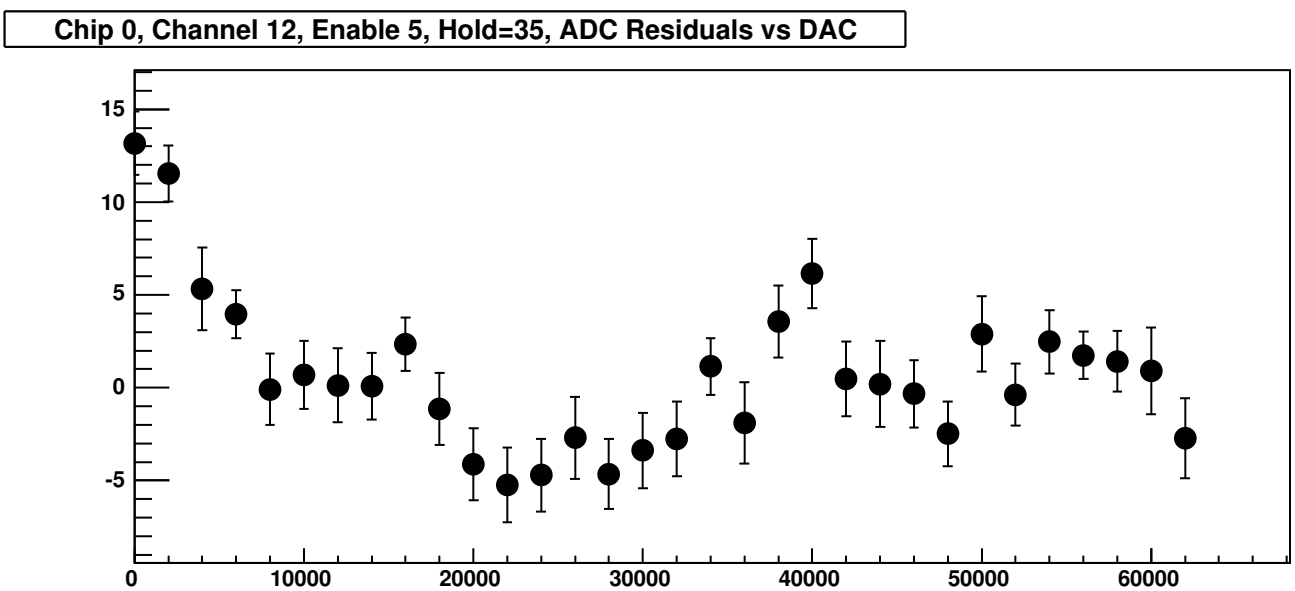

Chip 0, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.84 / 23
p0 579.3 ± 0.8161
p1 0.003482 ± 2.431e-05
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000
70000

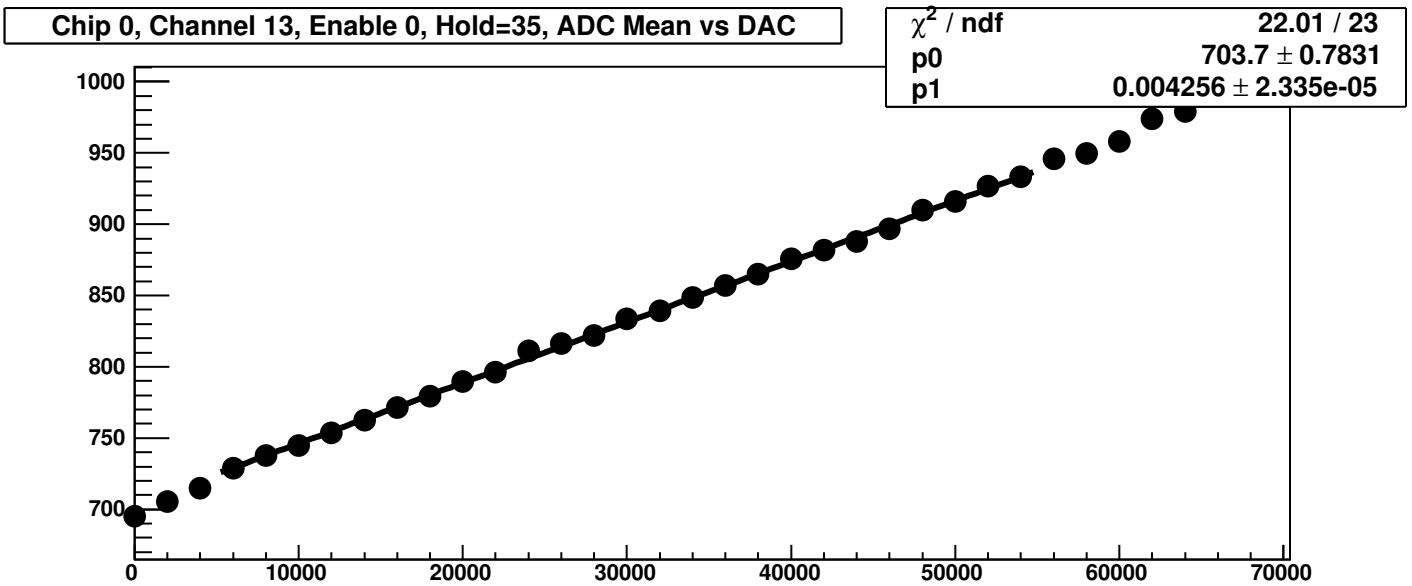

Chip 0, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

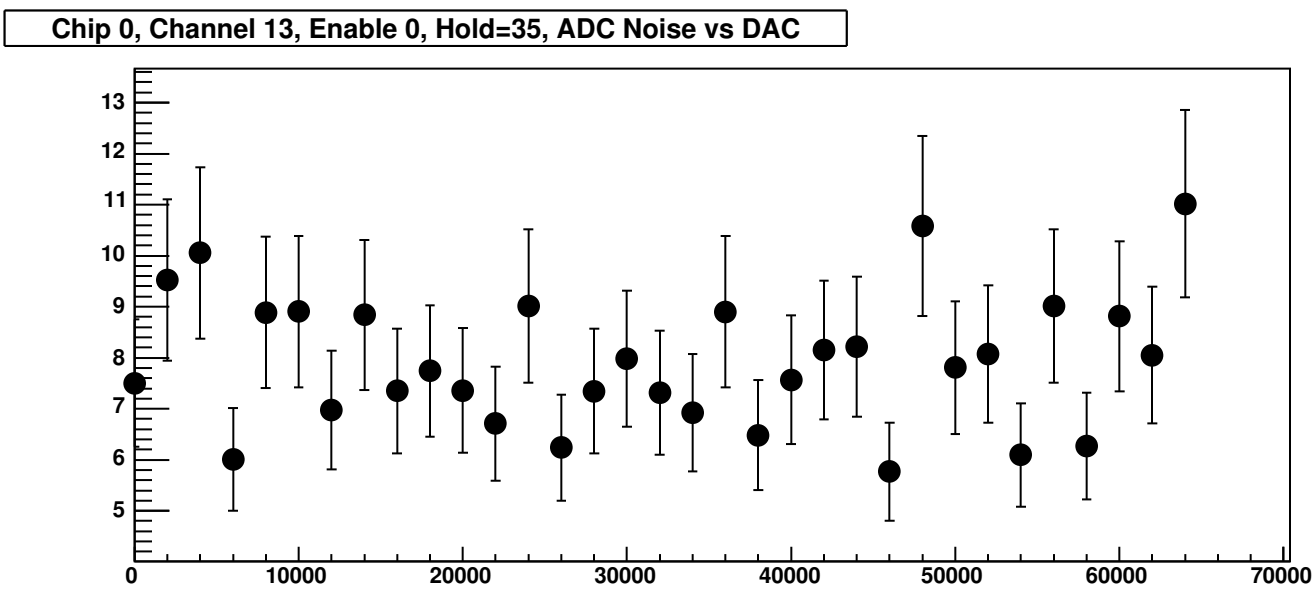

Chip 0, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

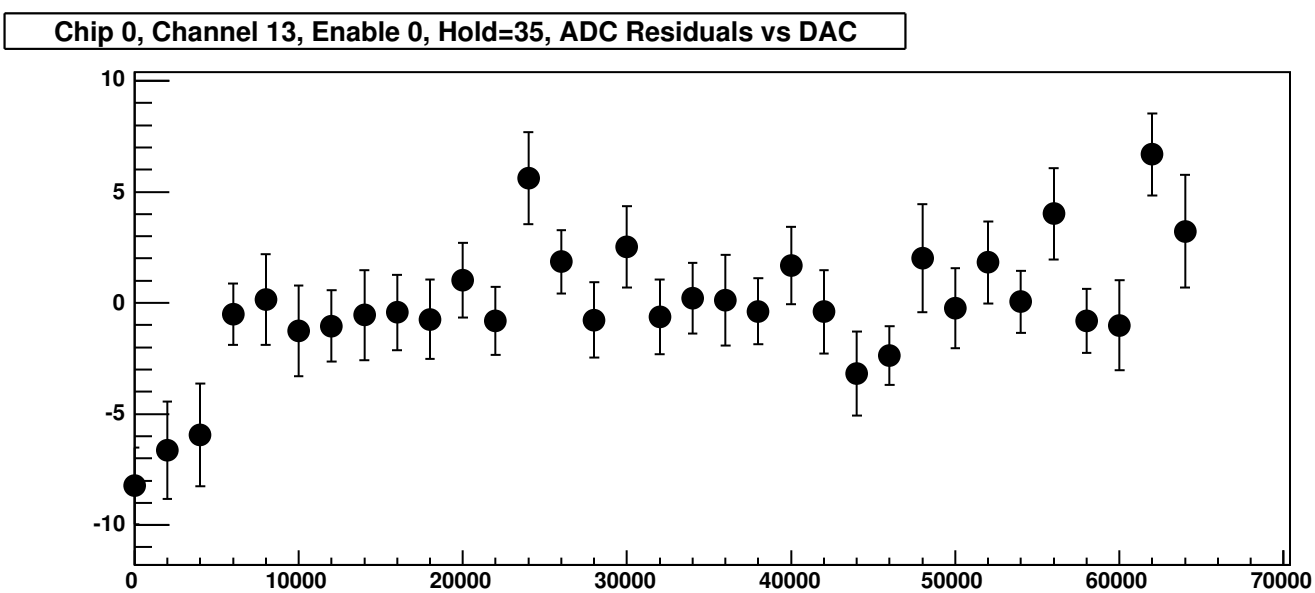

Chip 0, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 64.09 / 23
p0 576.4 ± 0.7865
p1 0.003262 ± 2.404e-05
780
760
740
720
700
680
660
640
620
600
580
0
10000
20000
30000
40000
50000
60000

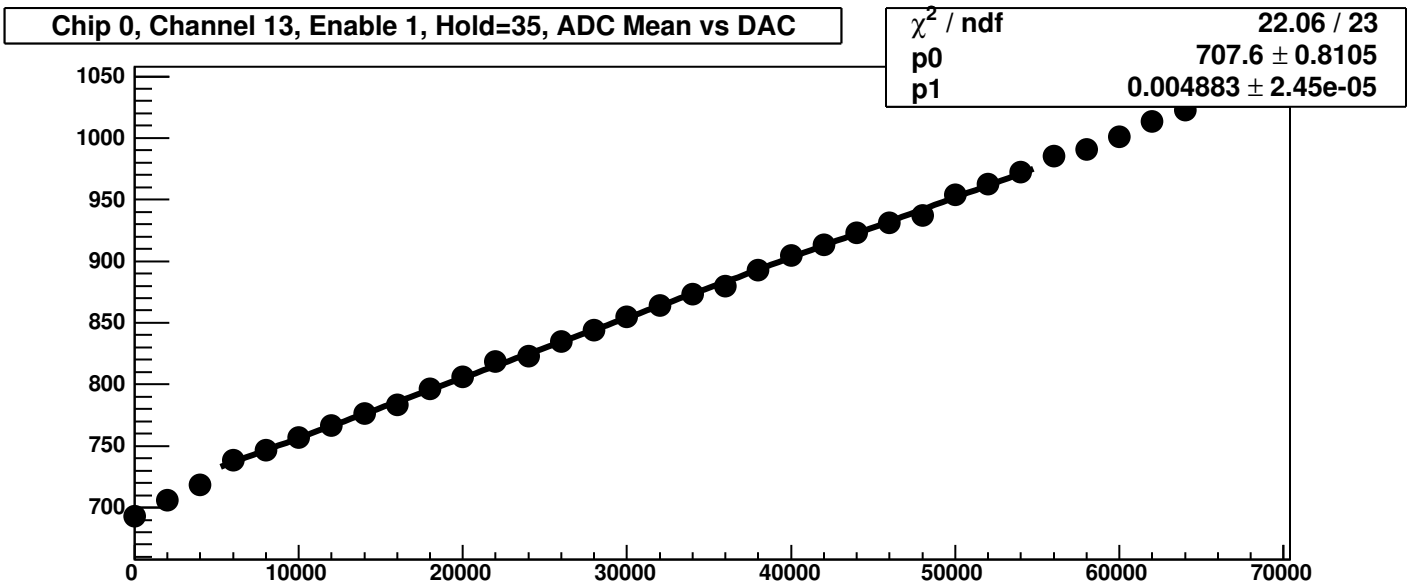

Chip 0, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

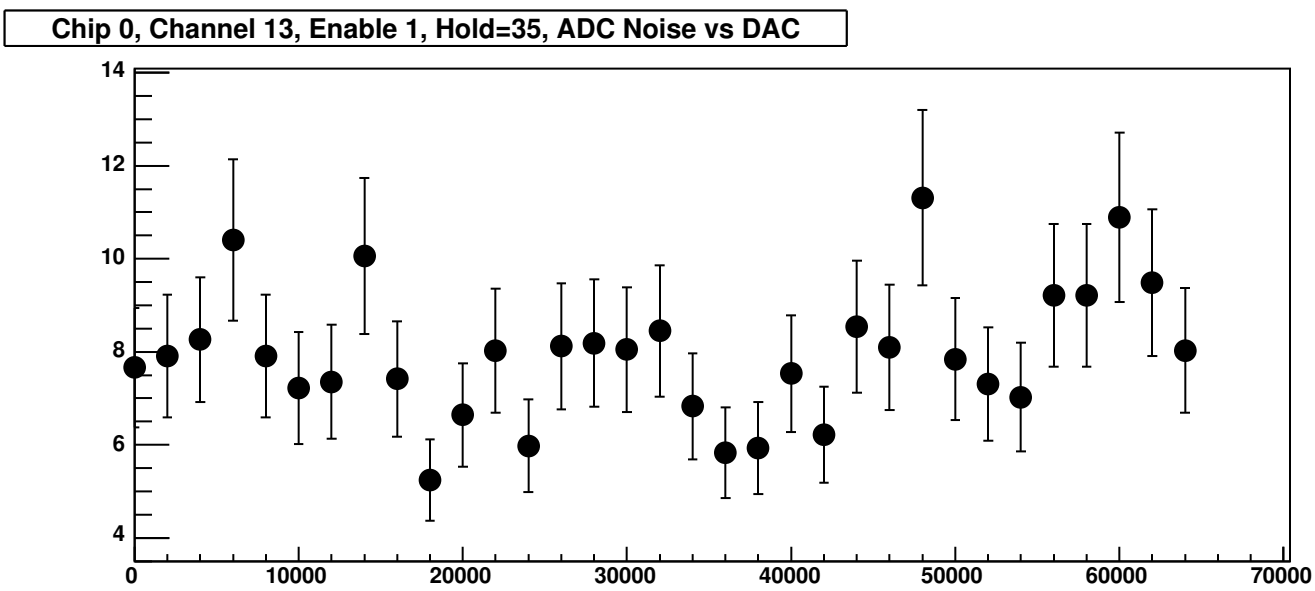

Chip 0, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000

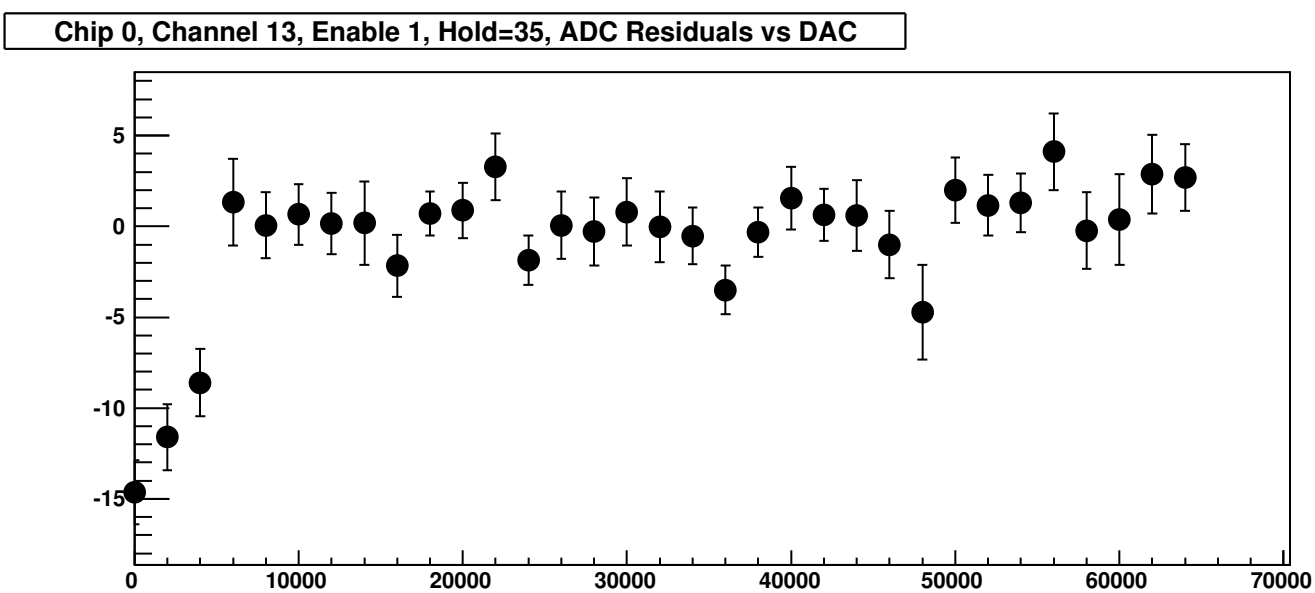

Chip 0, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 22.01 / 23
p0 703.7 ± 0.7831
p1 0.004256 ± 2.335e-05
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000

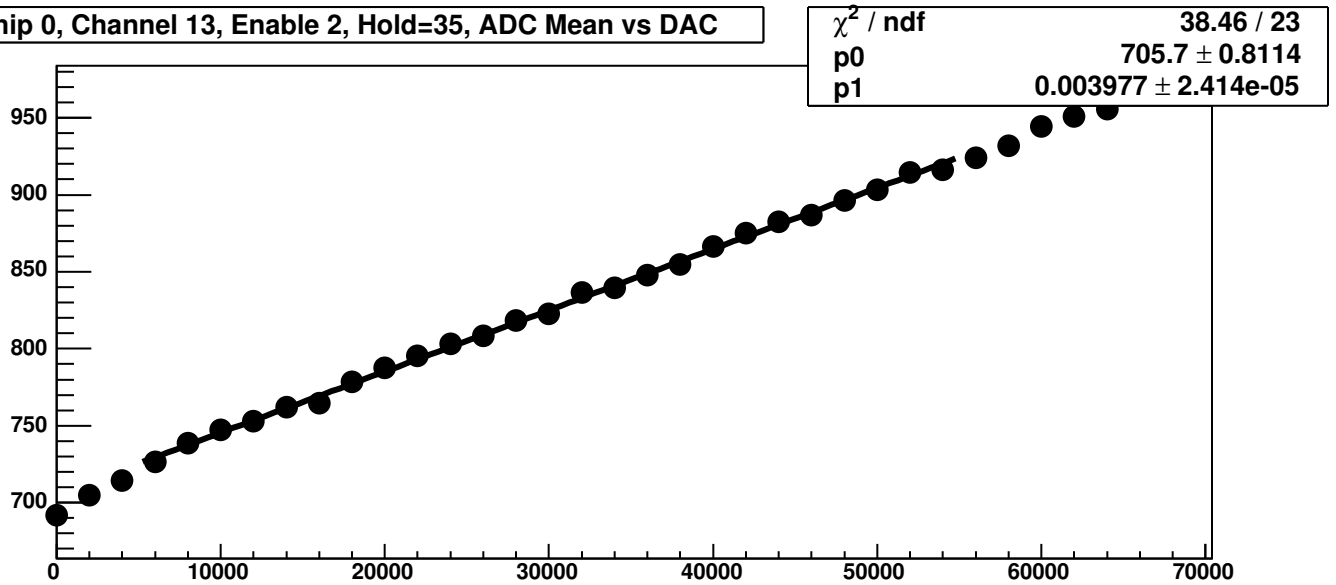

Chip 0, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 22.06 / 23
p0 707.6 ± 0.8105
p1 0.004883 ± 2.45e-05
1050
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000
70000

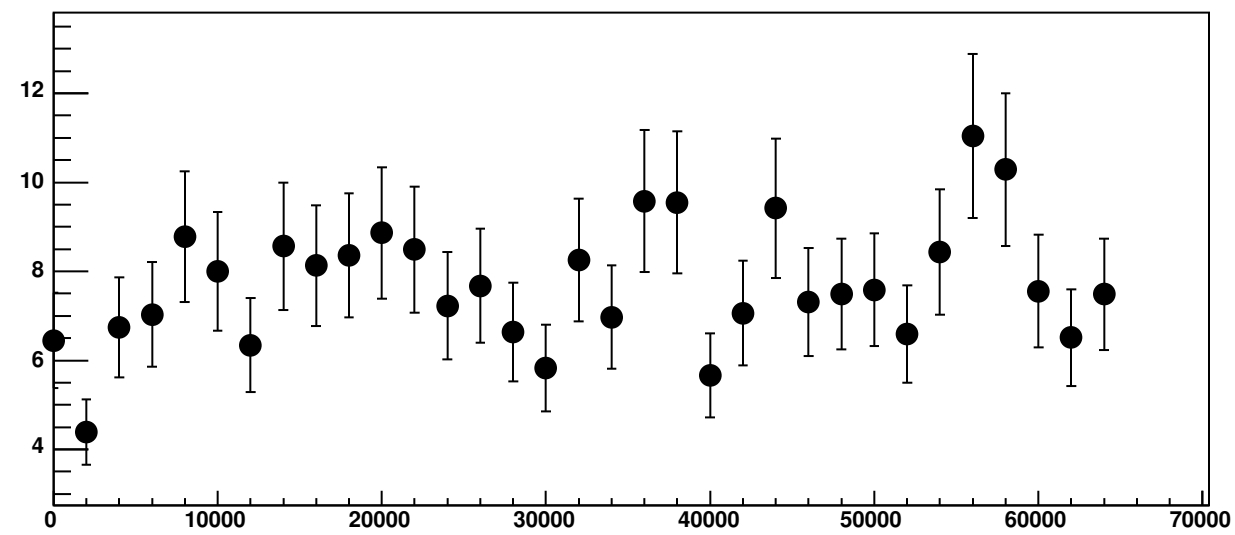

Chip 0, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

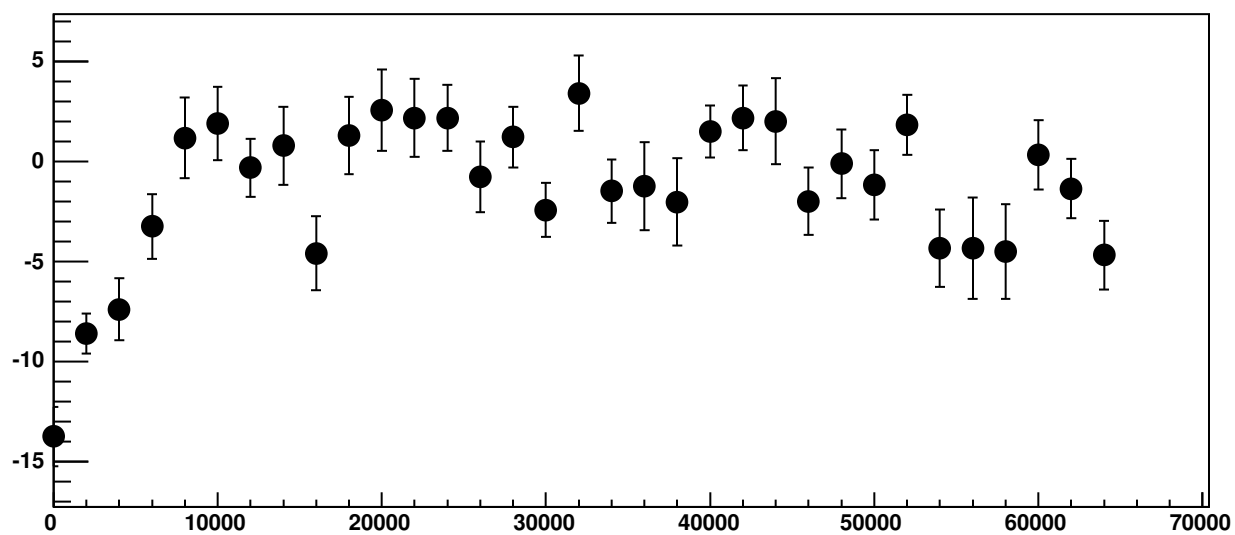
Chip 0, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

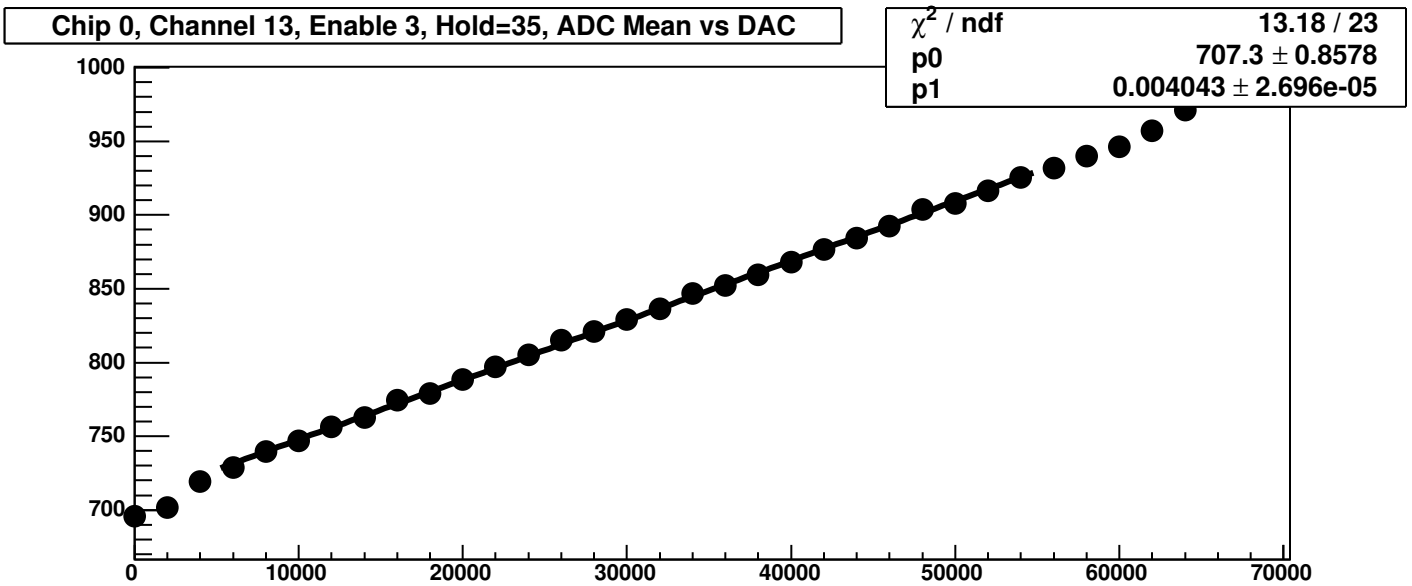


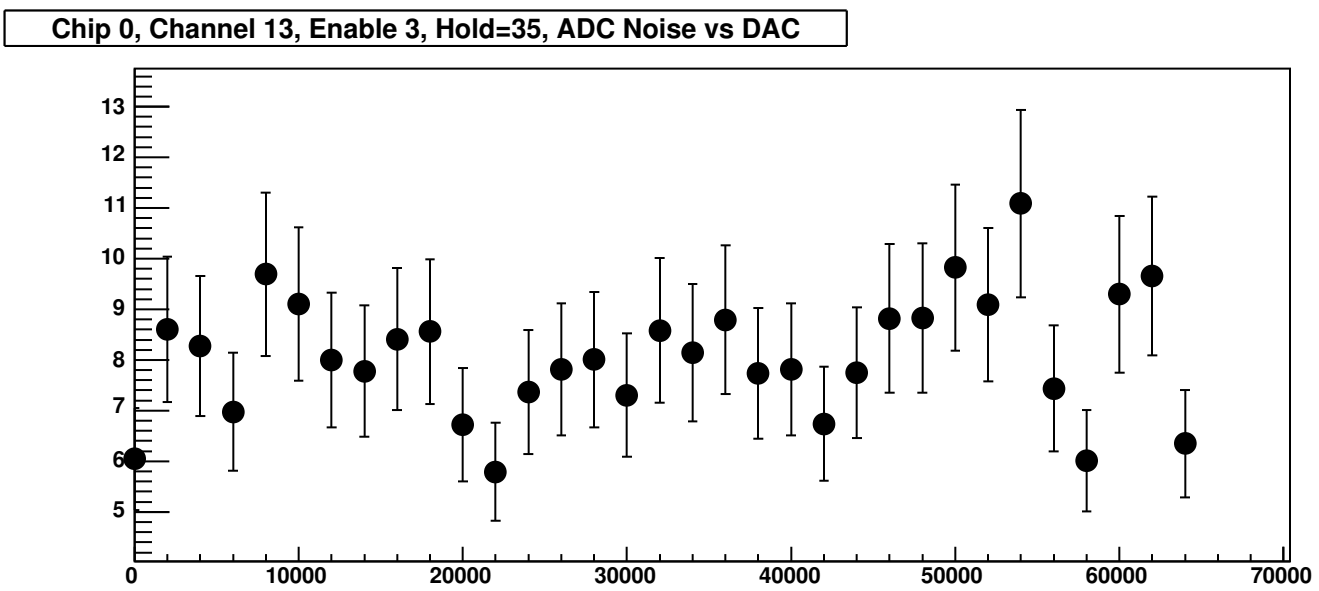
Chip 0, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC

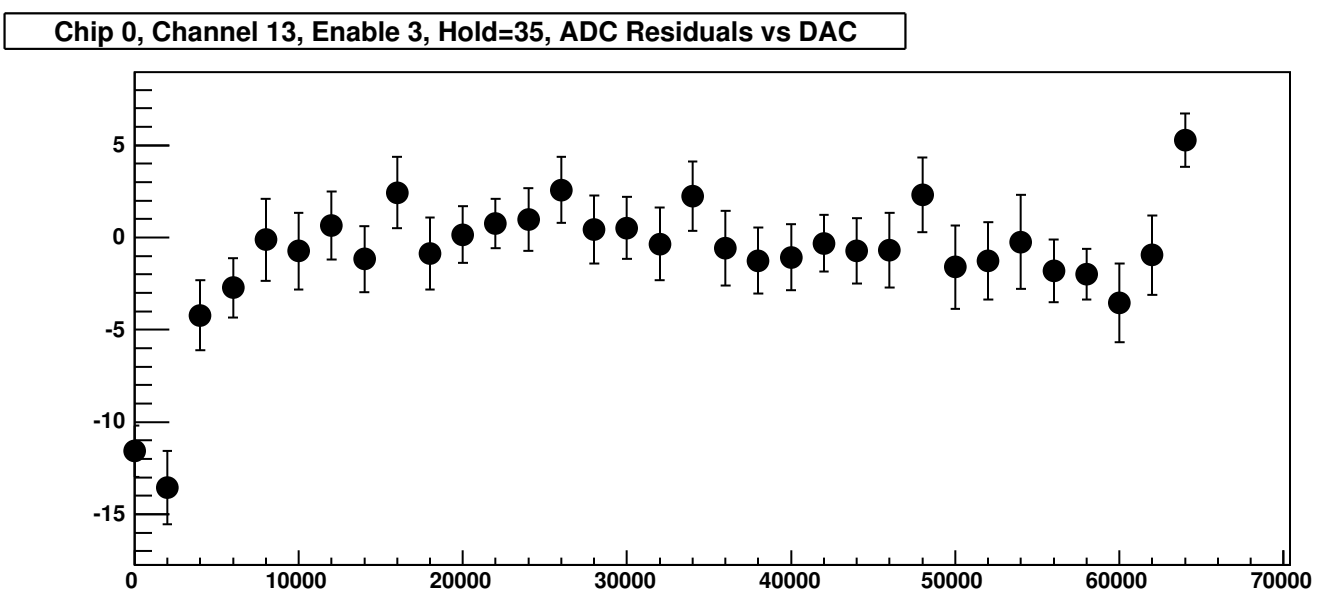


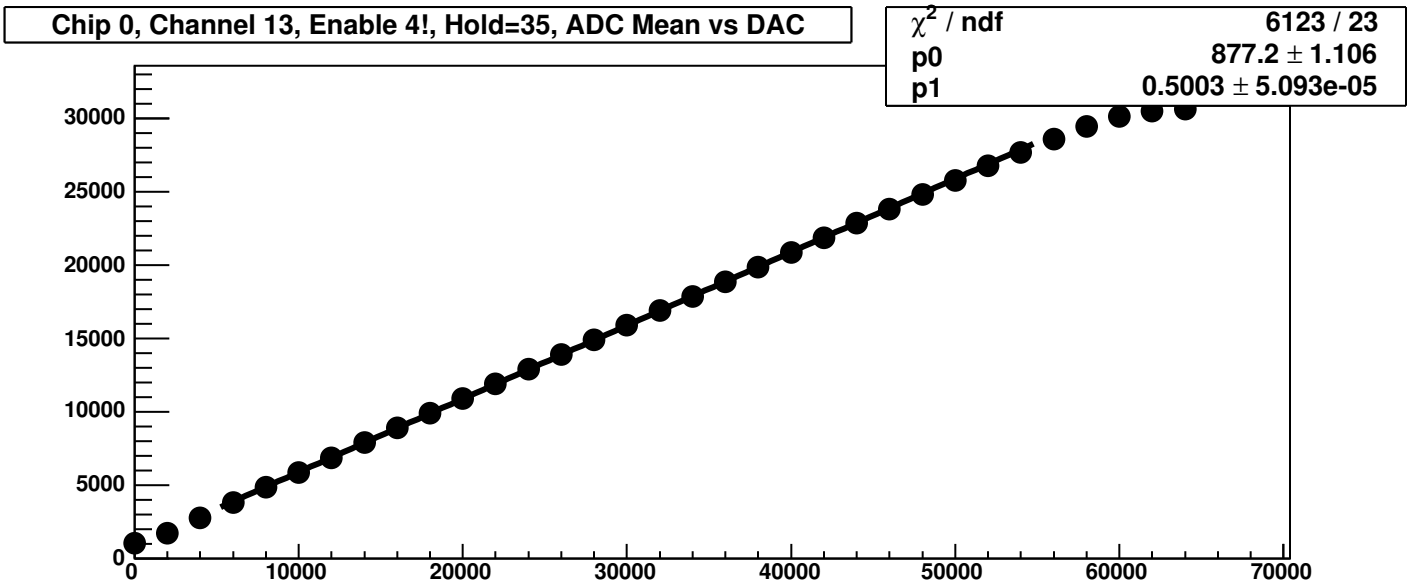
Chip 0, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

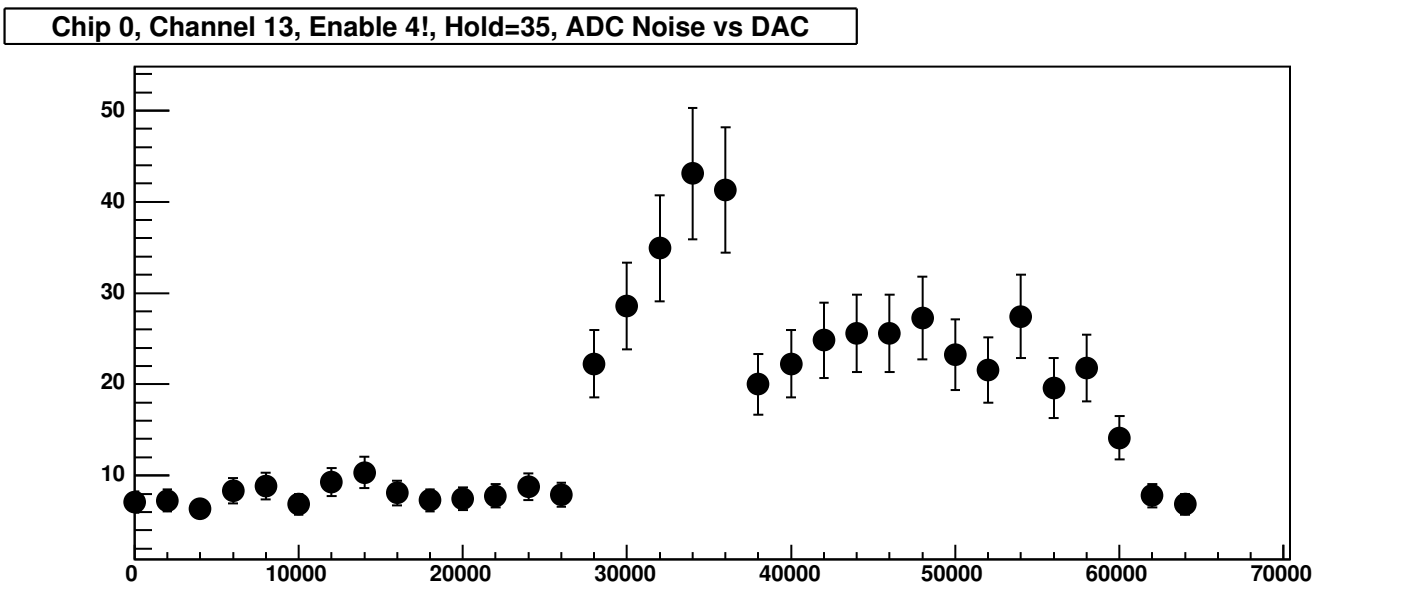


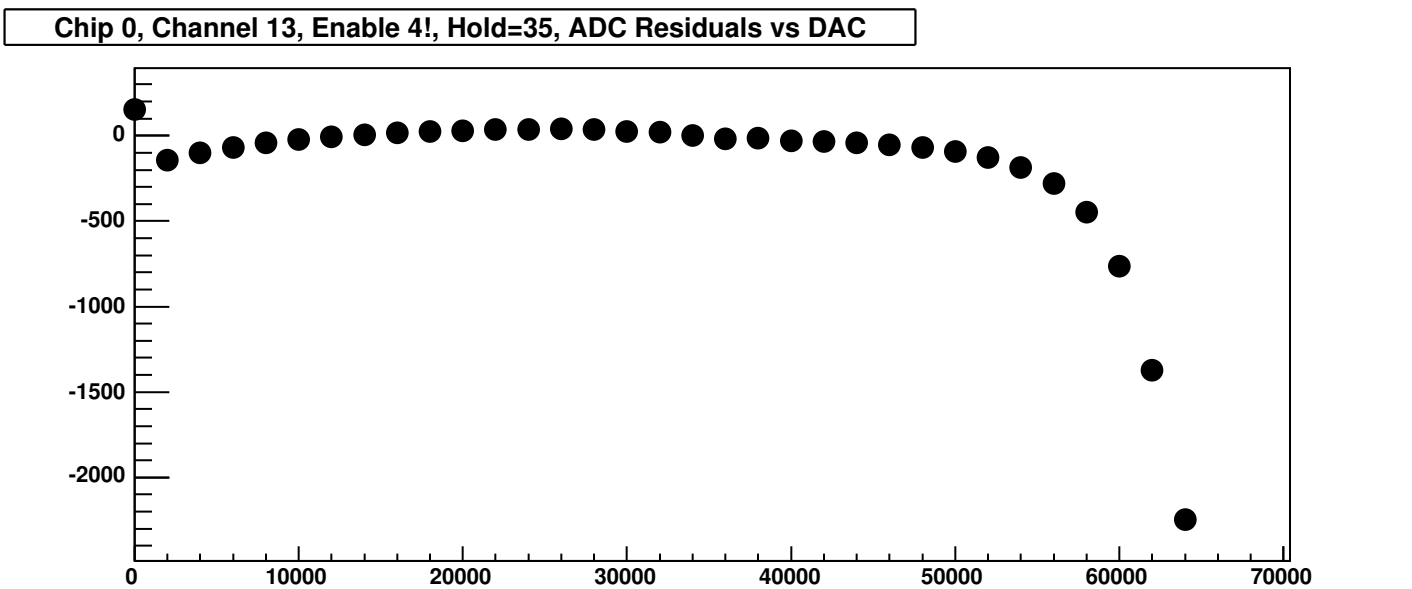

Chip 0, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 13.18 / 23
p0 707.3 ± 0.8578
p1 0.004043 ± 2.696e-05
1000
950
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000

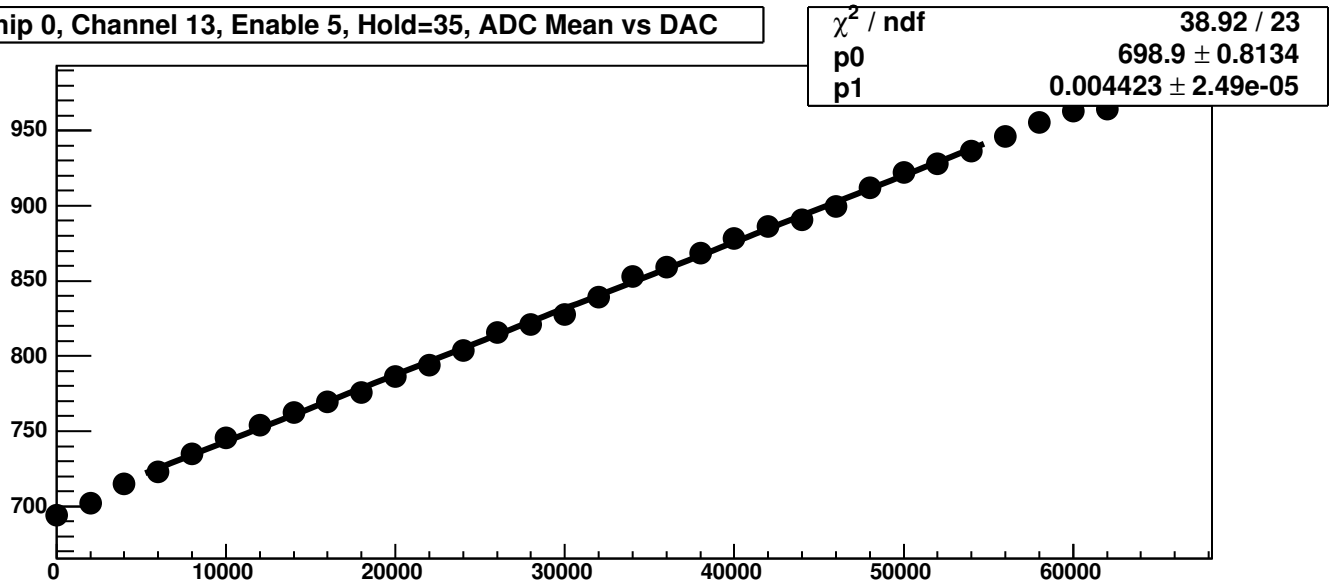

Chip 0, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 6123 / 23
p0 877.2 ± 1.106
p1 0.5003 ± 5.093e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

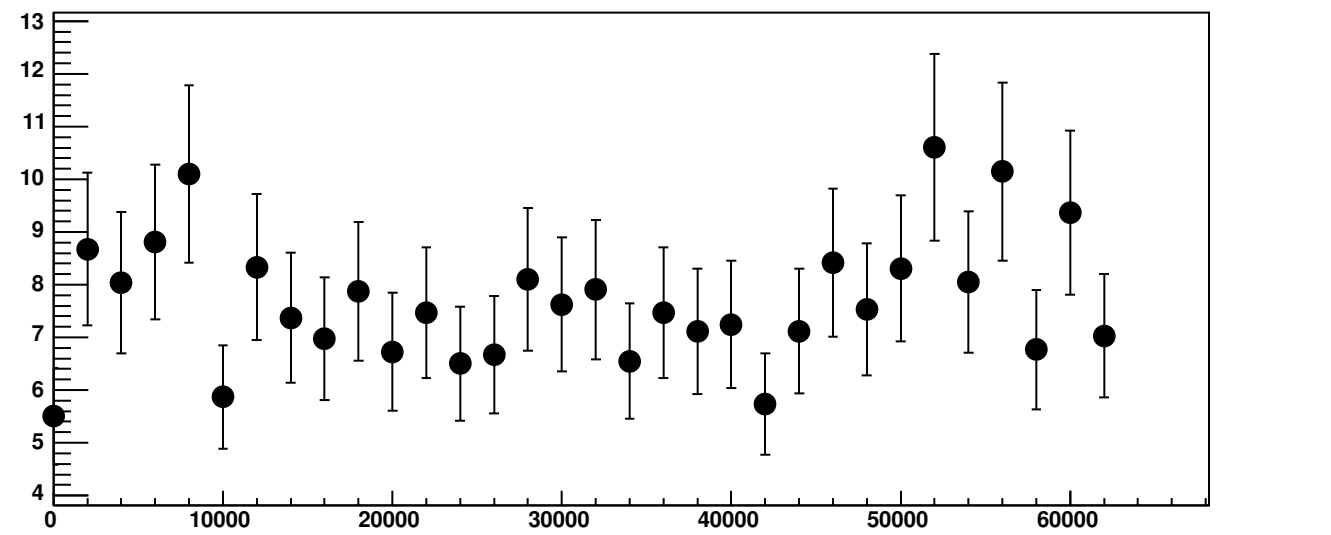

Chip 0, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000


Chip 0, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000

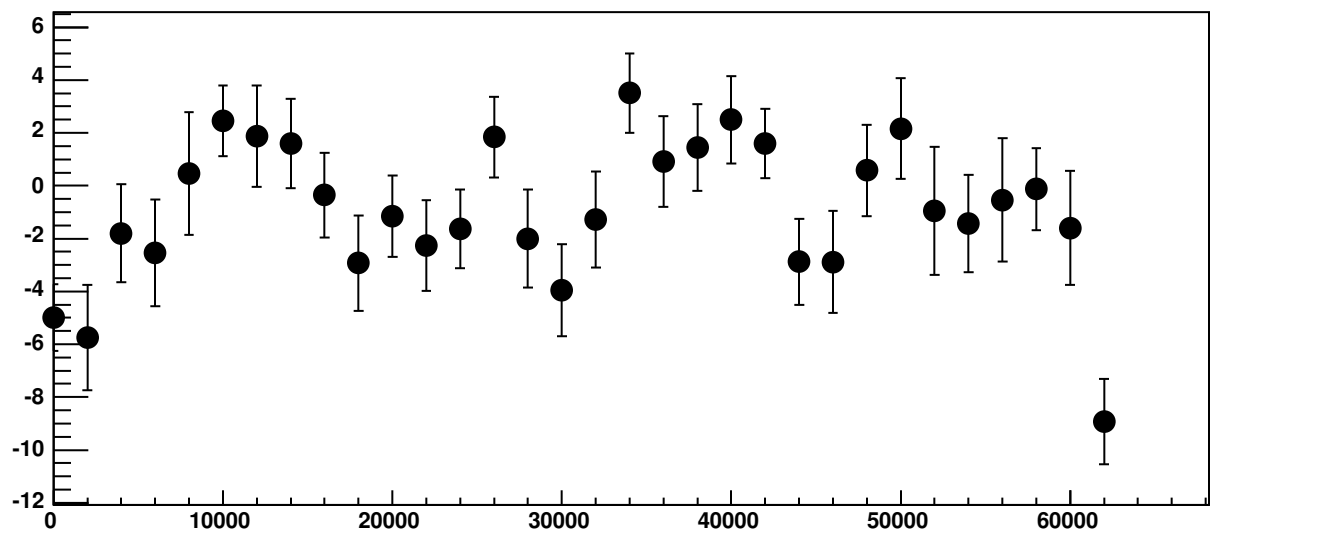
Chip 0, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC

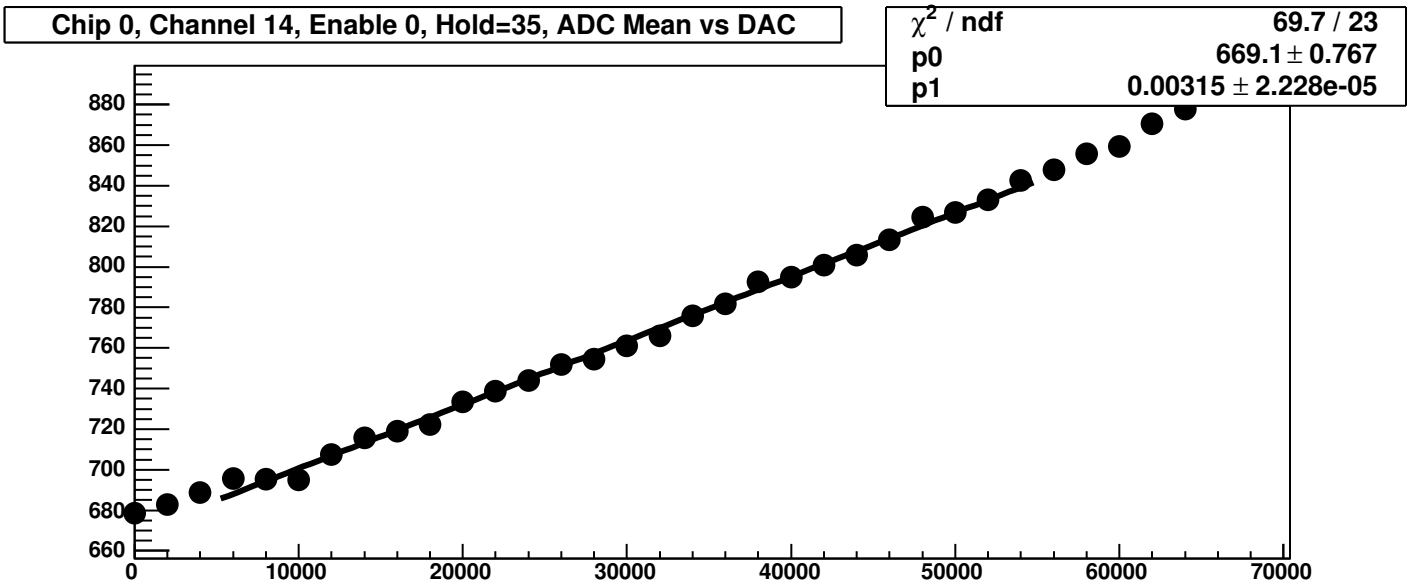


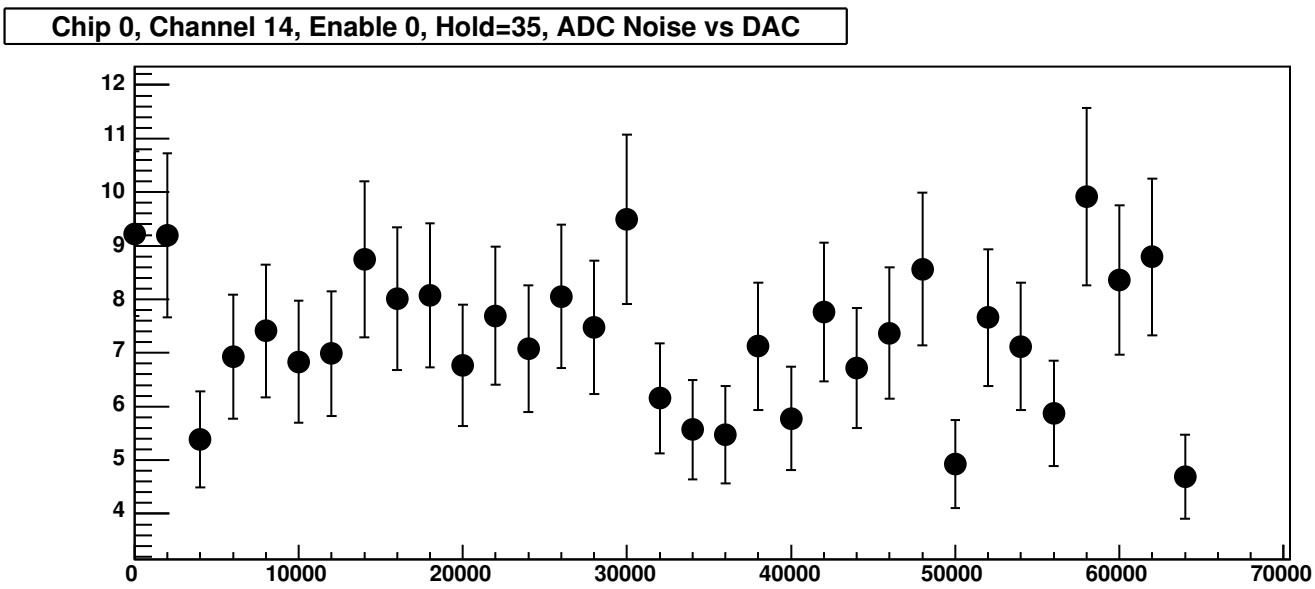
Chip 0, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC

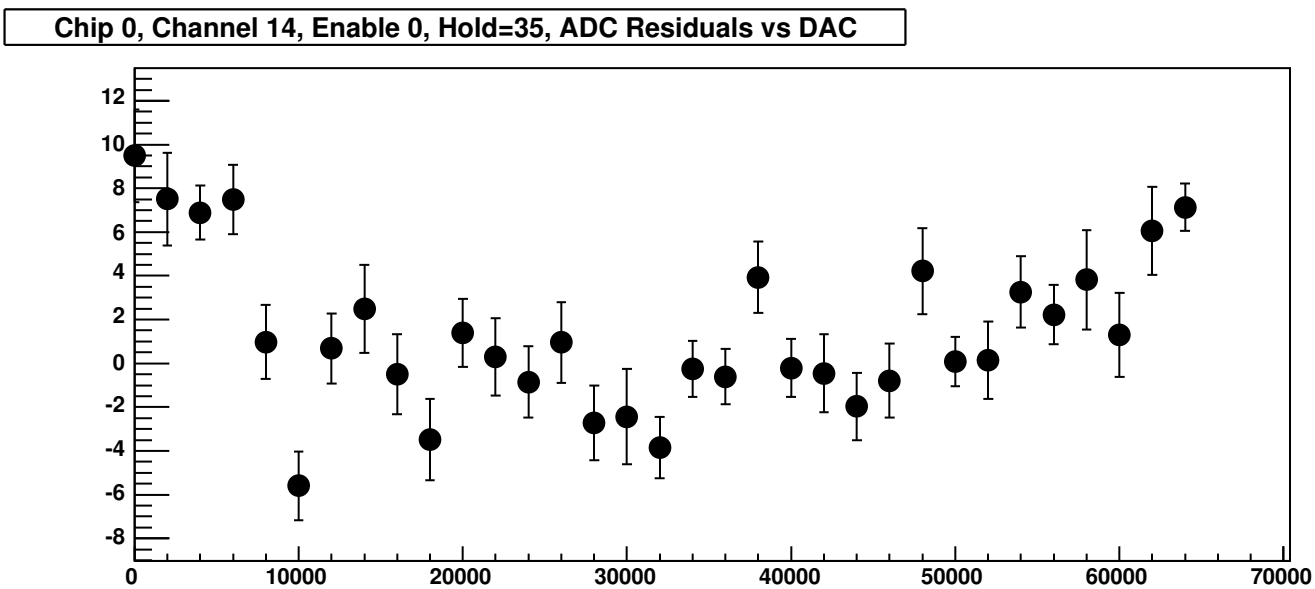


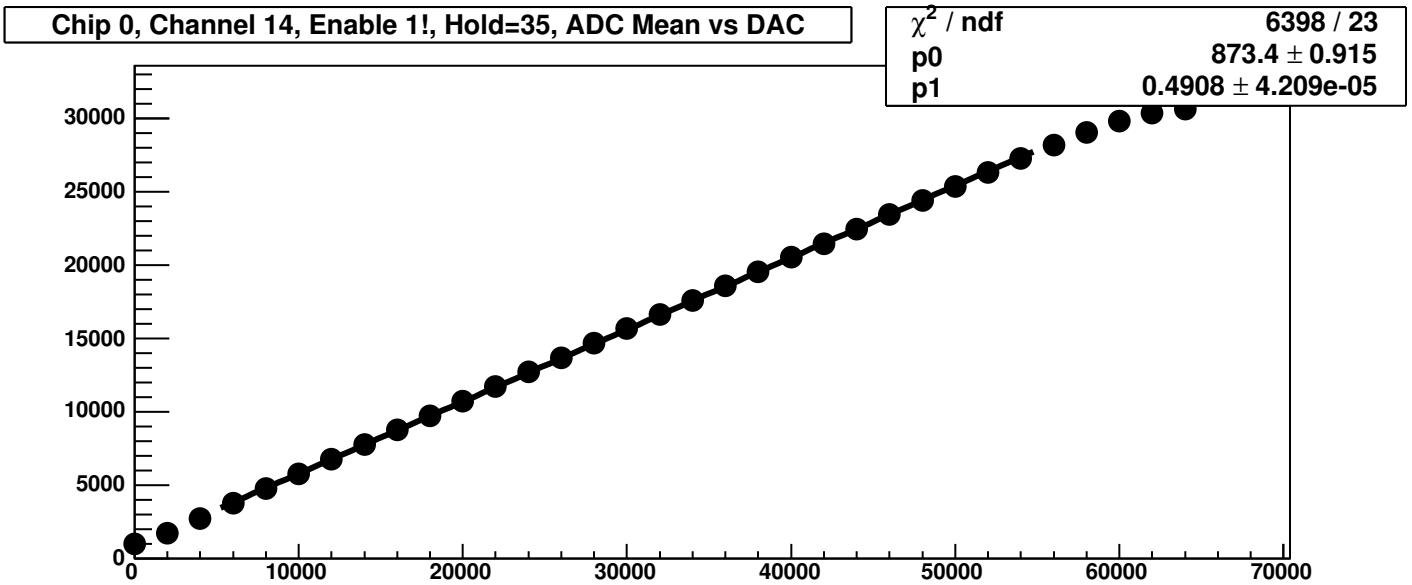
Chip 0, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC

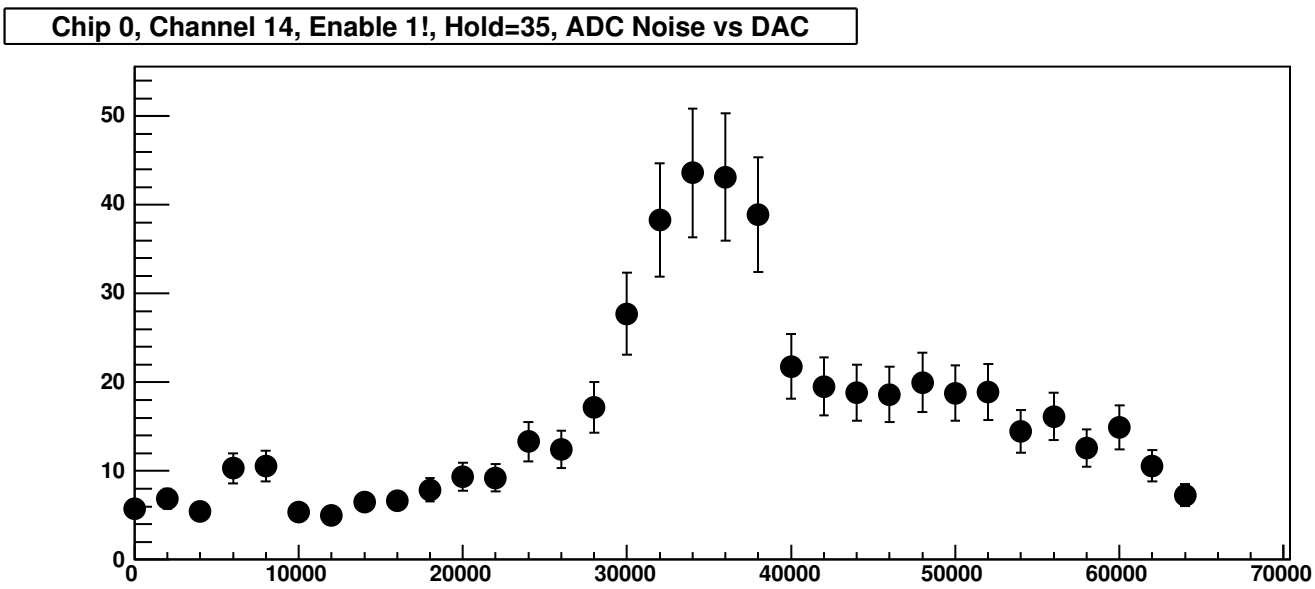


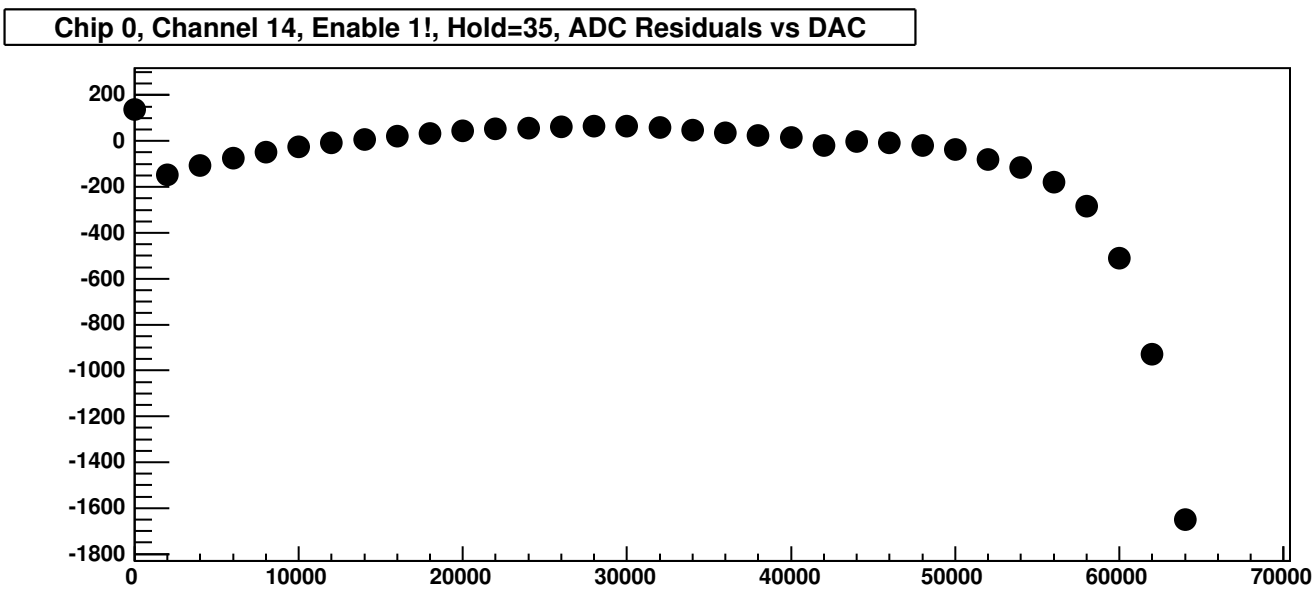

Chip 0, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 69.7 / 23
p0 669.1 ± 0.767
p1 0.00315 ± 2.228e-05
880
860
840
820
800
780
760
740
720
700
680
660
0
10000
20000
30000
40000
50000
60000
70000

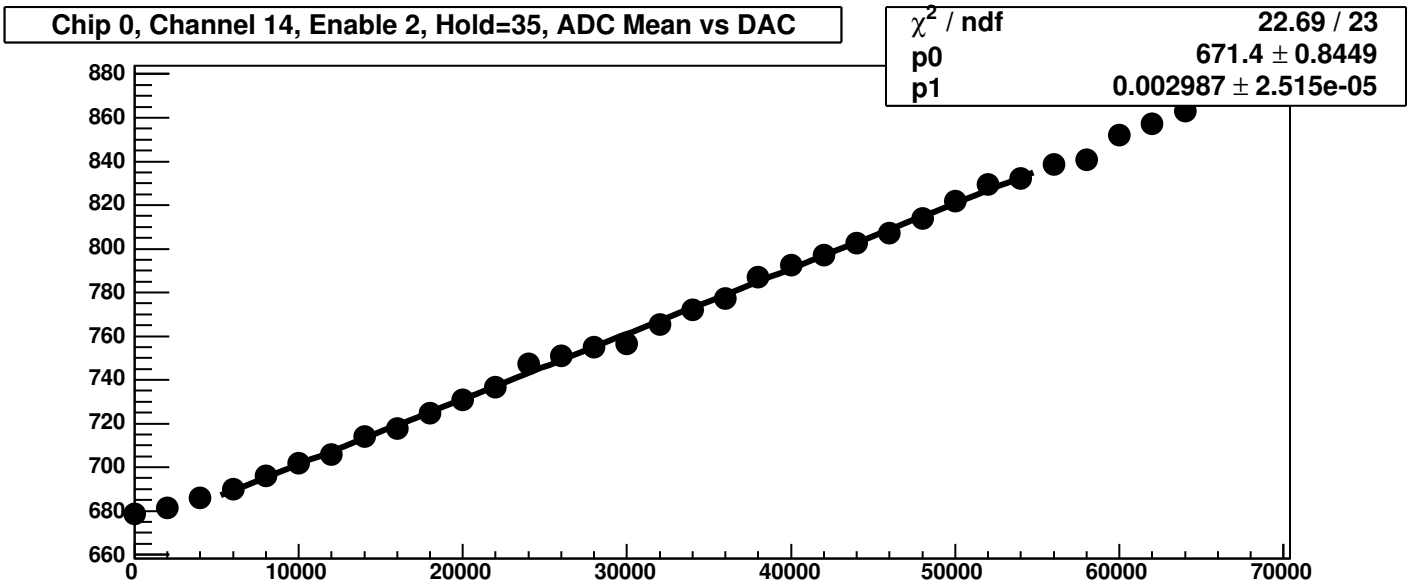

Chip 0, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

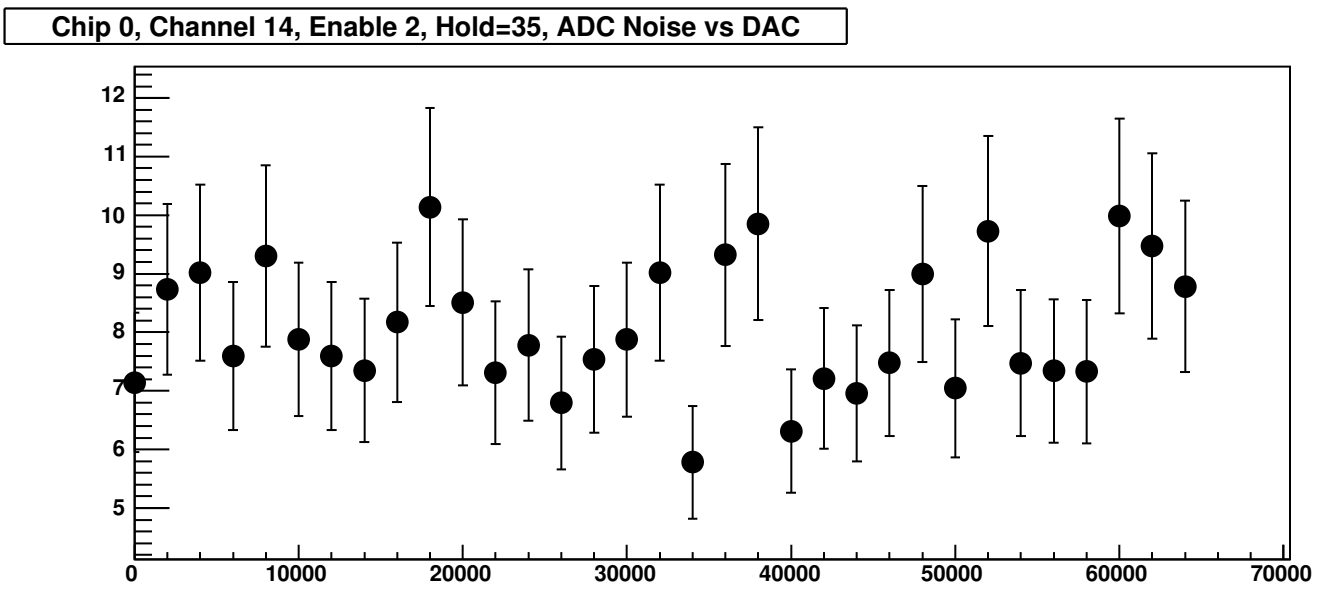

Chip 0, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

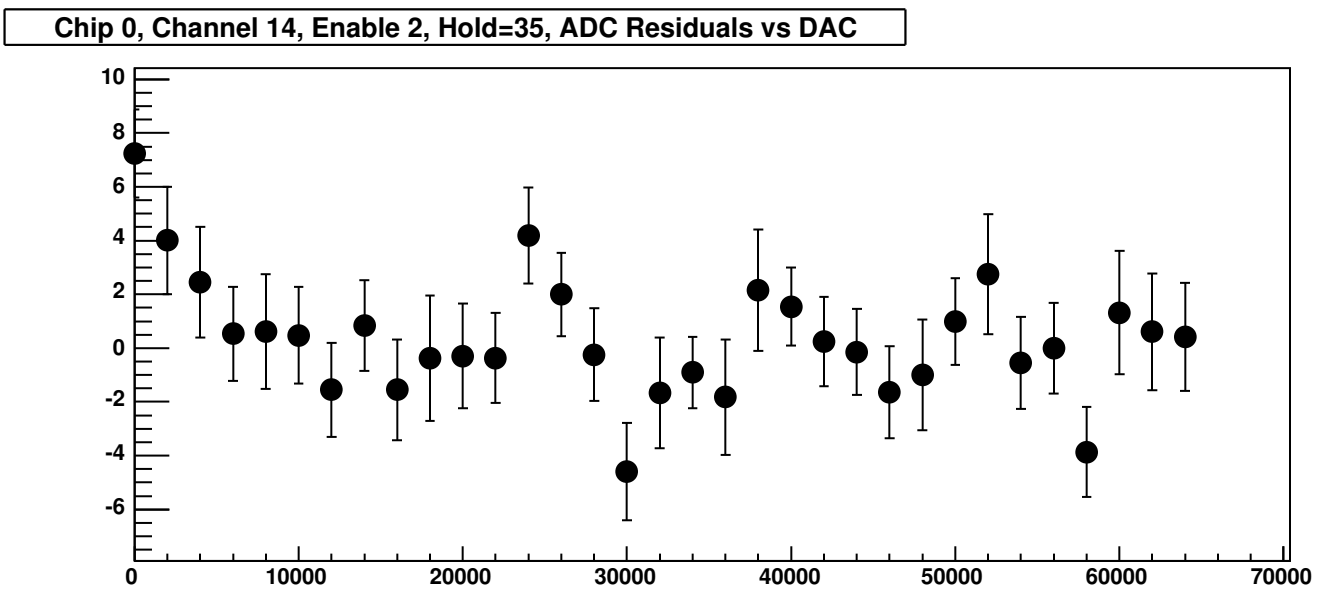

Chip 0, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 6398 / 23
p0 873.4 ± 0.915
p1 0.4908 ± 4.209e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

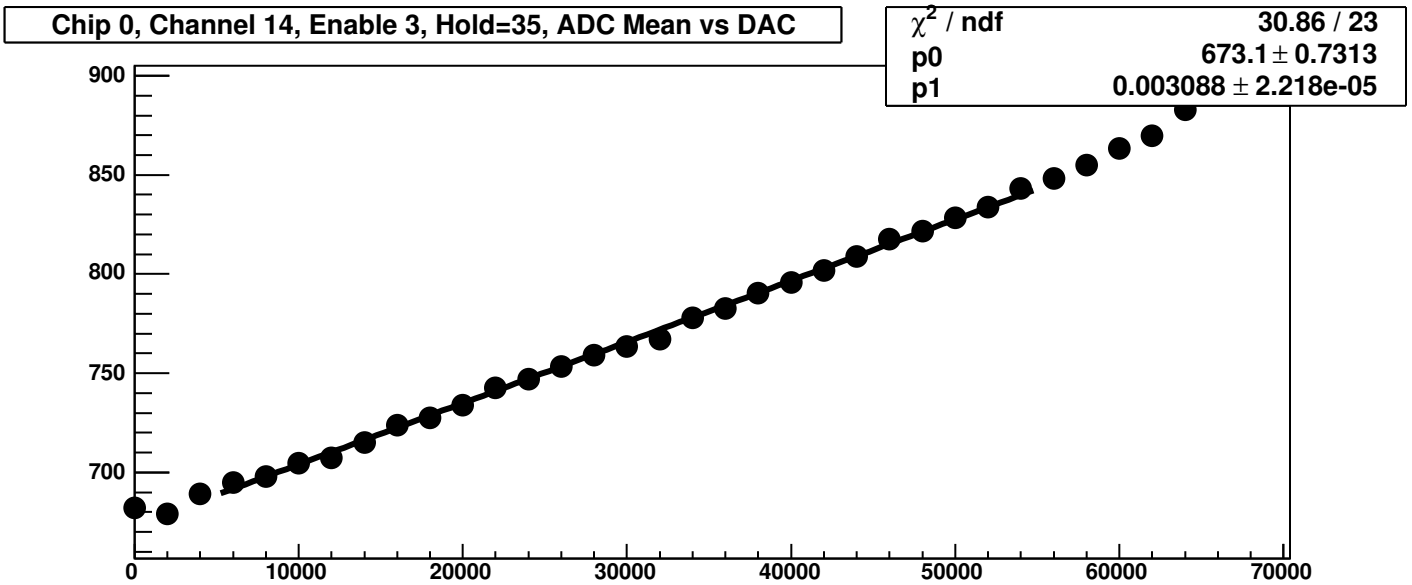

Chip 0, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

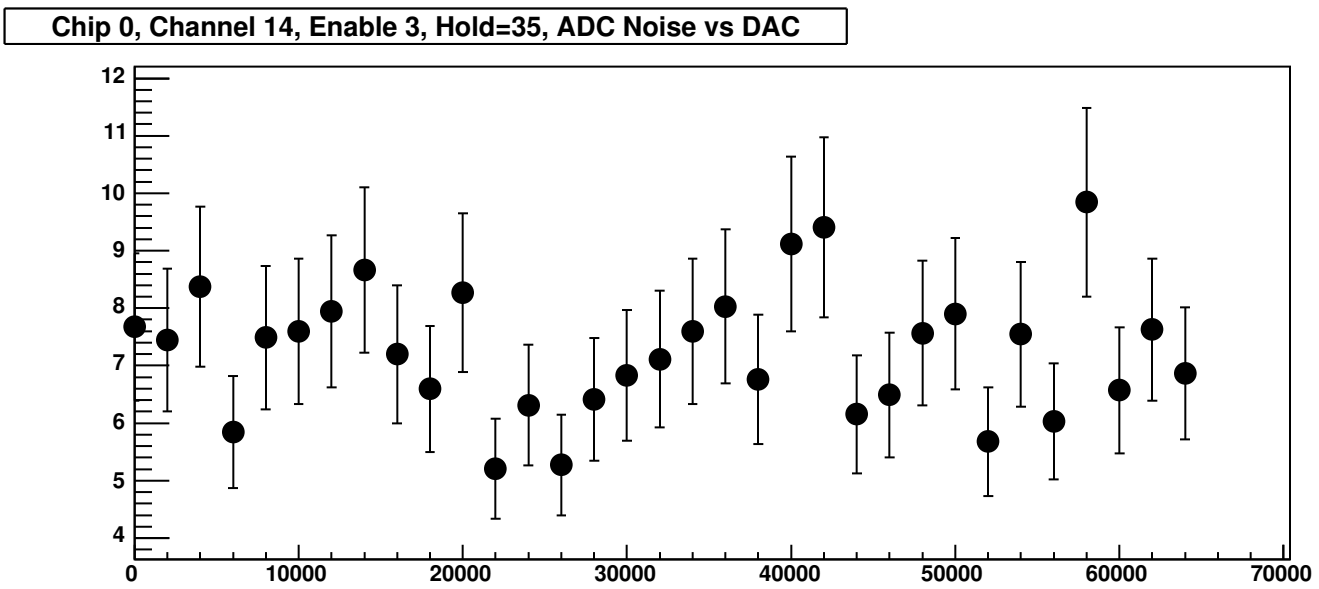

Chip 0, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
10000
20000
30000
40000
50000
60000
70000

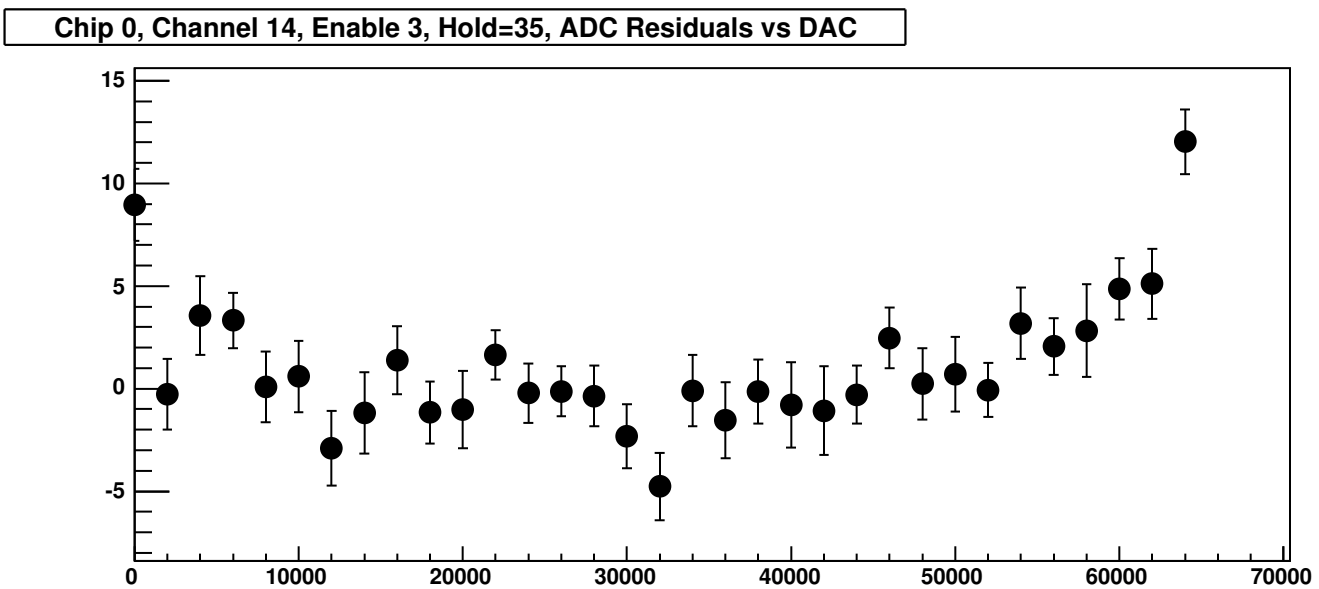

Chip 0, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 22.69 / 23
p0 671.4 ± 0.8449
p1 0.002987 ± 2.515e-05
880
860
840
820
800
780
760
740
720
700
680
660
0
10000
20000
30000
40000
50000
60000
70000

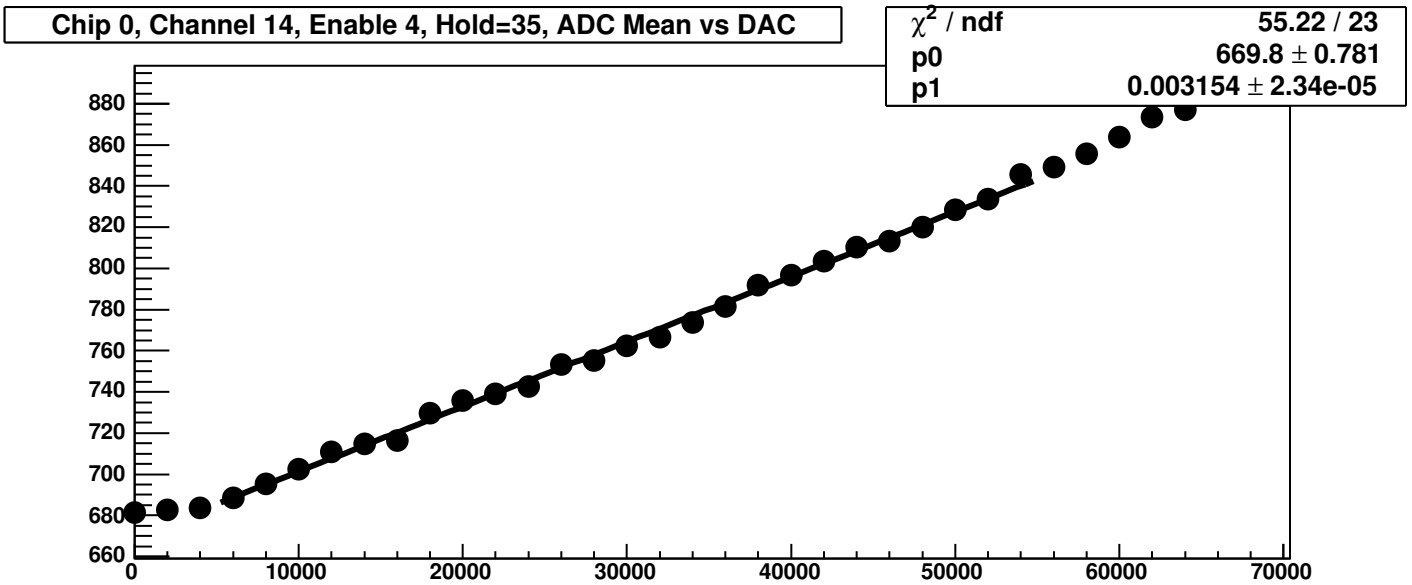

Chip 0, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000

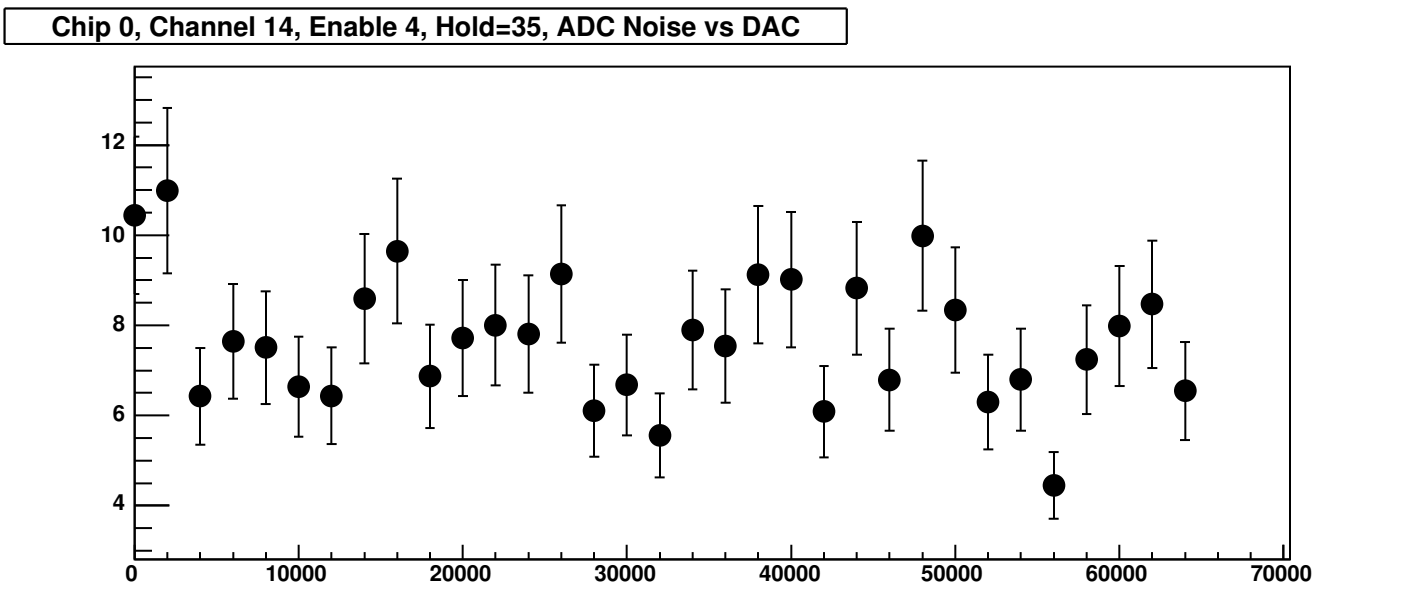

Chip 0, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

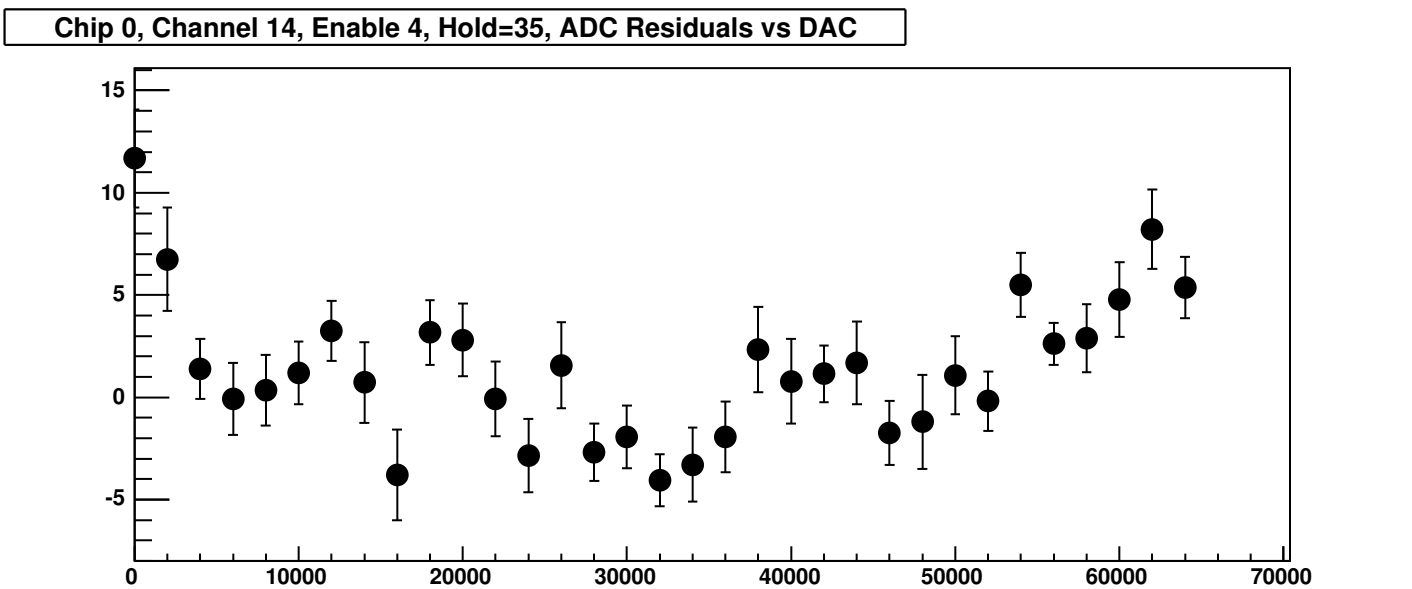

Chip 0, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 30.86 / 23
p0 673.1 ± 0.7313
p1 0.003088 ± 2.218e-05
900
850
800
750
700
0
10000
20000
30000
40000
50000
60000
70000

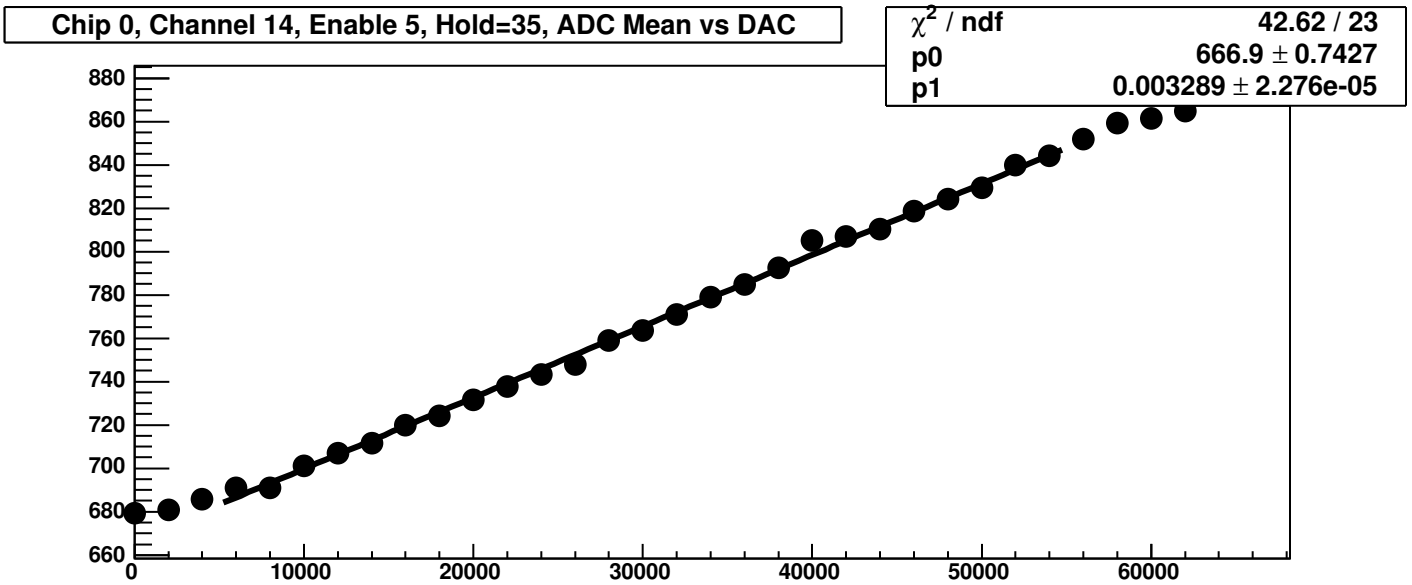

Chip 0, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

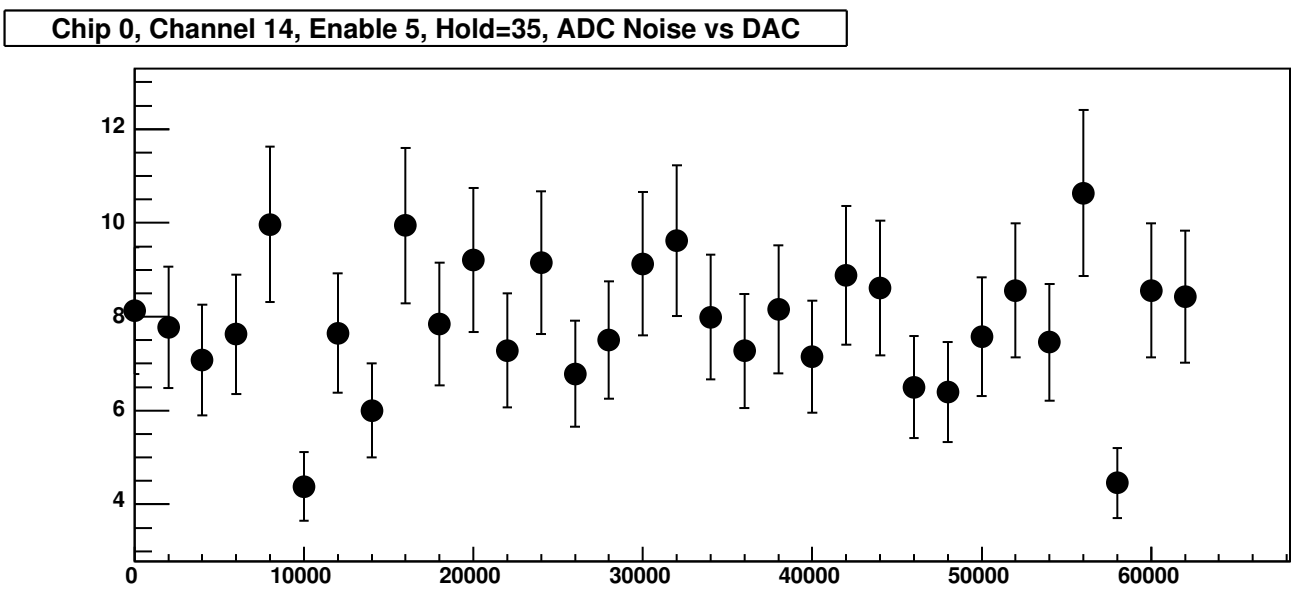

Chip 0, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

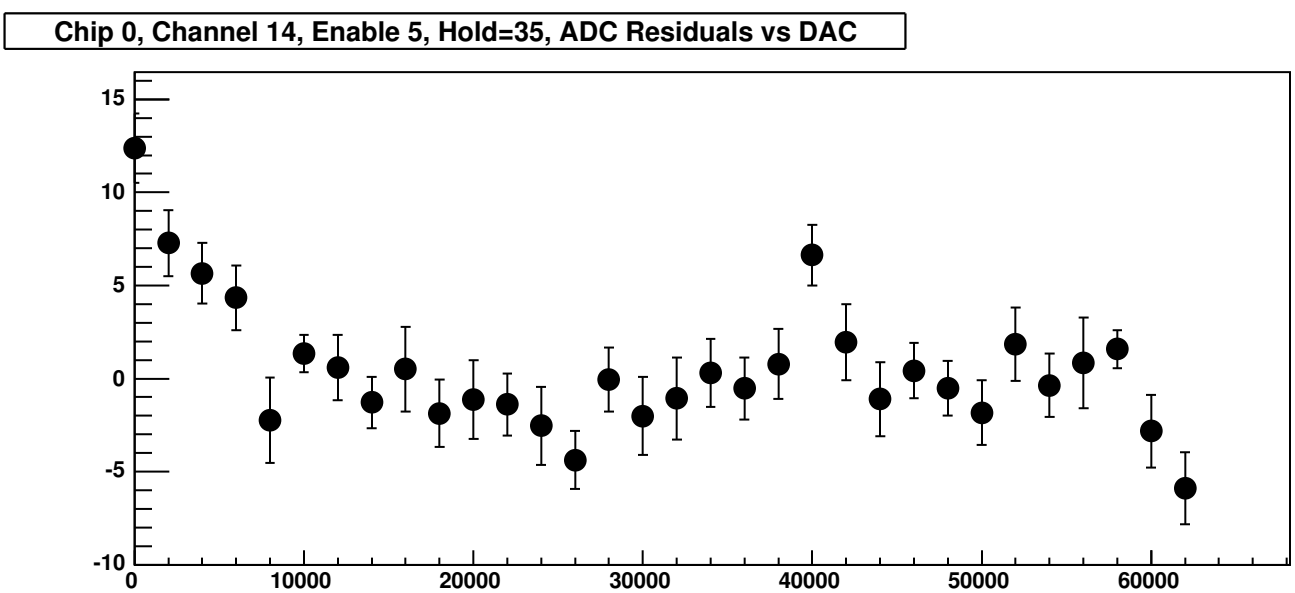

Chip 0, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 55.22 / 23
p0 669.8 ± 0.781
p1 0.003154 ± 2.34e-05
880
860
840
820
800
780
760
740
720
700
680
660
0
10000
20000
30000
40000
50000
60000
70000

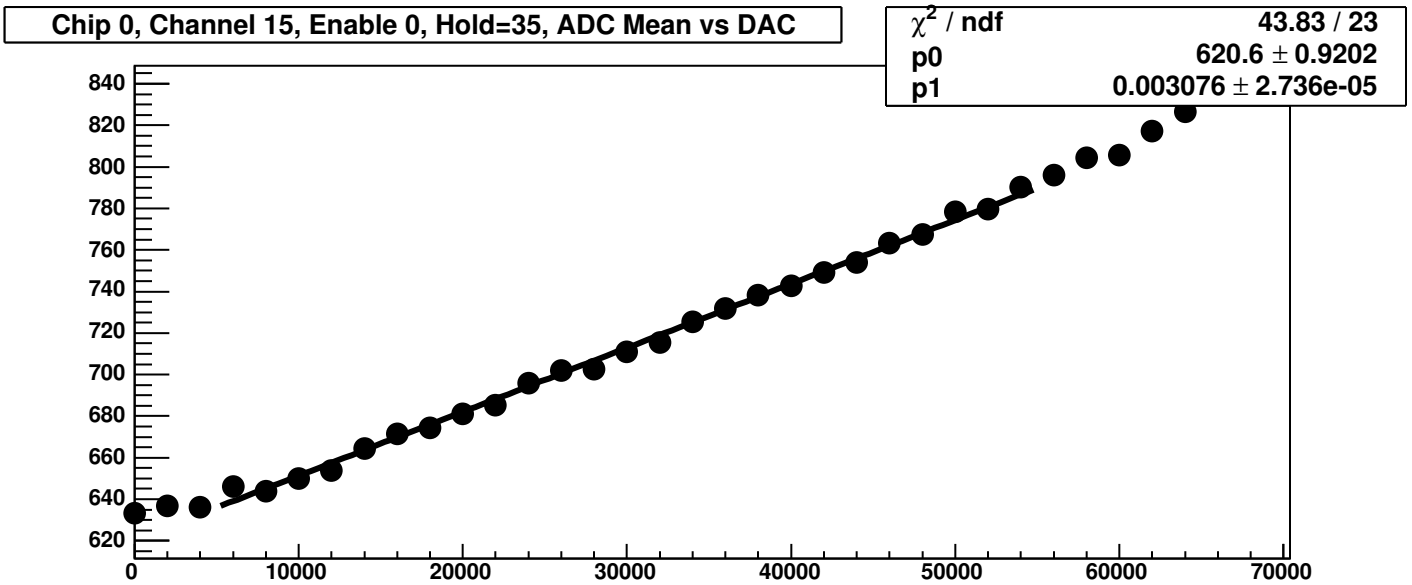

Chip 0, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

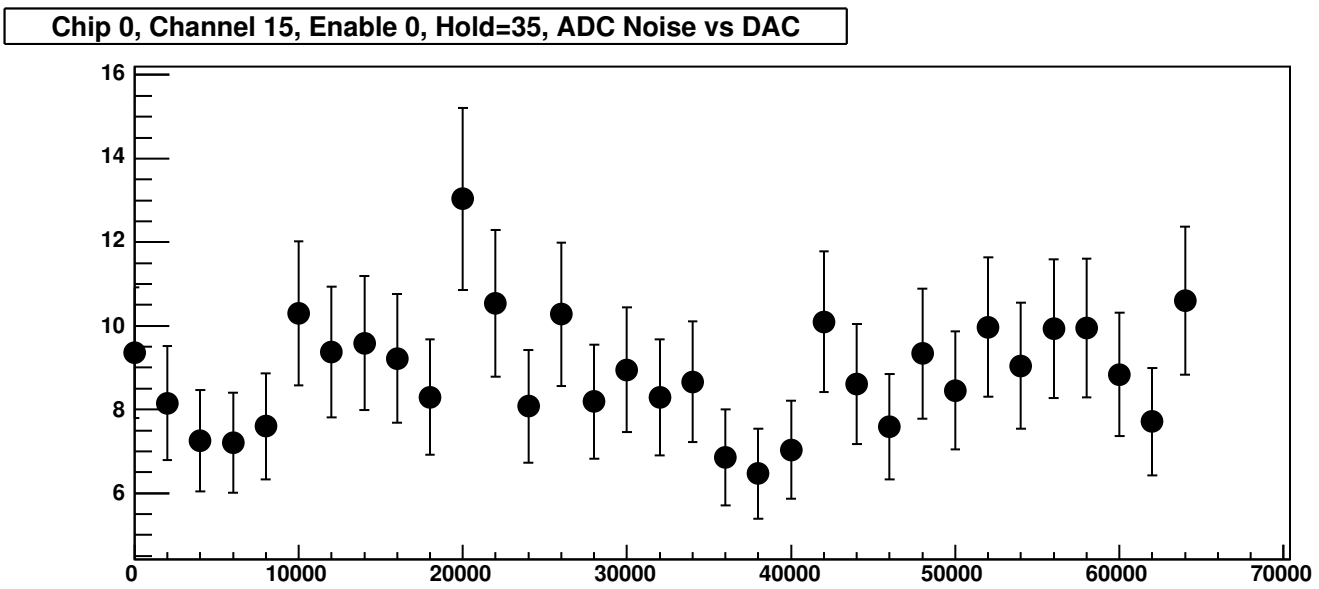

Chip 0, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

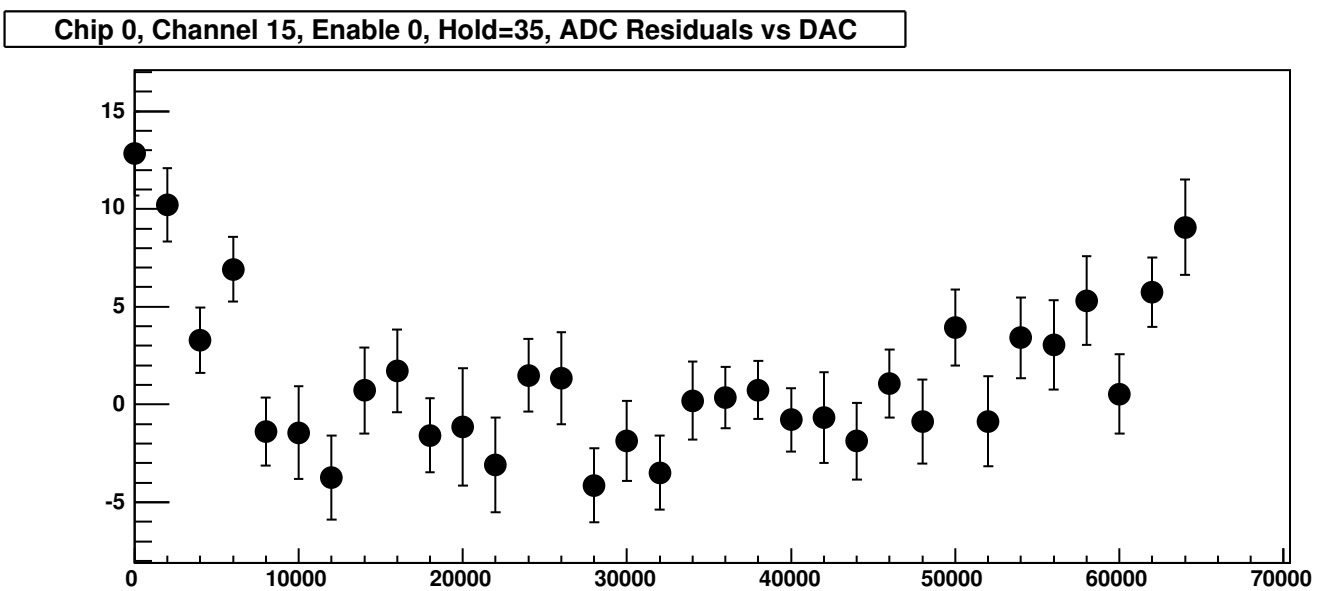

Chip 0, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 42.62 / 23
p0 666.9 ± 0.7427
p1 0.003289 ± 2.276e-05
880
860
840
820
800
780
760
740
720
700
680
660
0
10000
20000
30000
40000
50000
60000

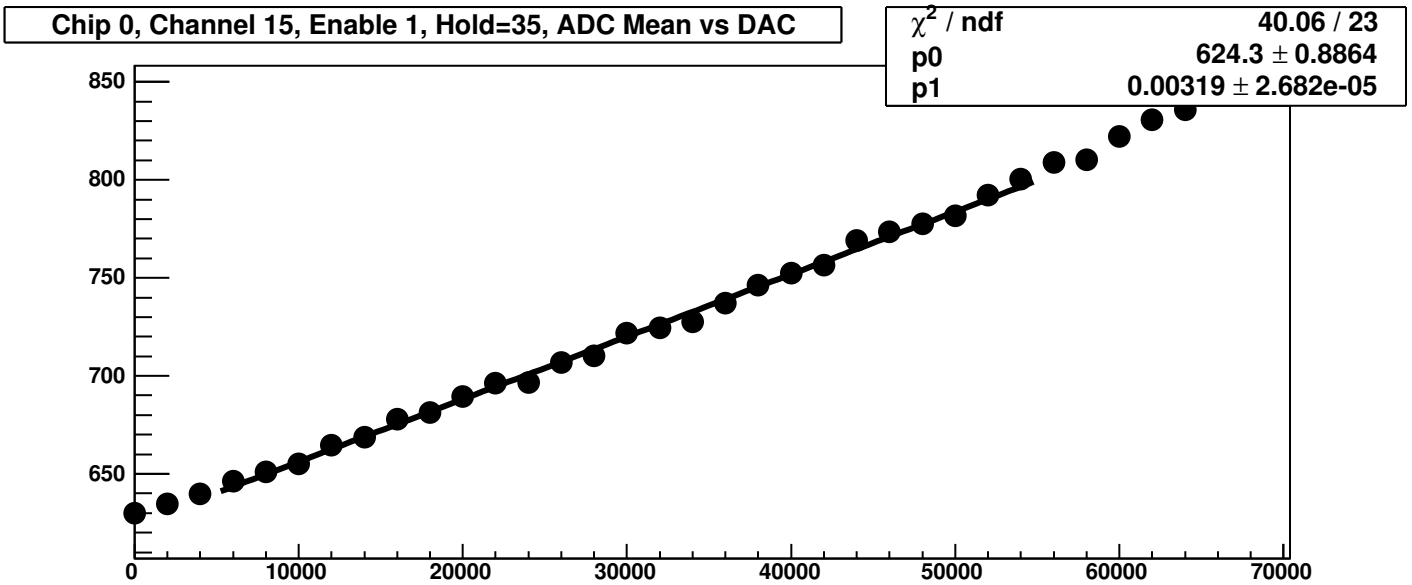

Chip 0, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000

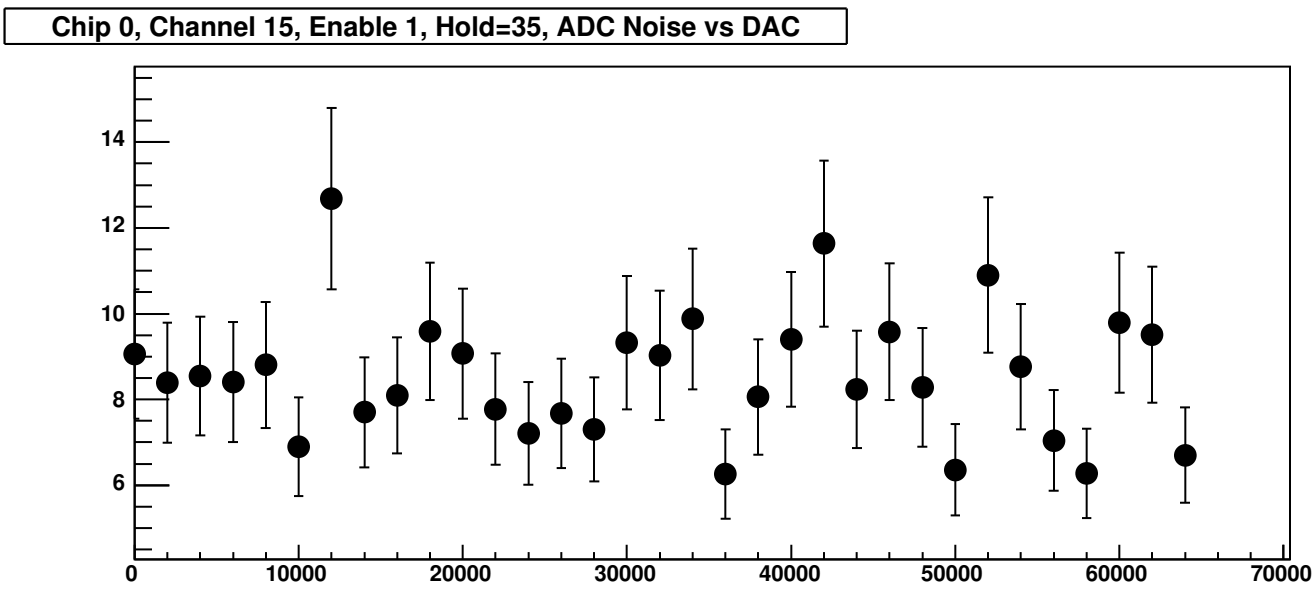

Chip 0, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000

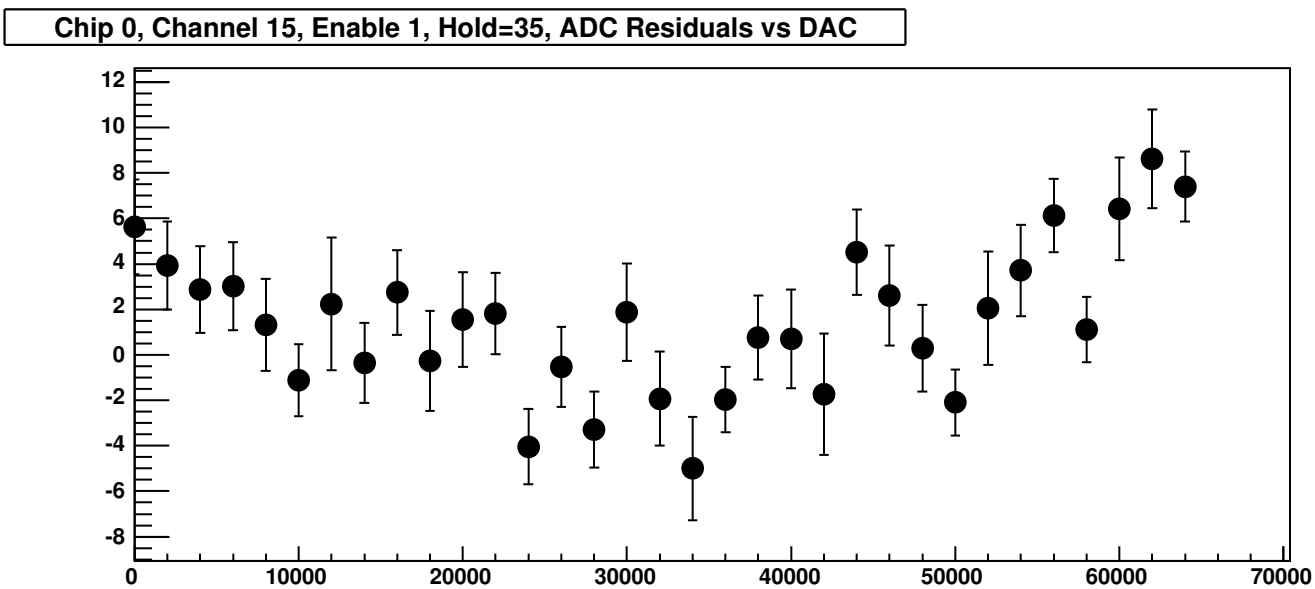

Chip 0, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 43.83 / 23
p0 620.6 ± 0.9202
p1 0.003076 ± 2.736e-05
840
820
800
780
760
740
720
700
680
660
640
620
0
10000
20000
30000
40000
50000
60000
70000

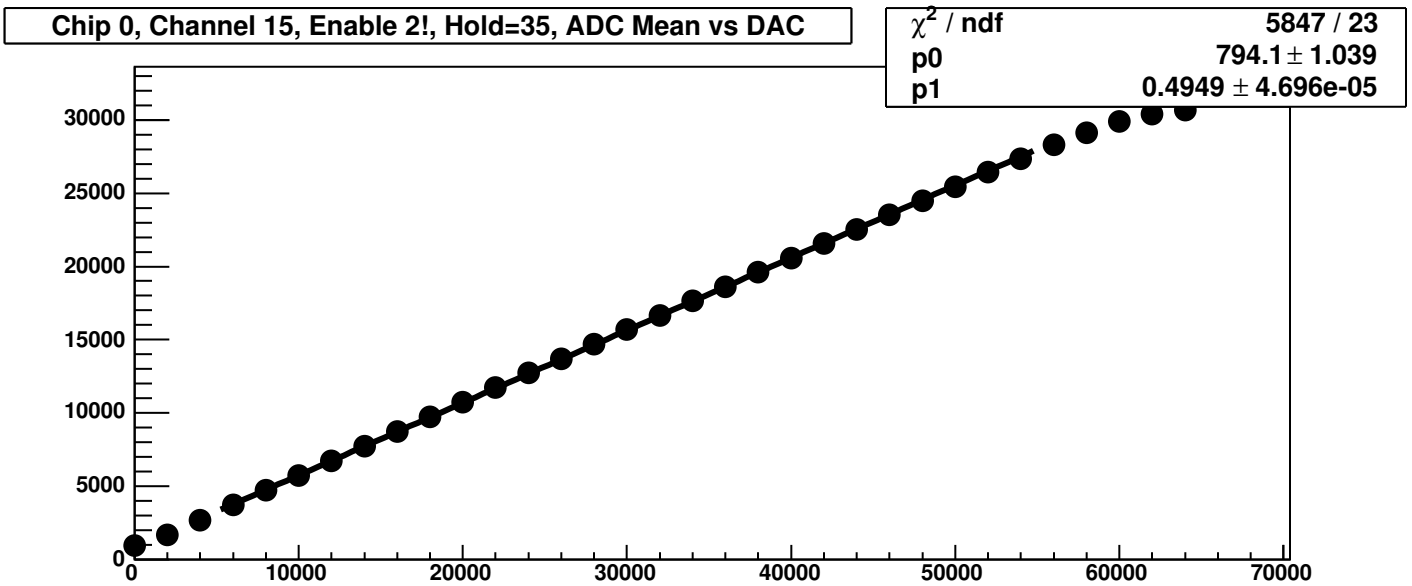

Chip 0, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
0
10000
20000
30000
40000
50000
60000
70000

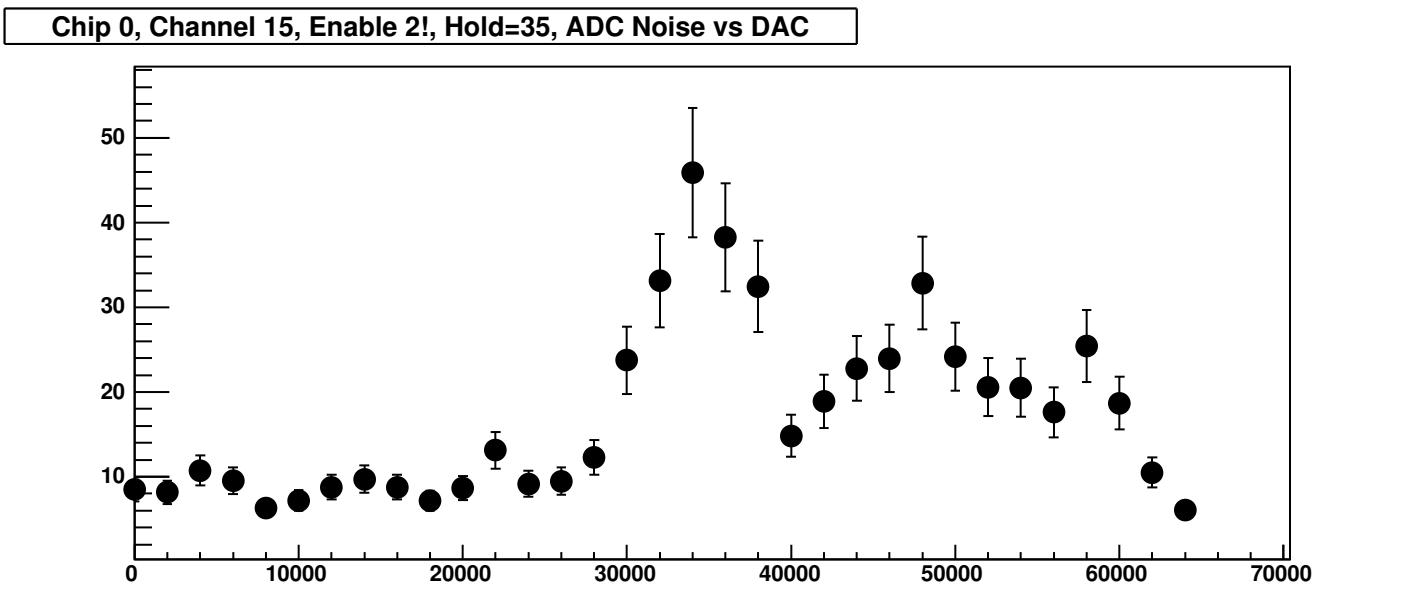

Chip 0, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

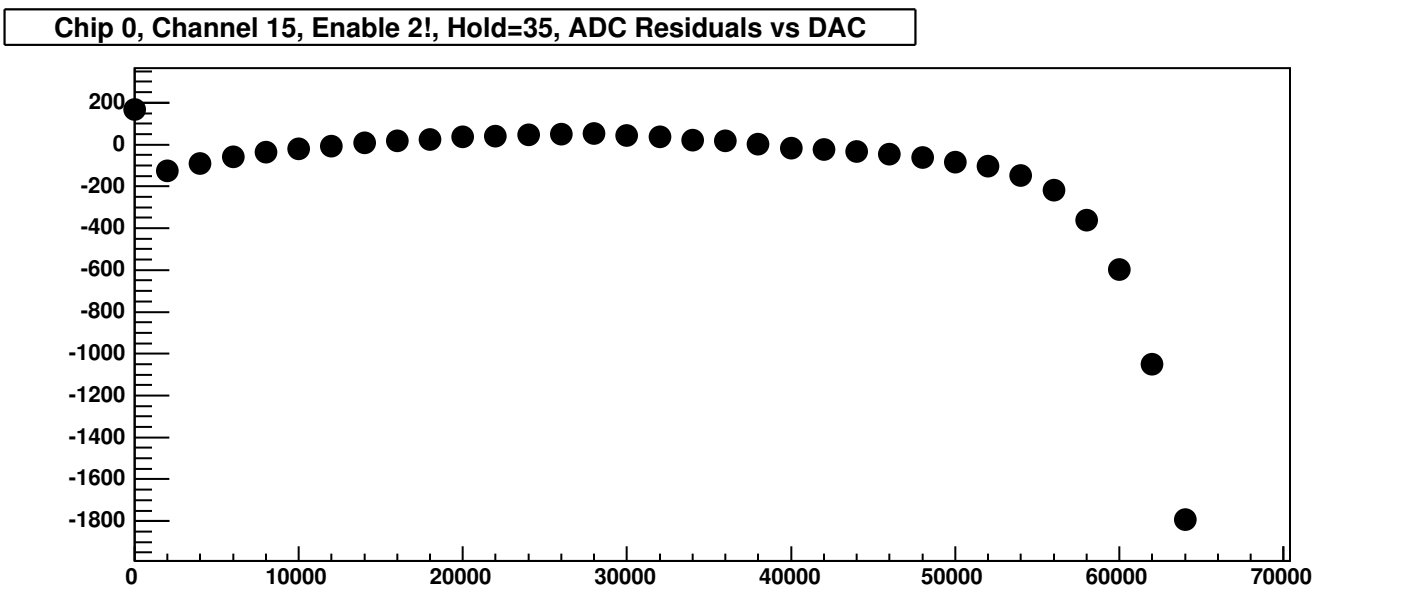

Chip 0, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 40.06 / 23
p0 624.3 ± 0.8864
p1 0.00319 ± 2.682e-05
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000
70000

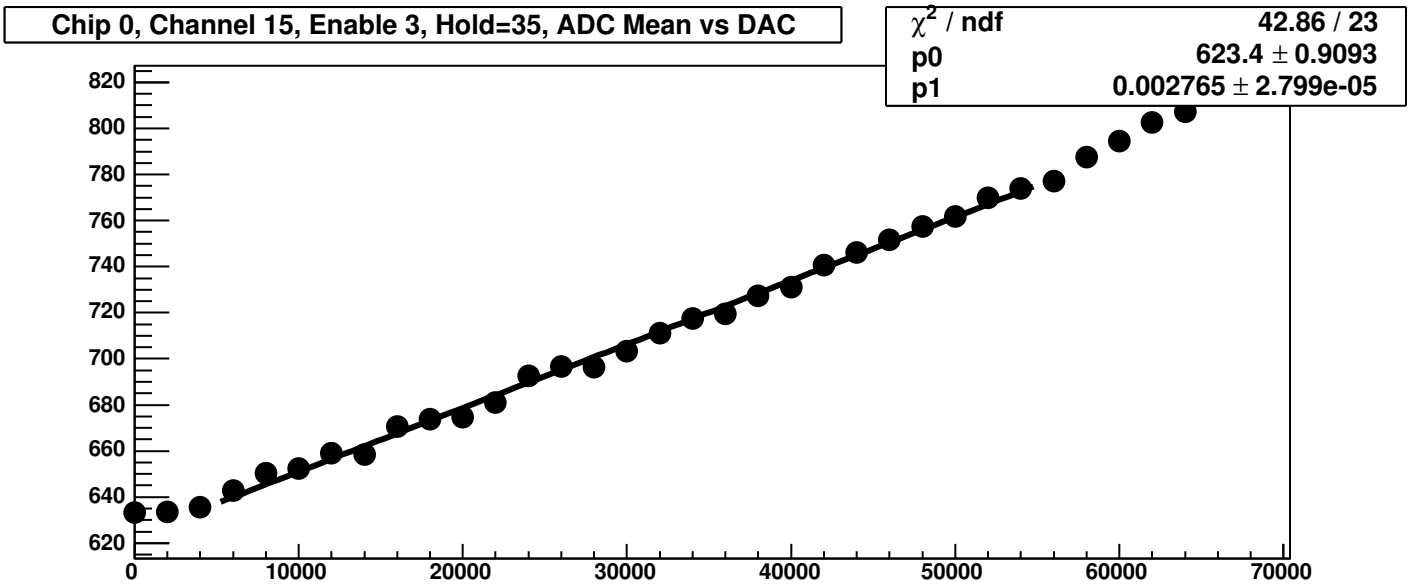

Chip 0, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
10000
20000
30000
40000
50000
60000
70000

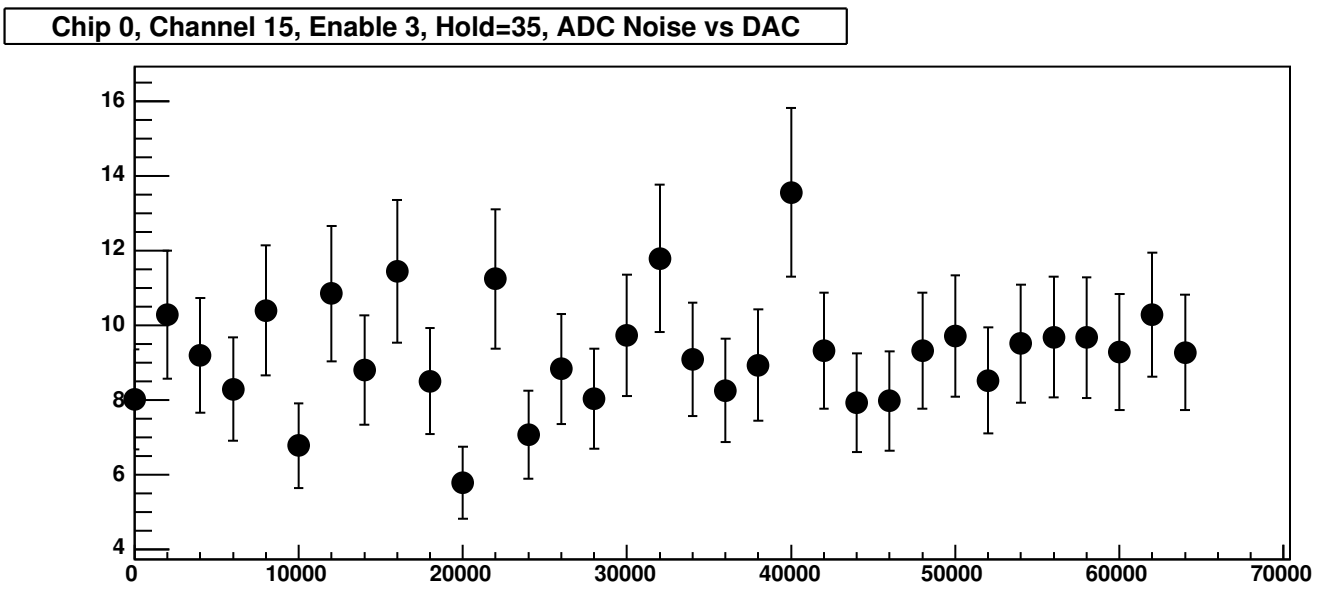

Chip 0, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

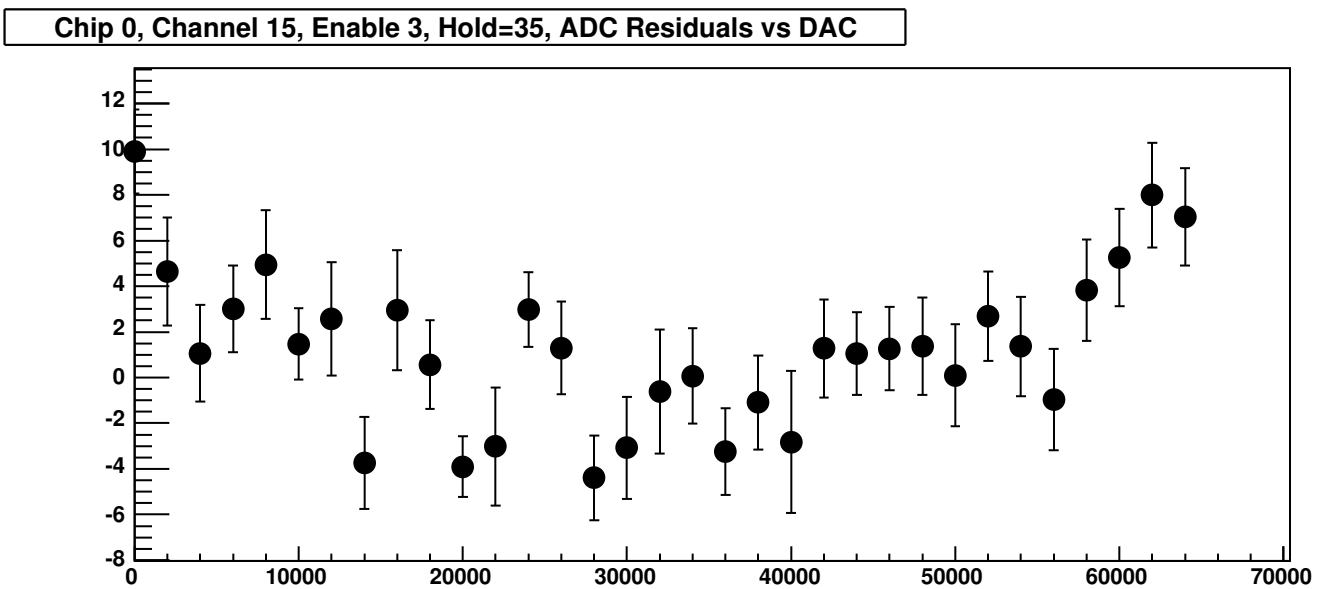

Chip 0, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 5847 / 23
p0 794.1 ± 1.039
p1 0.4949 ± 4.696e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

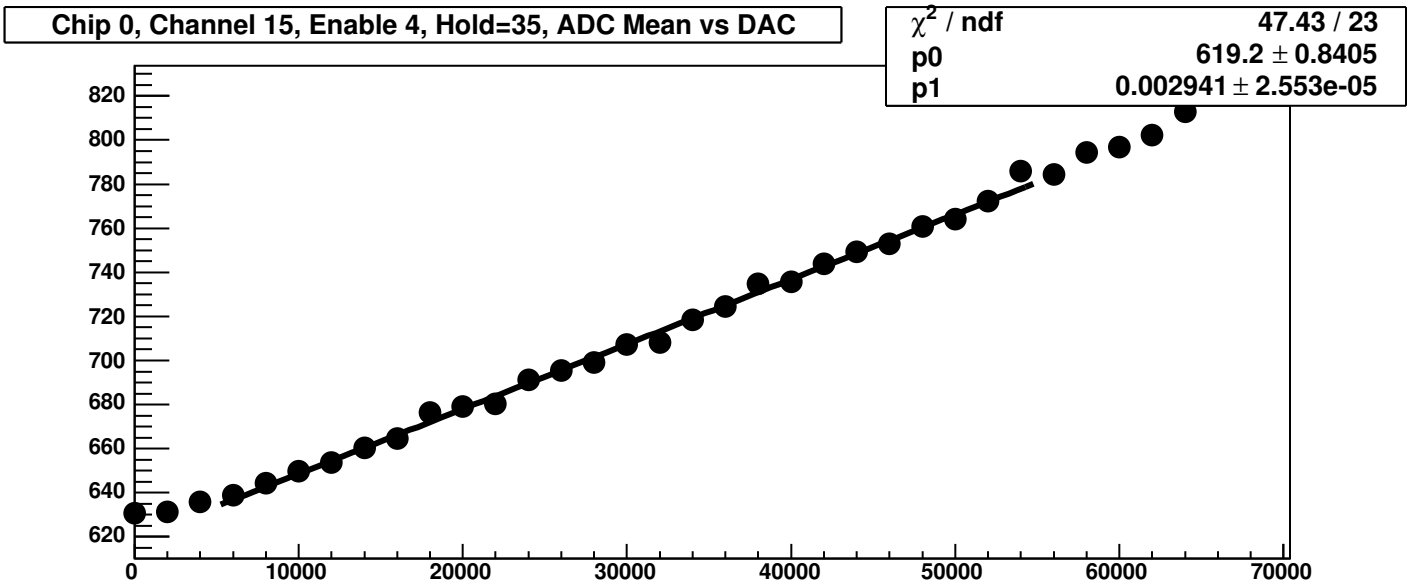

Chip 0, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000

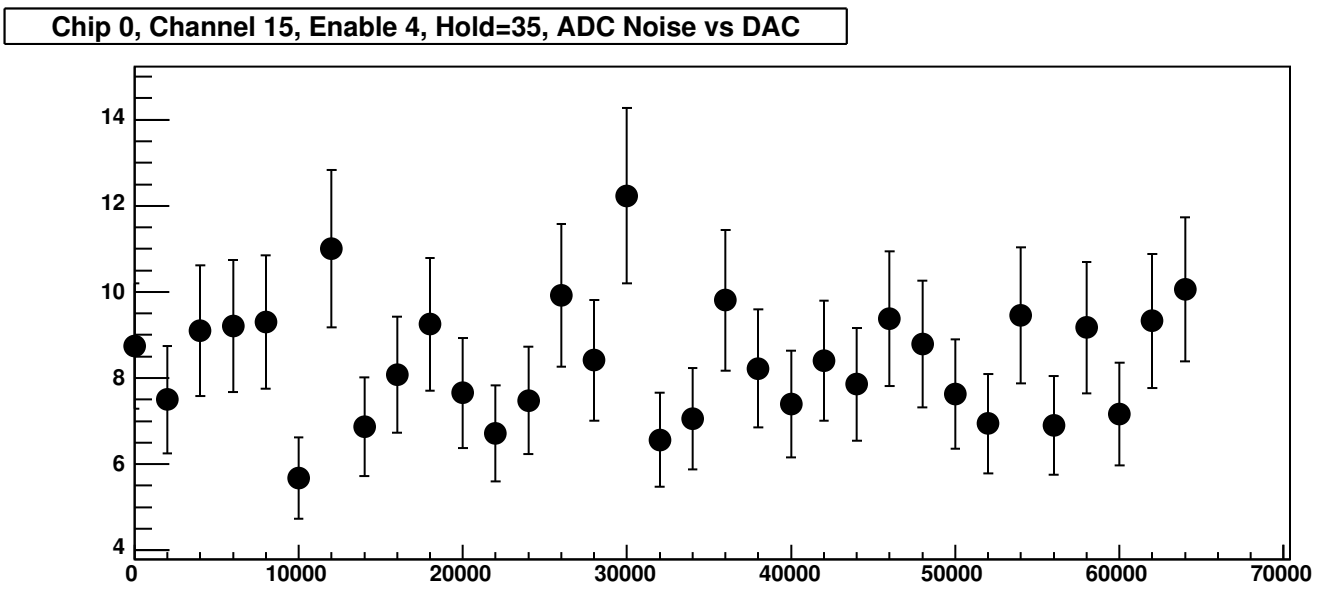

Chip 0, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
10000
20000
30000
40000
50000
60000
70000

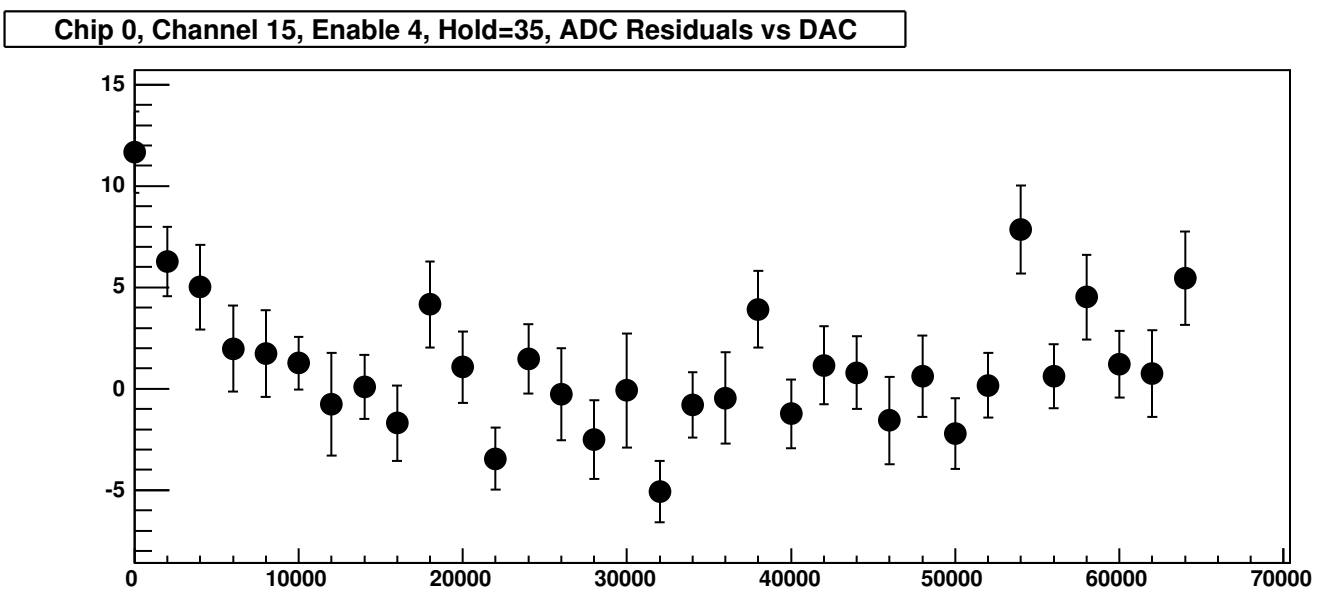

Chip 0, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 42.86 / 23
p0 623.4 ± 0.9093
p1 0.002765 ± 2.799e-05
820
800
780
760
740
720
700
680
660
640
620
0
10000
20000
30000
40000
50000
60000
70000

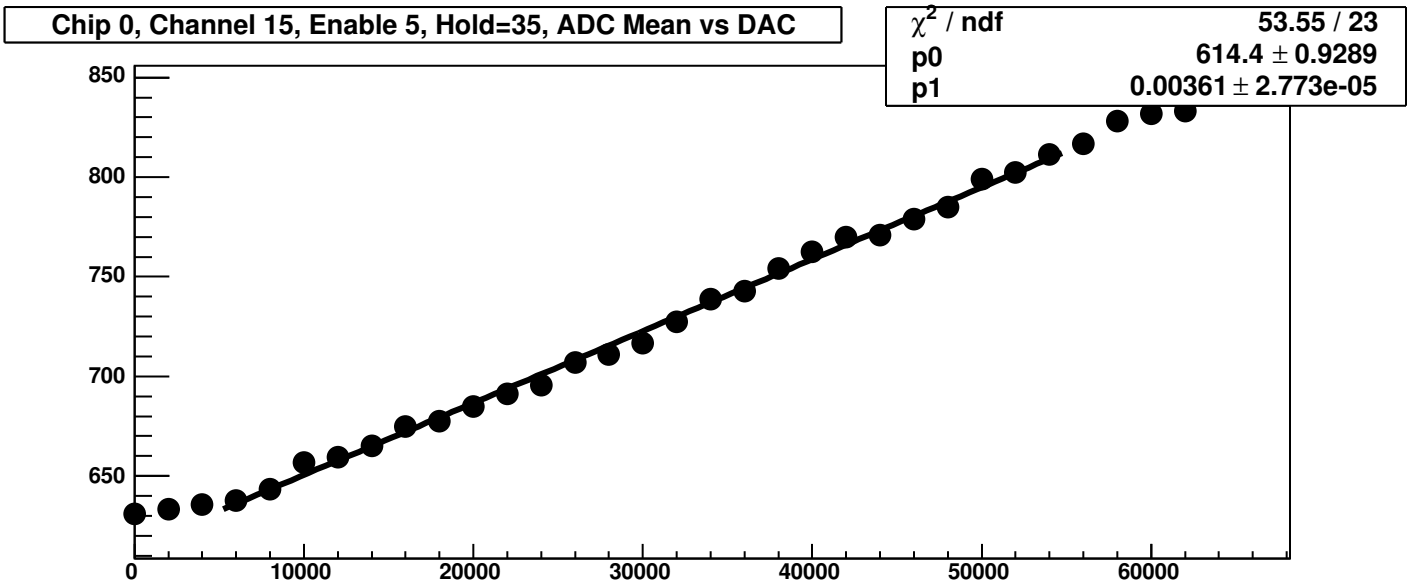

Chip 0, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

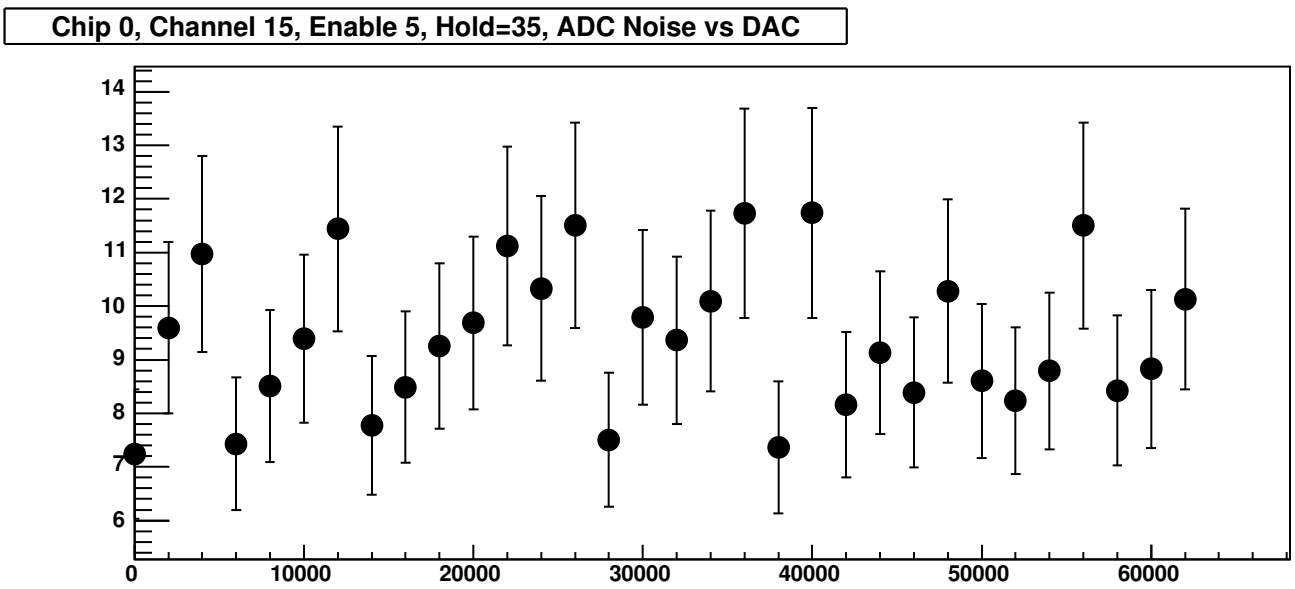

Chip 0, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

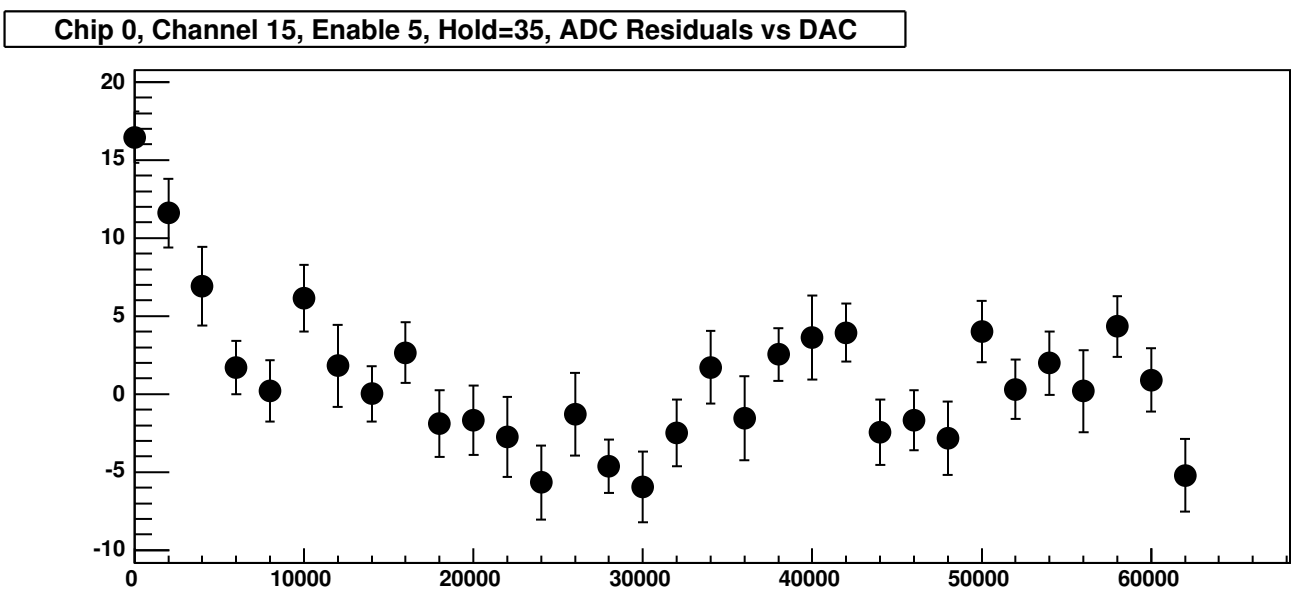

Chip 0, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 47.43 / 23
p0 619.2 ± 0.8405
p1 0.002941 ± 2.553e-05
820
800
780
760
740
720
700
680
660
640
620
0
10000
20000
30000
40000
50000
60000
70000

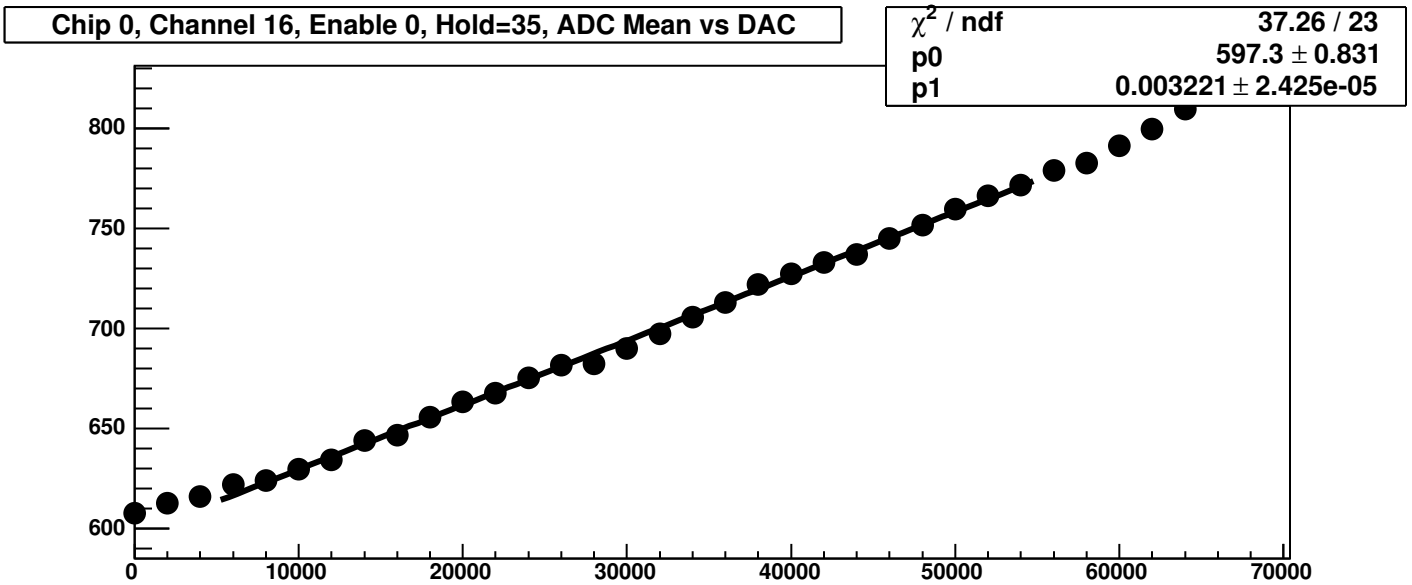

Chip 0, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

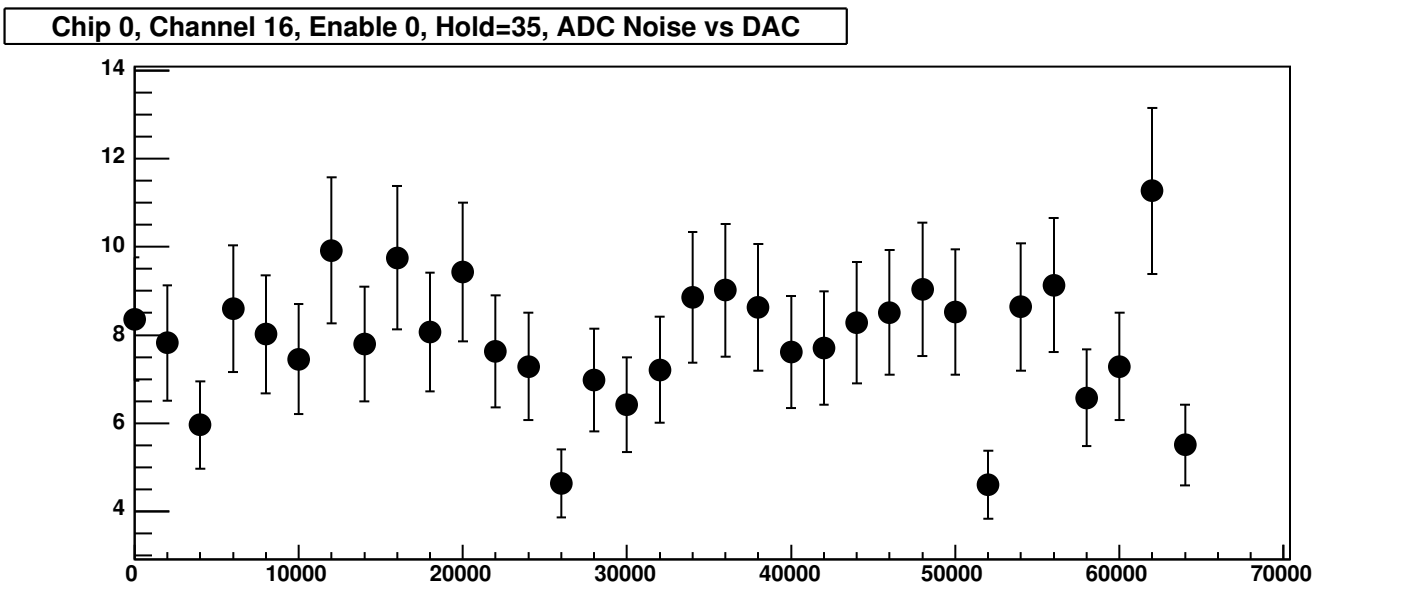

Chip 0, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

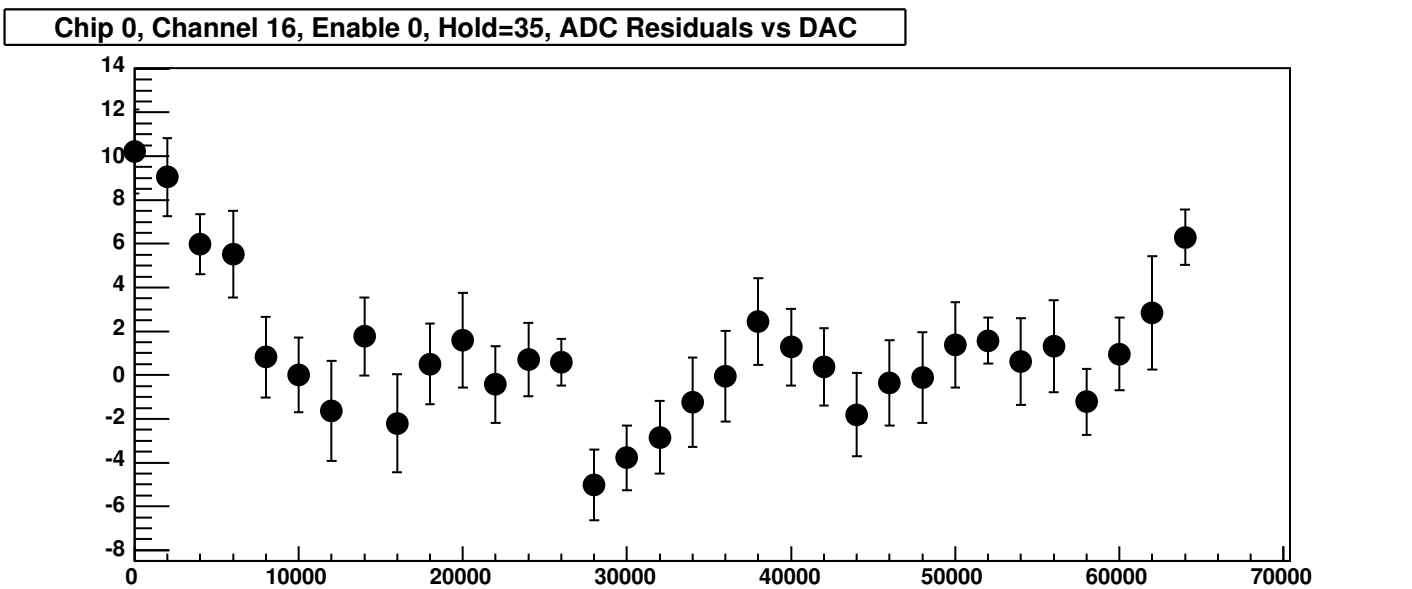

Chip 0, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 53.55 / 23
p0 614.4 ± 0.9289
p1 0.00361 ± 2.773e-05
850
800
750
700
650
0
10000
20000
30000
40000
50000
60000

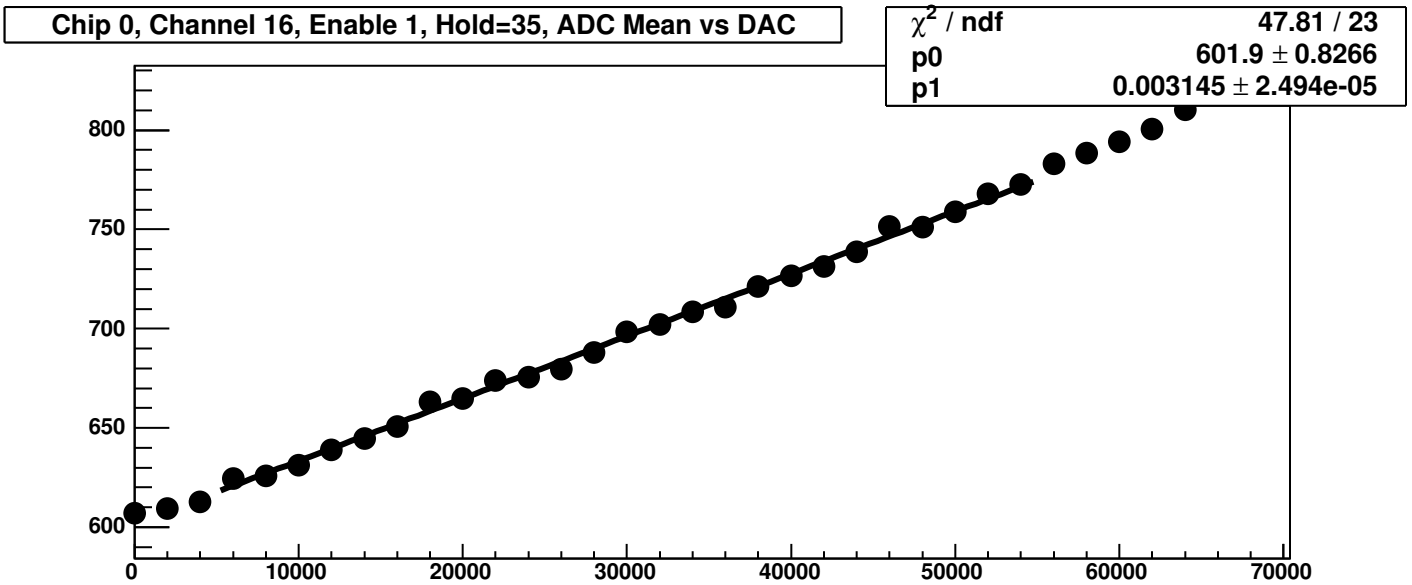

Chip 0, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC
14
13
12
11
10
9
8
7
6
0
10000
20000
30000
40000
50000
60000

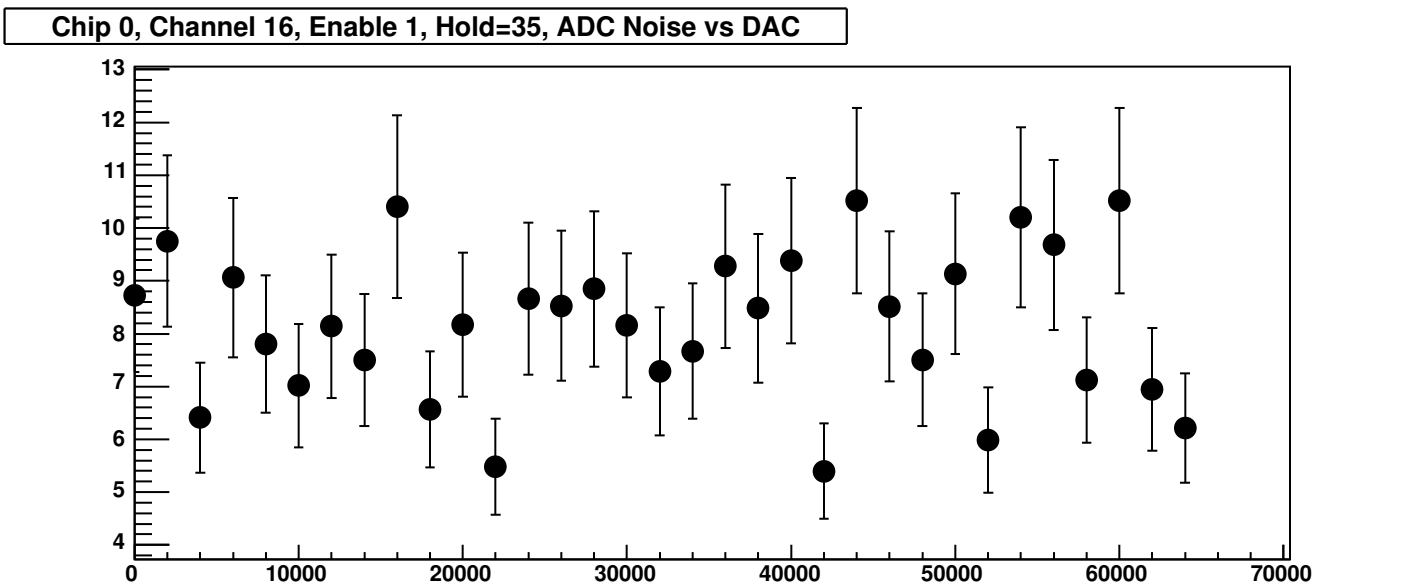

Chip 0, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC
20
15
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000

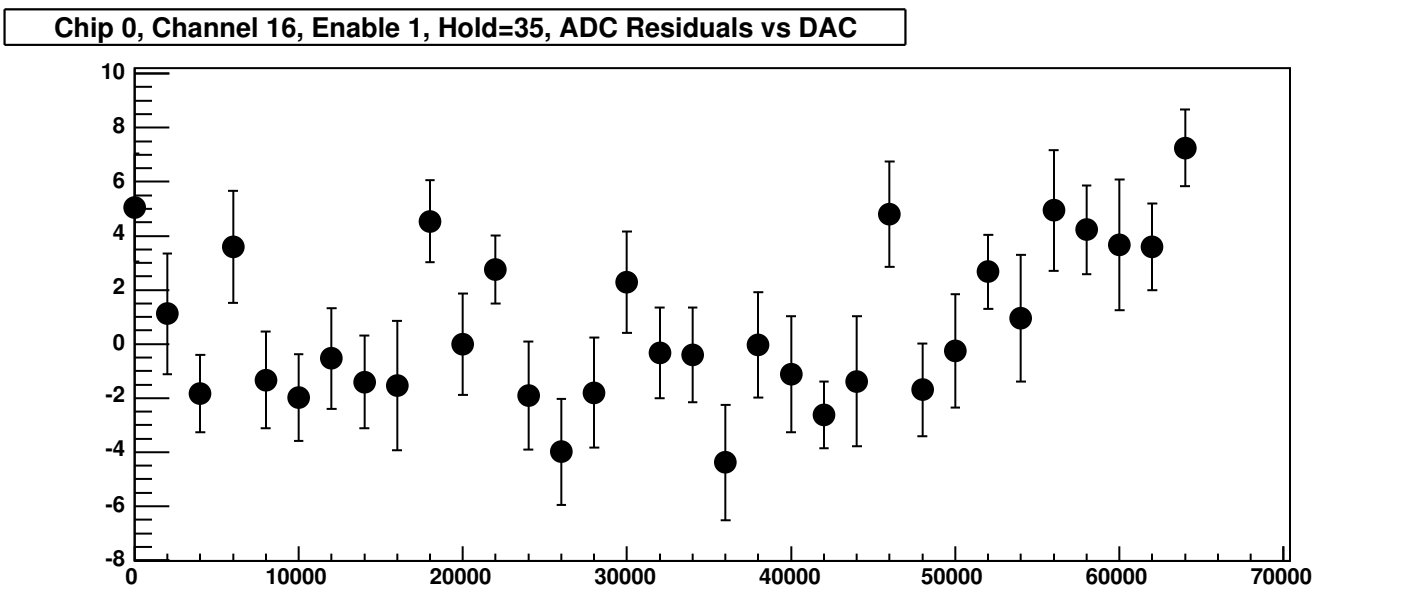

Chip 0, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 37.26 / 23
p0 597.3 ± 0.831
p1 0.003221 ± 2.425e-05
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000
70000

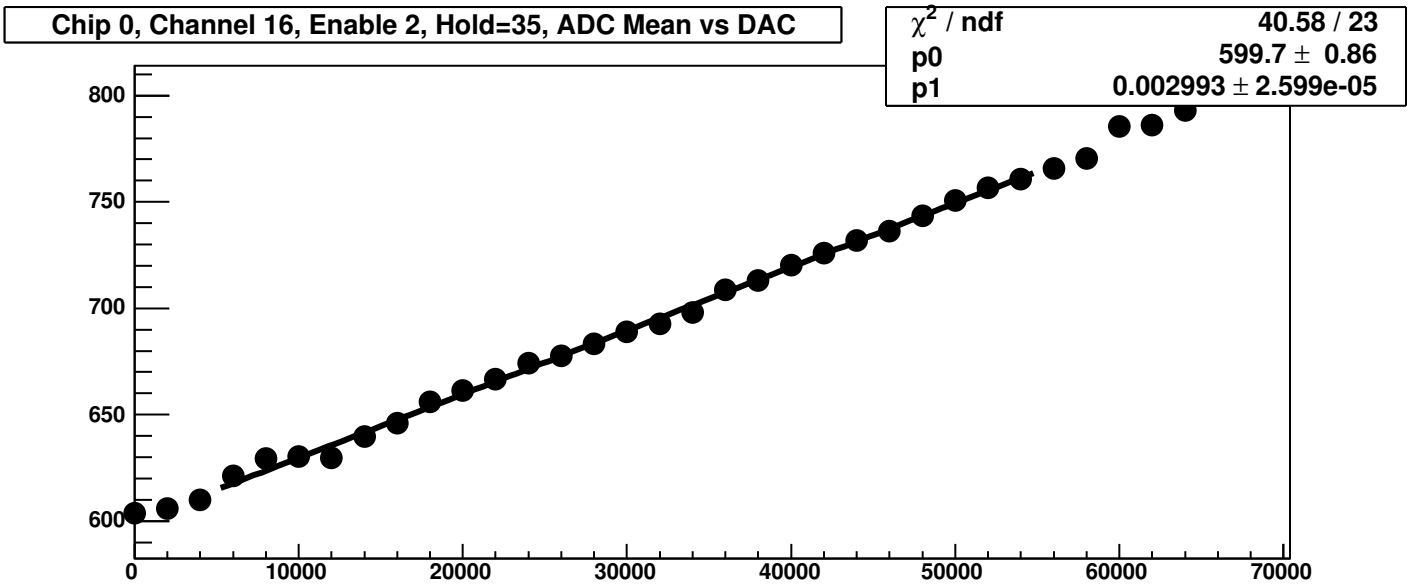

Chip 0, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

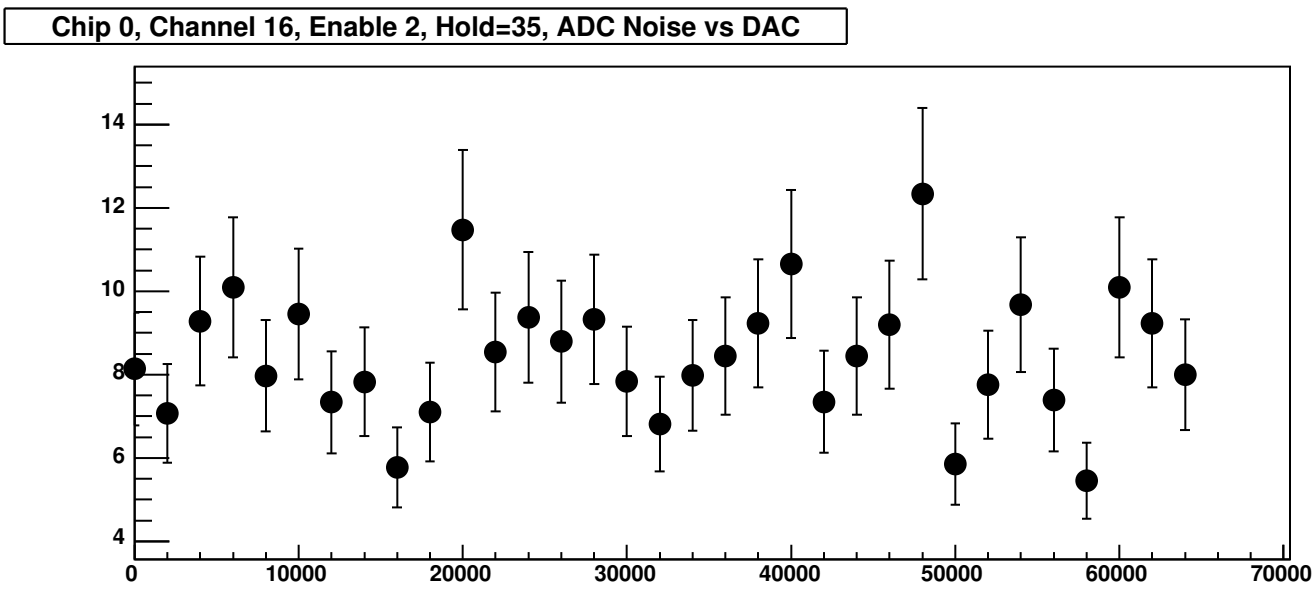

Chip 0, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

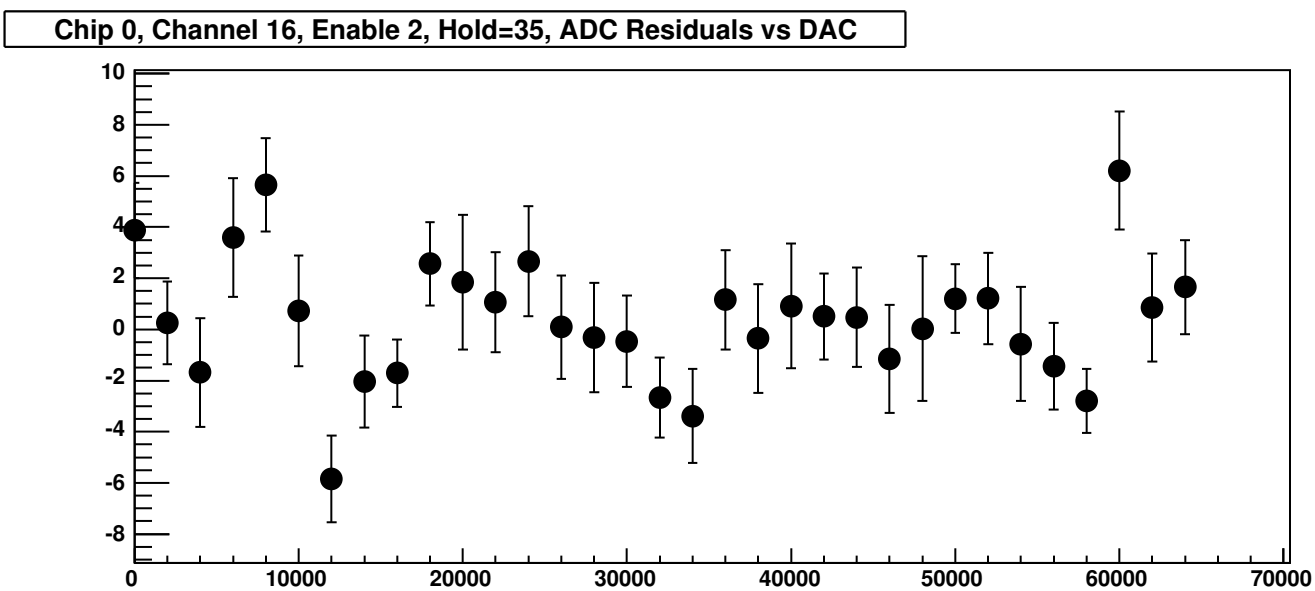

Chip 0, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 47.81 / 23
p0 601.9 ± 0.8266
p1 0.003145 ± 2.494e-05
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000
70000

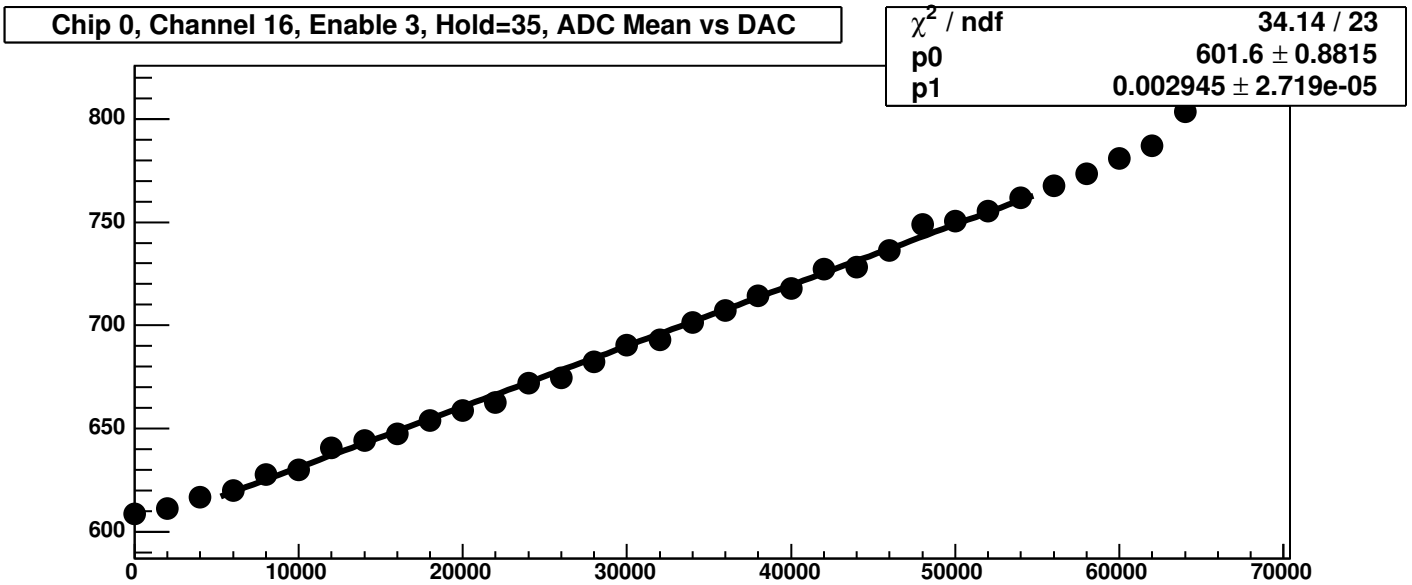

Chip 0, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

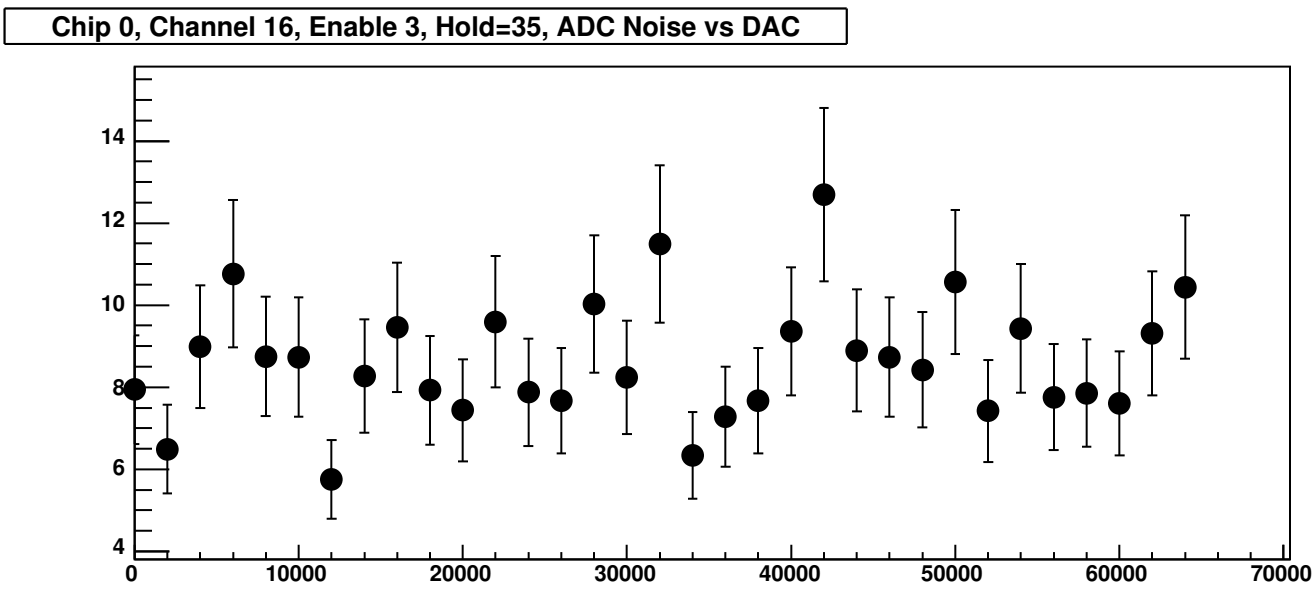

Chip 0, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

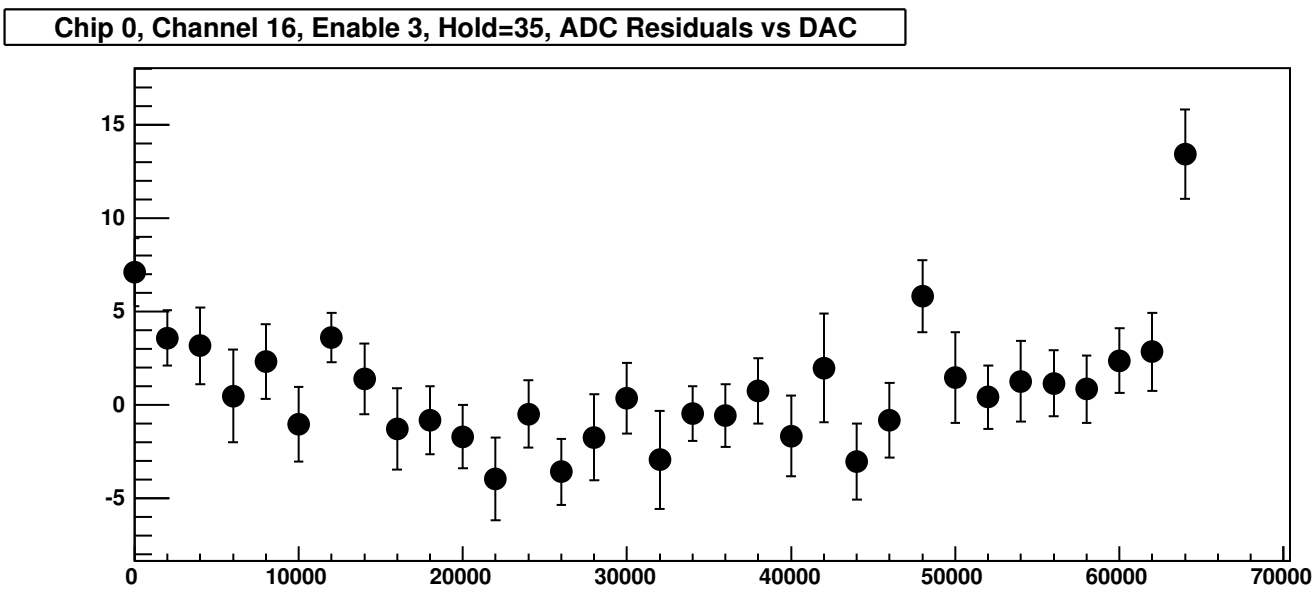

Chip 0, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 40.58 / 23
p0 599.7 ± 0.86
p1 0.002993 ± 2.599e-05
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000
70000

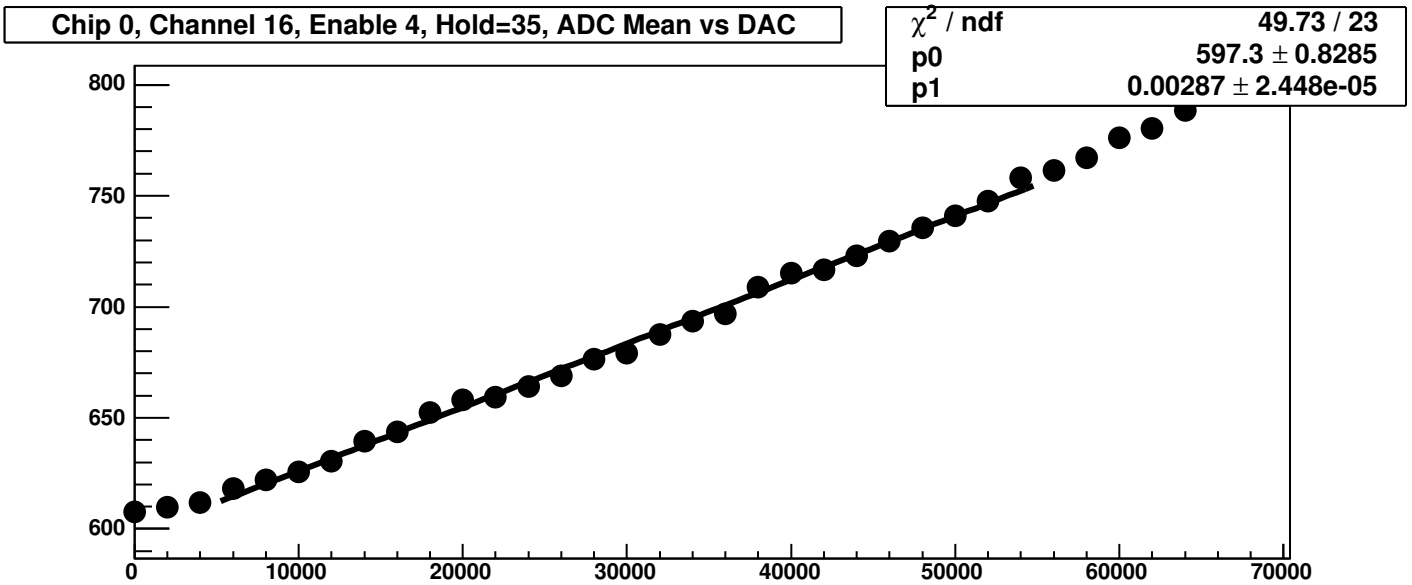

Chip 0, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

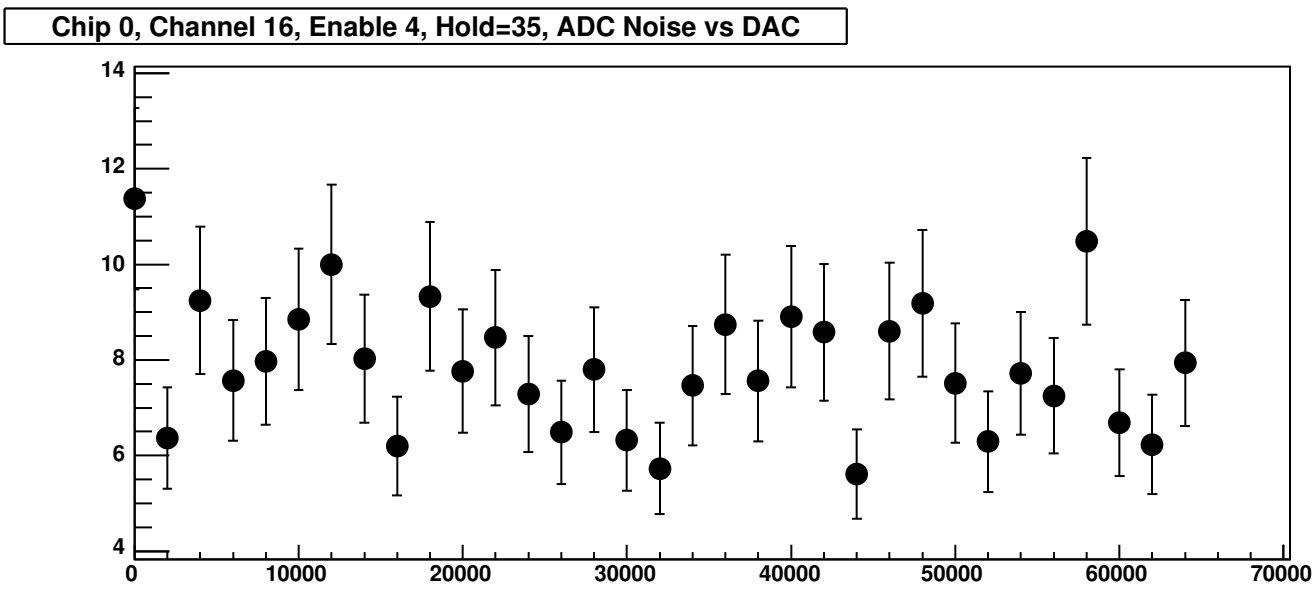

Chip 0, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

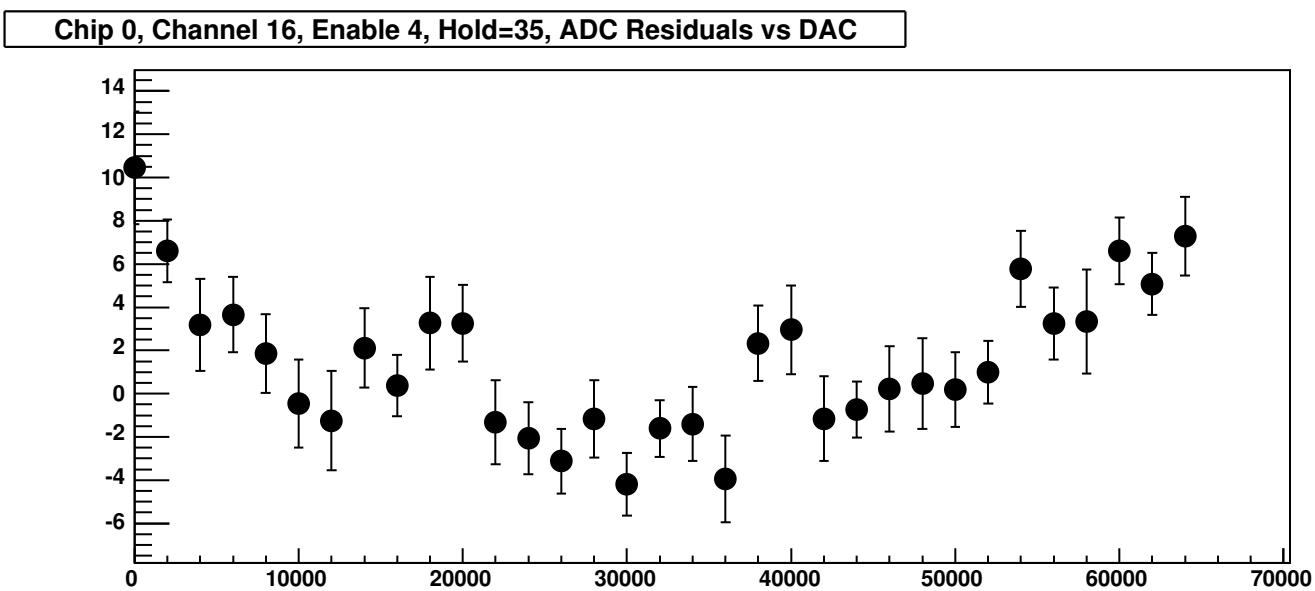

Chip 0, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 34.14 / 23
p0 601.6 ± 0.8815
p1 0.002945 ± 2.719e-05
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000
70000

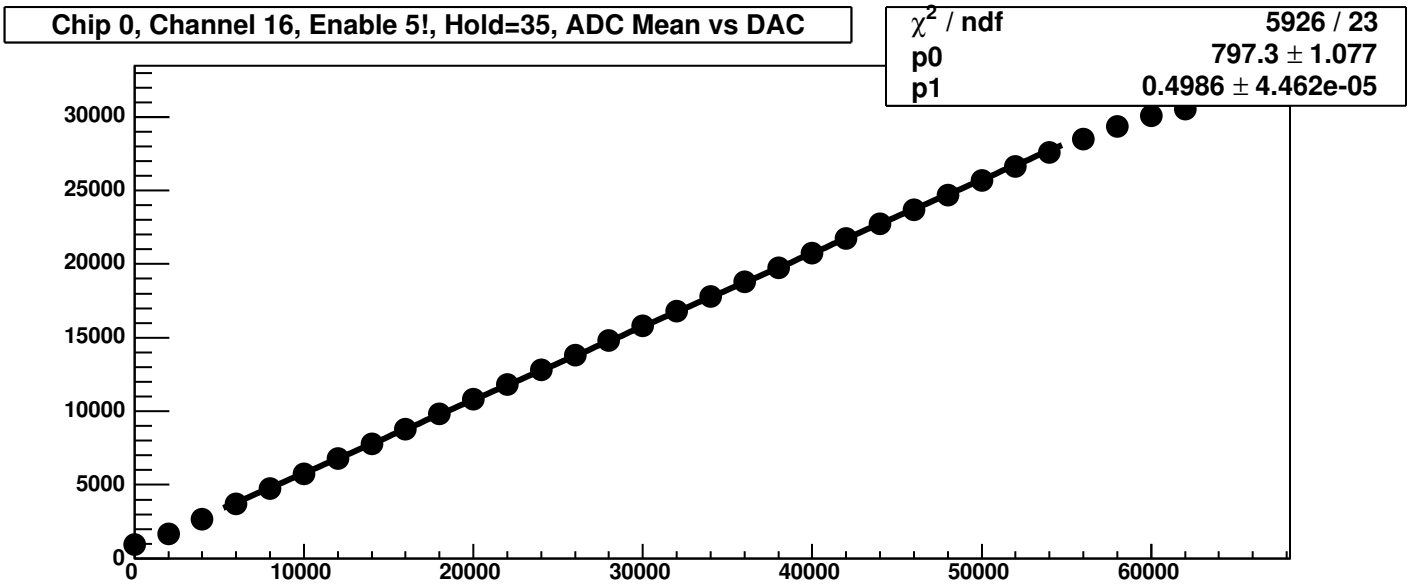

Chip 0, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

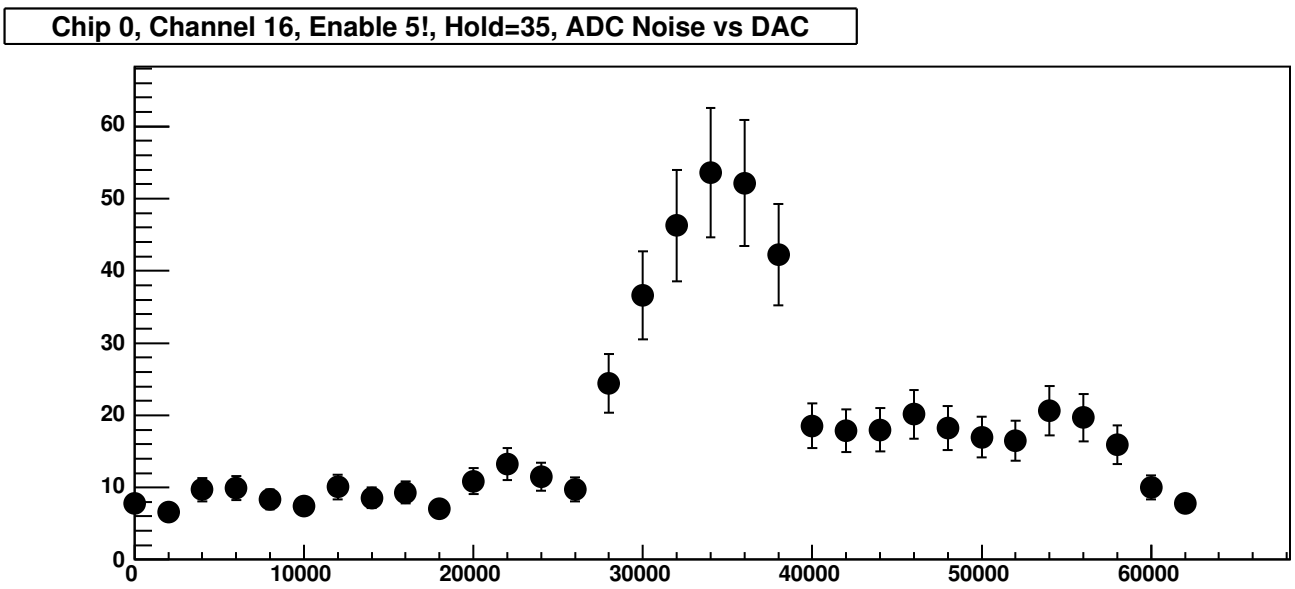

Chip 0, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

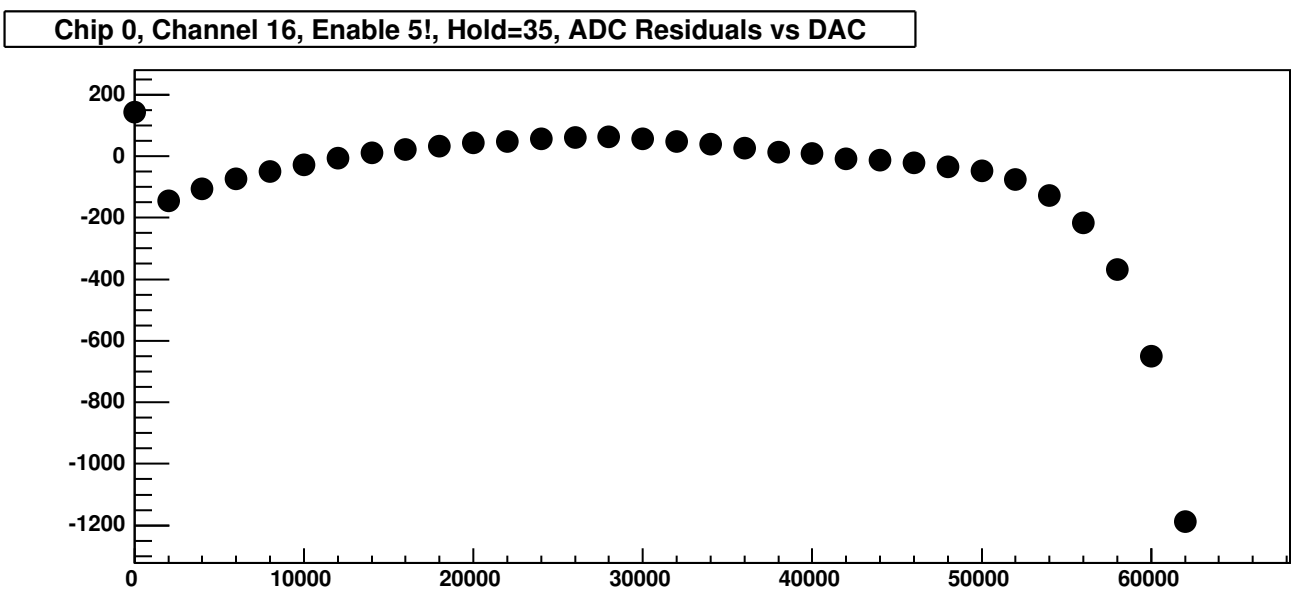

Chip 0, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 49.73 / 23
p0 597.3 ± 0.8285
p1 0.00287 ± 2.448e-05
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000
70000

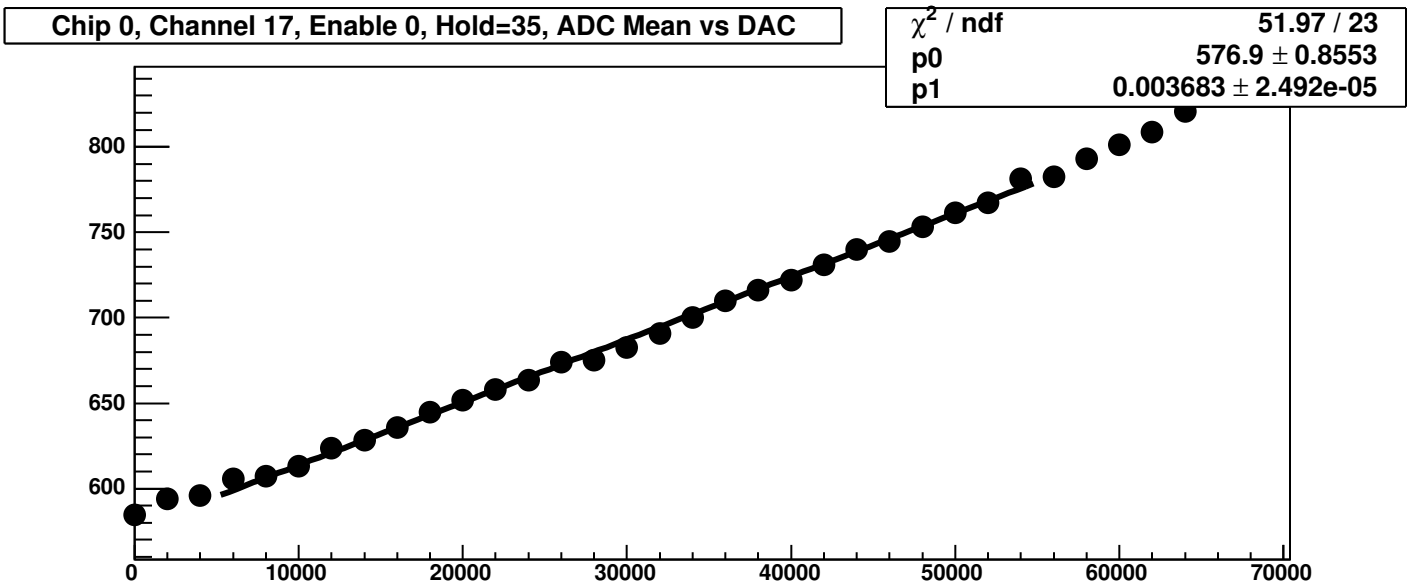

Chip 0, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

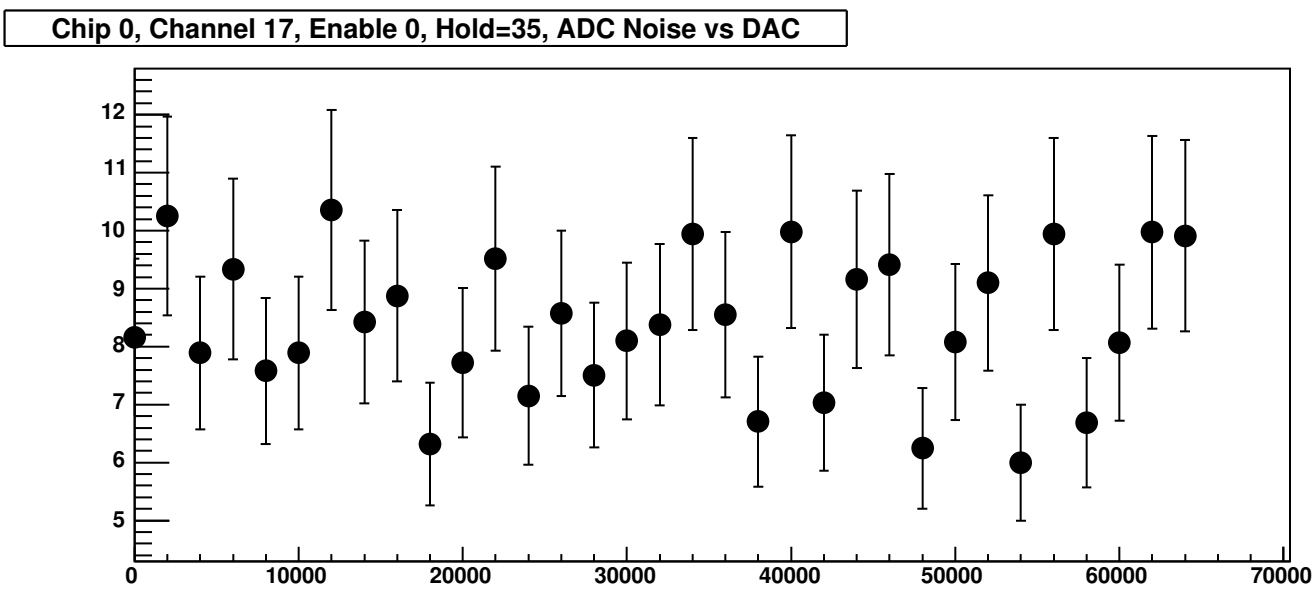

Chip 0, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

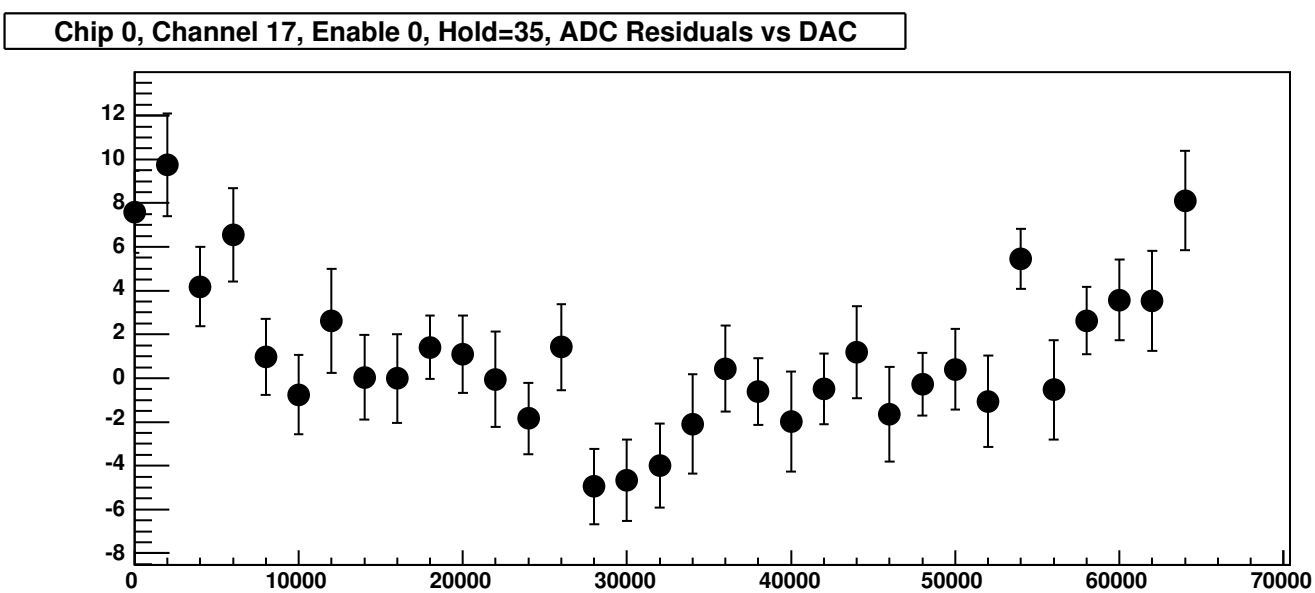

Chip 0, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 5926 / 23
p0 797.3 ± 1.077
p1 0.4986 ± 4.462e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

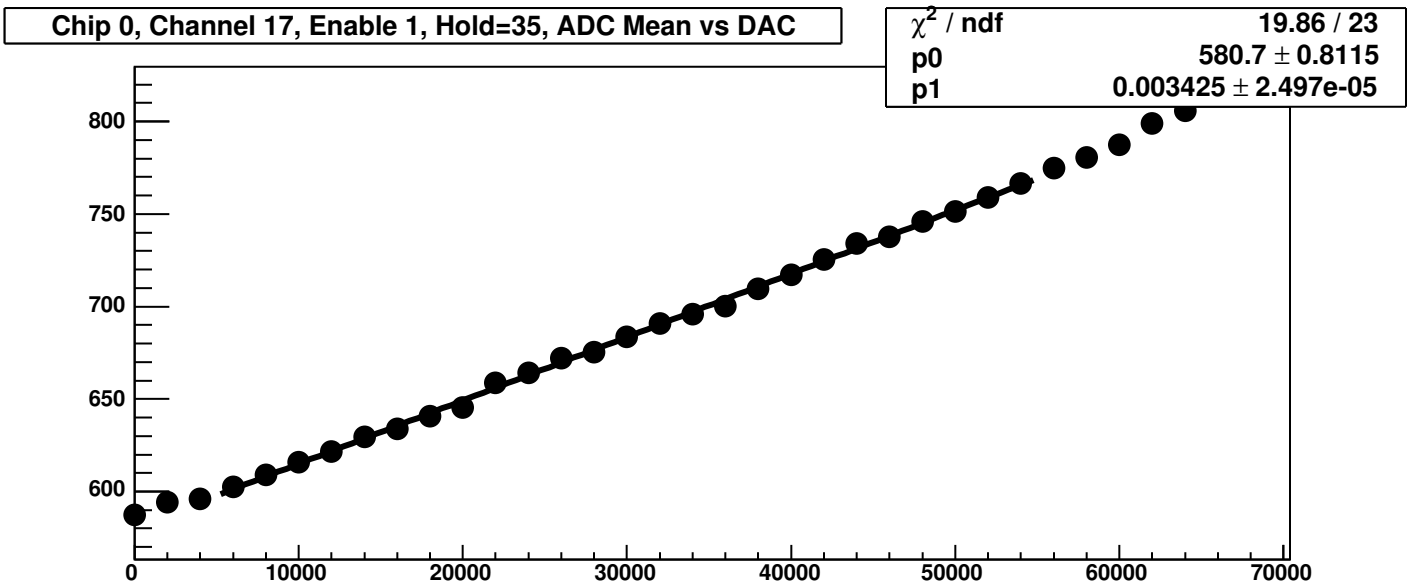

Chip 0, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000

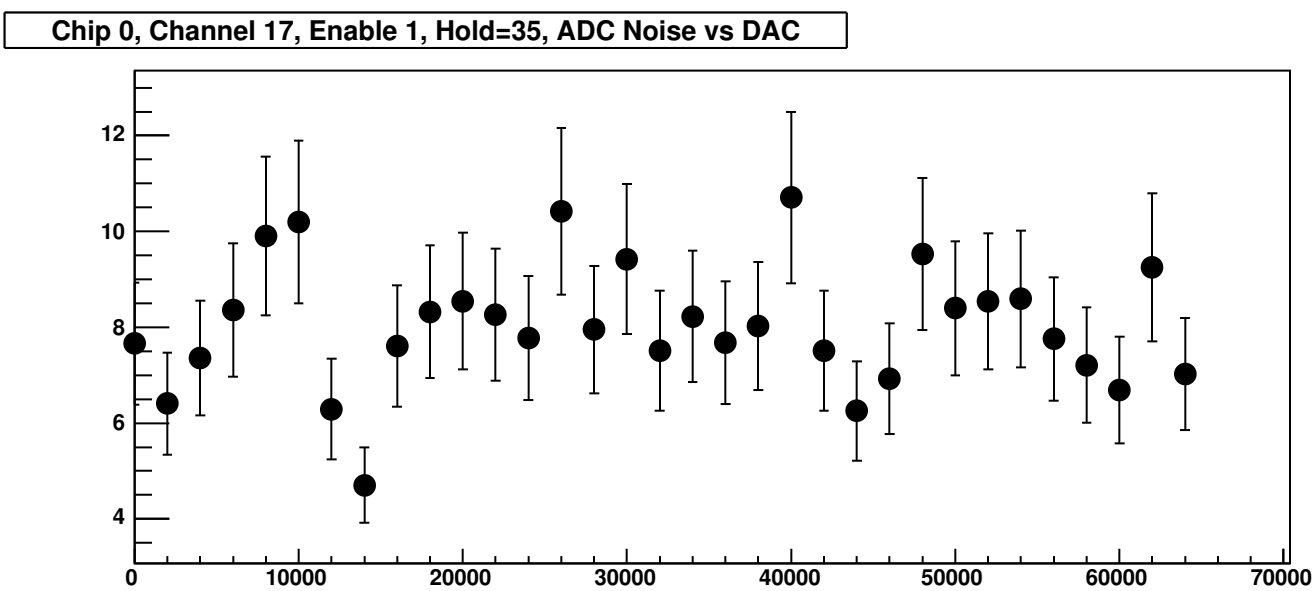

Chip 0, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
0
10000
20000
30000
40000
50000
60000

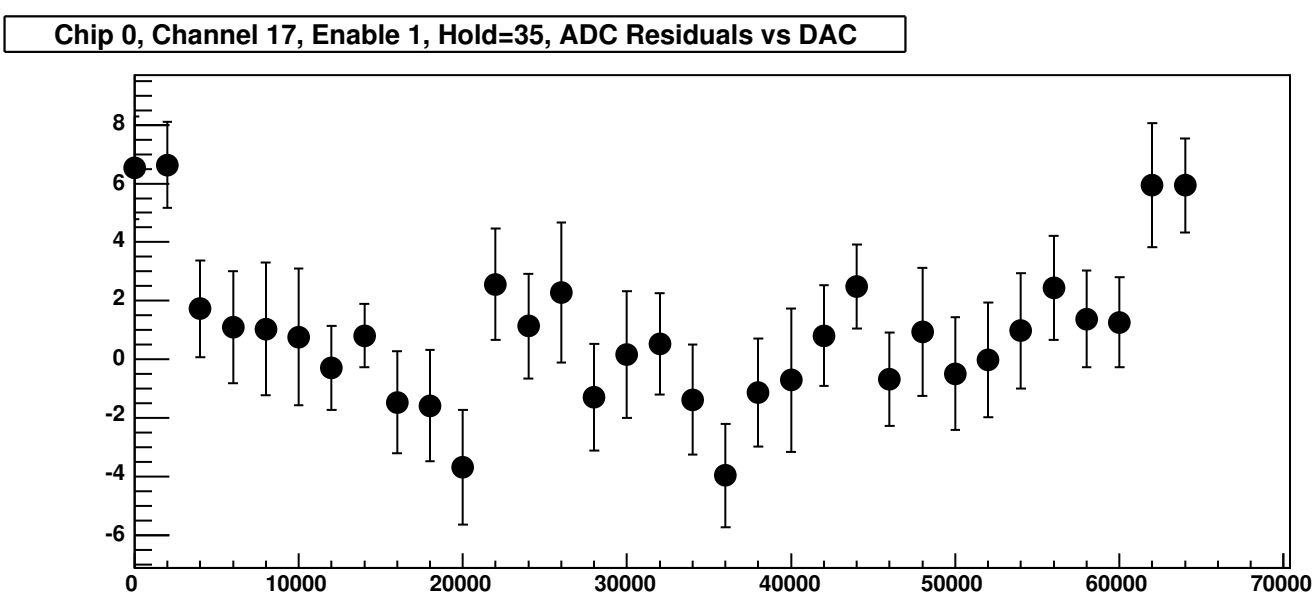

Chip 0, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 51.97 / 23
p0 576.9 ± 0.8553
p1 0.003683 ± 2.492e-05
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000
70000

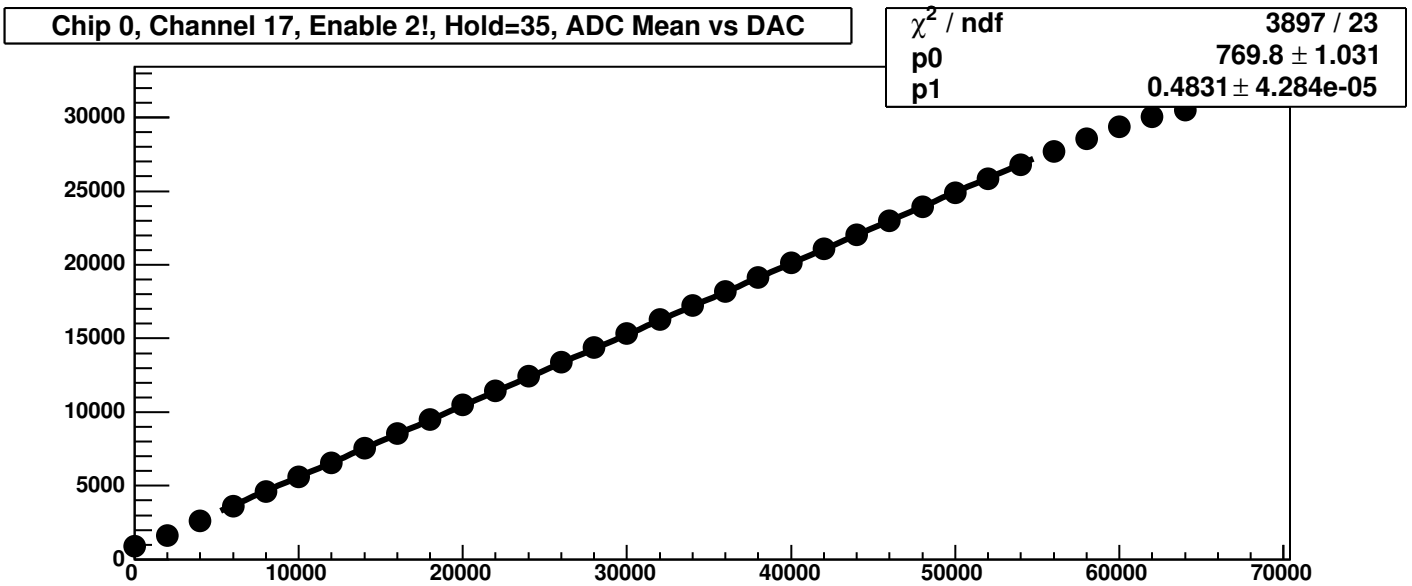

Chip 0, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000

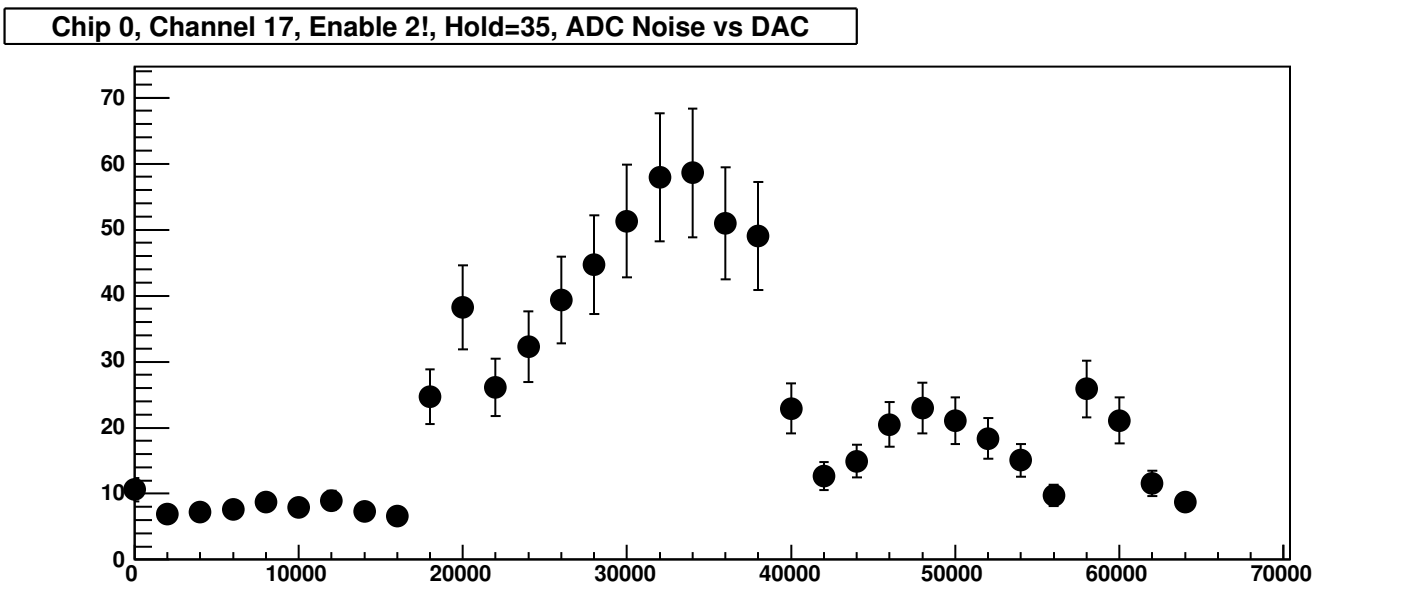

Chip 0, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

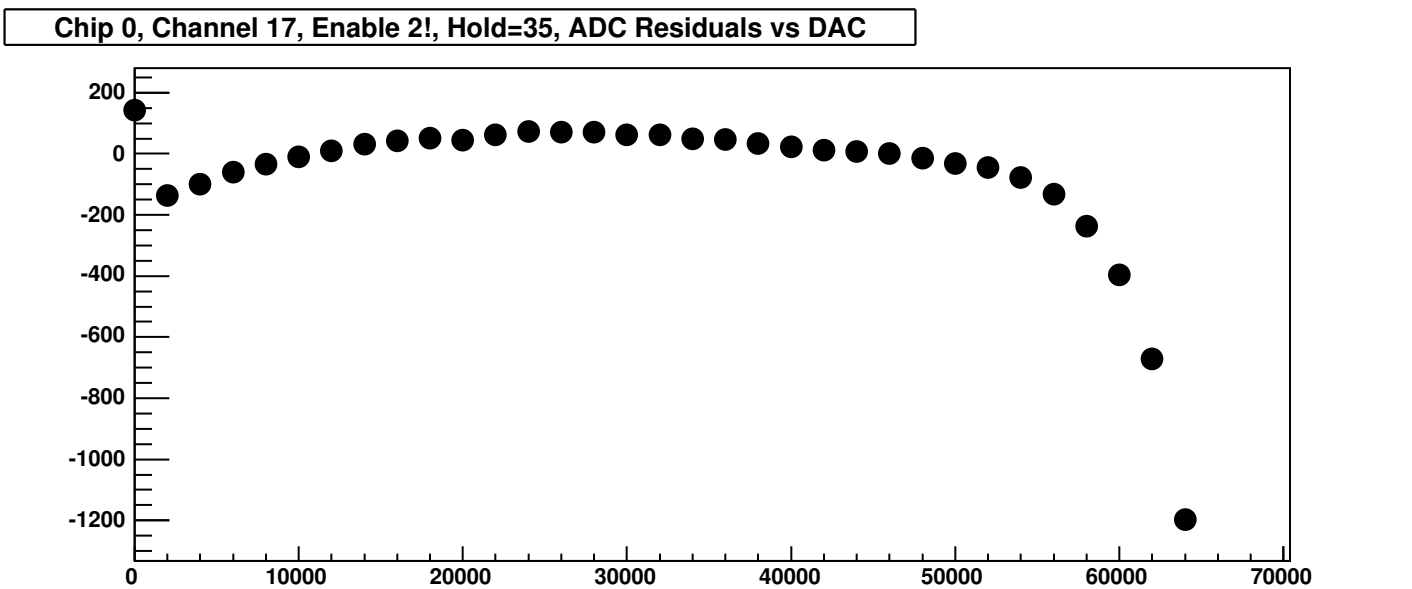

Chip 0, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 19.86 / 23
p0 580.7 ± 0.8115
p1 0.003425 ± 2.497e-05
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000
70000

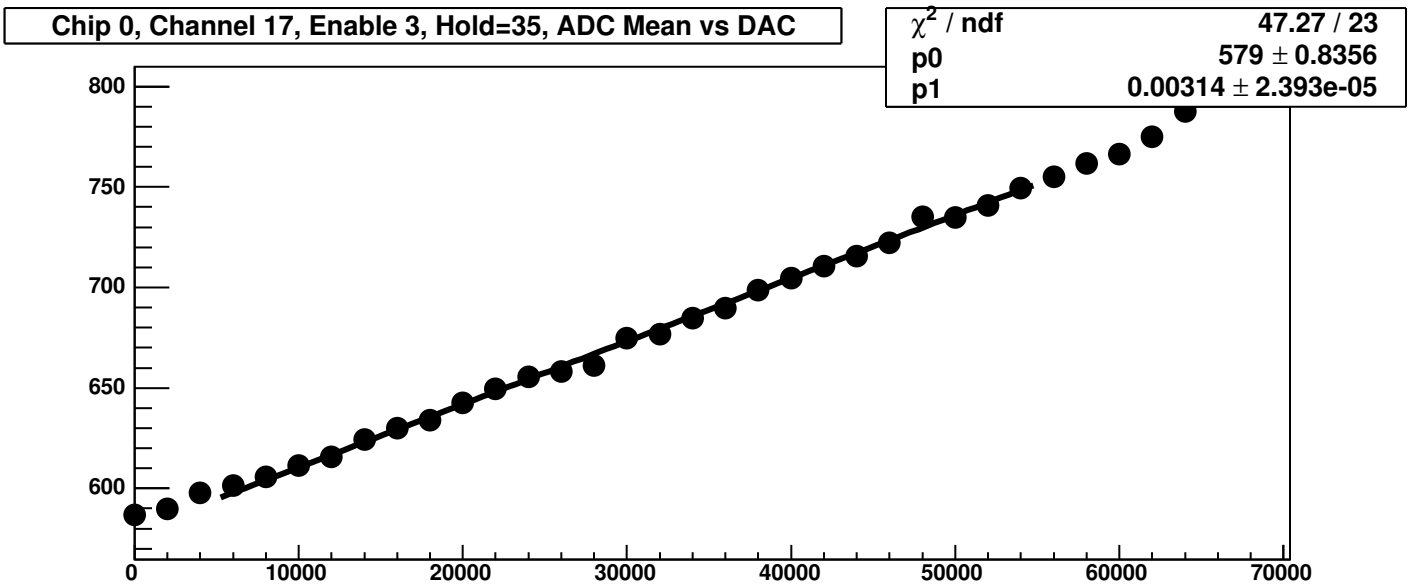

Chip 0, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

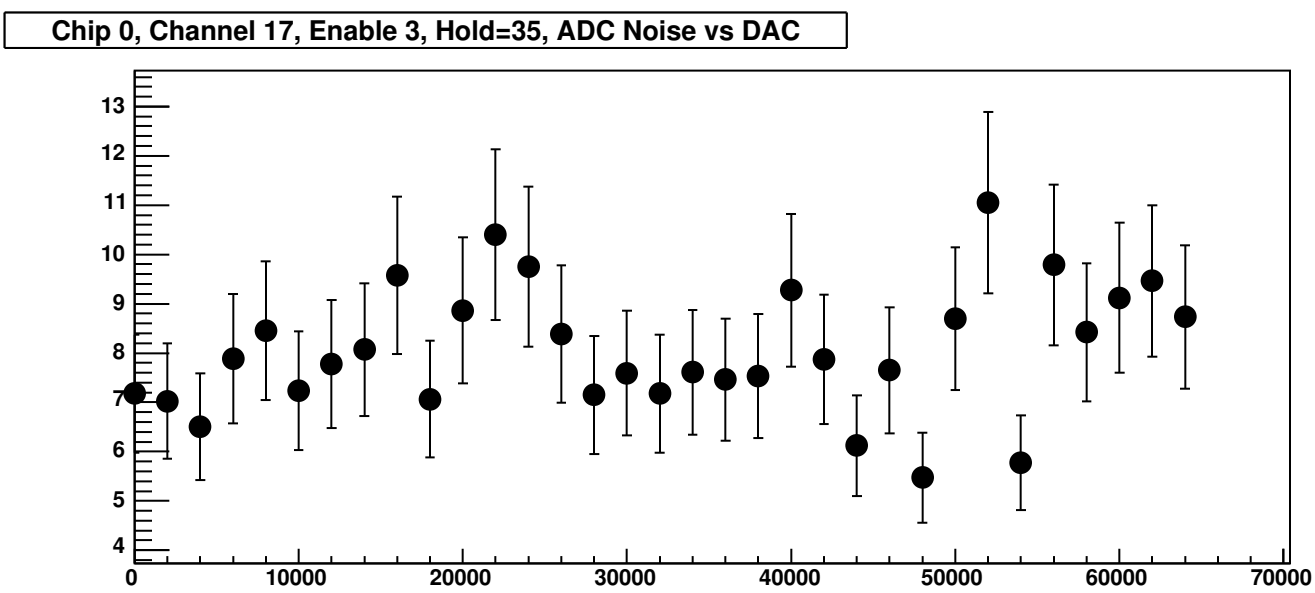

Chip 0, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

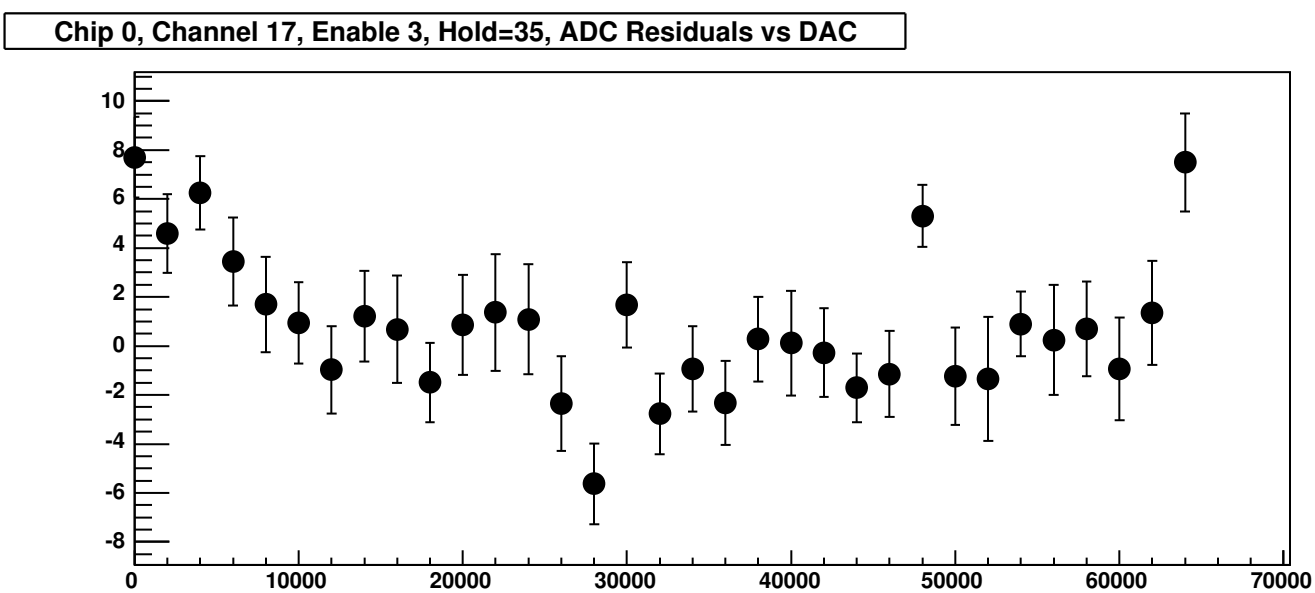

Chip 0, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 3897 / 23
p0 769.8 ± 1.031
p1 0.4831 ± 4.284e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

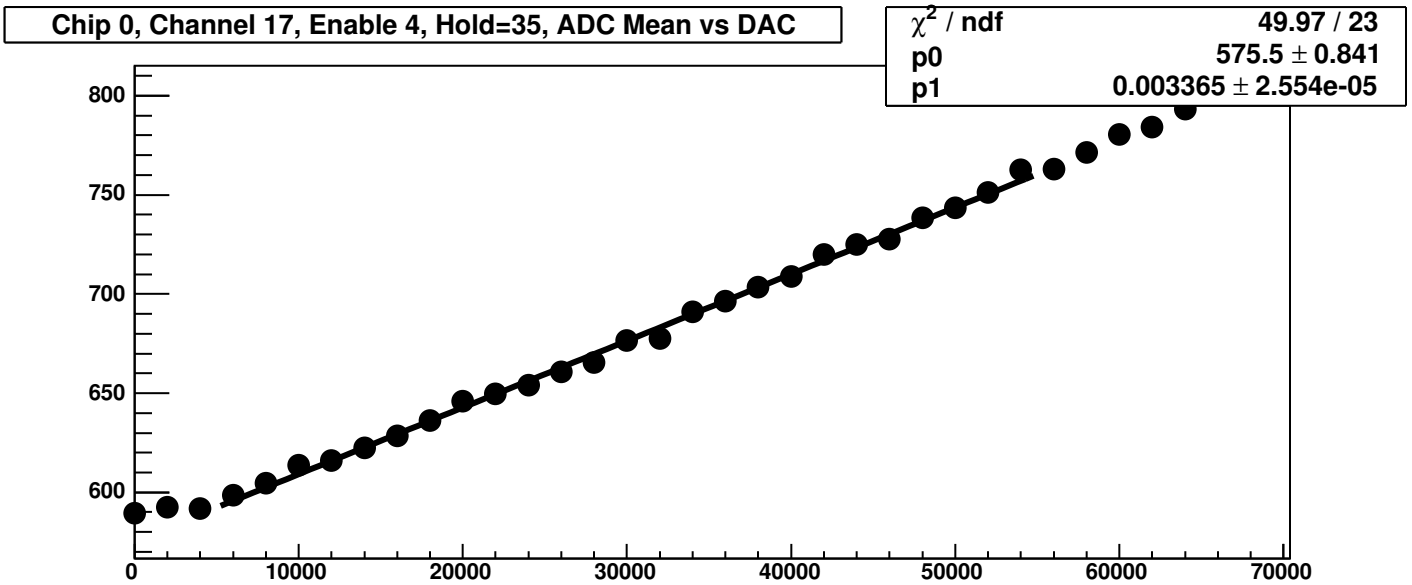

Chip 0, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

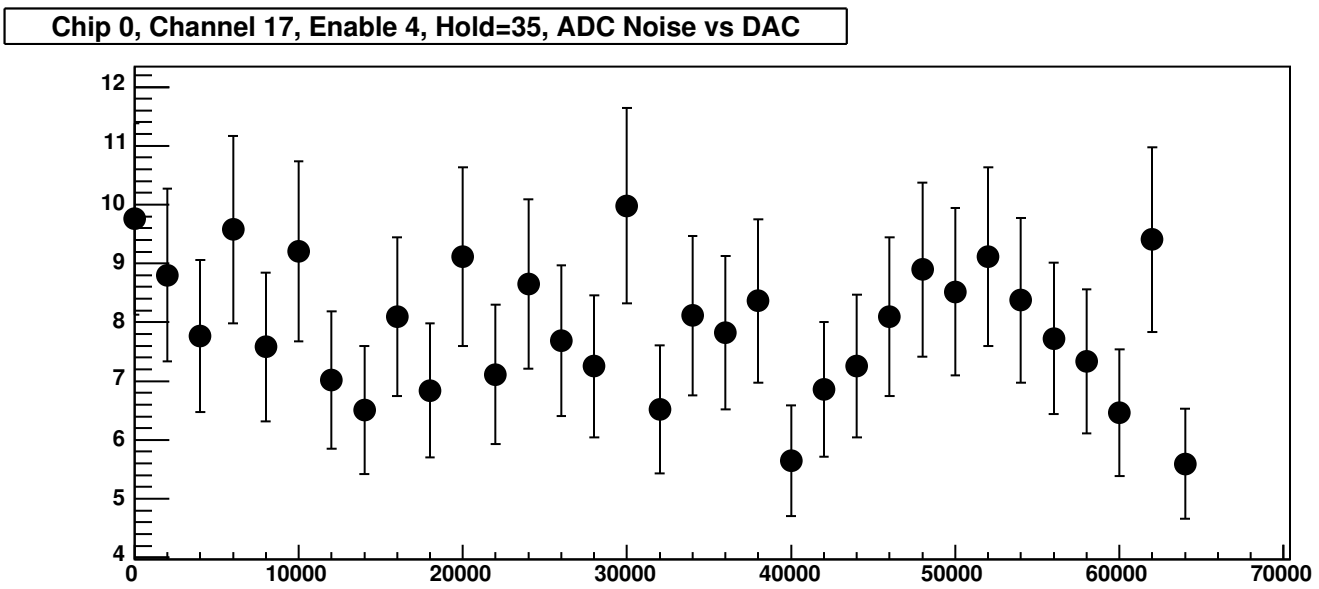

Chip 0, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
0
10000
20000
30000
40000
50000
60000
70000

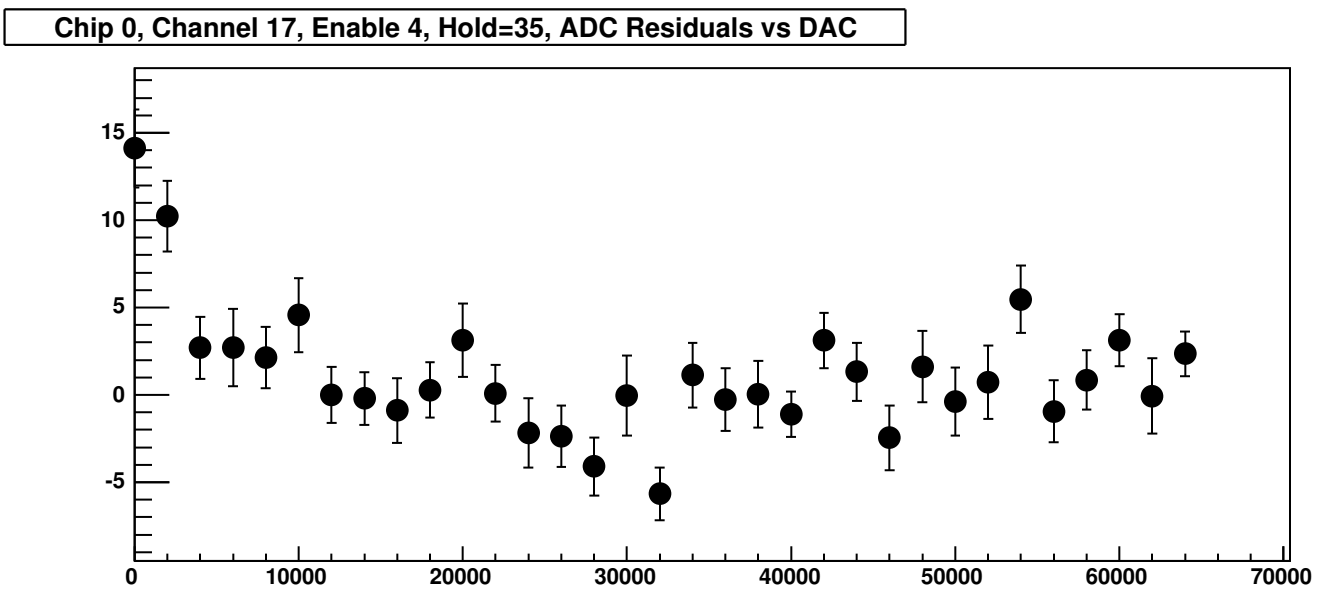

Chip 0, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 47.27 / 23
p0 579 ± 0.8356
p1 0.00314 ± 2.393e-05
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000
70000

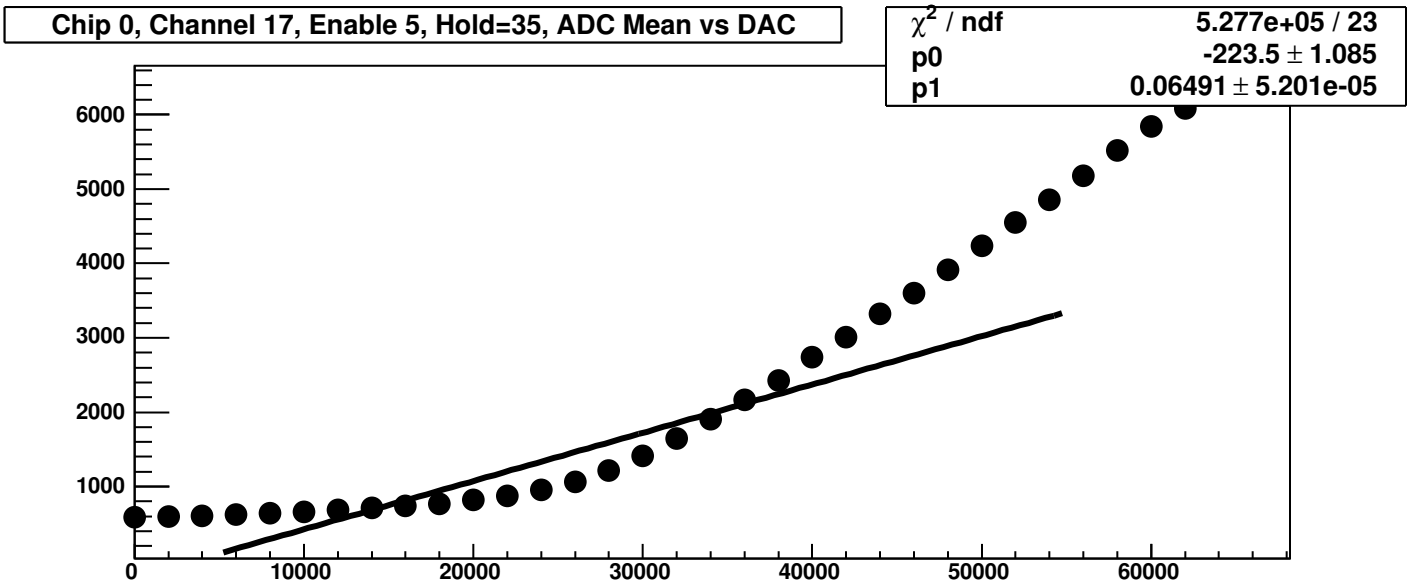

Chip 0, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

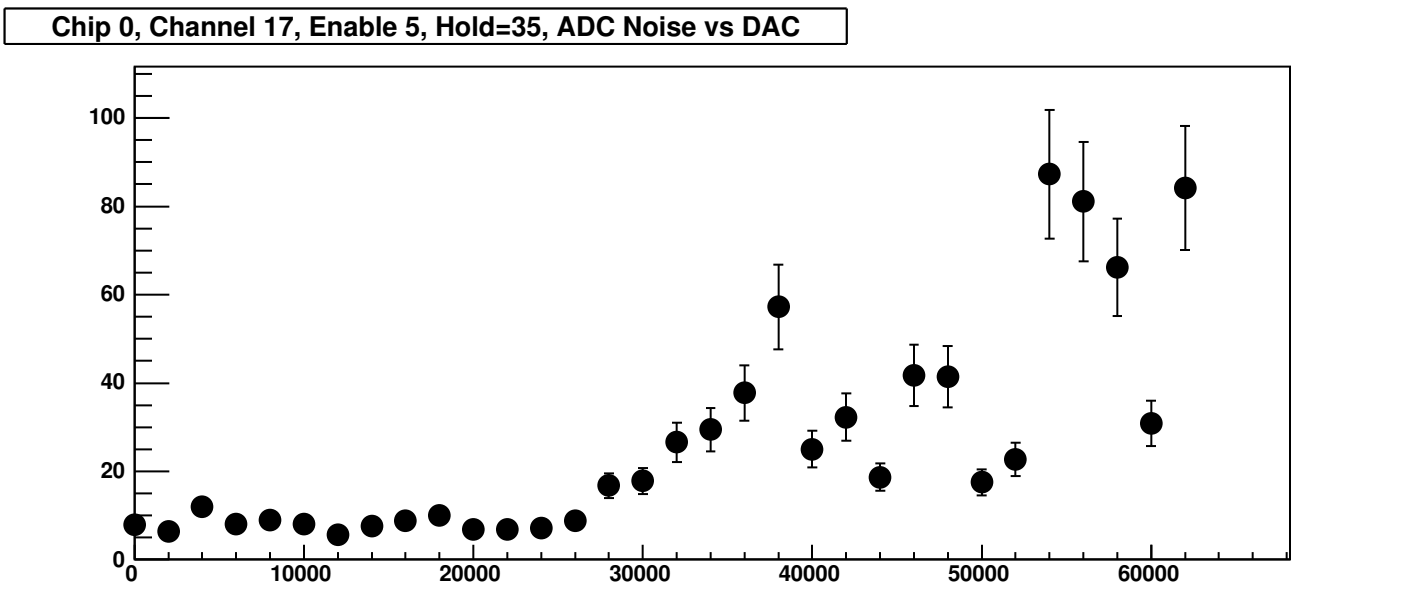

Chip 0, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

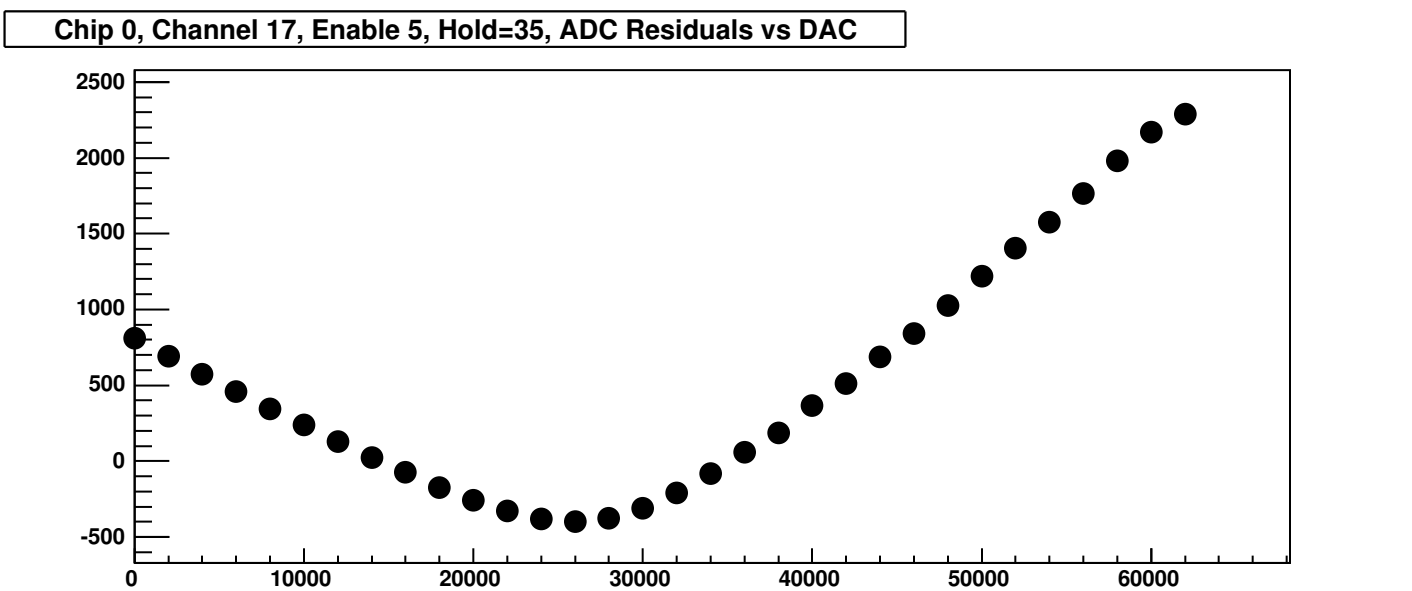

Chip 0, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 49.97 / 23
p0 575.5 ± 0.841
p1 0.003365 ± 2.554e-05
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000
70000

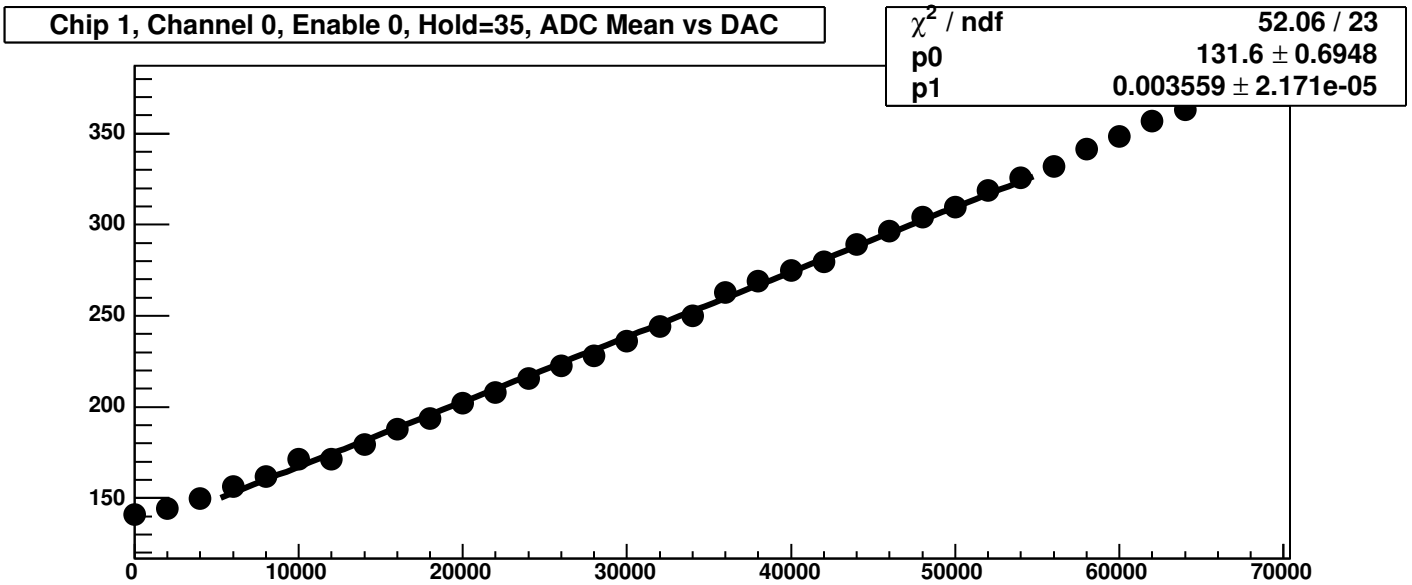

Chip 0, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

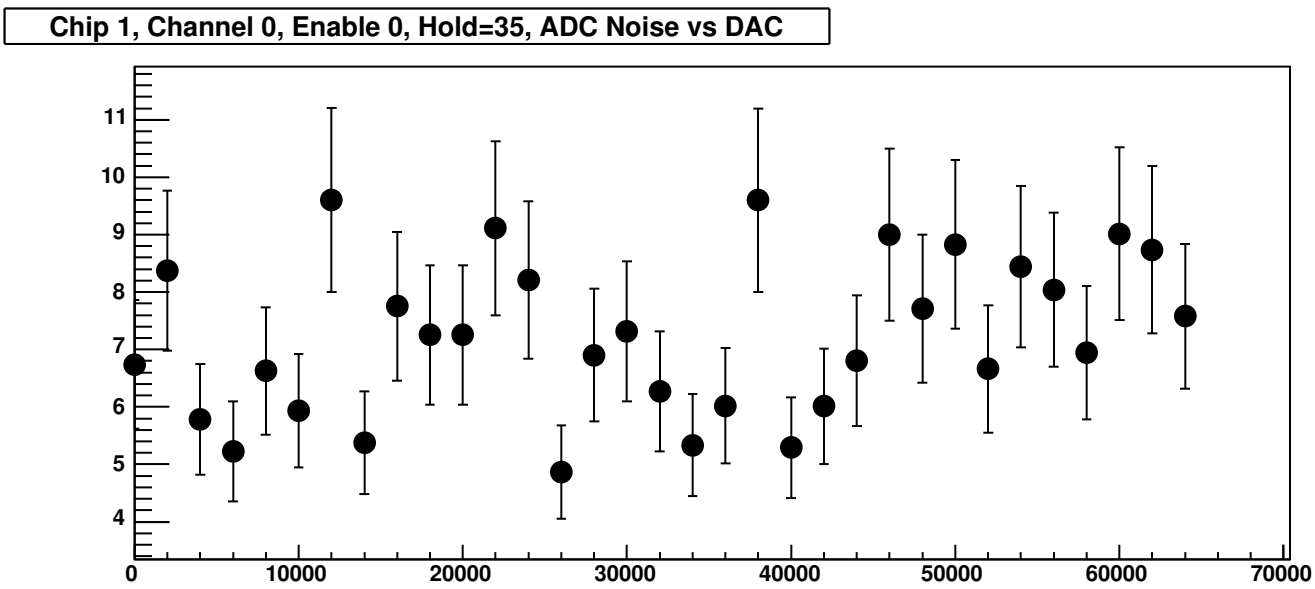

Chip 0, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

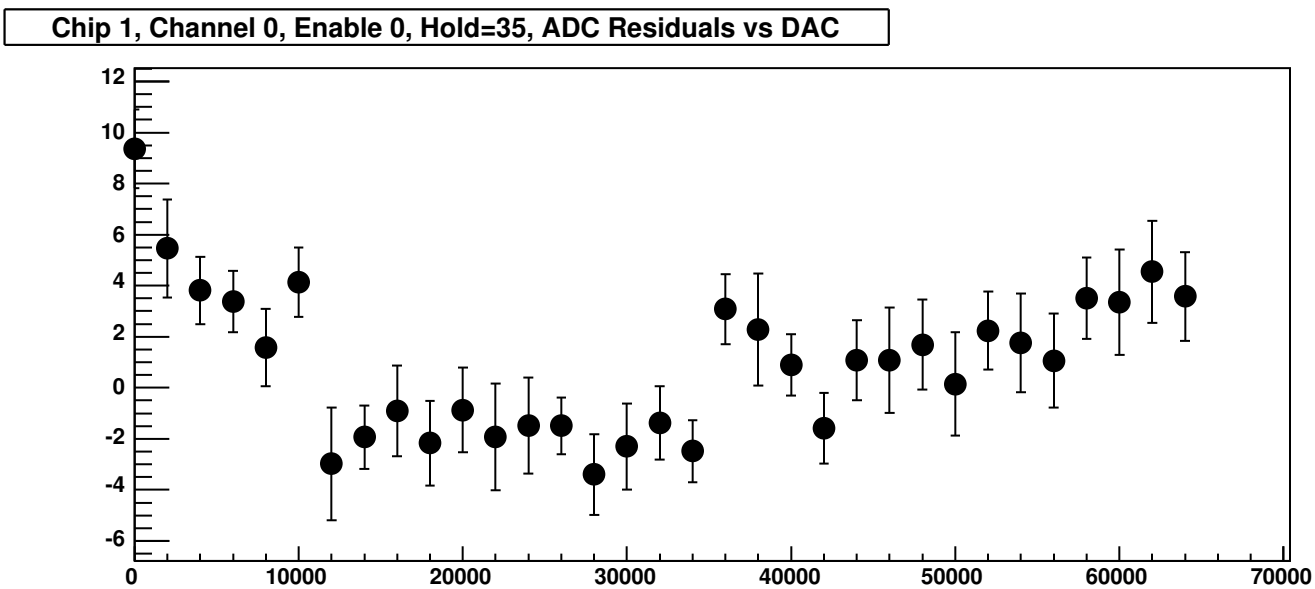

Chip 0, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5.277e+05 / 23
p0 -223.5 ± 1.085
p1 0.06491 ± 5.201e-05
6000
5000
4000
3000
2000
1000
0
10000
20000
30000
40000
50000
60000

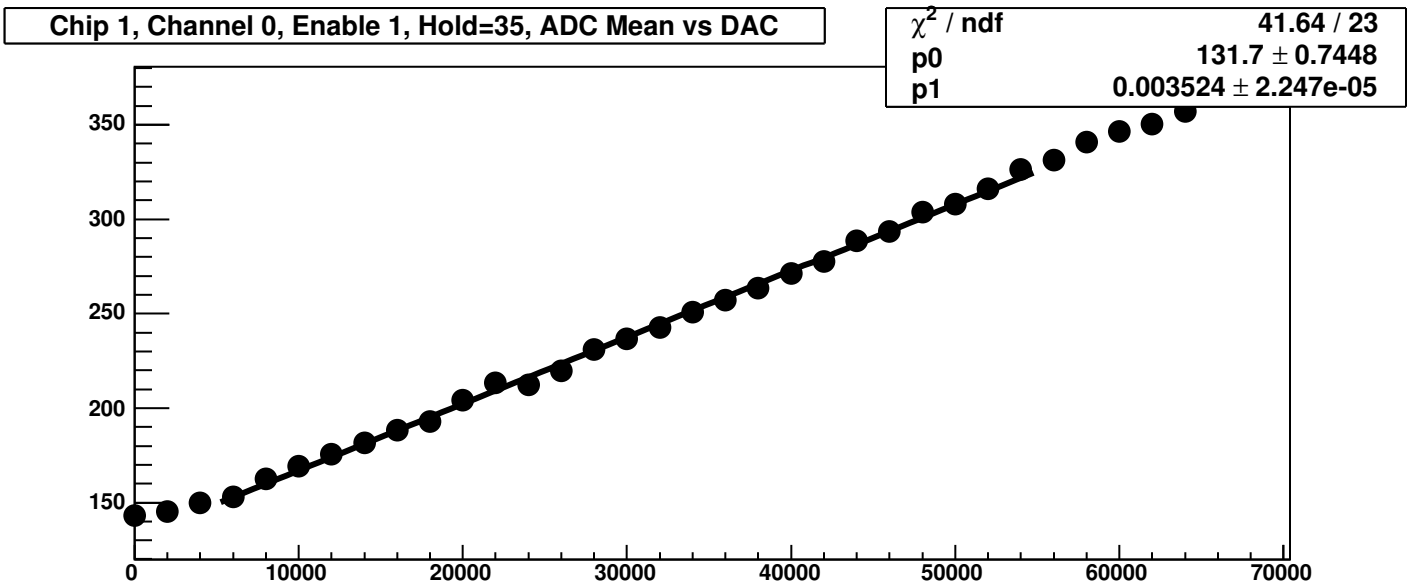

Chip 0, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

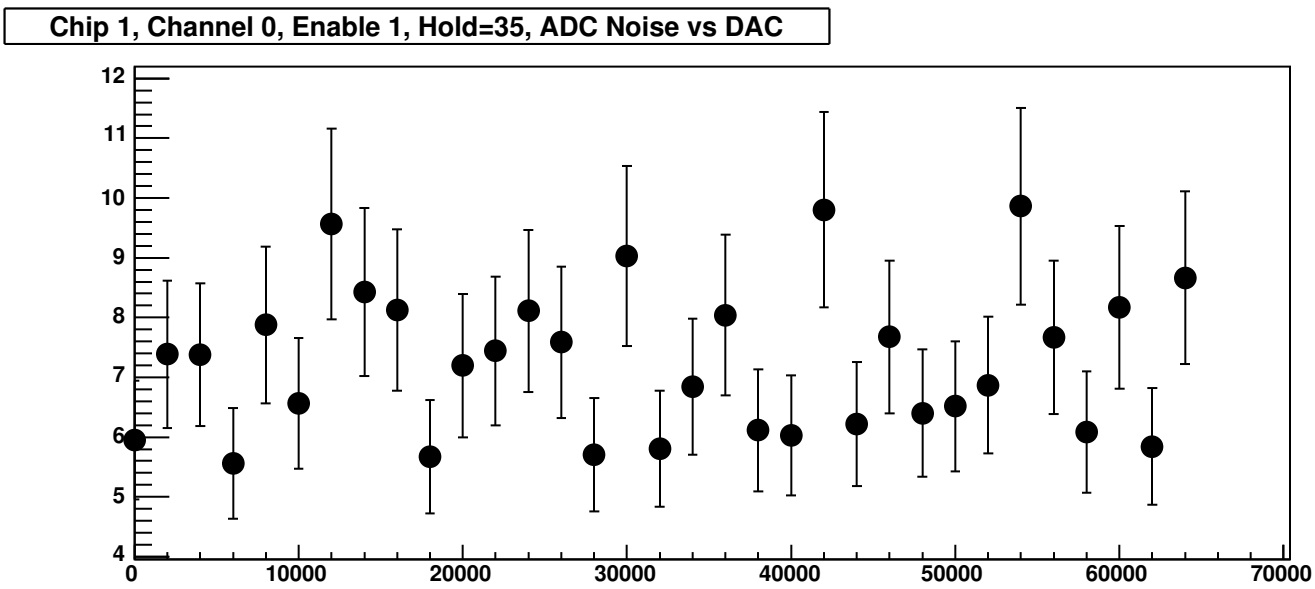

Chip 0, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
2500
2000
1500
1000
500
0
-500
0
10000
20000
30000
40000
50000
60000

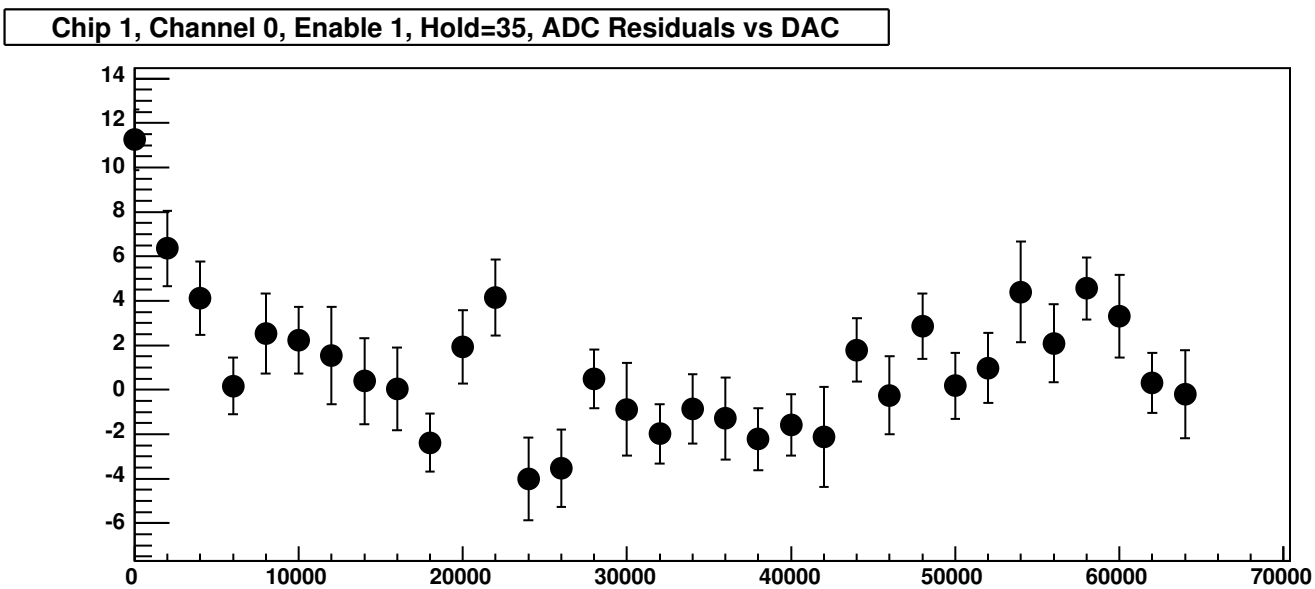

Chip 1, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 52.06 / 23
p0 131.6 ± 0.6948
p1 0.003559 ± 2.171e-05
350
300
250
200
150
0
10000
20000
30000
40000
50000
60000
70000

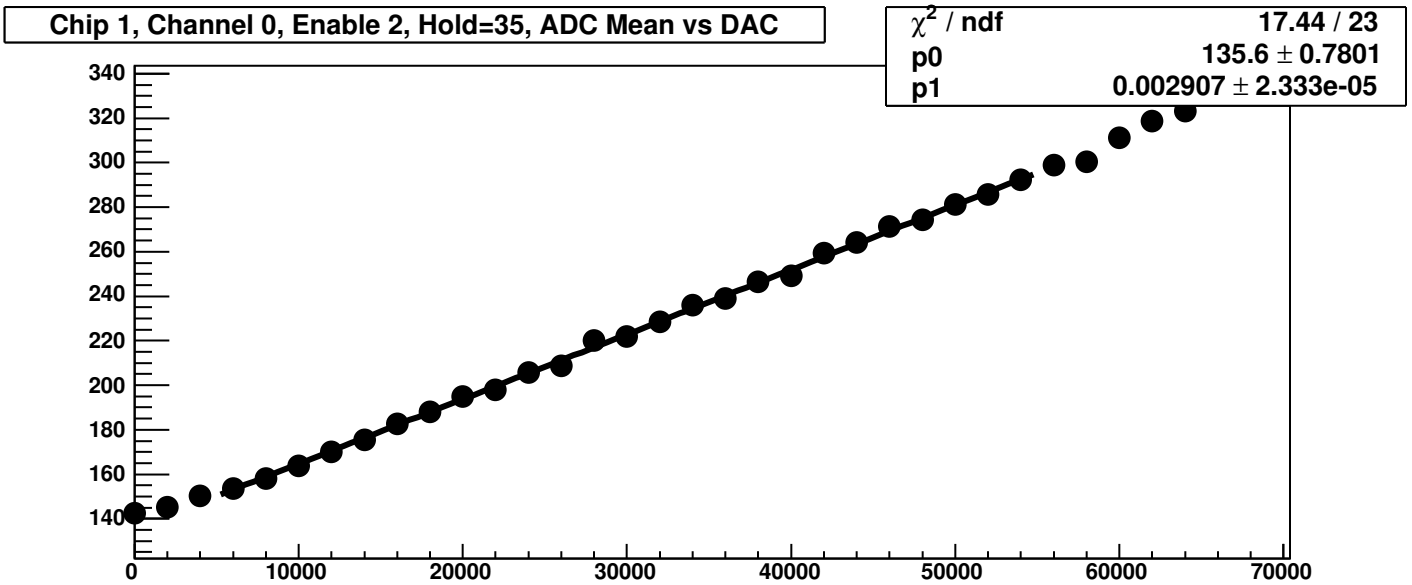

Chip 1, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

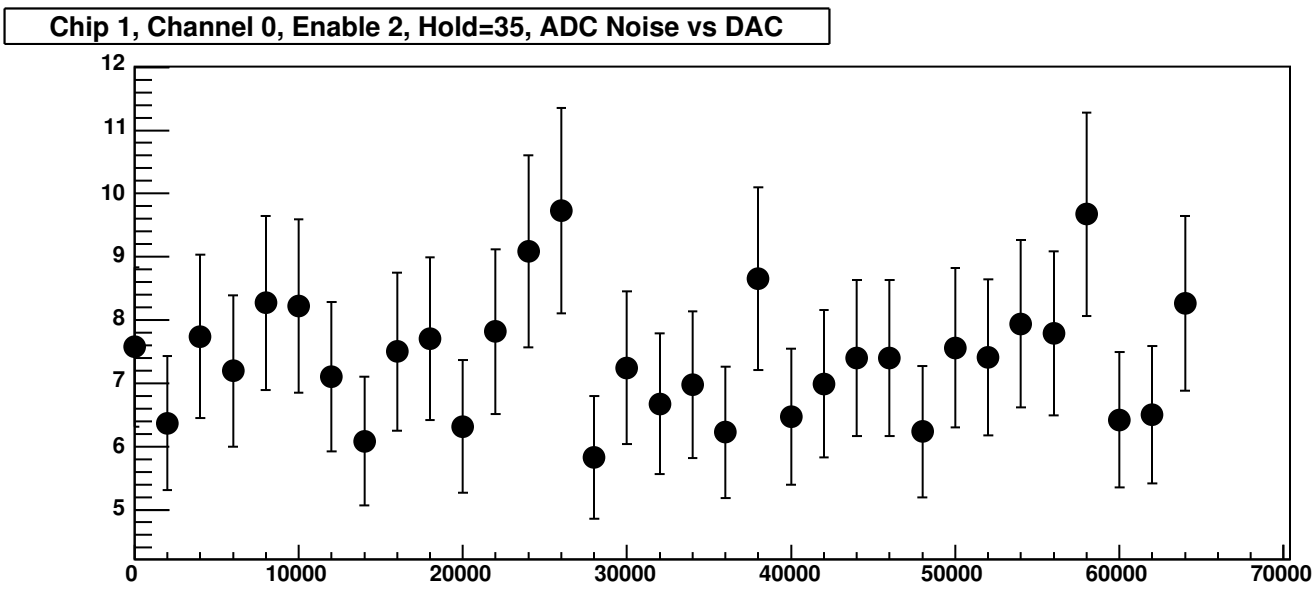

Chip 1, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

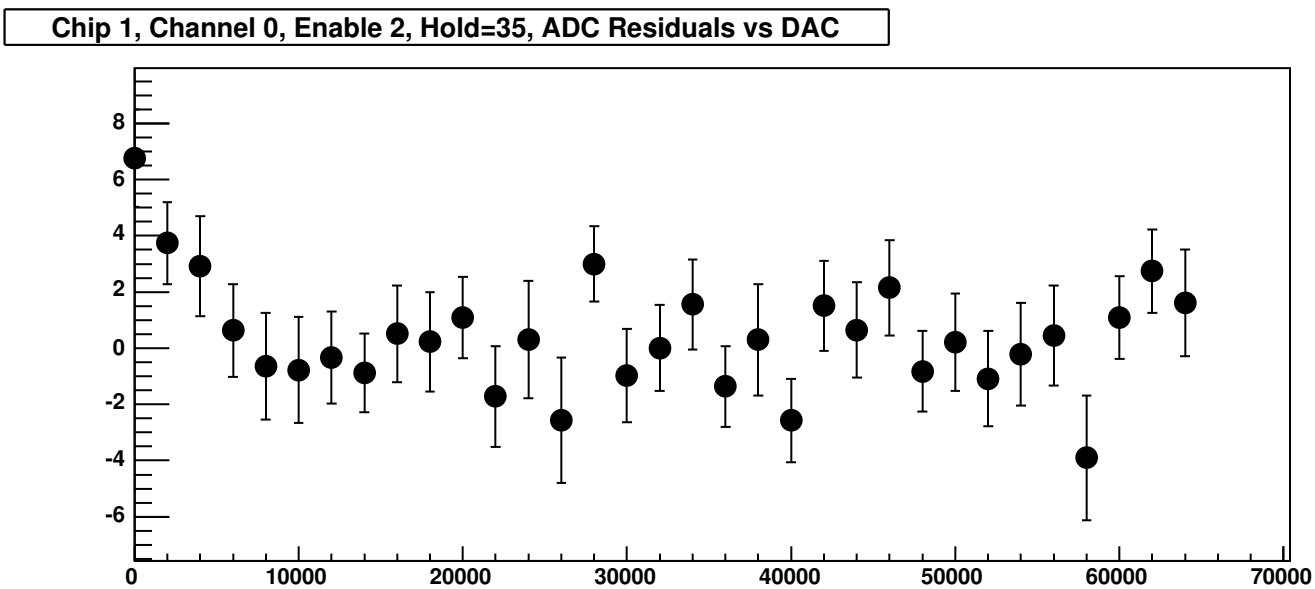

Chip 1, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 41.64 / 23
p0 131.7 ± 0.7448
p1 0.003524 ± 2.247e-05
350
300
250
200
150
0
10000
20000
30000
40000
50000
60000
70000

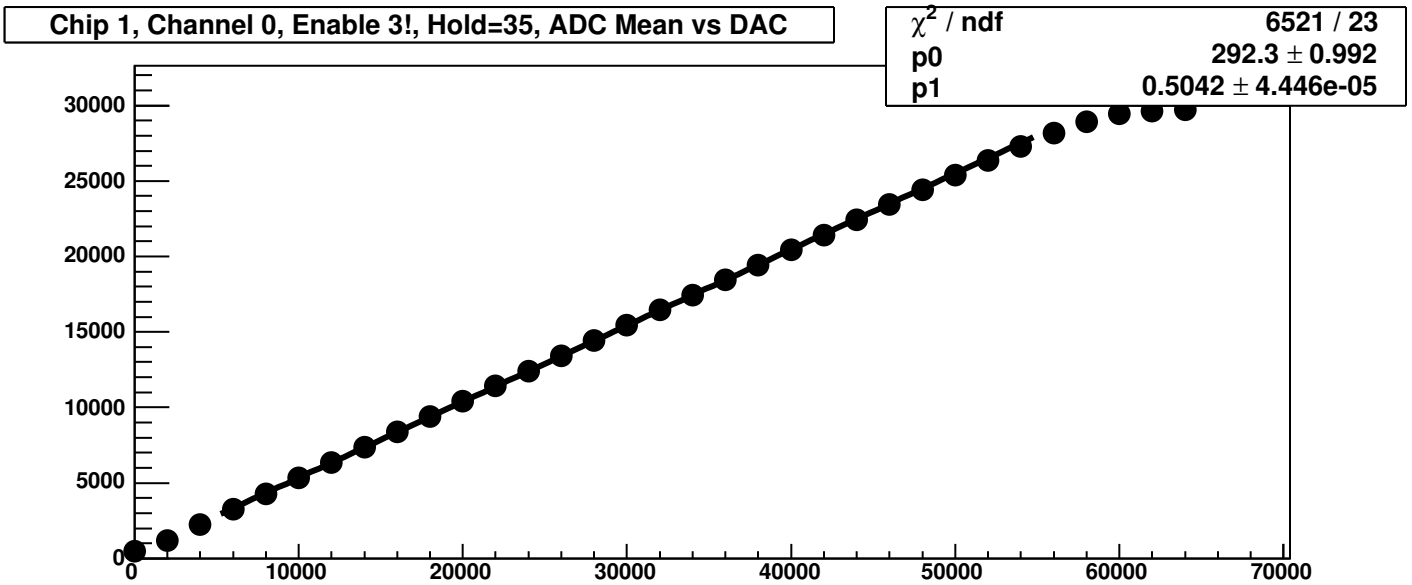

Chip 1, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

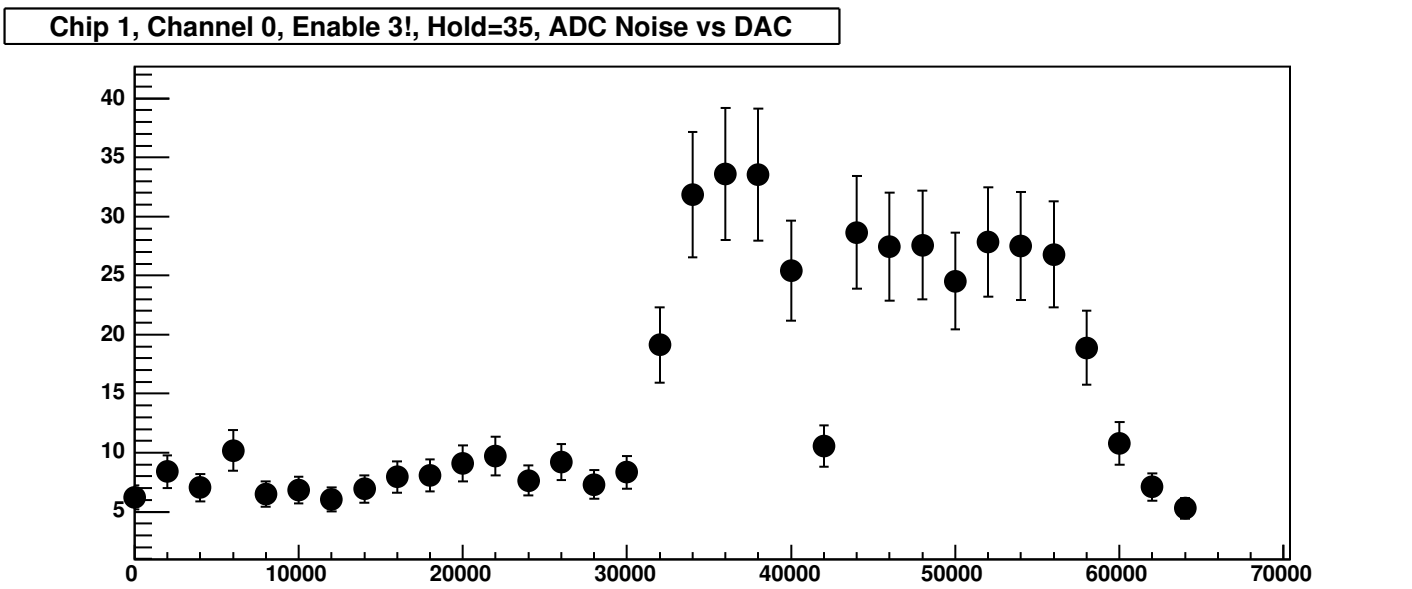

Chip 1, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

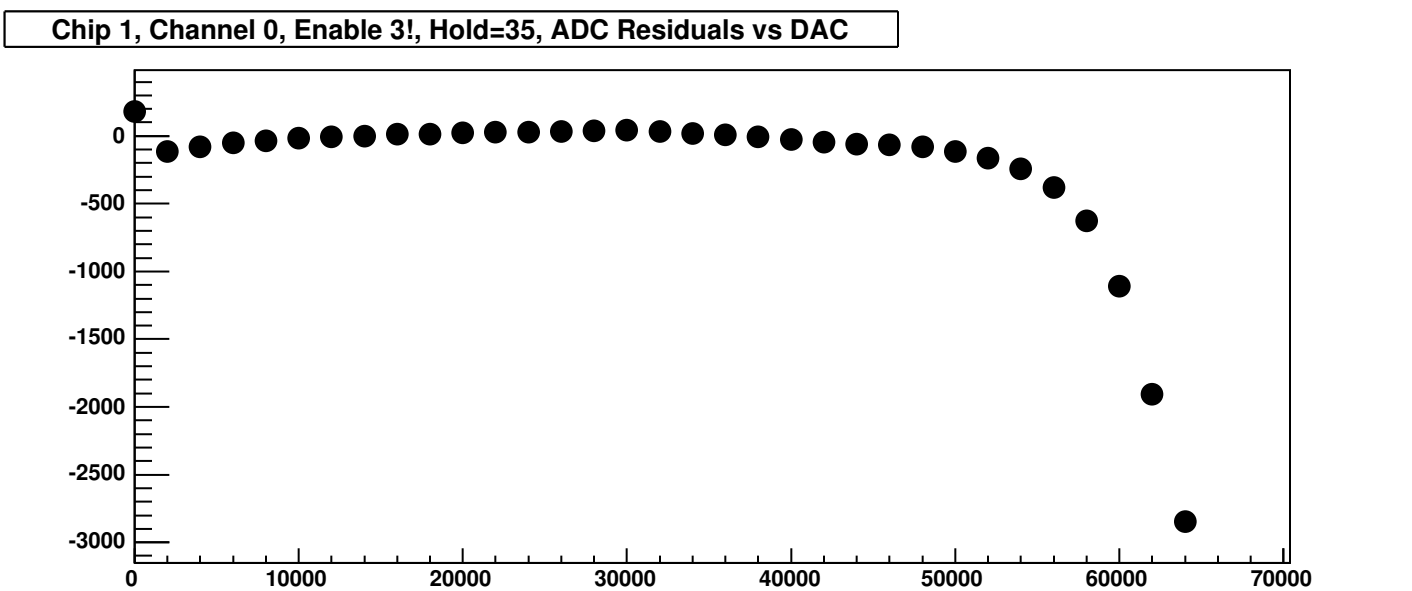

Chip 1, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 17.44 / 23
p0 135.6 ± 0.7801
p1 0.002907 ± 2.333e-05
340
320
300
280
260
240
220
200
180
160
140
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000

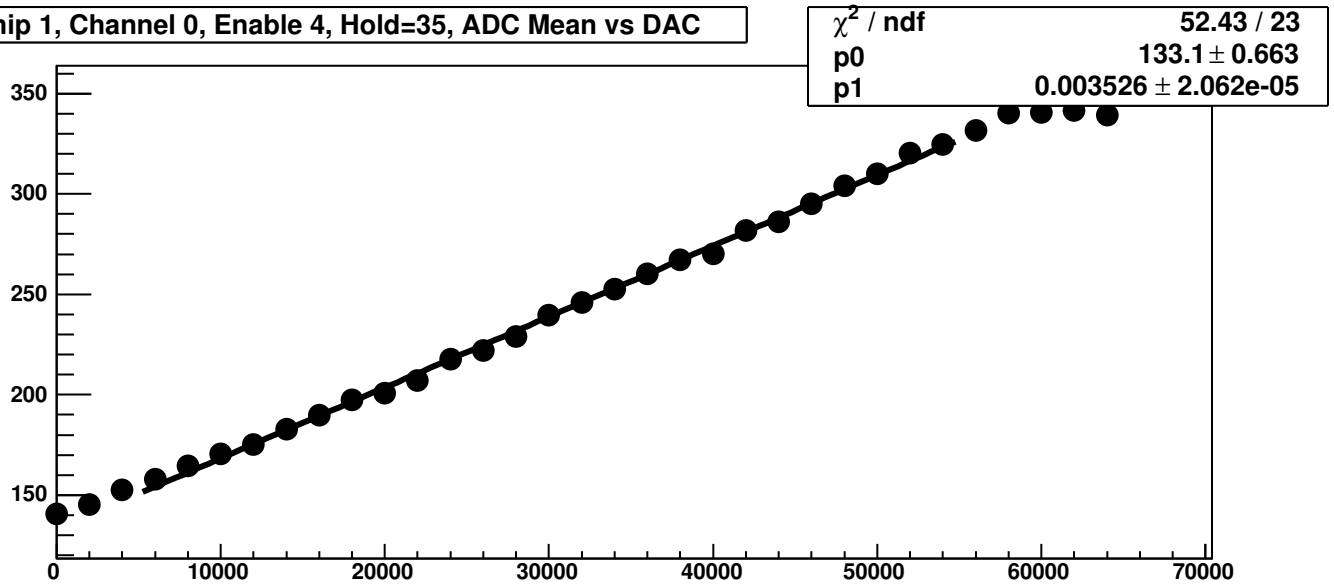

Chip 1, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 6521 / 23
p0 292.3 ± 0.992
p1 0.5042 ± 4.446e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

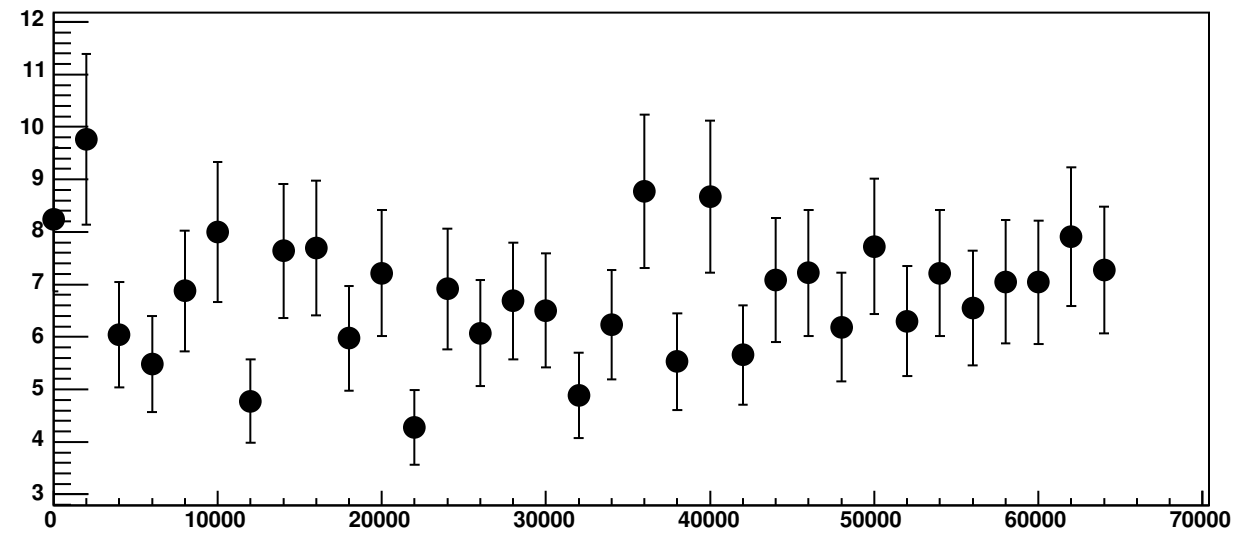

Chip 1, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
0
10000
20000
30000
40000
50000
60000
70000

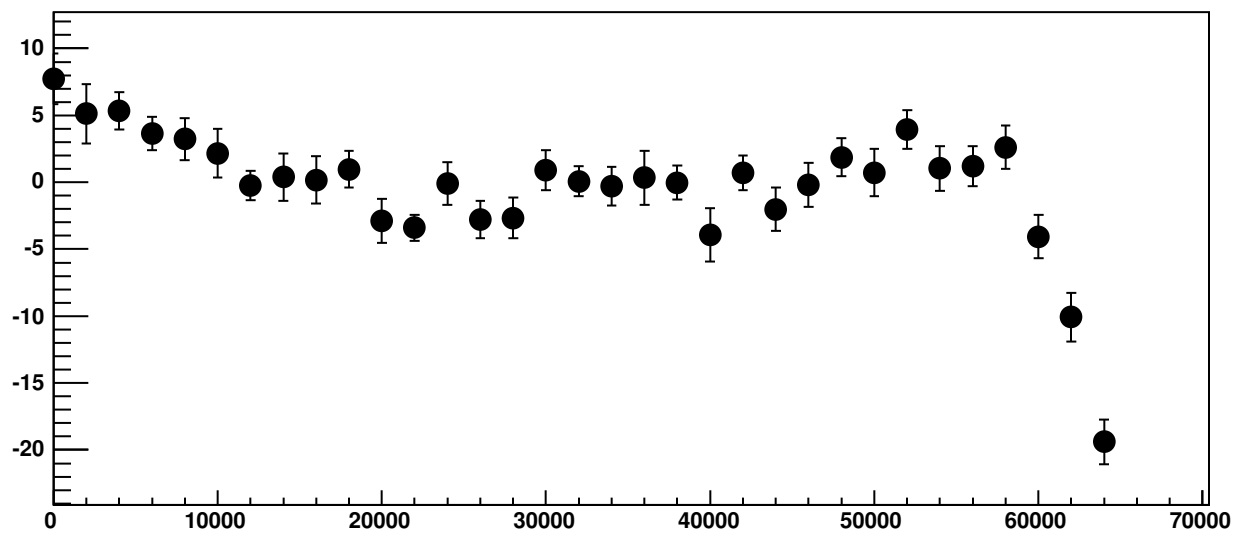
Chip 1, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

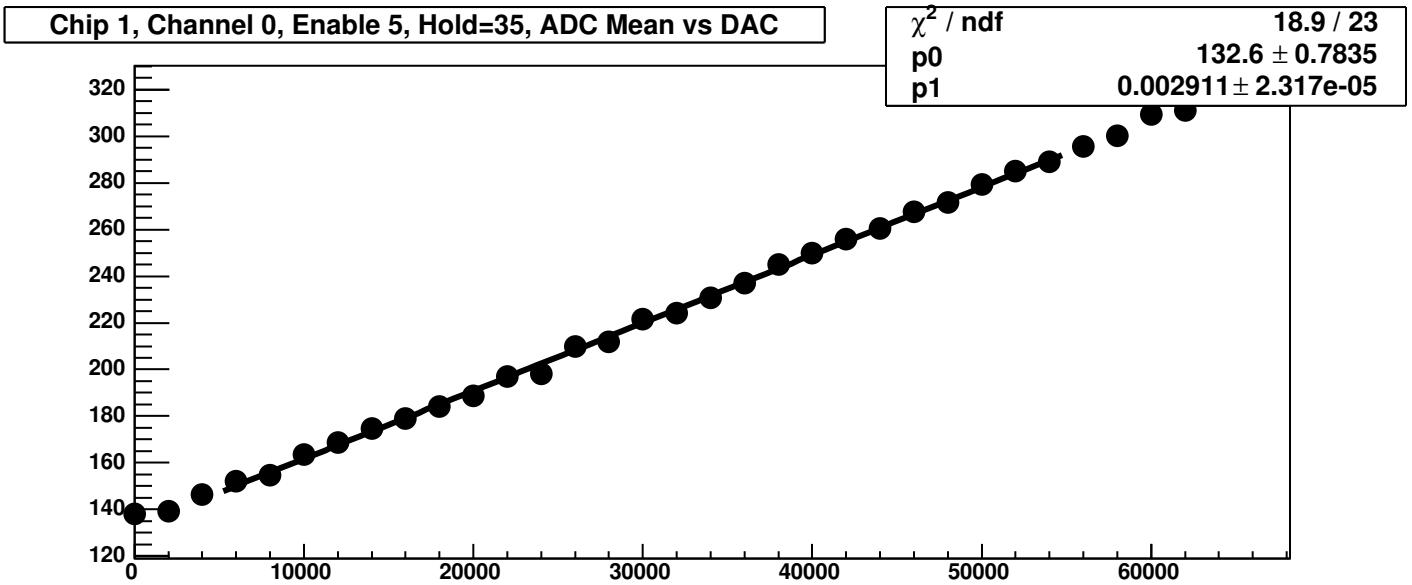


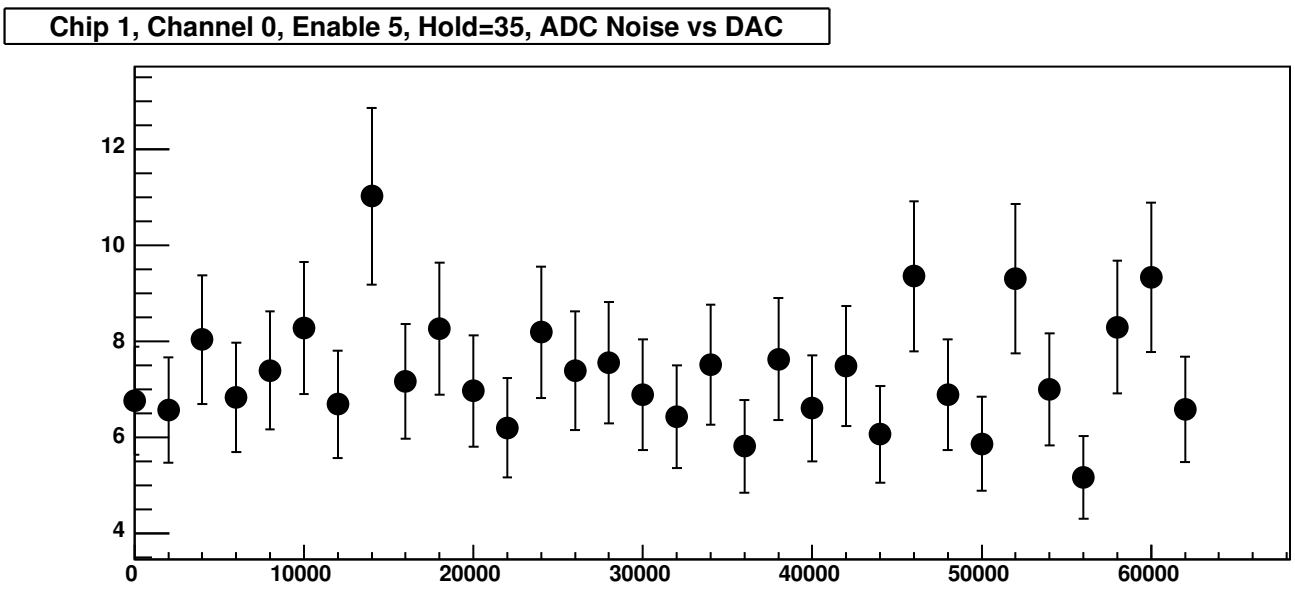
Chip 1, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

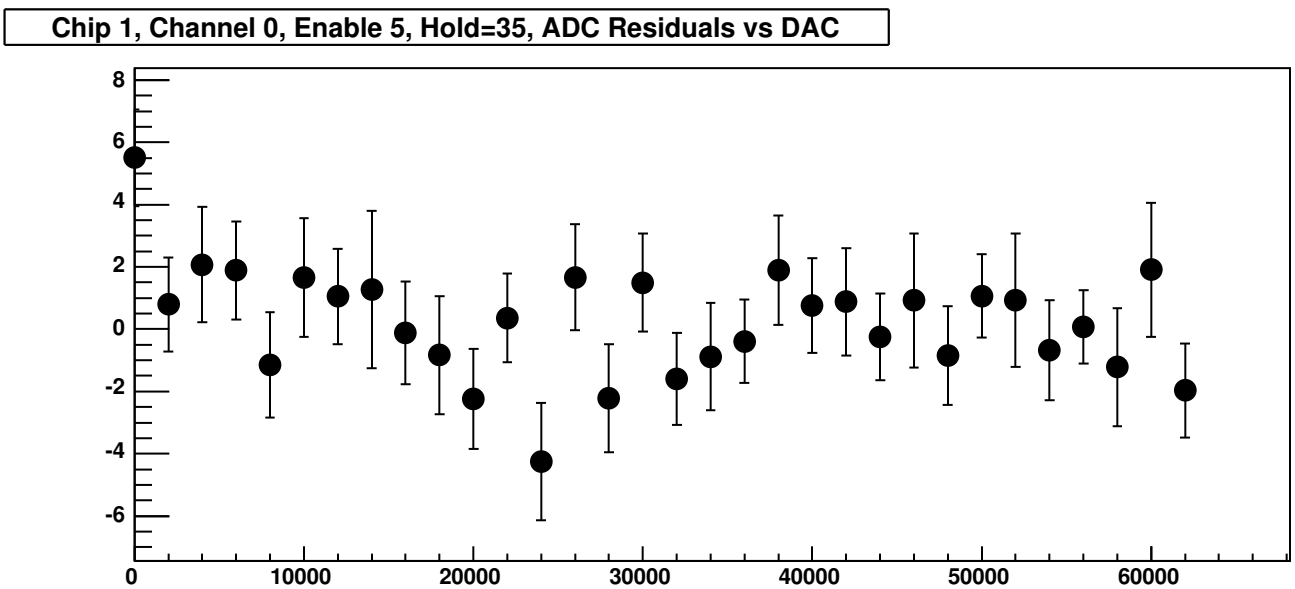


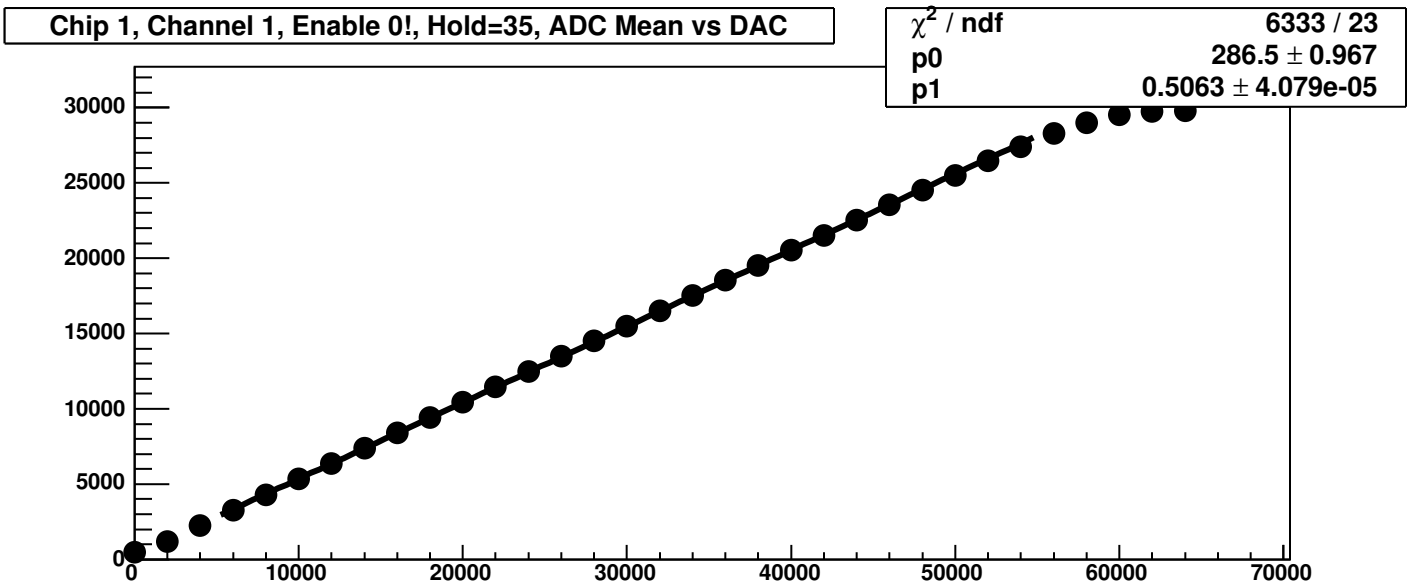
Chip 1, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

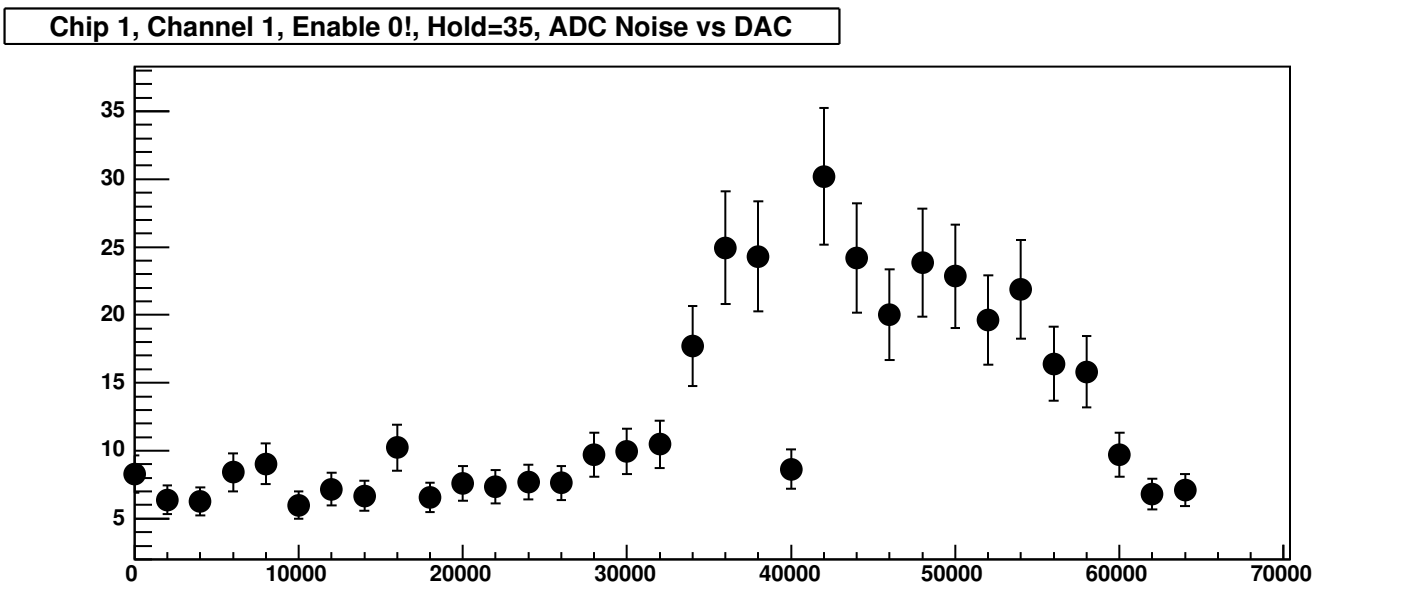


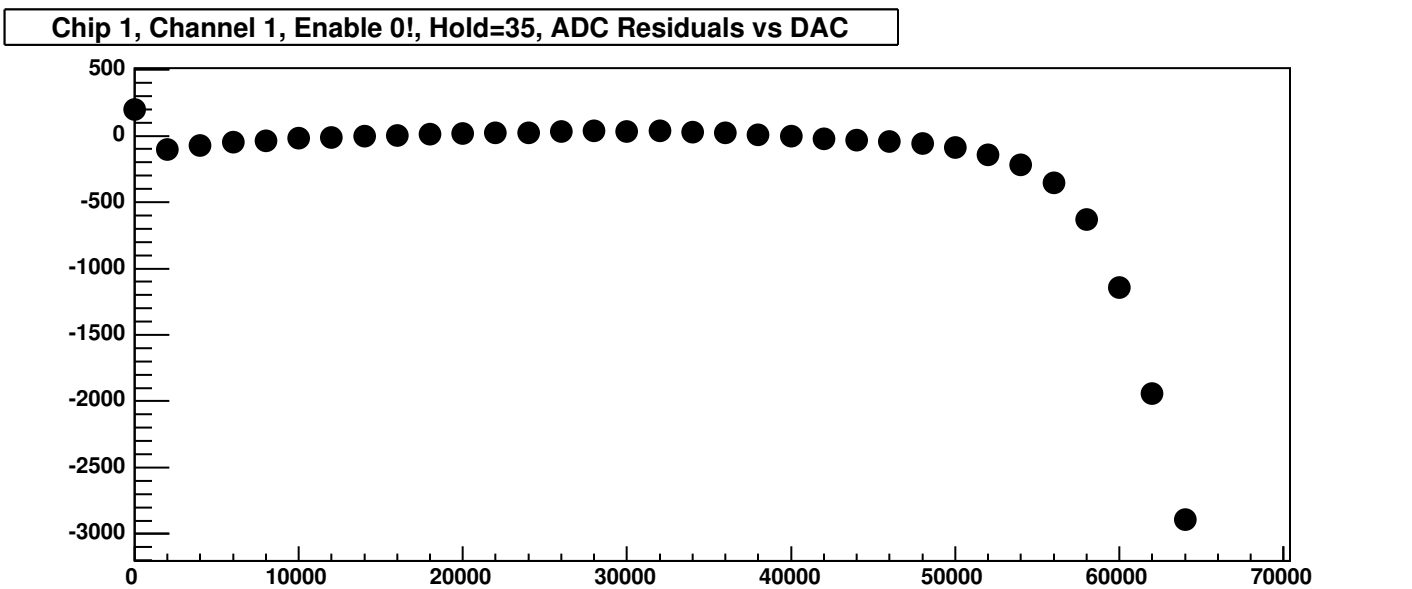

Chip 1, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 18.9 / 23
p0 132.6 ± 0.7835
p1 0.002911 ± 2.317e-05
320
300
280
260
240
220
200
180
160
140
120
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000

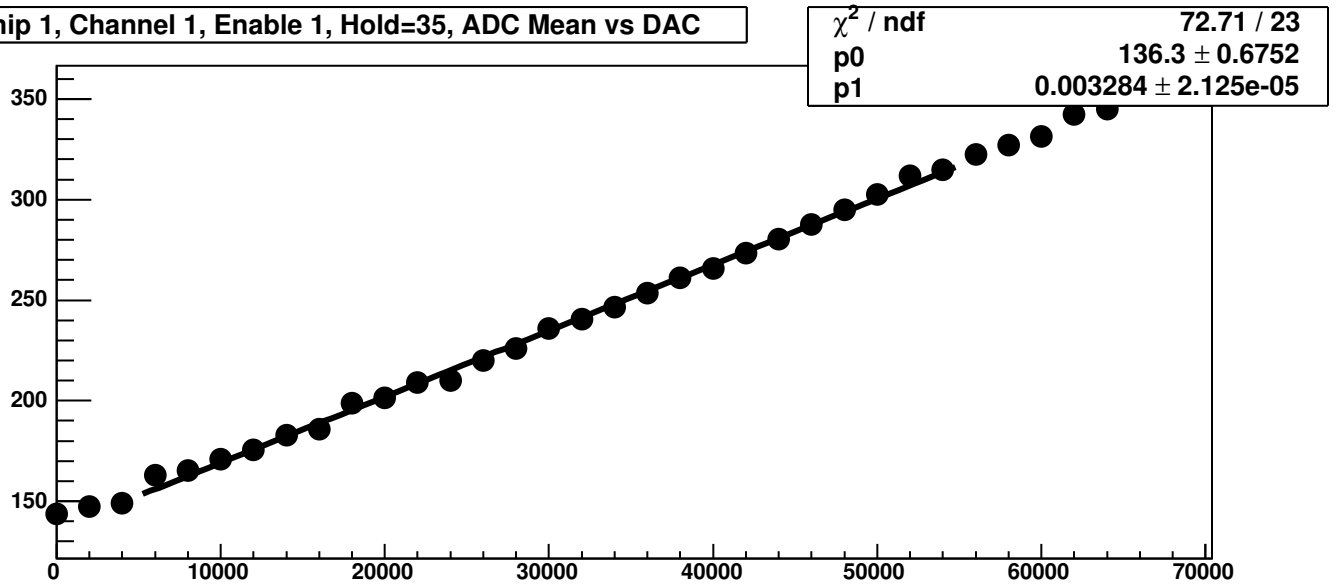

Chip 1, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 6333 / 23
p0 286.5 ± 0.967
p1 0.5063 ± 4.079e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

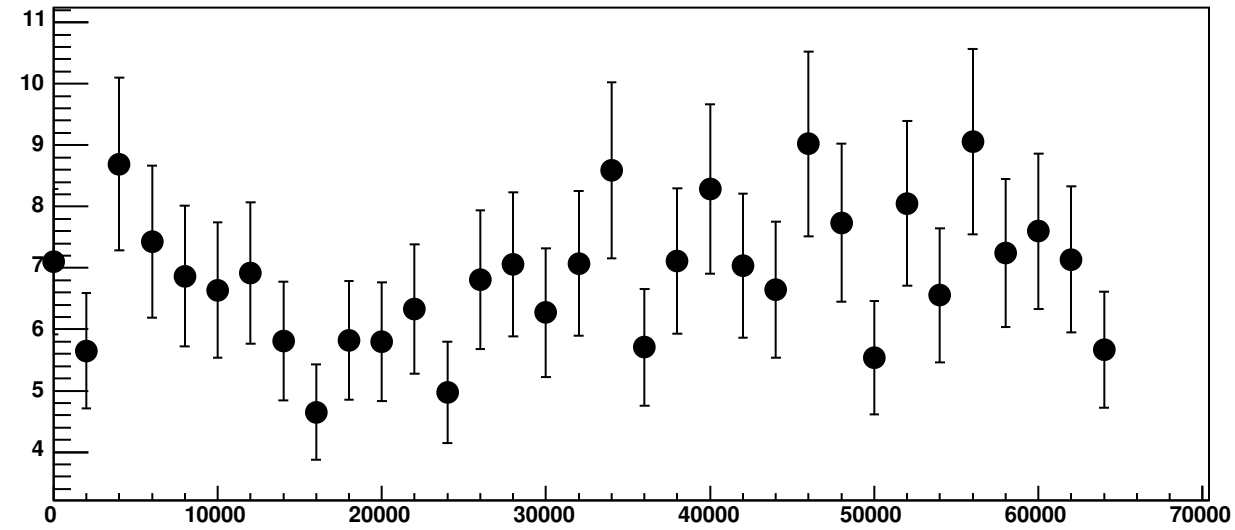

Chip 1, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
10000
20000
30000
40000
50000
60000
70000

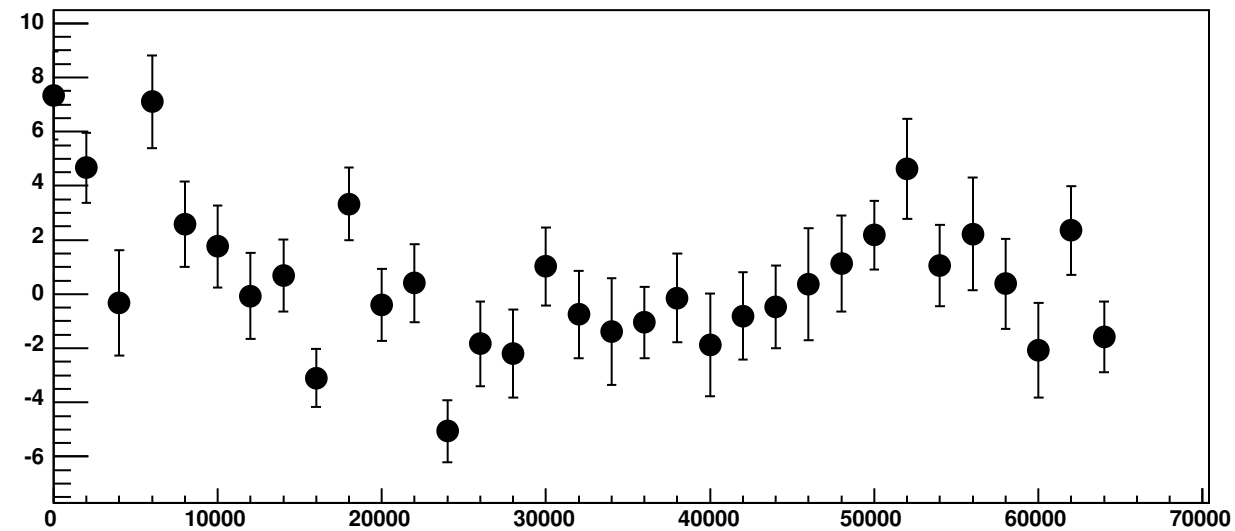
Chip 1, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

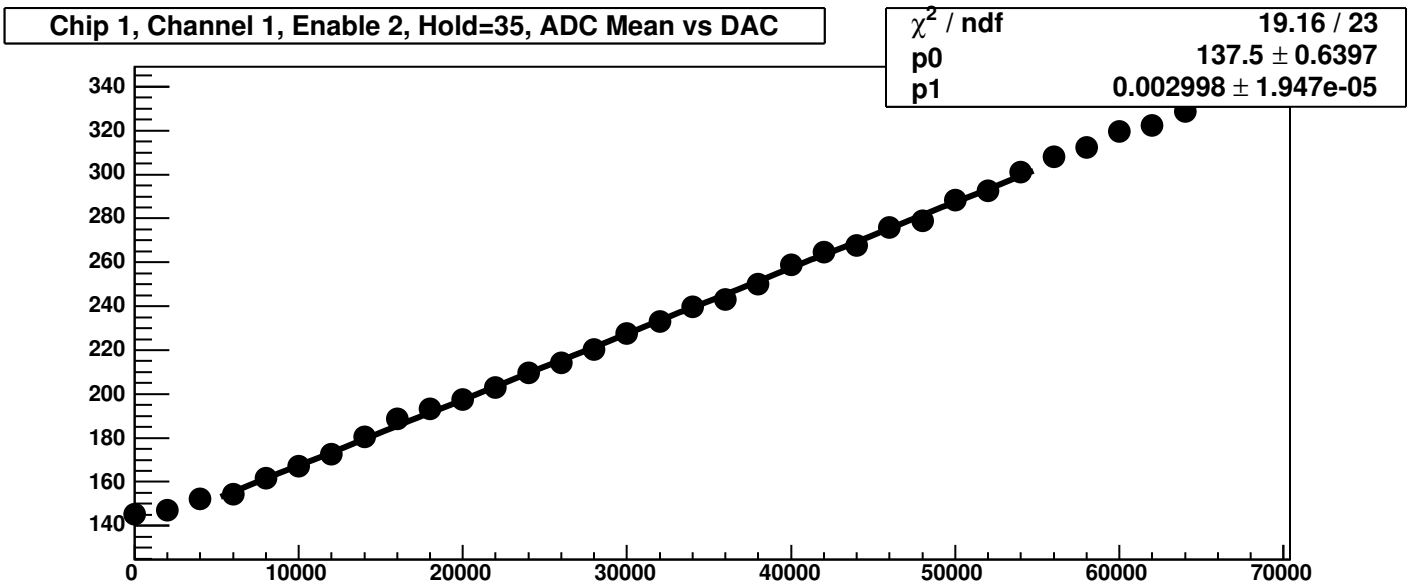


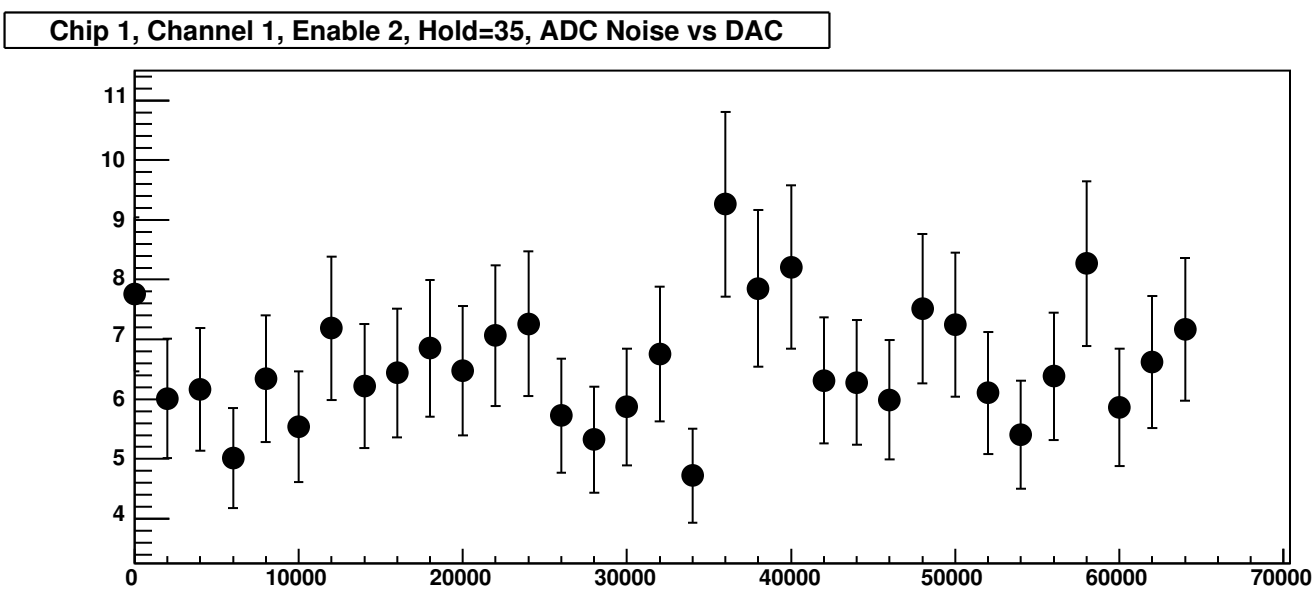
Chip 1, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC

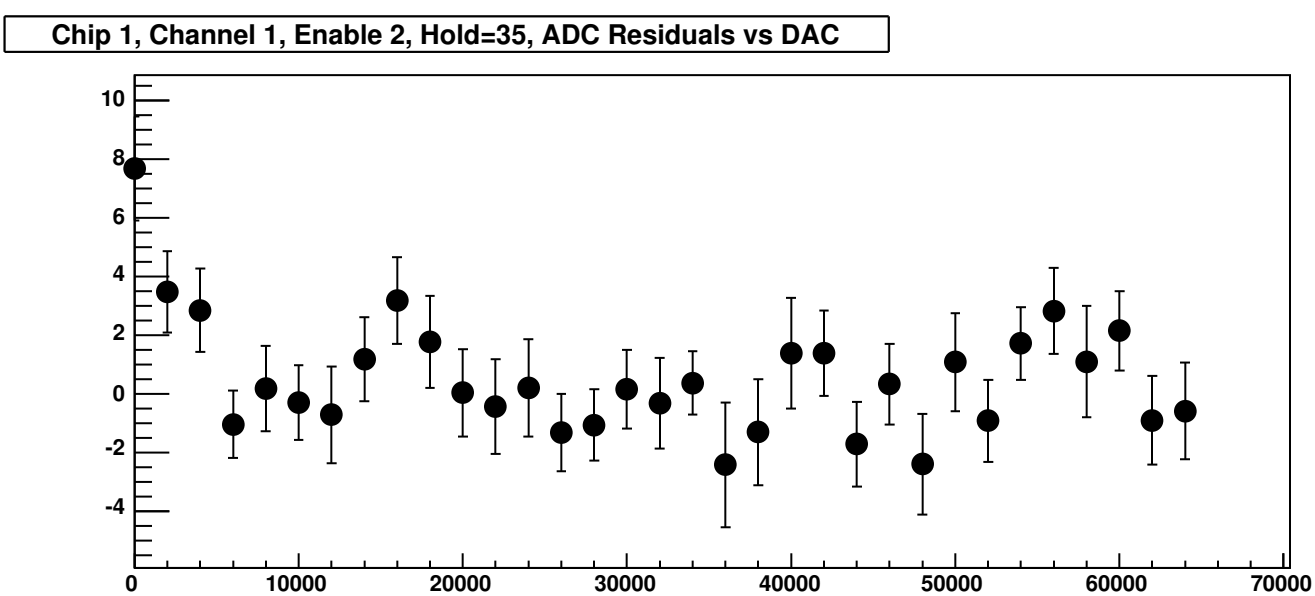


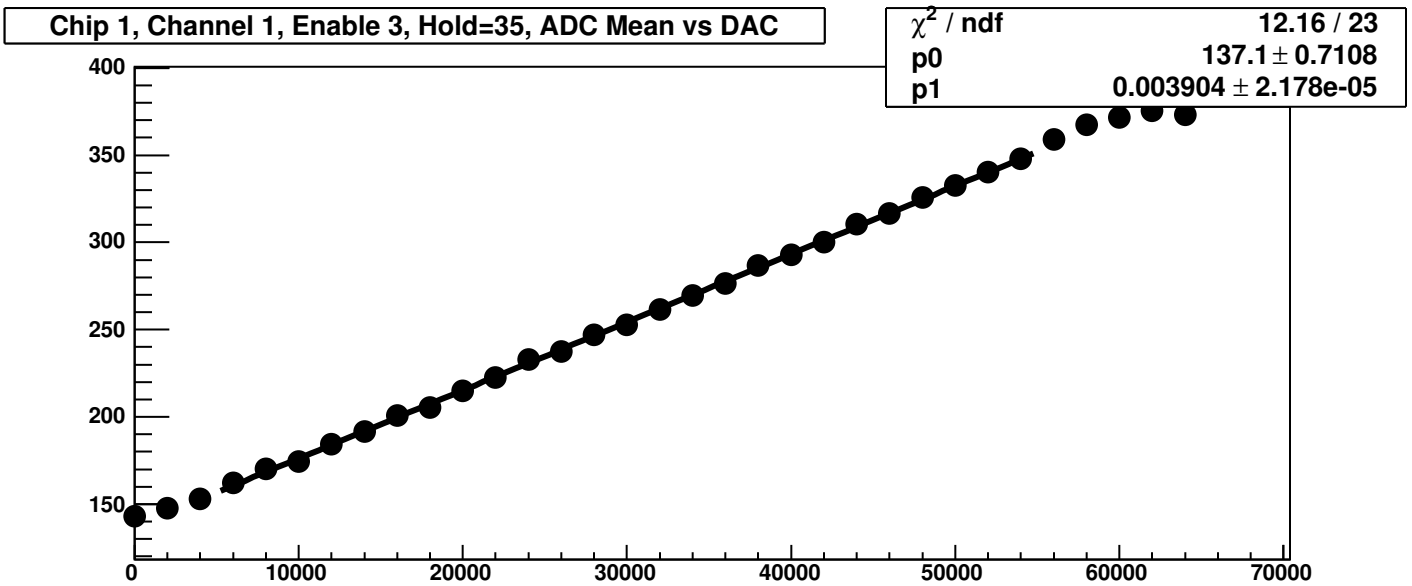
Chip 1, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

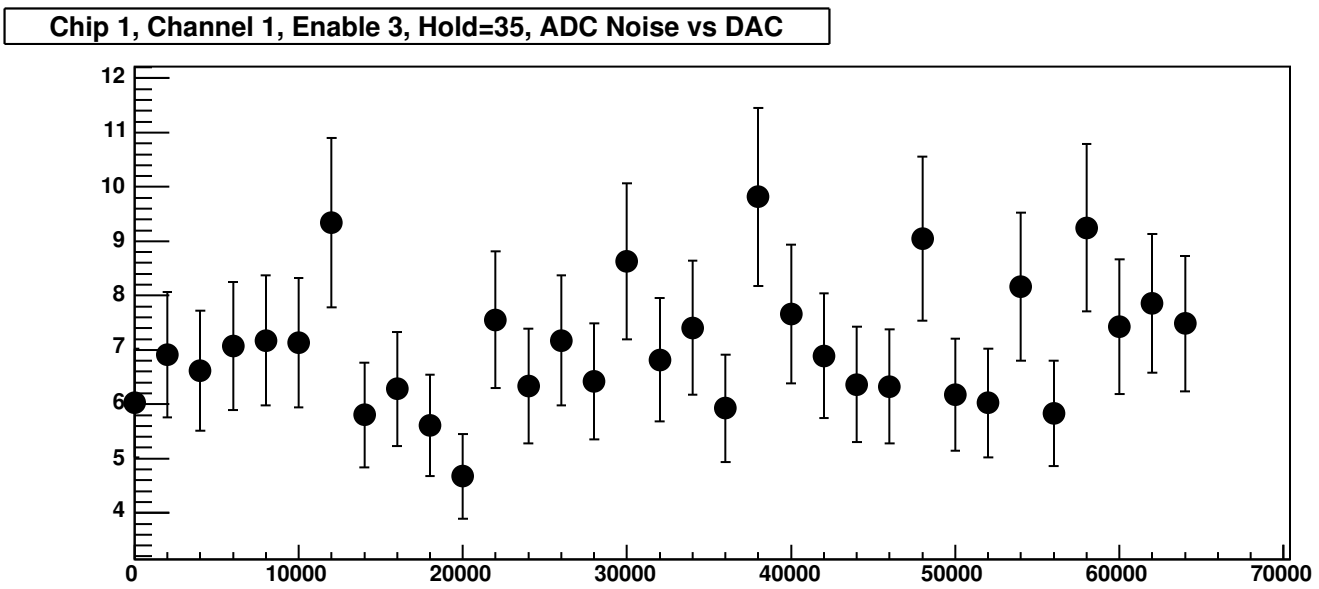


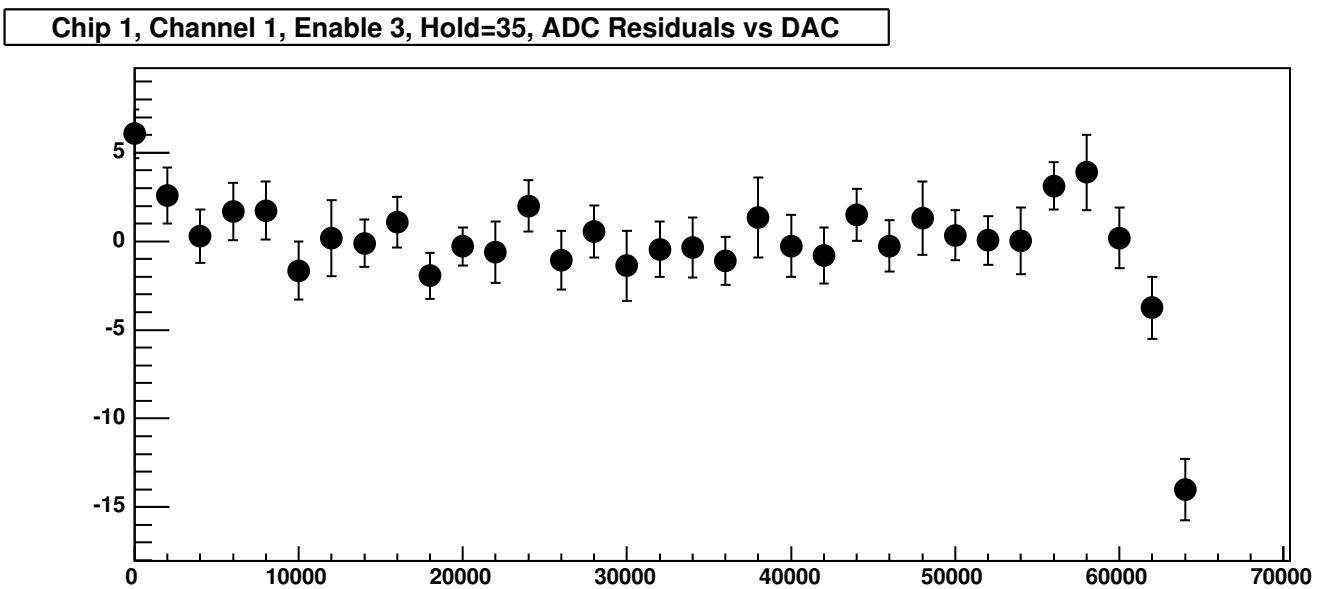

Chip 1, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 19.16 / 23
p0 137.5 ± 0.6397
p1 0.002998 ± 1.947e-05
340
320
300
280
260
240
220
200
180
160
140
0
10000
20000
30000
40000
50000
60000
70000

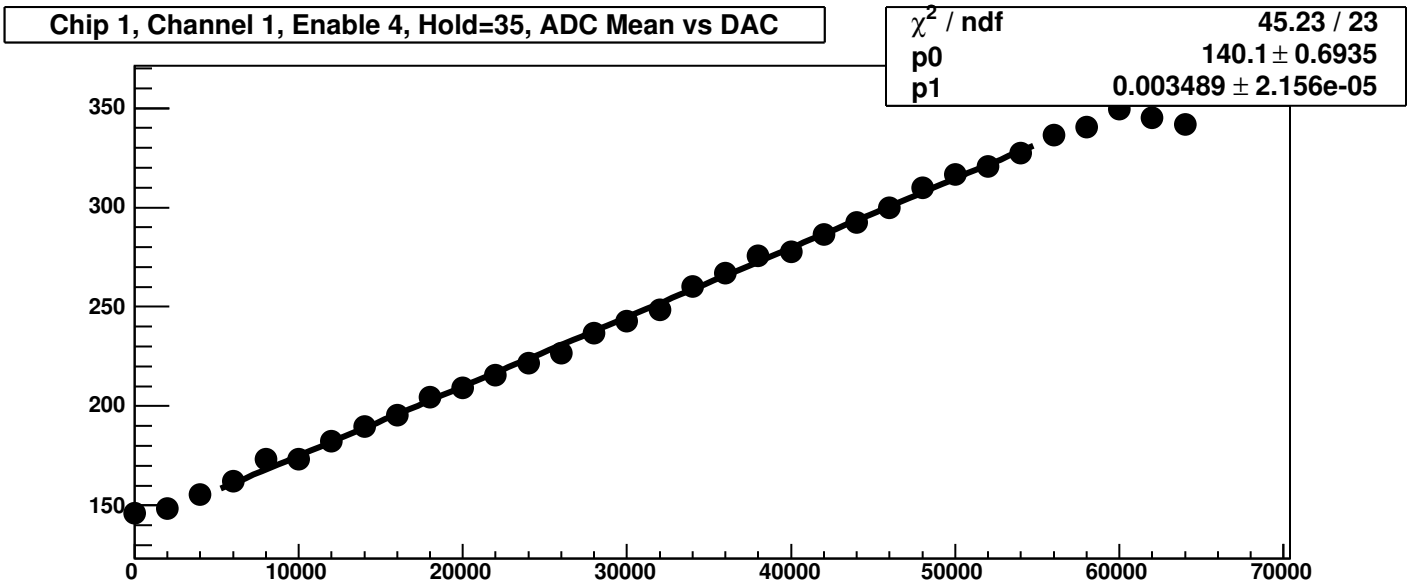

Chip 1, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

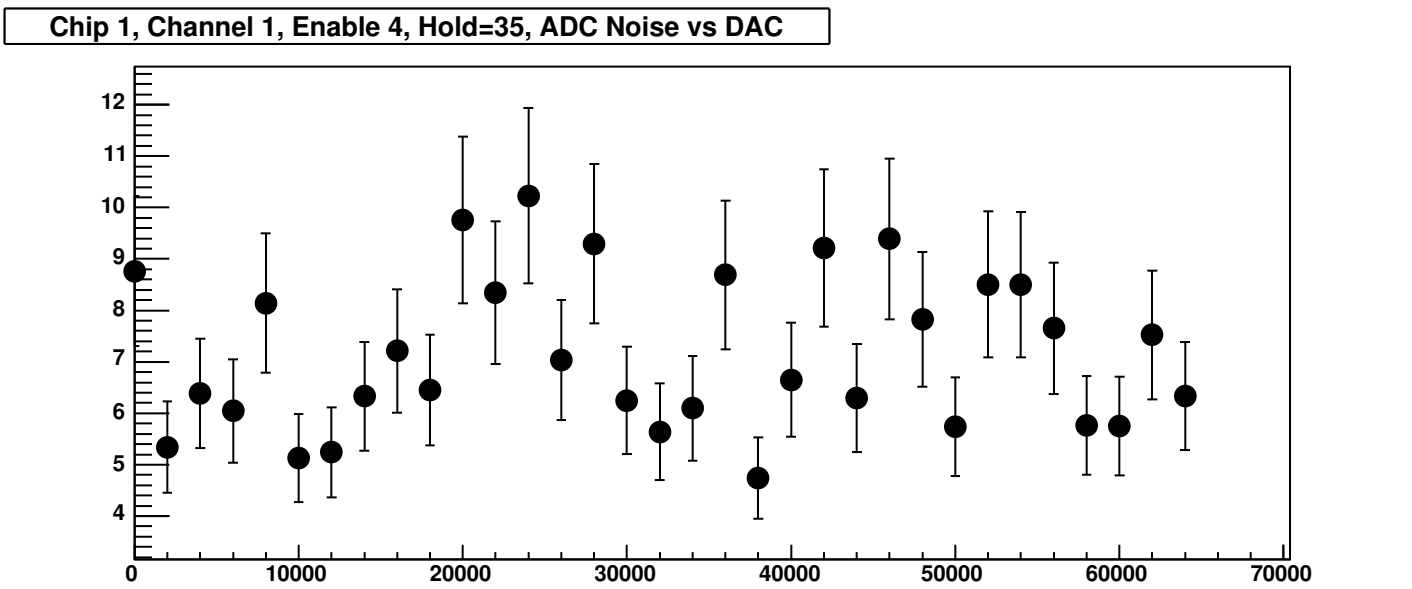

Chip 1, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
0
10000
20000
30000
40000
50000
60000
70000

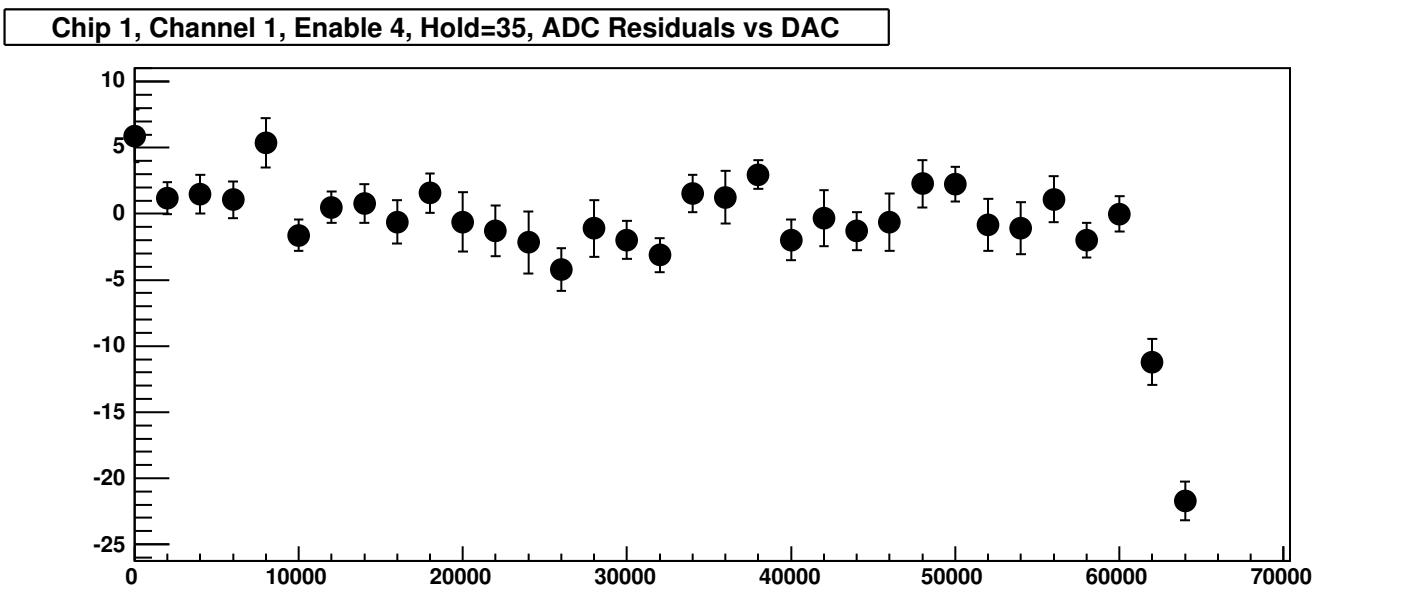

Chip 1, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 12.16 / 23
p0 137.1 ± 0.7108
p1 0.003904 ± 2.178e-05
400
350
300
250
200
150
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

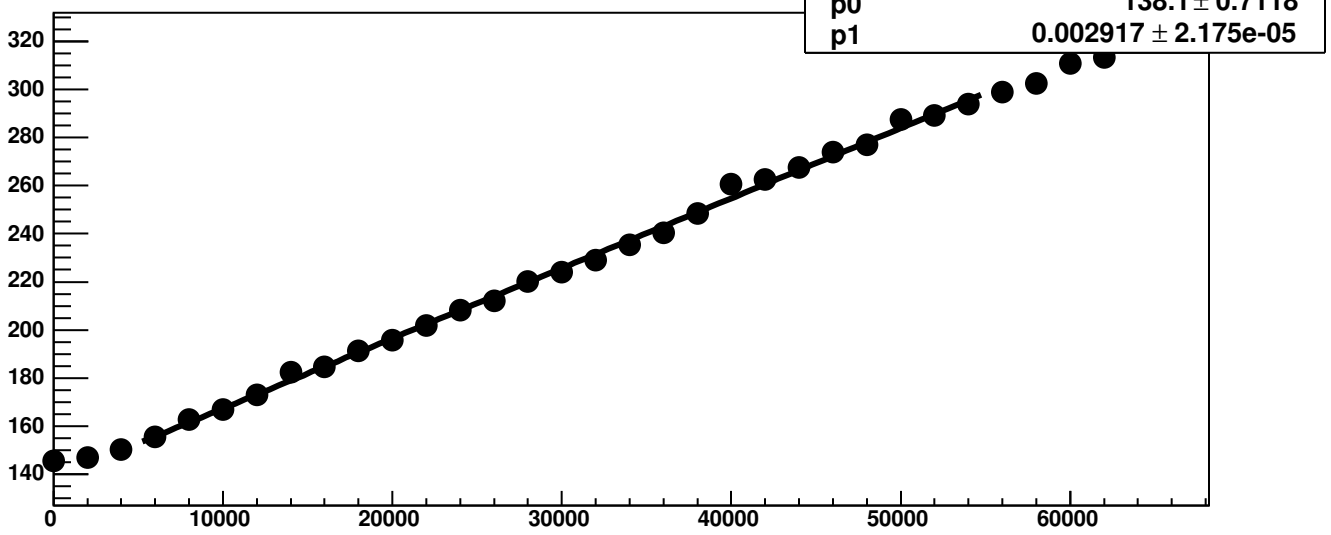

Chip 1, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 45.23 / 23
p0 140.1 ± 0.6935
p1 0.003489 ± 2.156e-05
350
300
250
200
150
0
10000
20000
30000
40000
50000
60000
70000

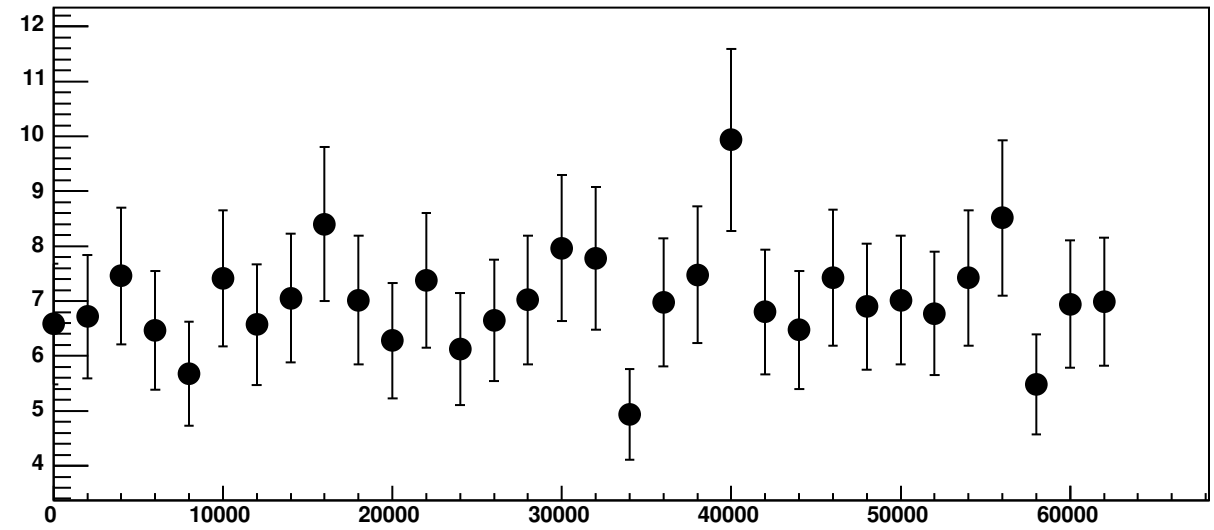

Chip 1, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
10000
20000
30000
40000
50000
60000
70000

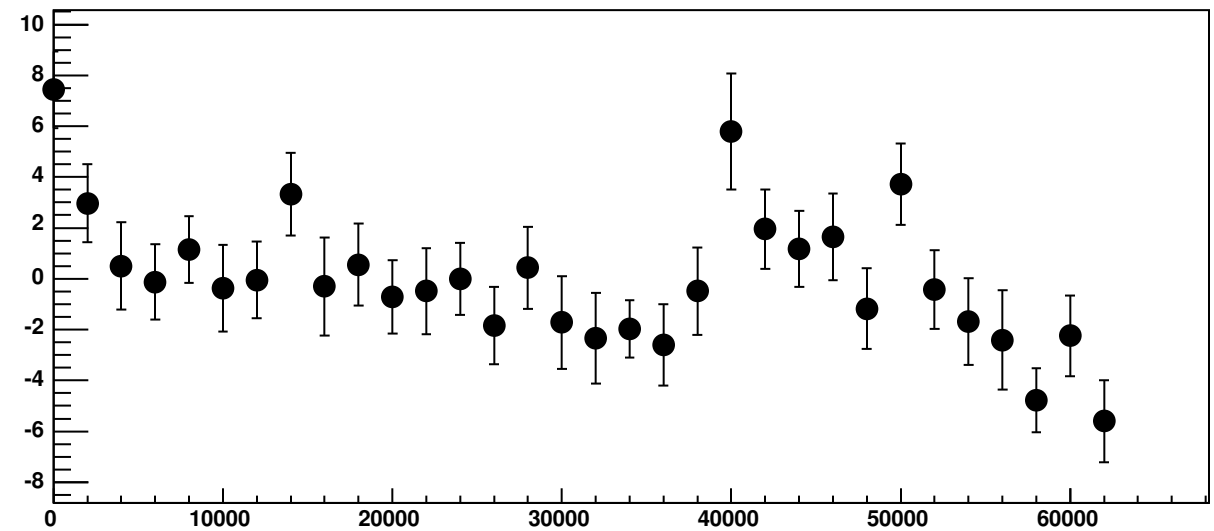
Chip 1, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC

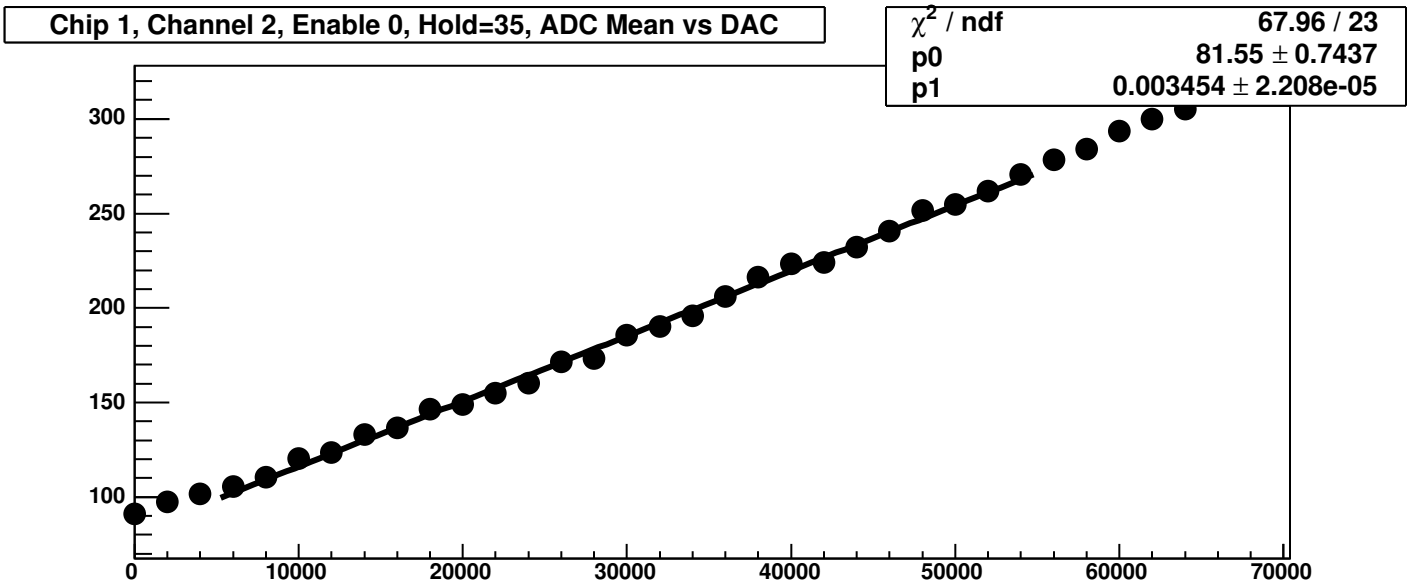


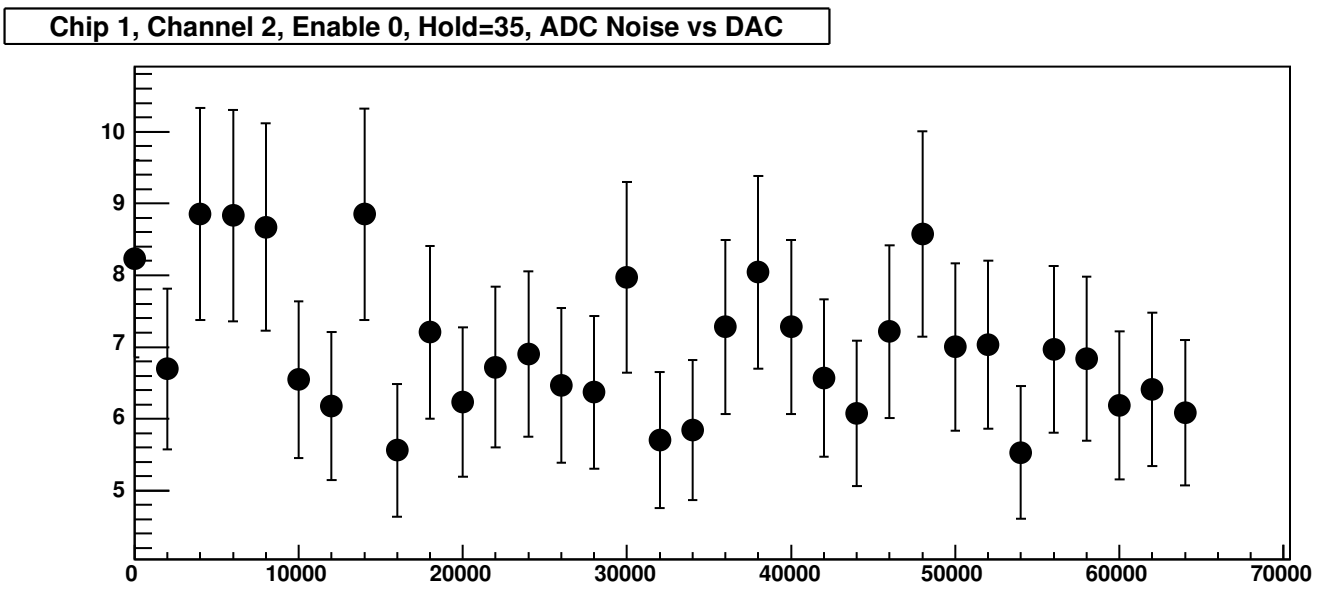
Chip 1, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC

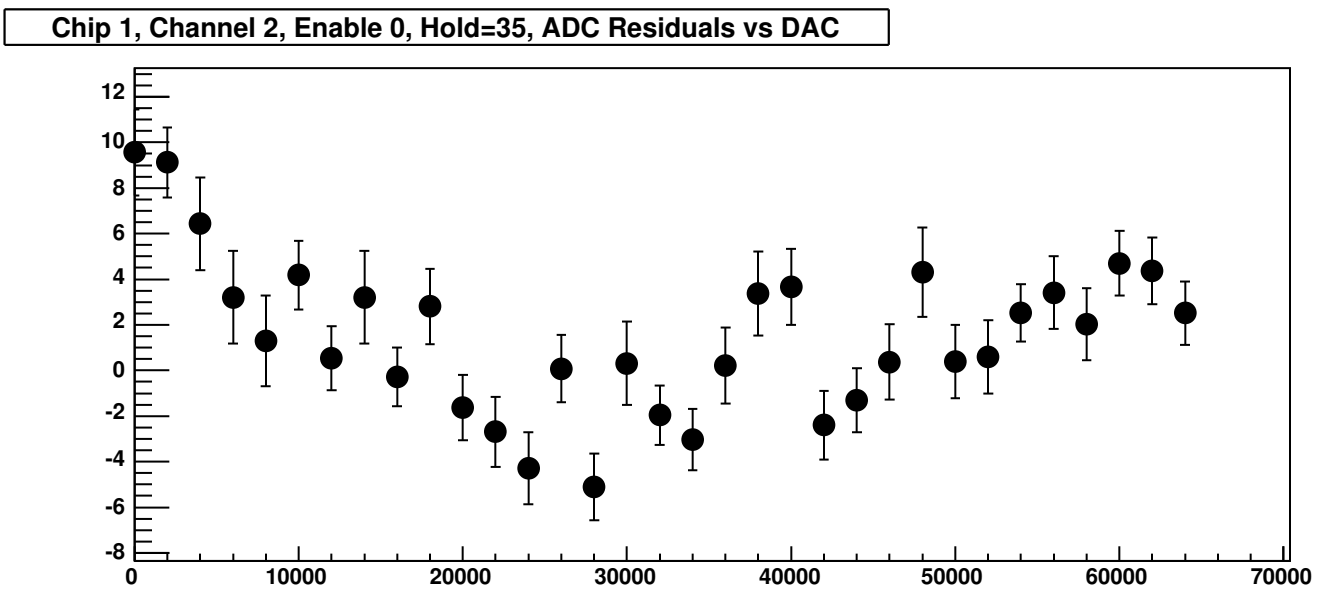


Chip 1, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC

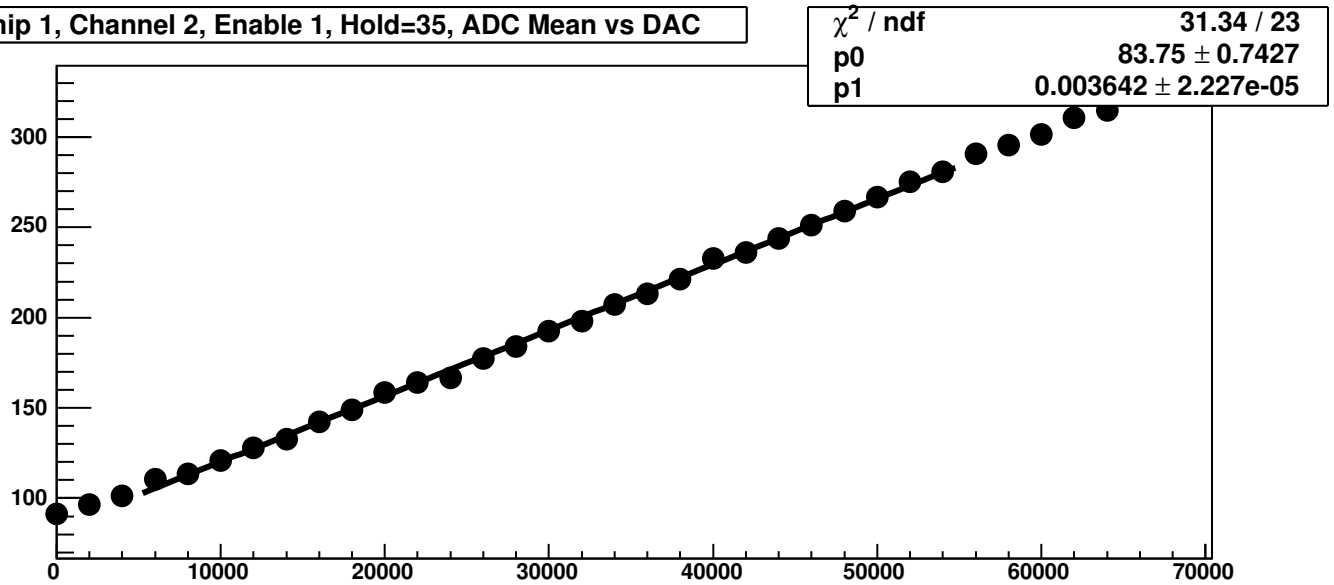



Chip 1, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 67.96 / 23
p0 81.55 ± 0.7437
p1 0.003454 ± 2.208e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

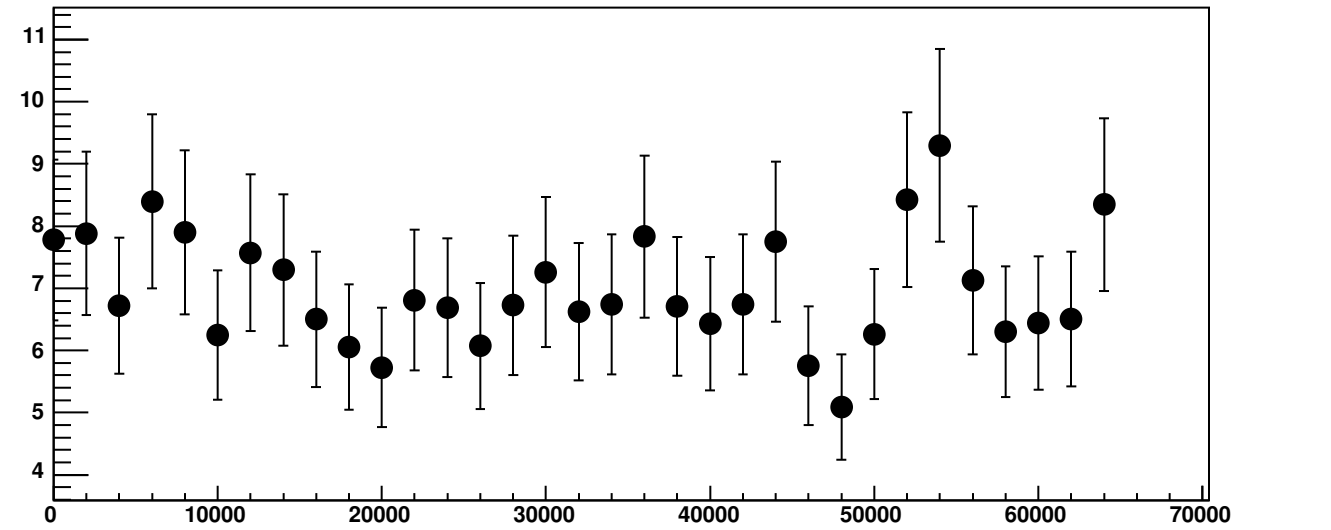

Chip 1, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

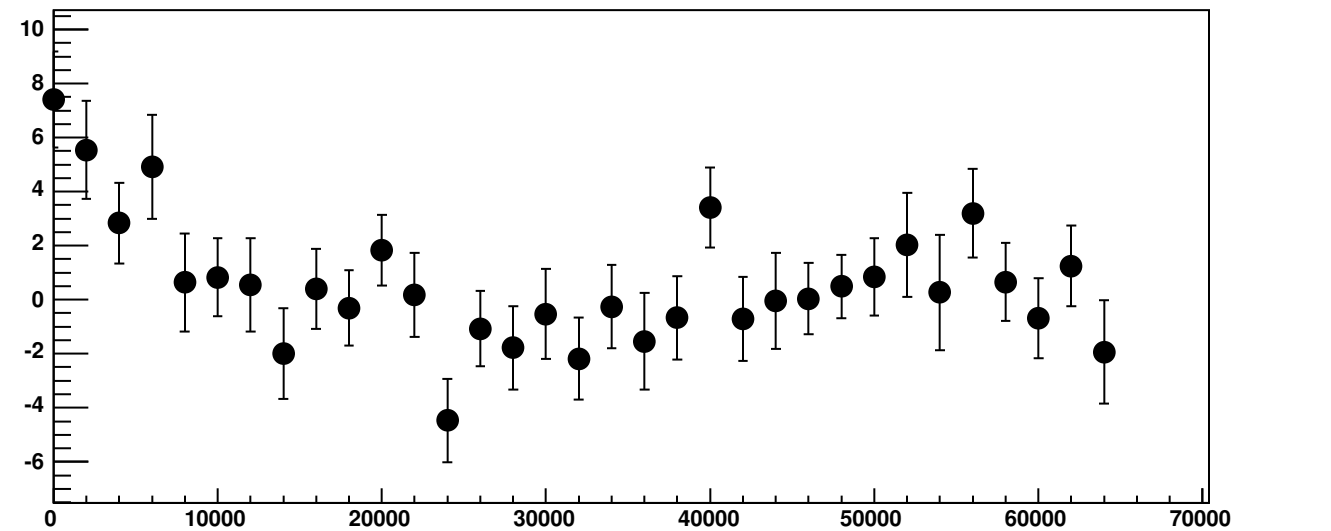
Chip 1, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

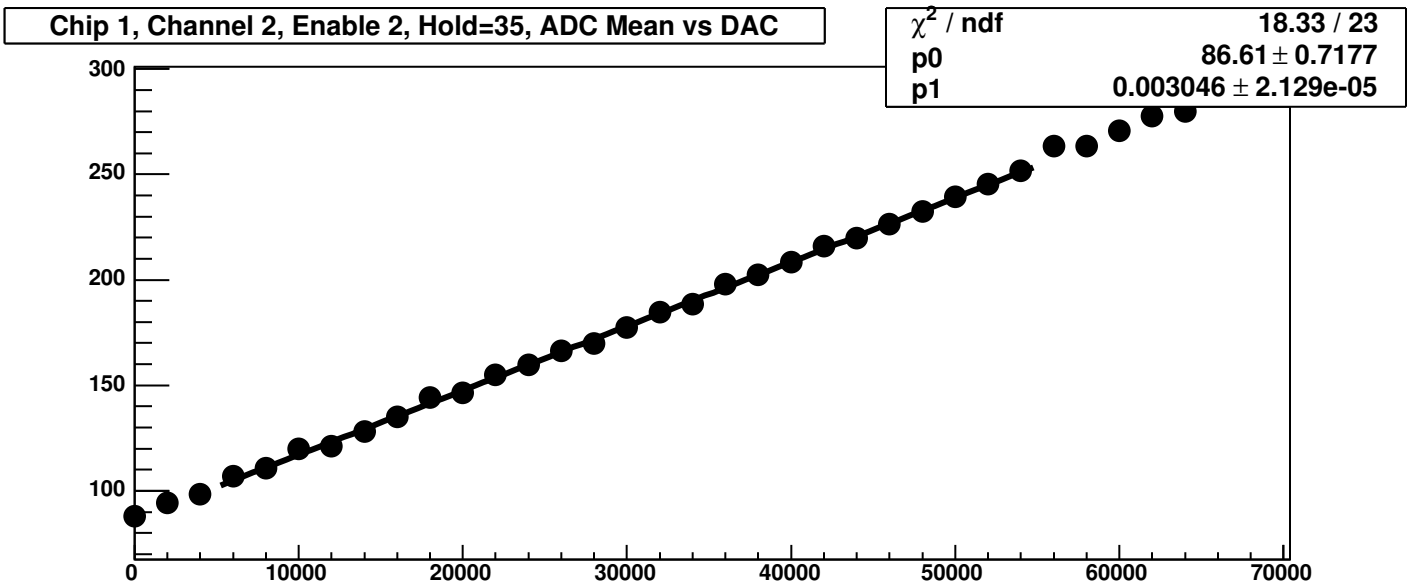


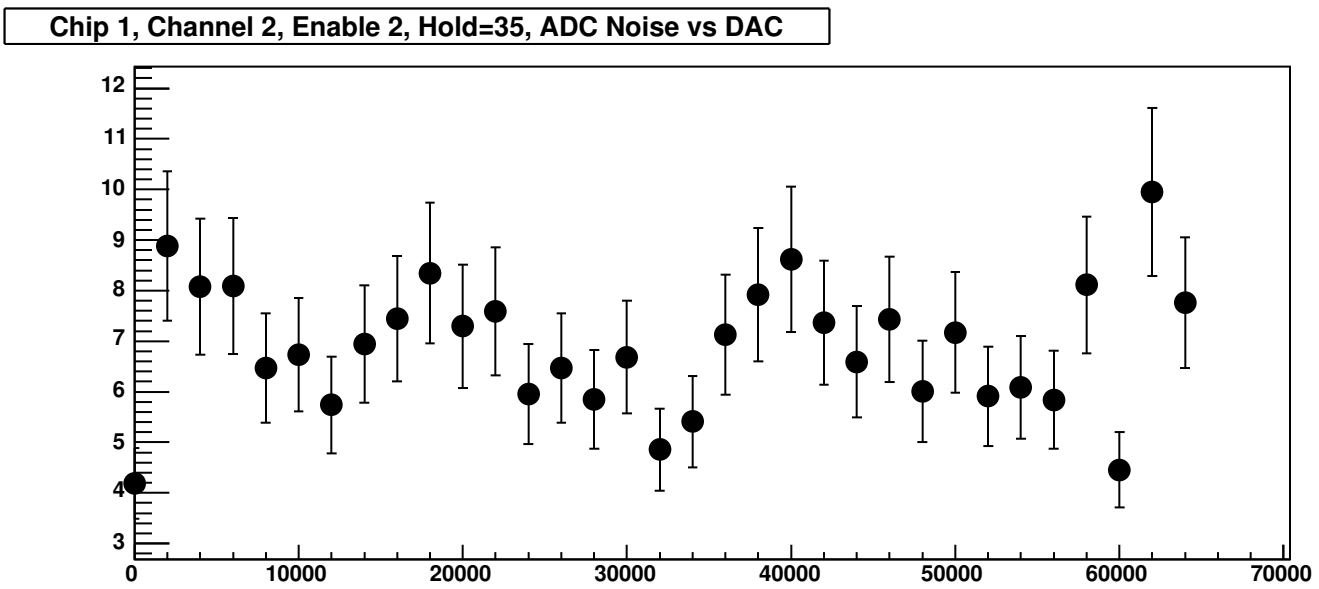
Chip 1, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC

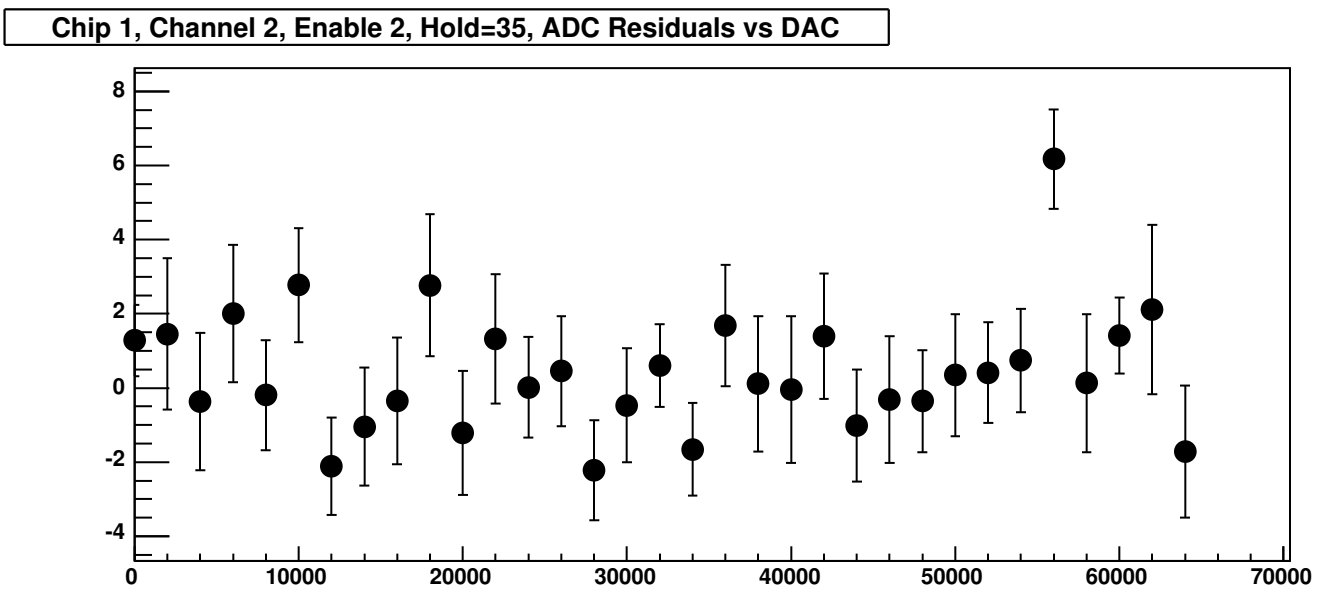


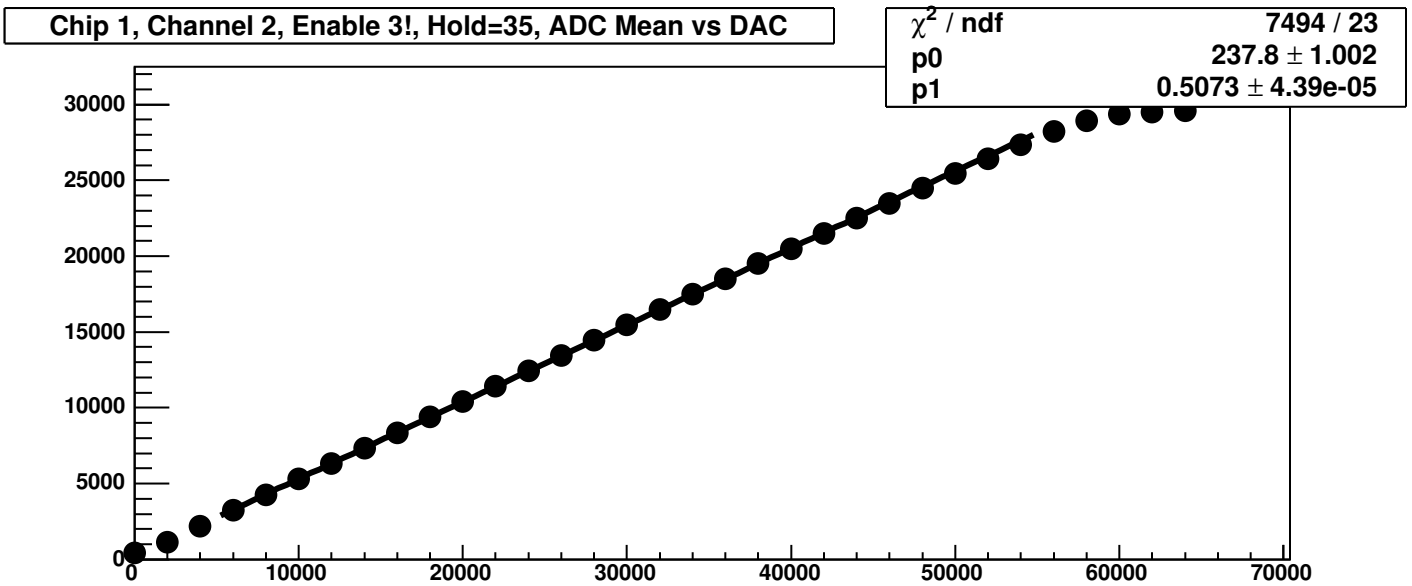
Chip 1, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

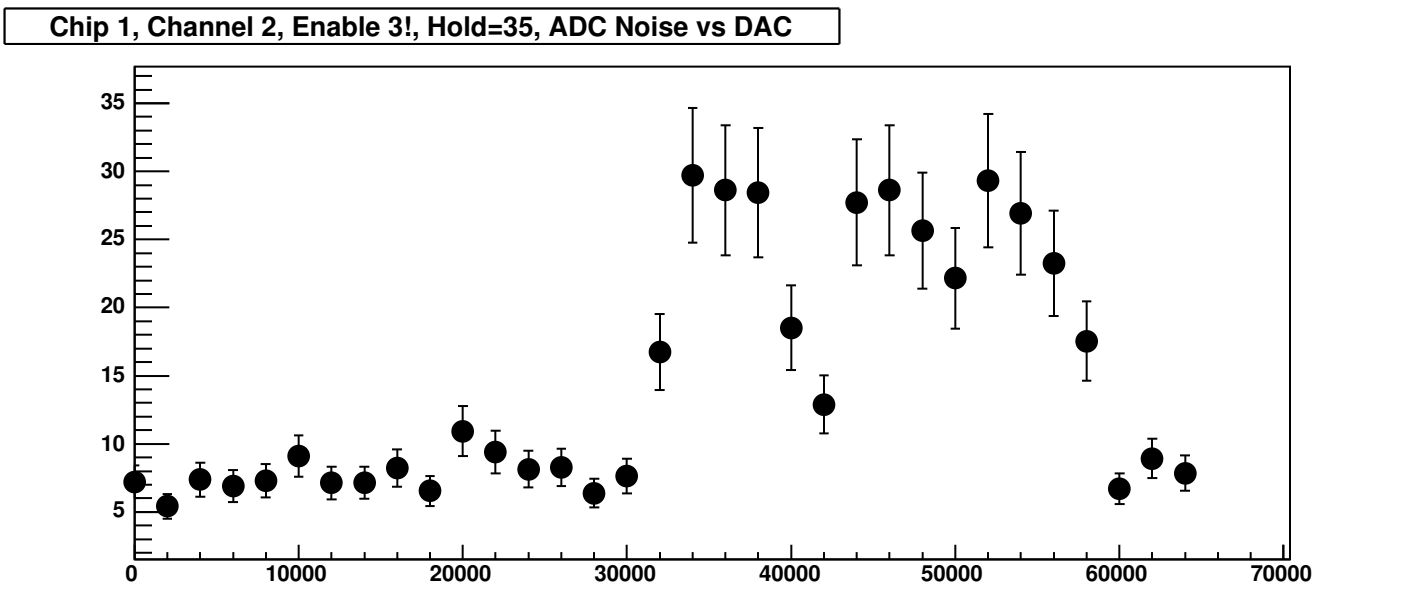


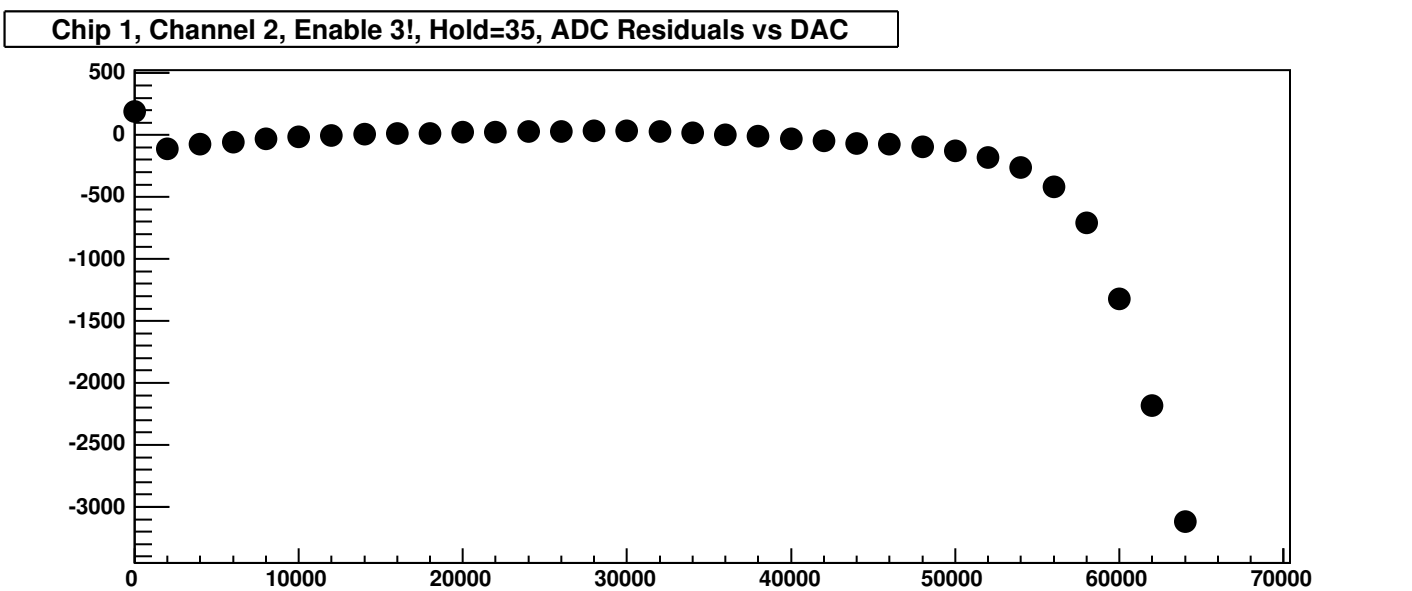

Chip 1, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 18.33 / 23
p0 86.61 ± 0.7177
p1 0.003046 ± 2.129e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

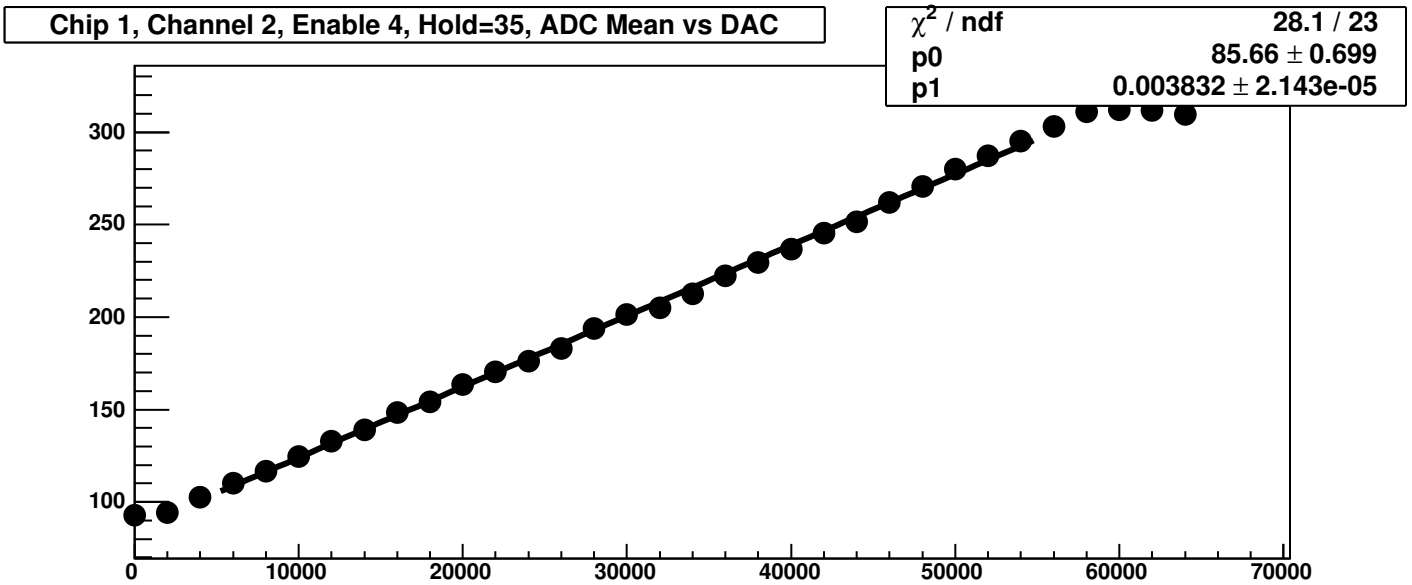

Chip 1, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

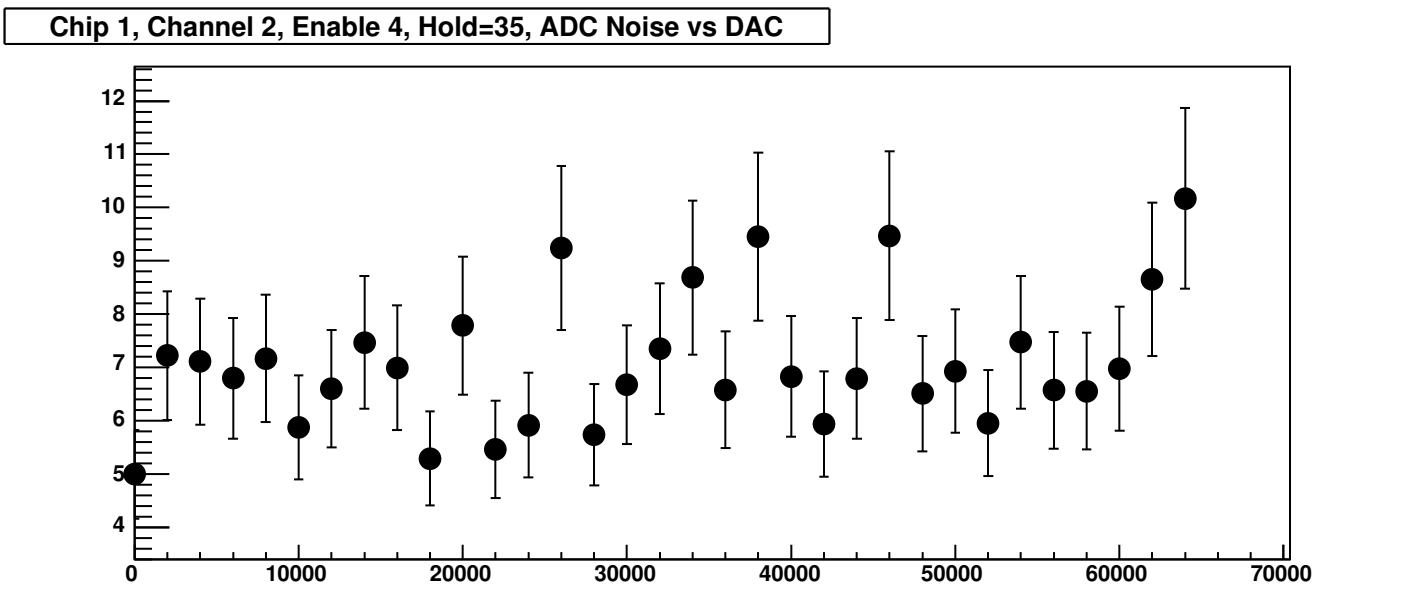

Chip 1, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
0
10000
20000
30000
40000
50000
60000
70000

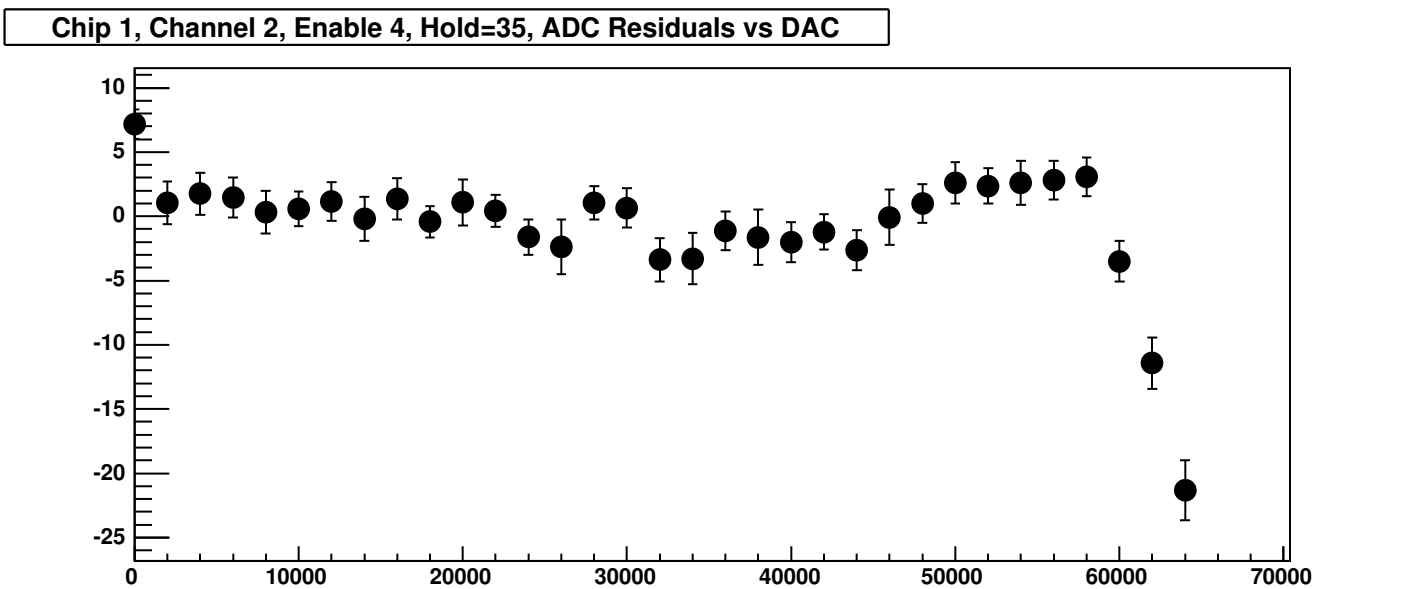

Chip 1, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 7494 / 23
p0 237.8 ± 1.002
p1 0.5073 ± 4.39e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

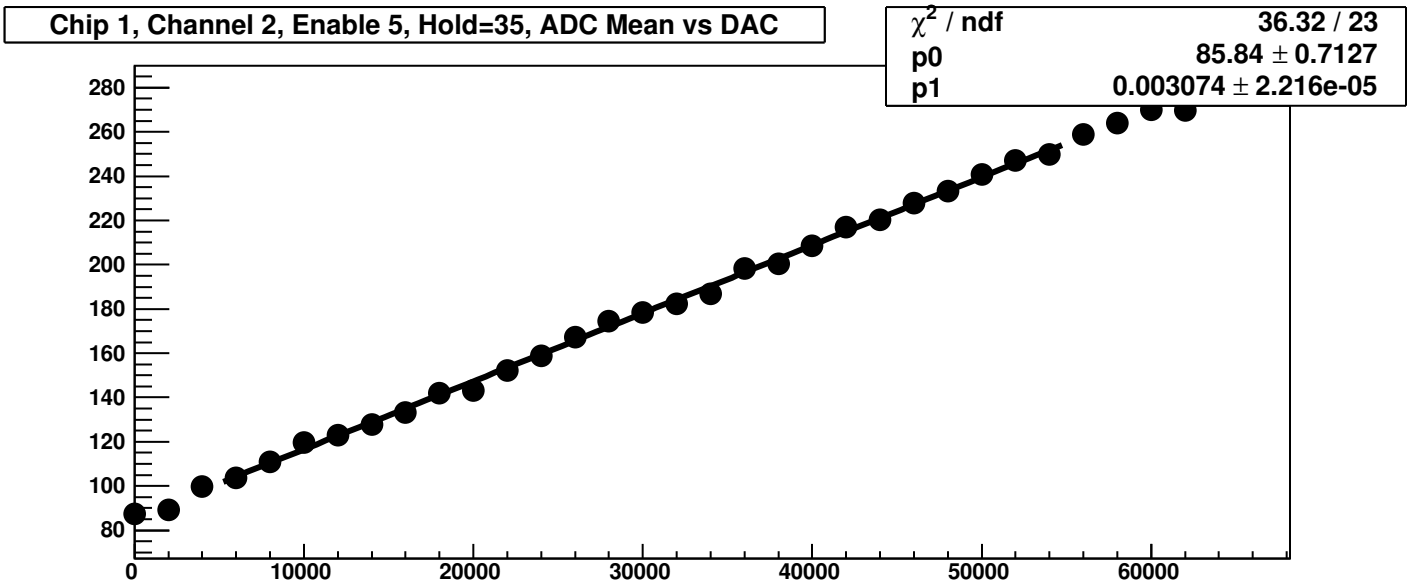

Chip 1, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000

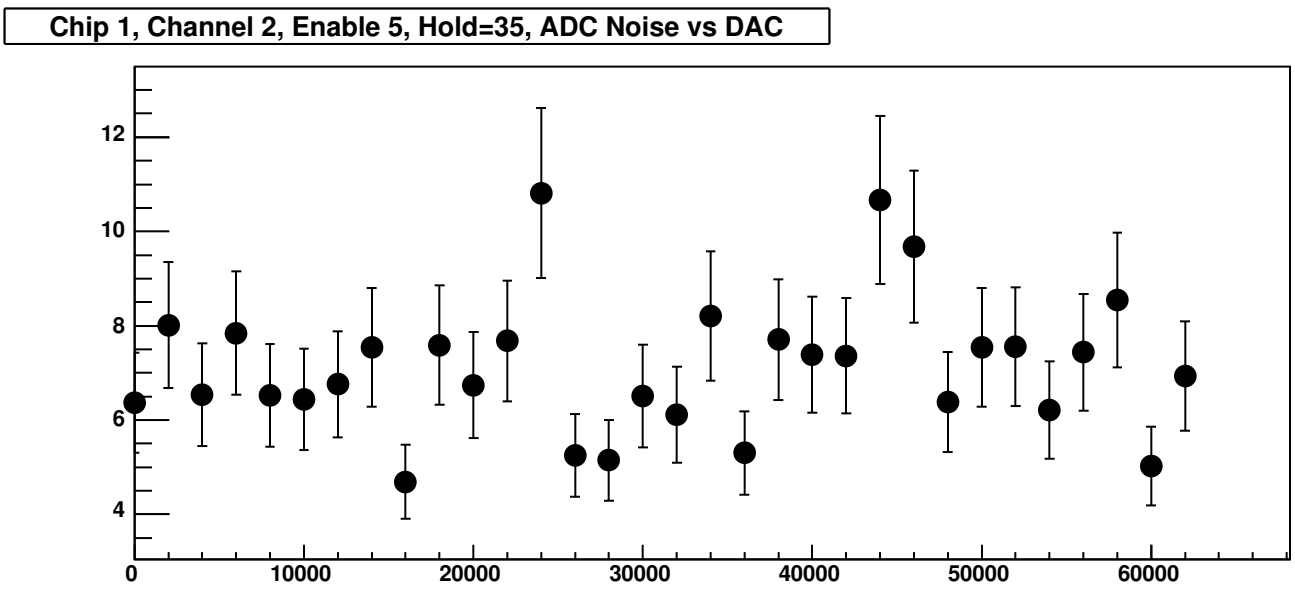

Chip 1, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
500
0
-500
-1000
-1500
-2000
-2500
-3000
0
10000
20000
30000
40000
50000
60000
70000

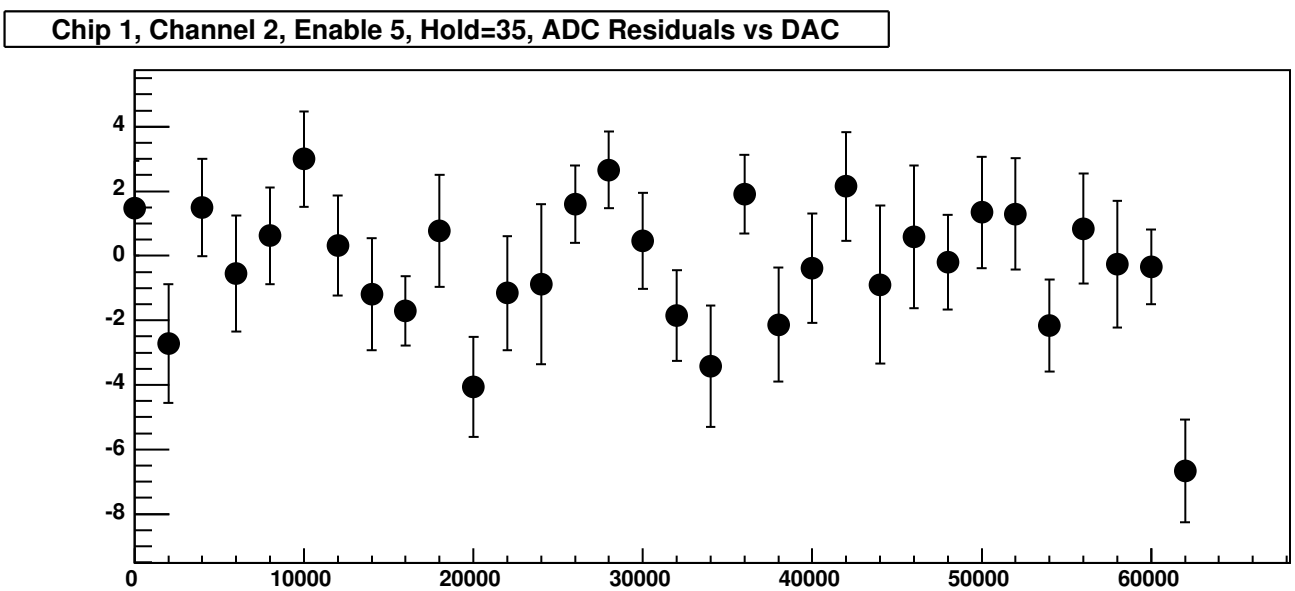

Chip 1, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 28.1 / 23
p0 85.66 ± 0.699
p1 0.003832 ± 2.143e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

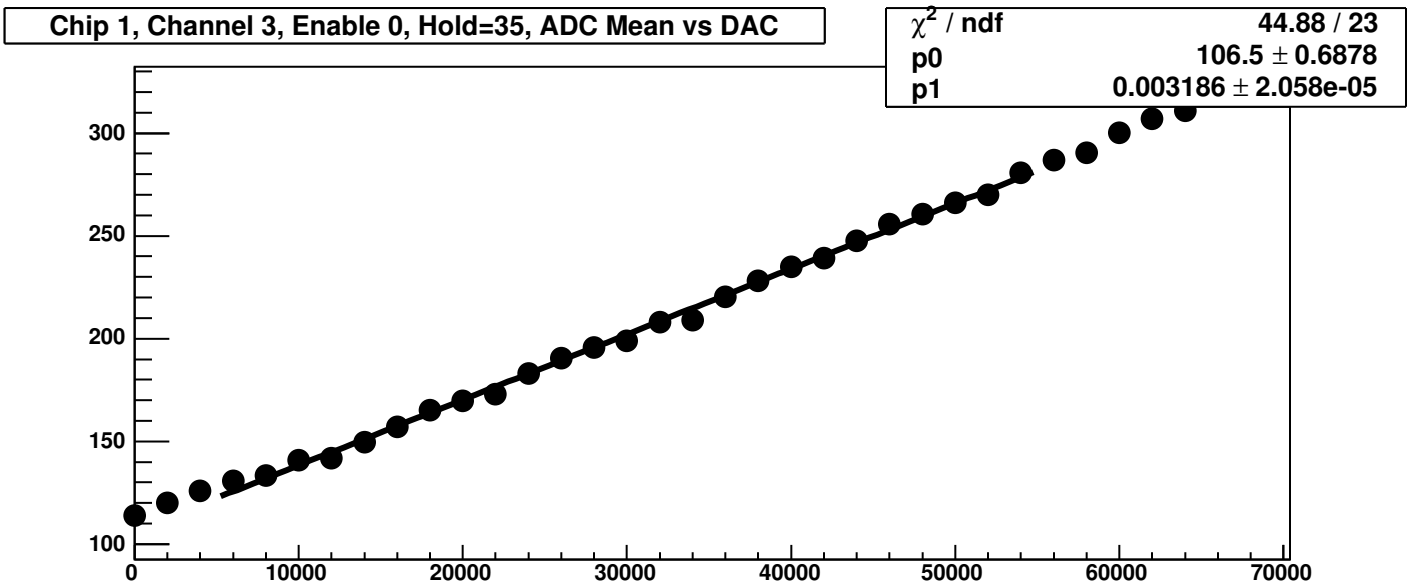

Chip 1, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

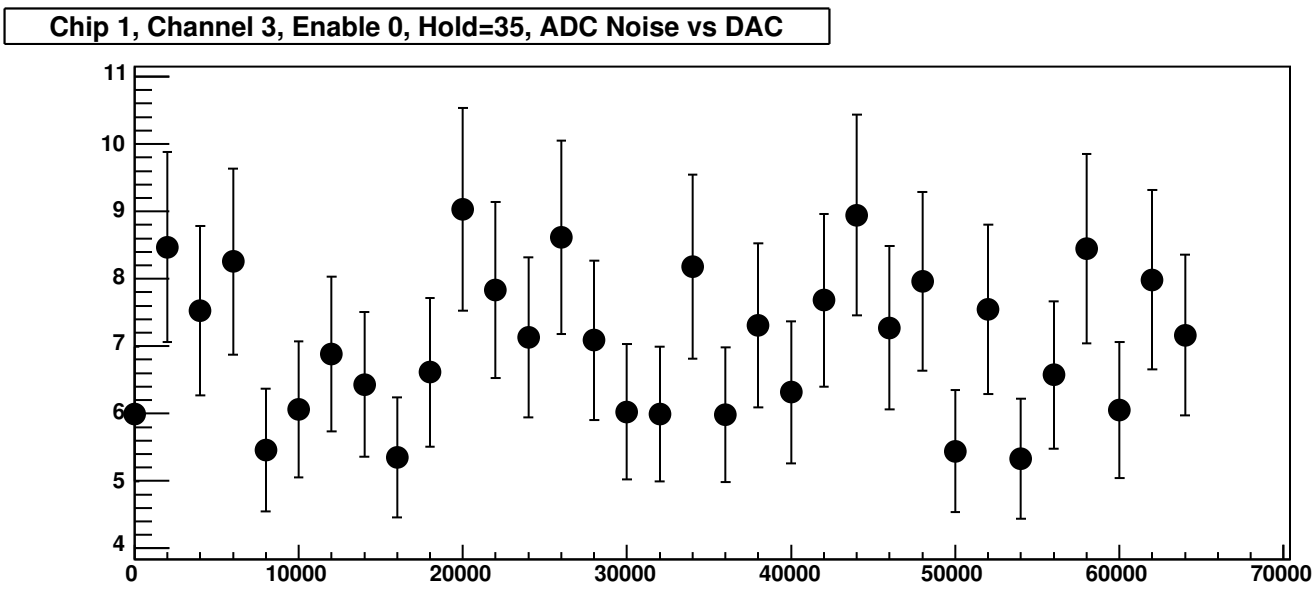

Chip 1, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
10000
20000
30000
40000
50000
60000
70000

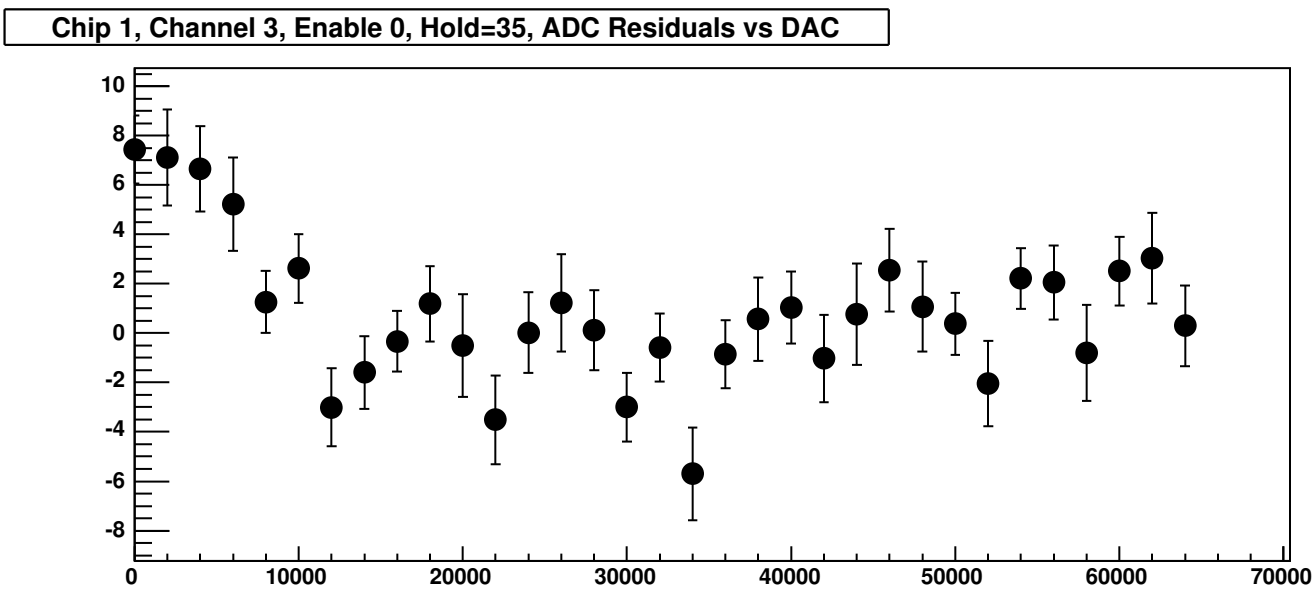

Chip 1, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 36.32 / 23
p0 85.84 ± 0.7127
p1 0.003074 ± 2.216e-05
280
260
240
220
200
180
160
140
120
100
80
0
10000
20000
30000
40000
50000
60000

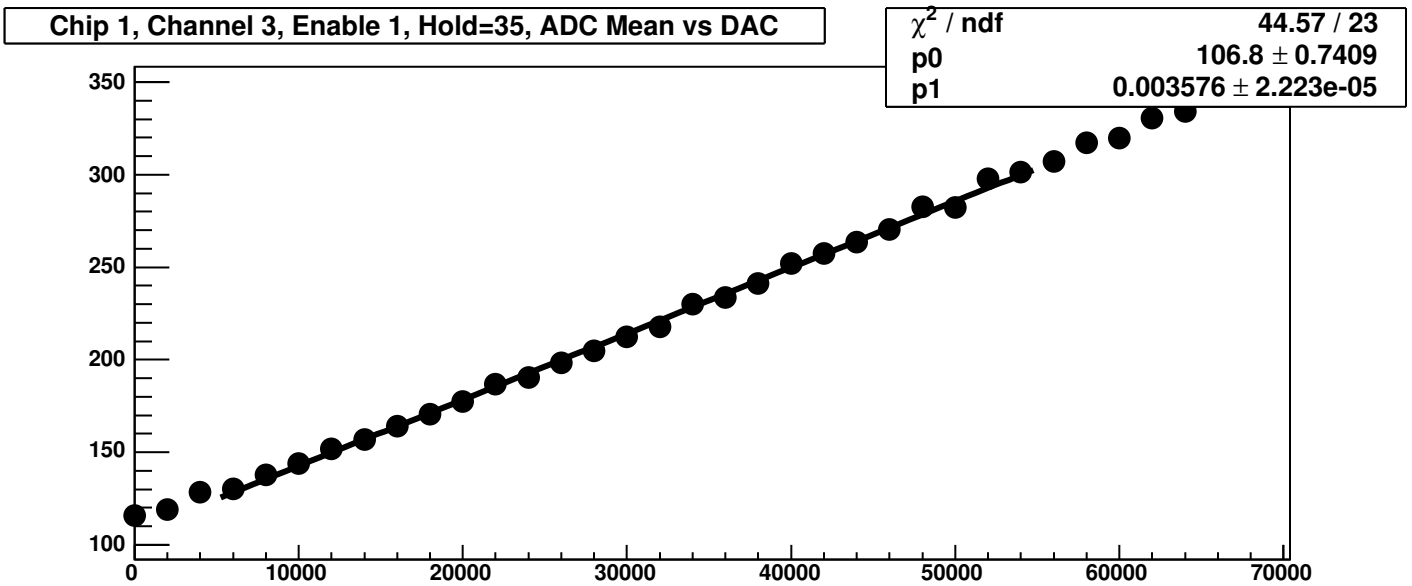

Chip 1, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000

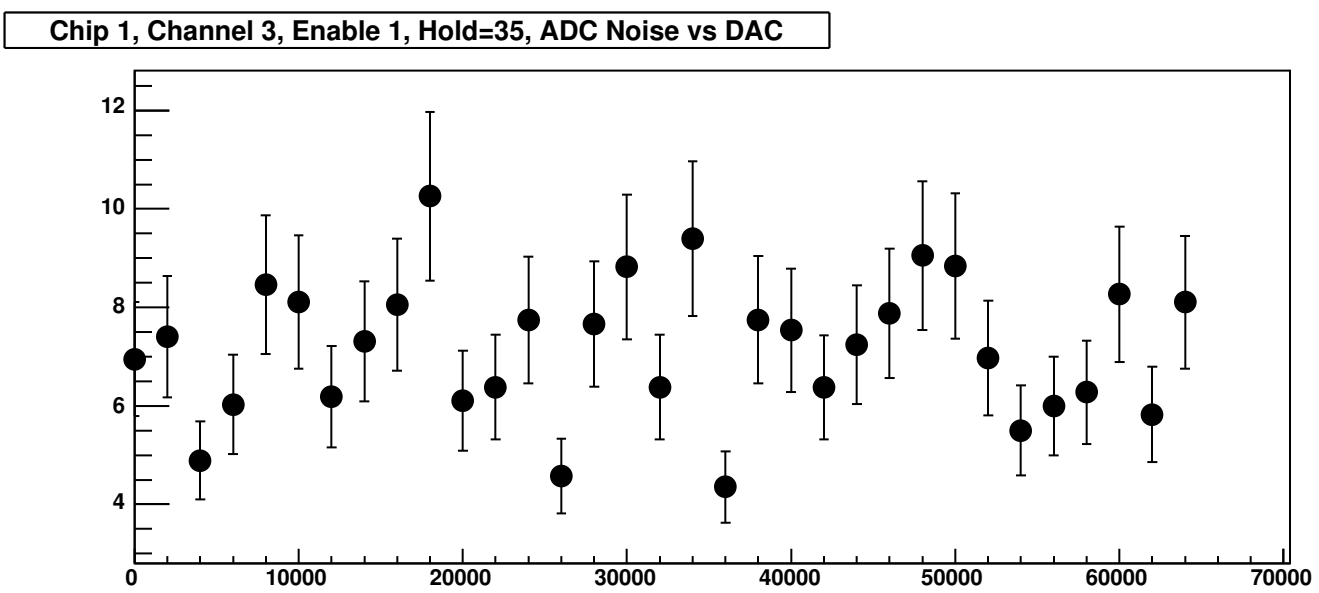

Chip 1, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000

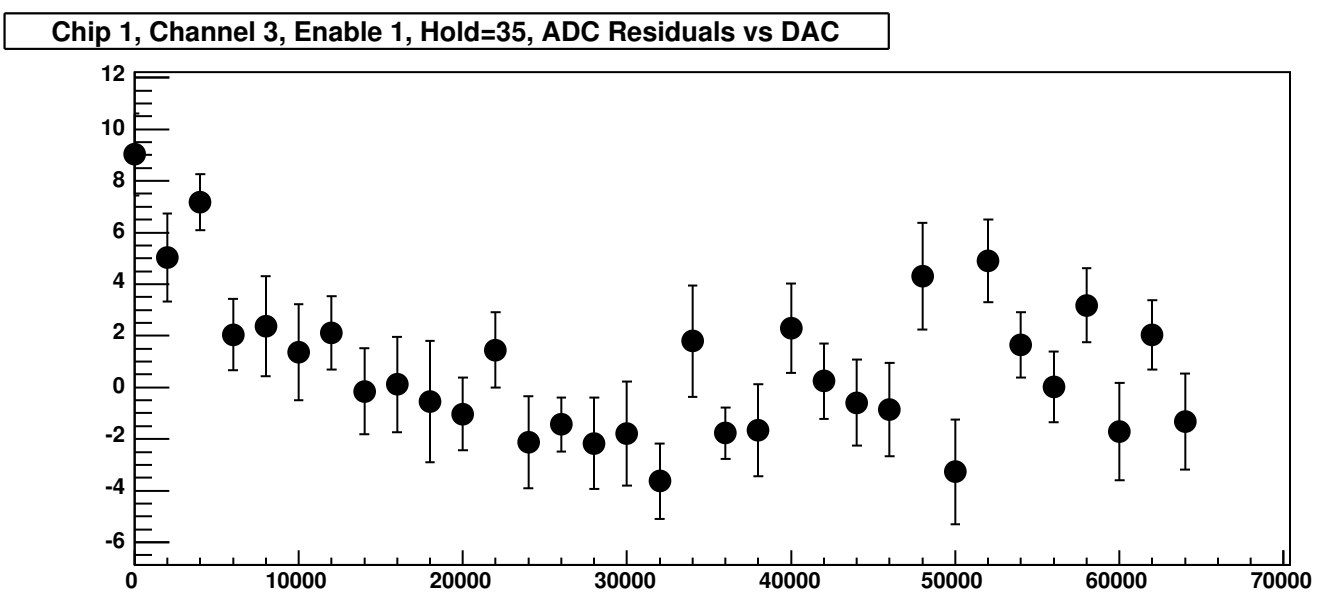

Chip 1, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 44.88 / 23
p0 106.5 ± 0.6878
p1 0.003186 ± 2.058e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

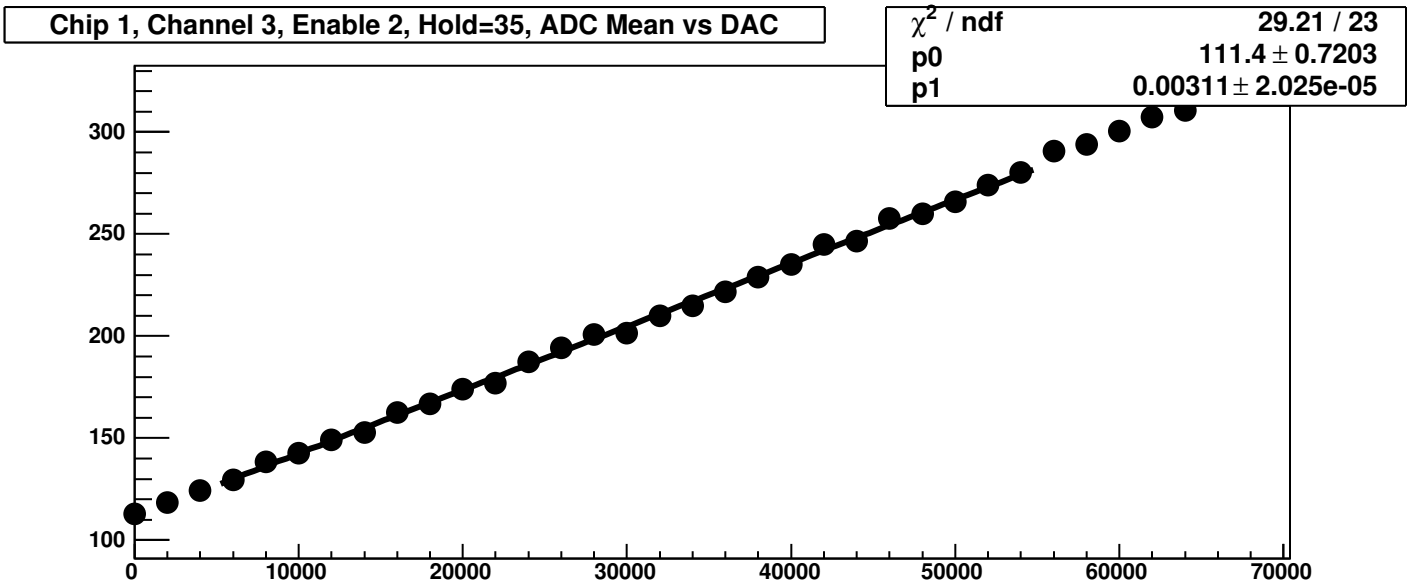

Chip 1, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

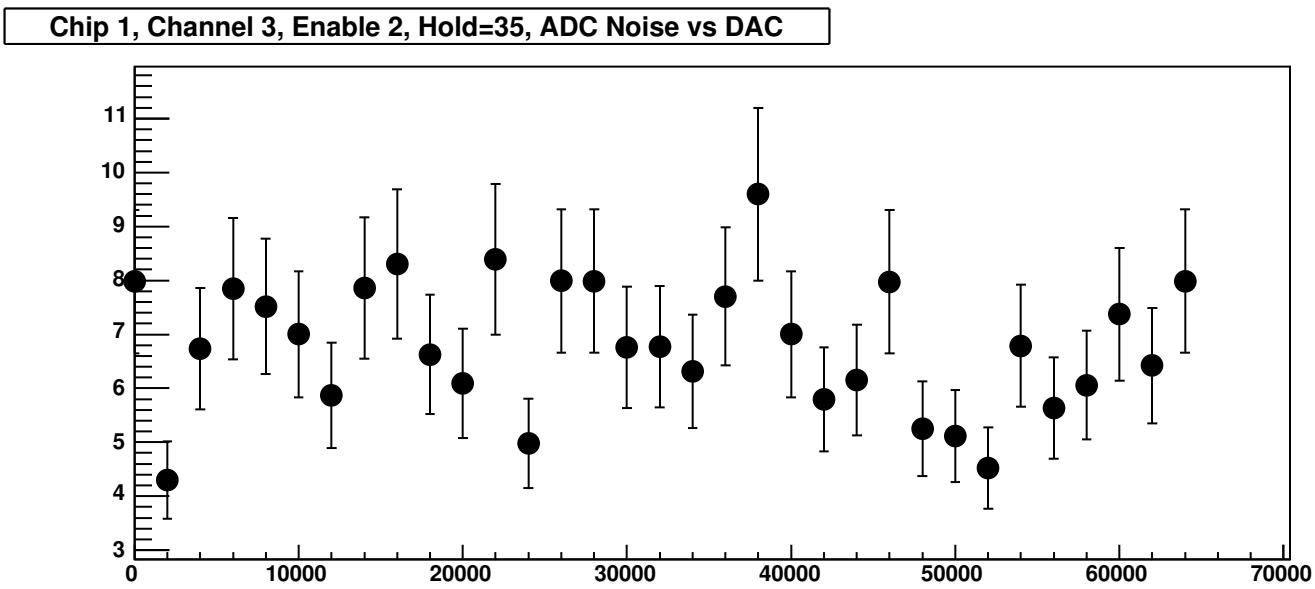

Chip 1, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

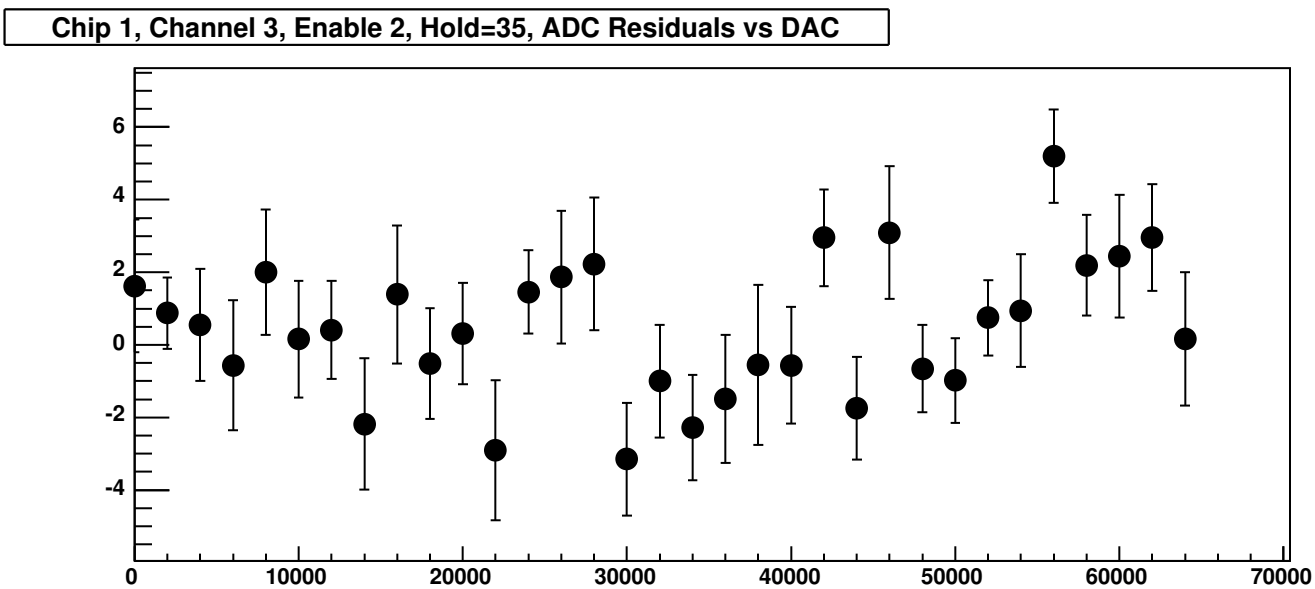

Chip 1, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 44.57 / 23
p0 106.8 ± 0.7409
p1 0.003576 ± 2.223e-05
350
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

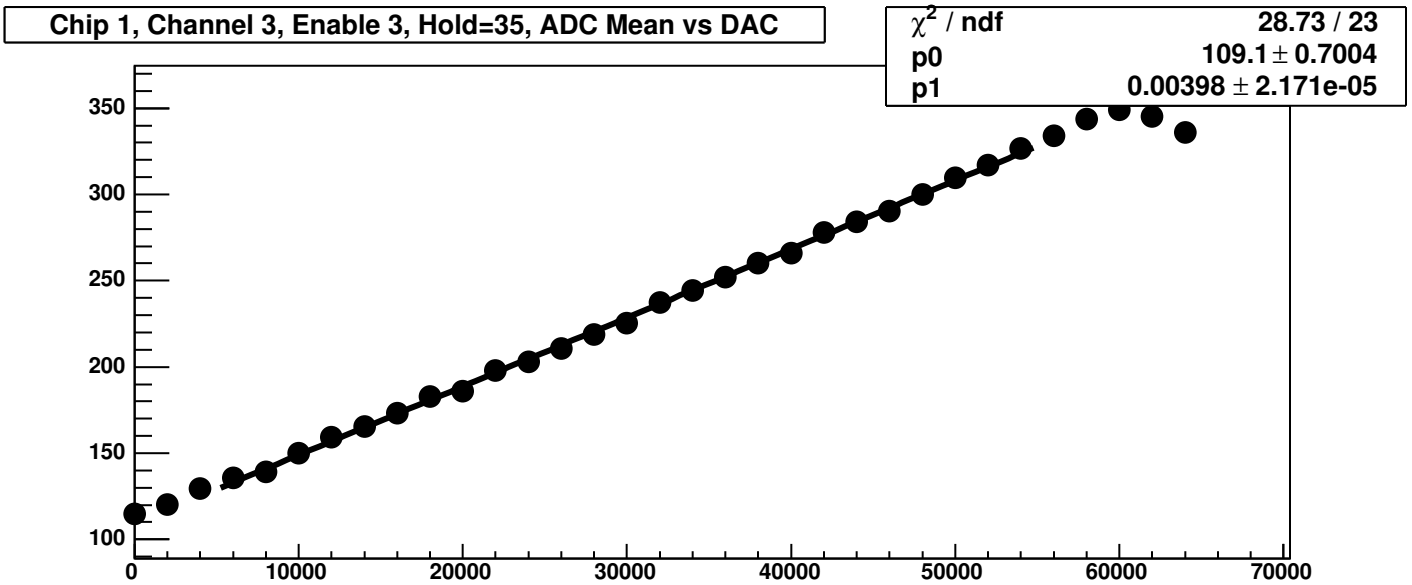

Chip 1, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

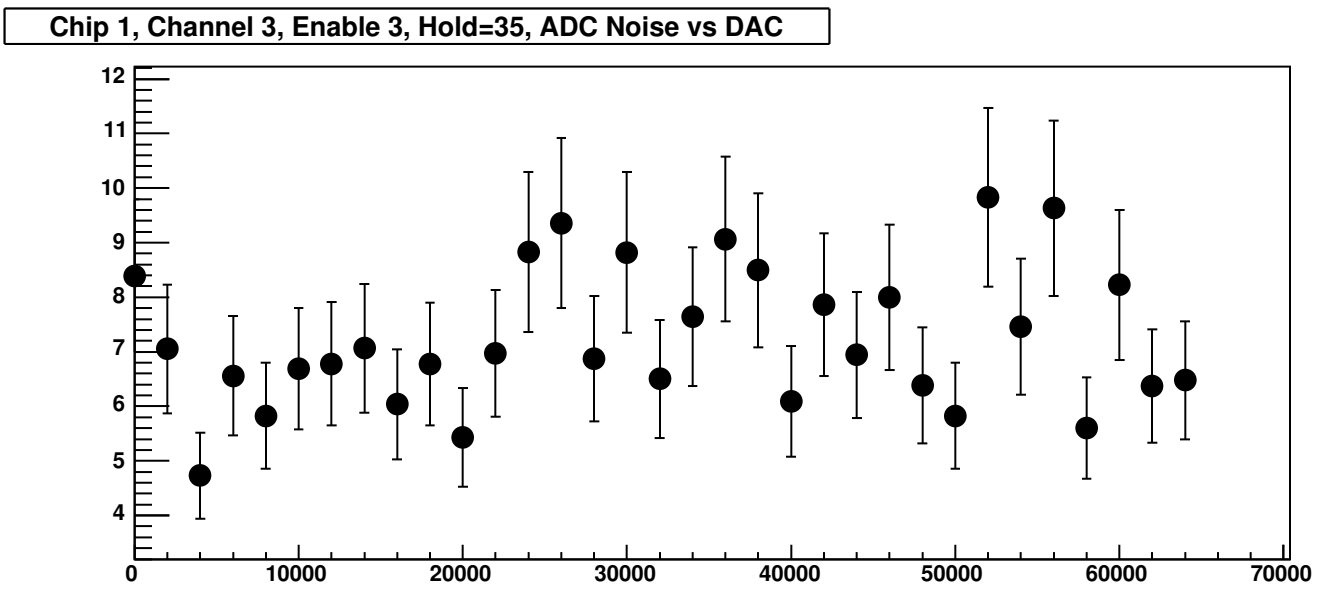

Chip 1, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

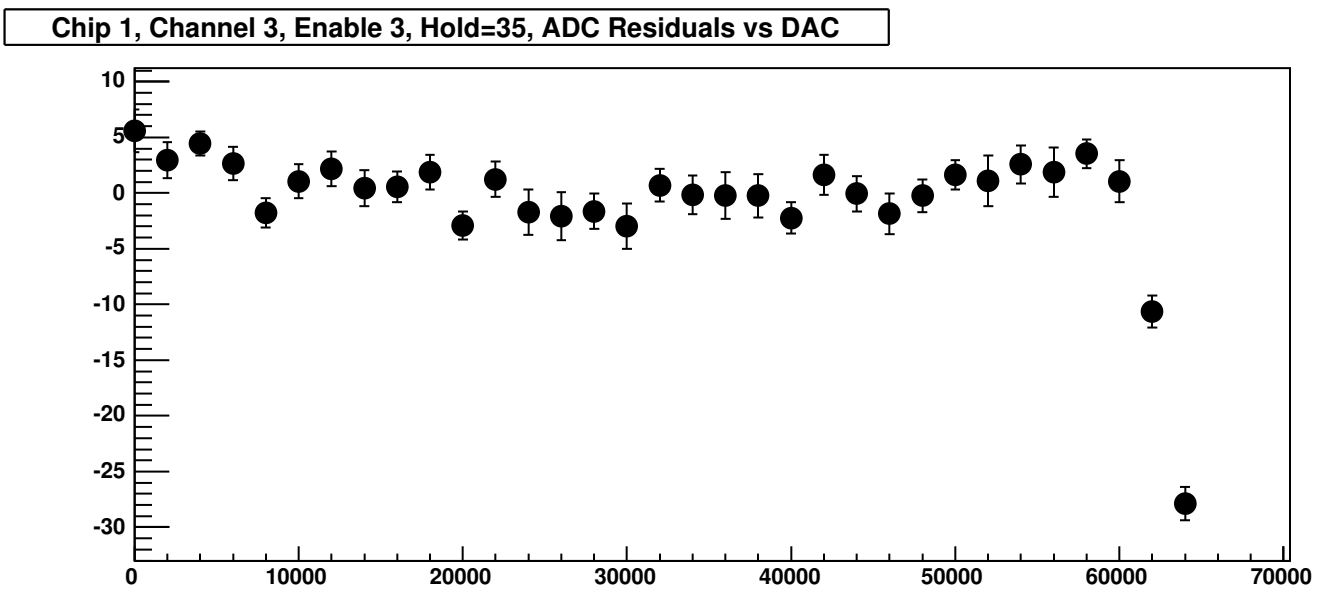

Chip 1, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 29.21 / 23
p0 111.4 ± 0.7203
p1 0.00311 ± 2.025e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

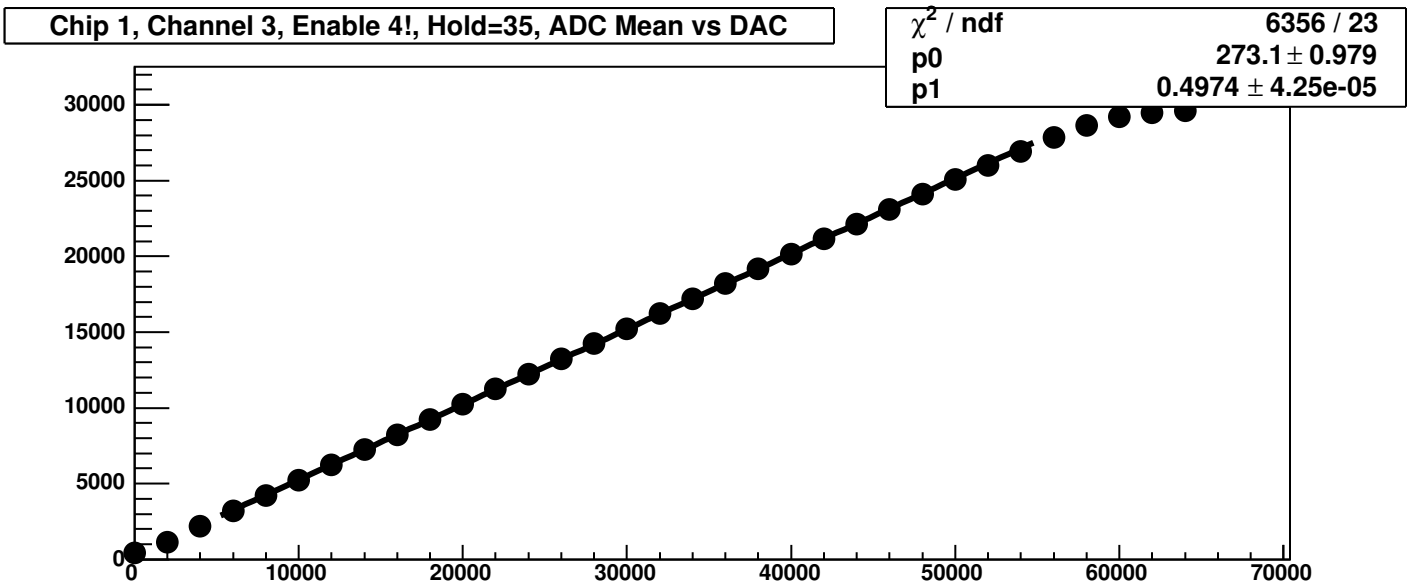

Chip 1, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

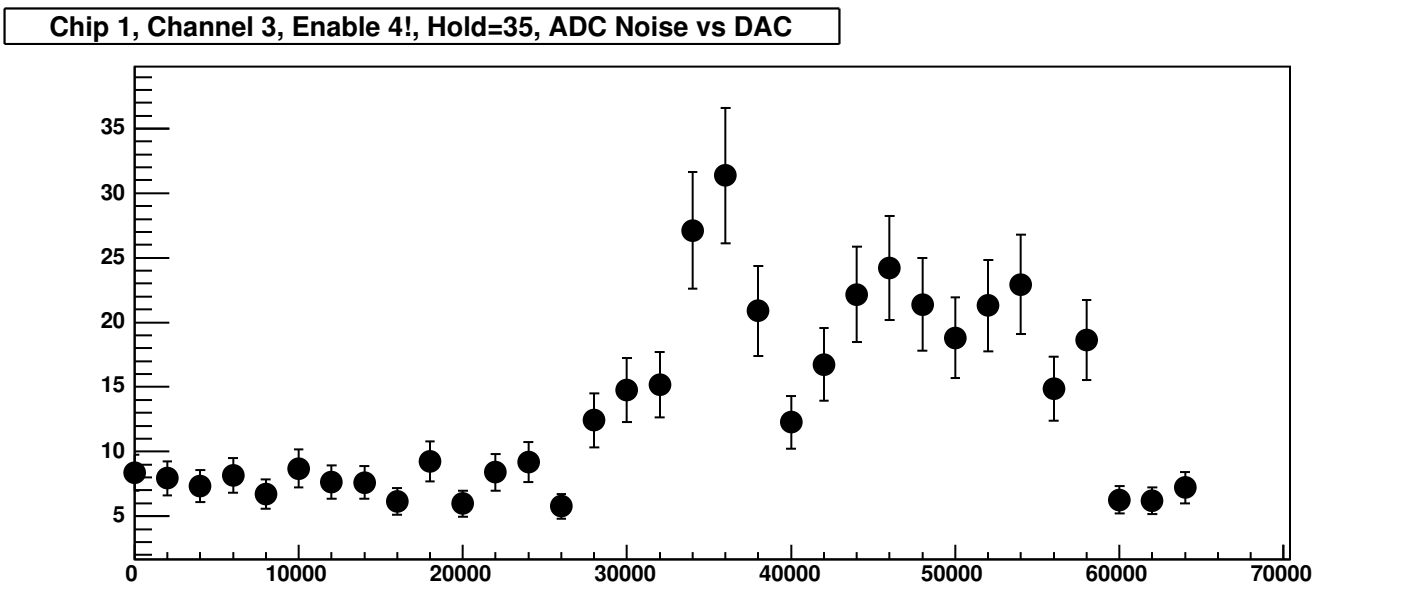

Chip 1, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
0
10000
20000
30000
40000
50000
60000
70000

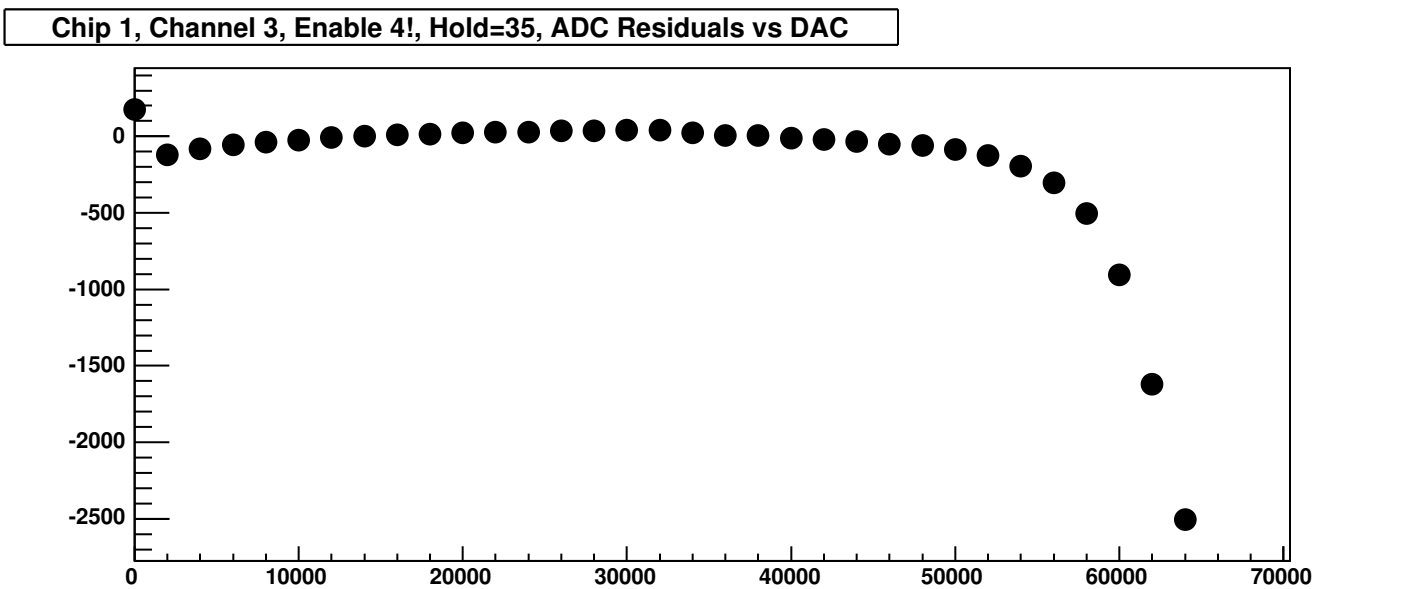

Chip 1, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.73 / 23
p0 109.1 ± 0.7004
p1 0.00398 ± 2.171e-05
350
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

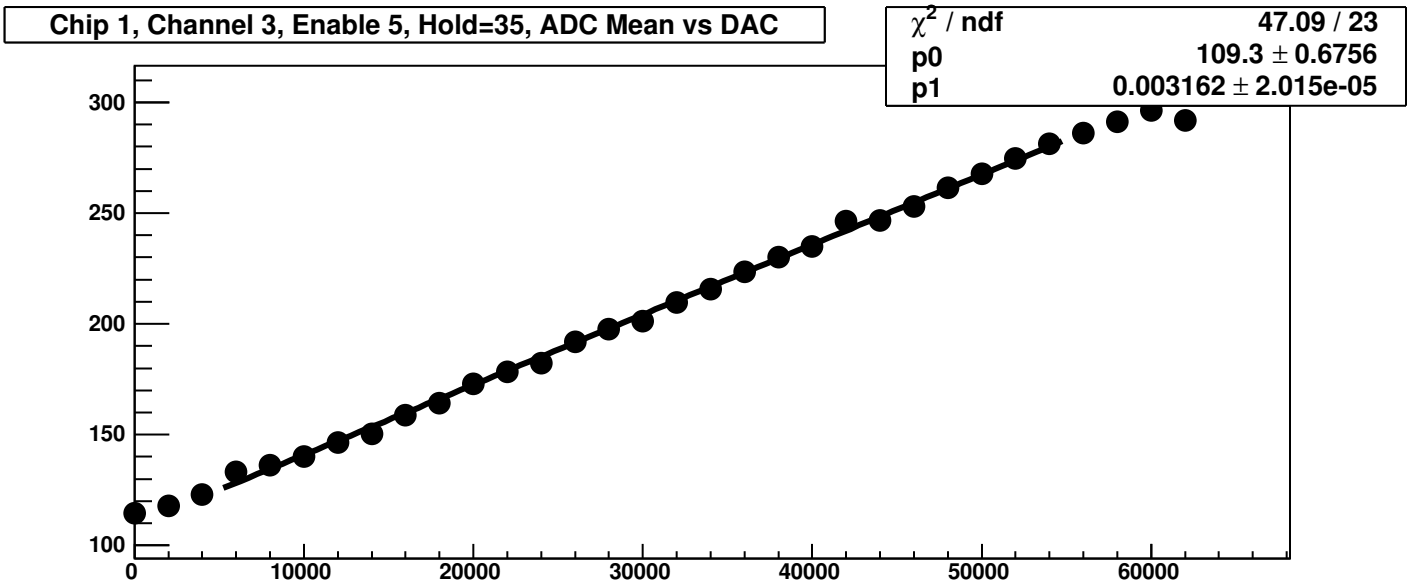

Chip 1, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

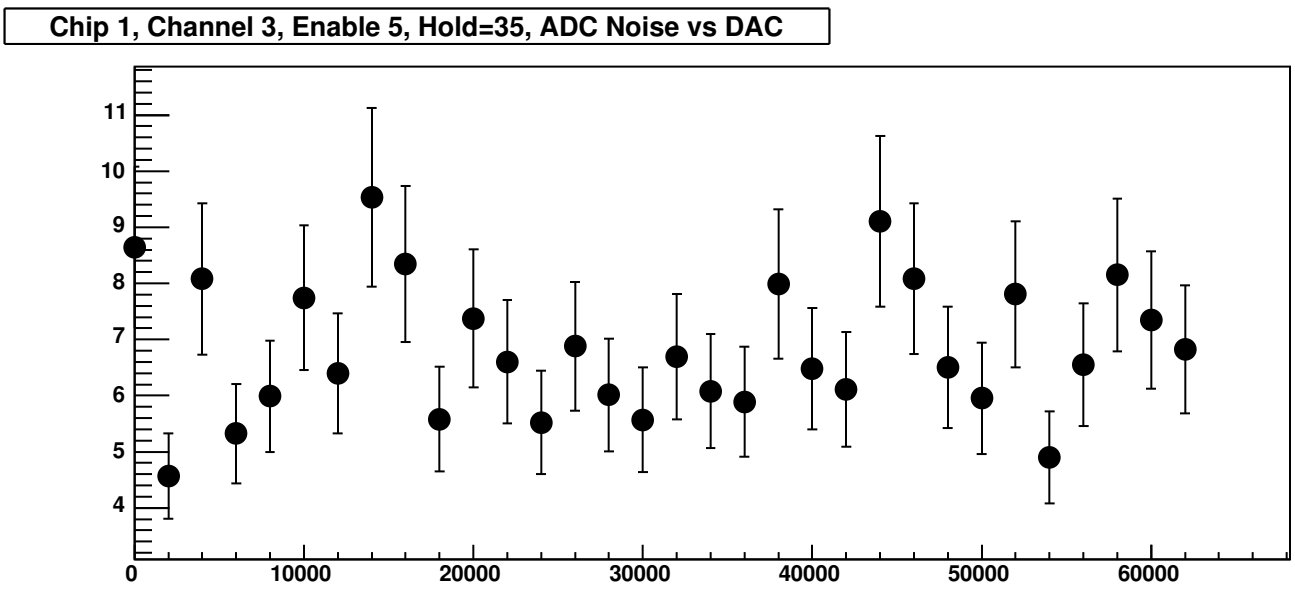

Chip 1, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
0
10000
20000
30000
40000
50000
60000
70000

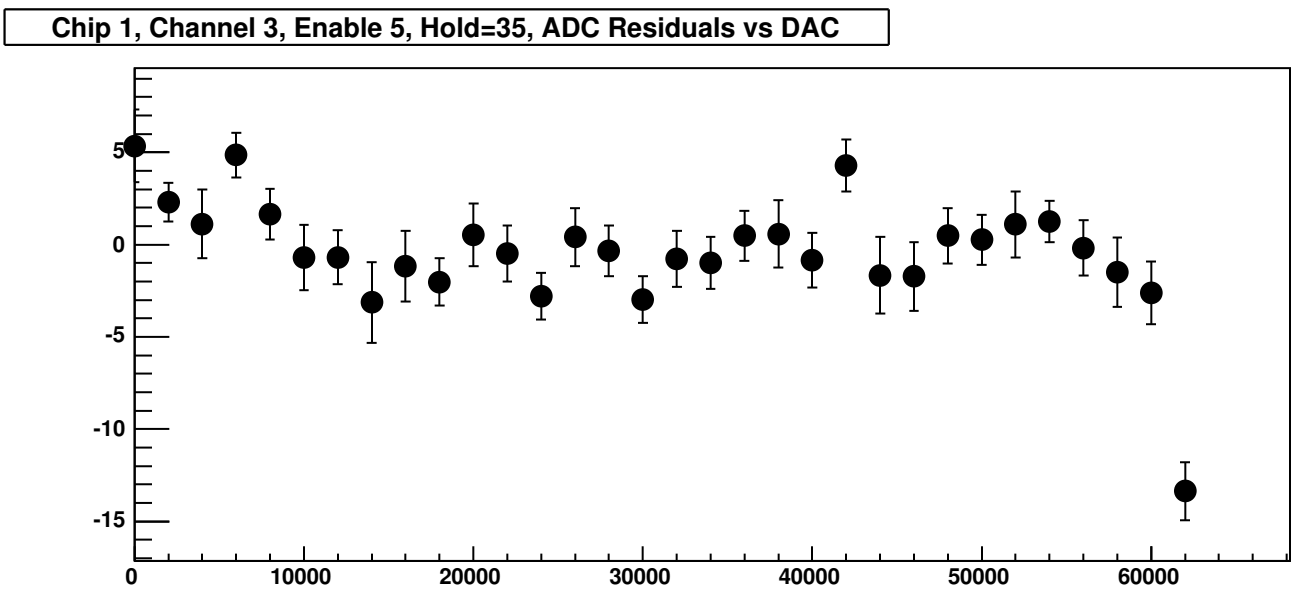

Chip 1, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 6356 / 23
p0 273.1 ± 0.979
p1 0.4974 ± 4.25e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

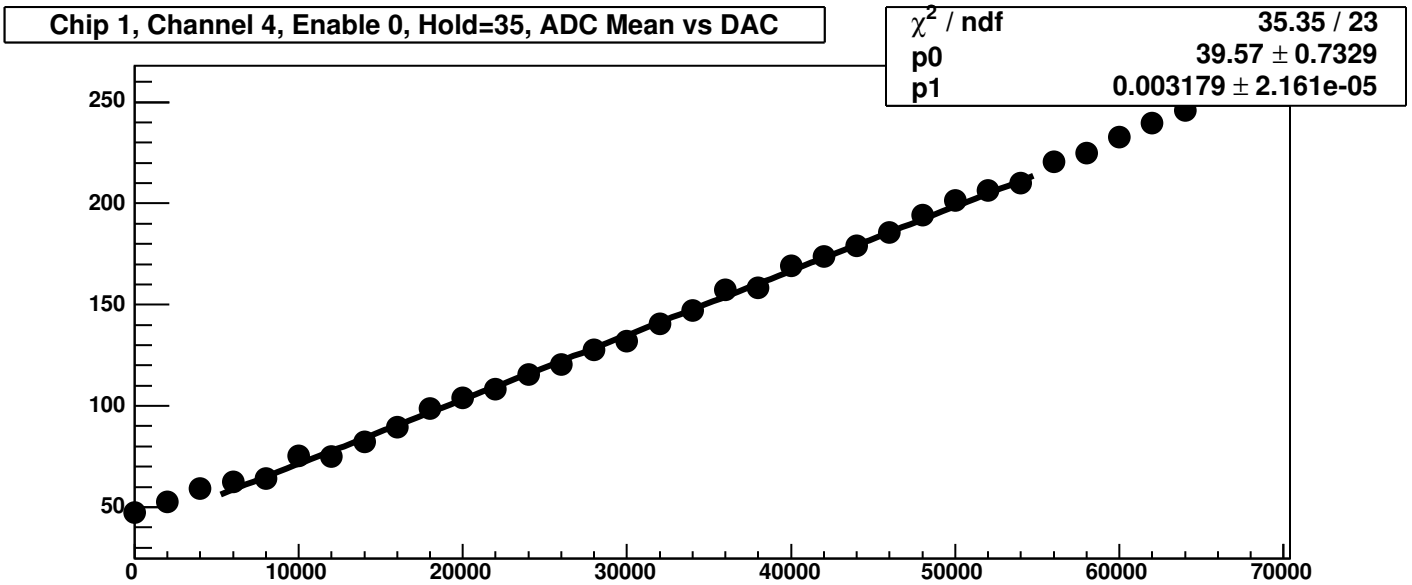

Chip 1, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000

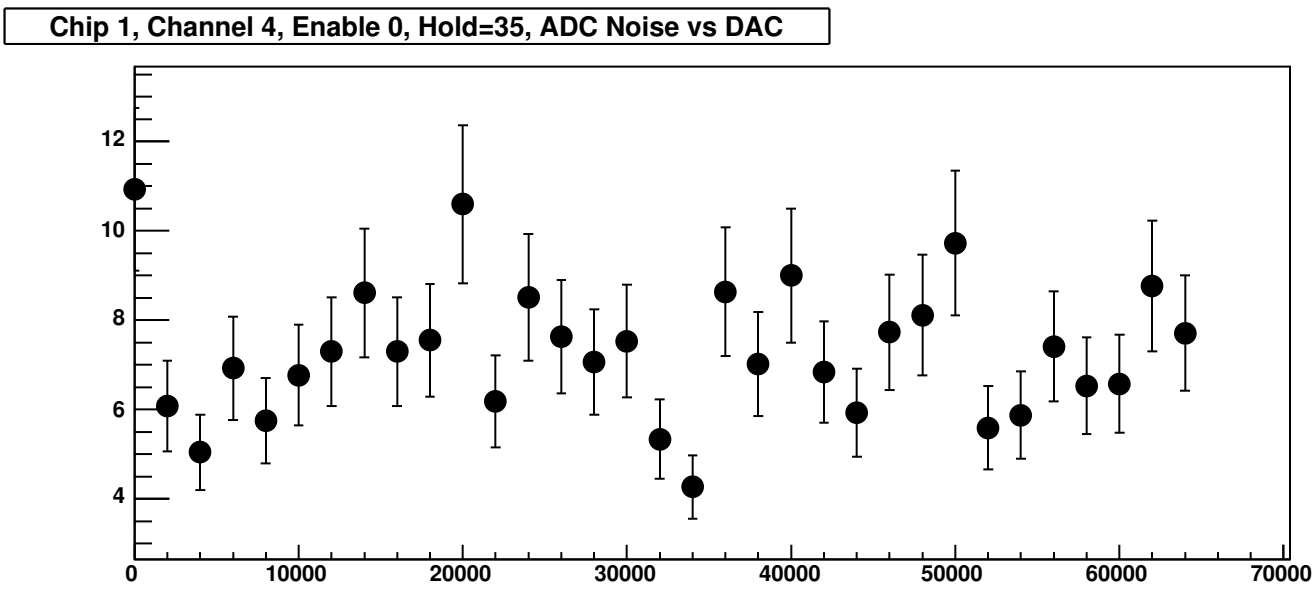

Chip 1, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
0
10000
20000
30000
40000
50000
60000
70000

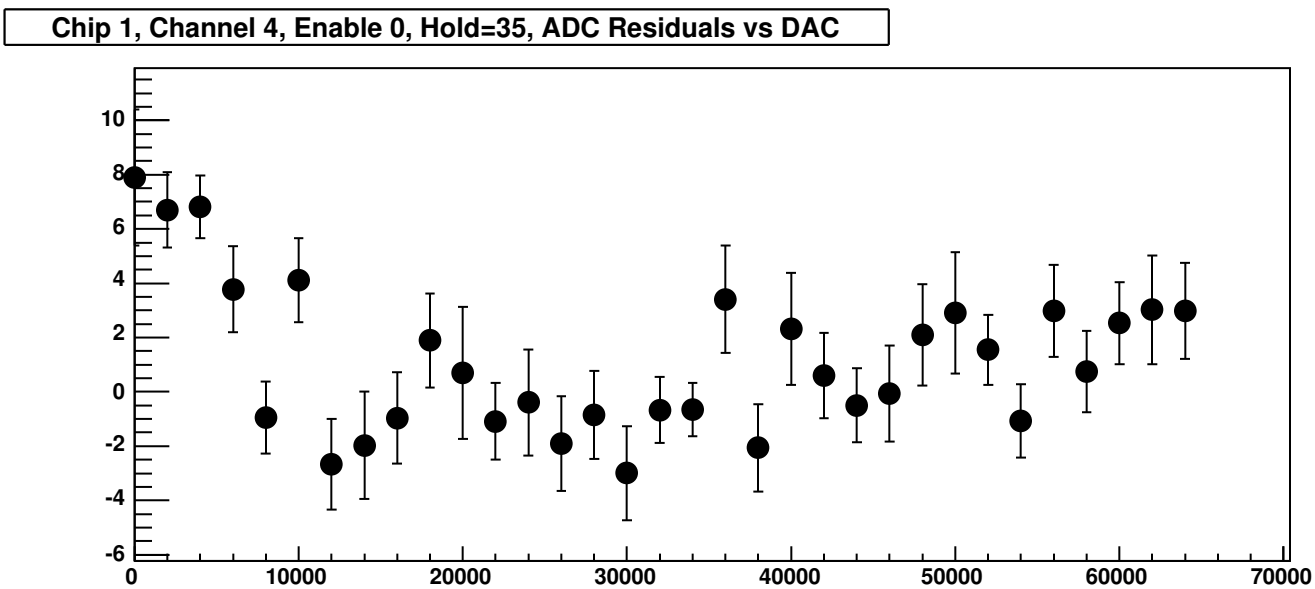

Chip 1, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 47.09 / 23
p0 109.3 ± 0.6756
p1 0.003162 ± 2.015e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000

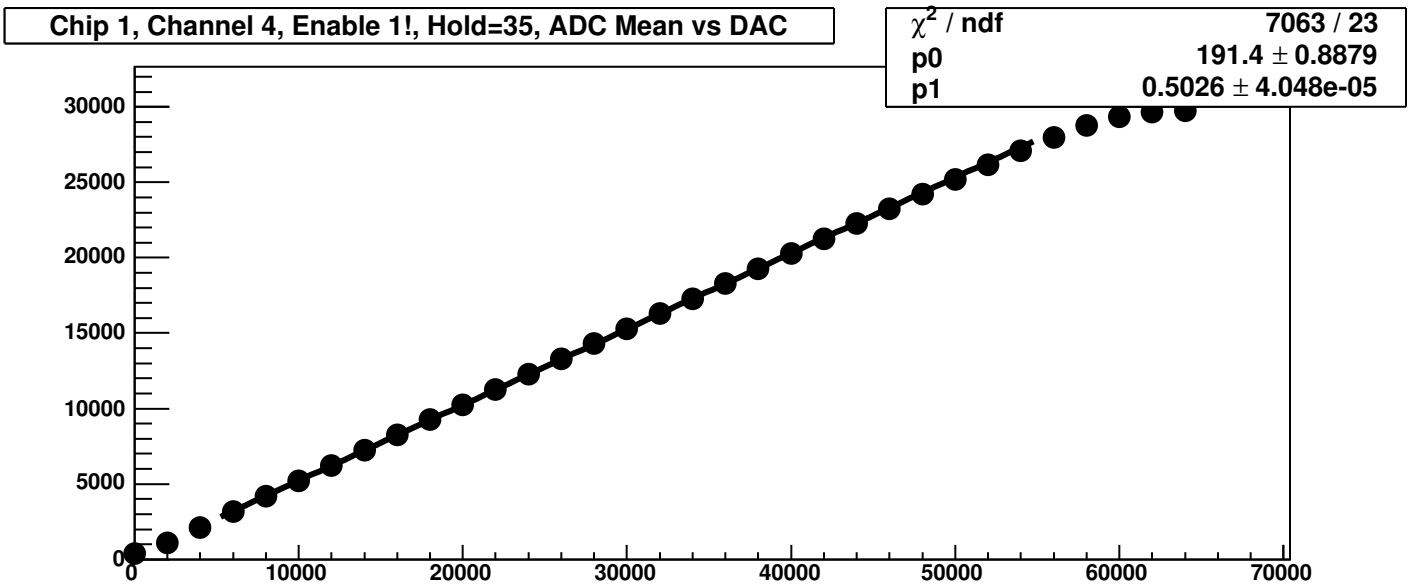

Chip 1, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

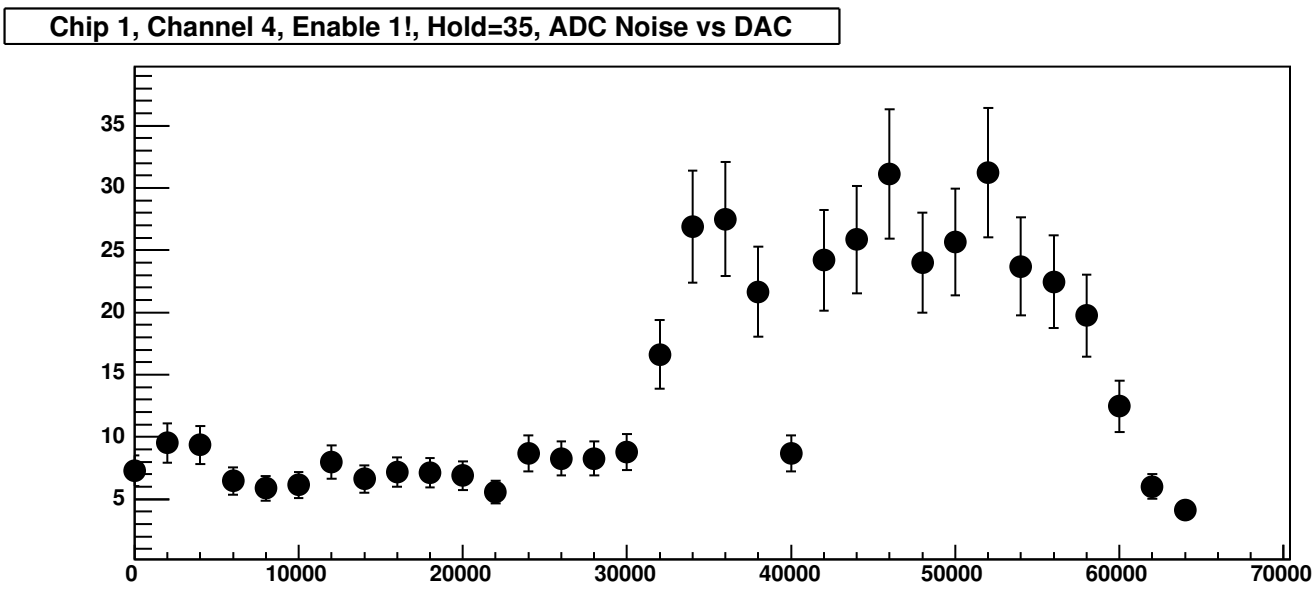

Chip 1, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000

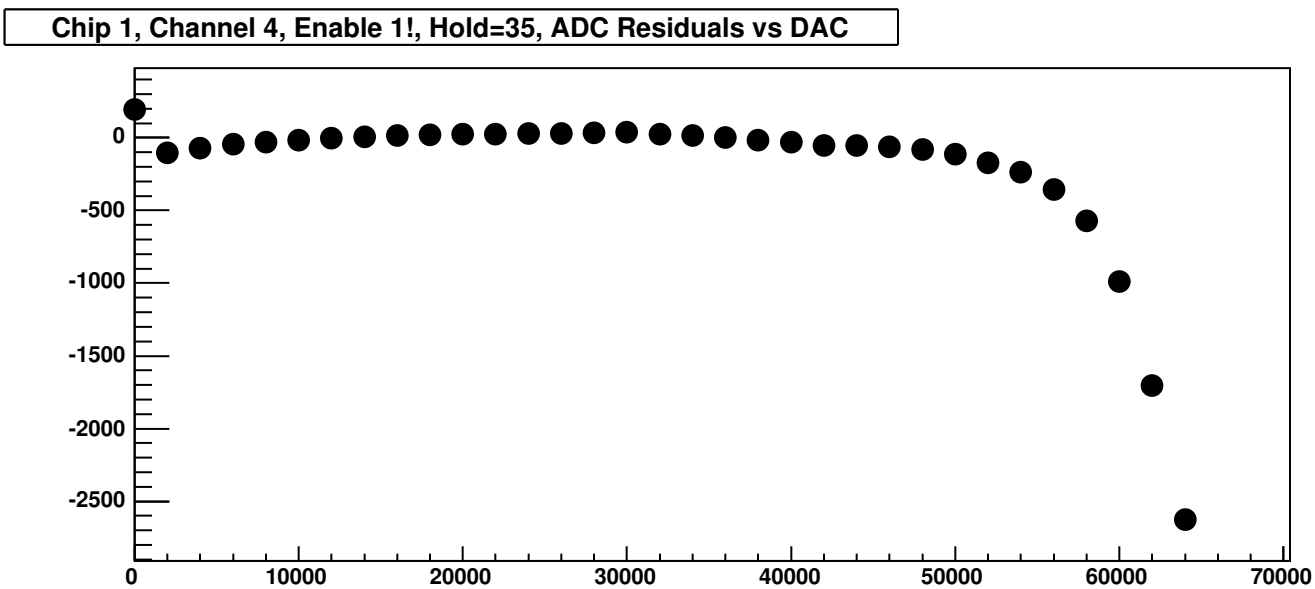

Chip 1, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 35.35 / 23
p0 39.57 ± 0.7329
p1 0.003179 ± 2.161e-05
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

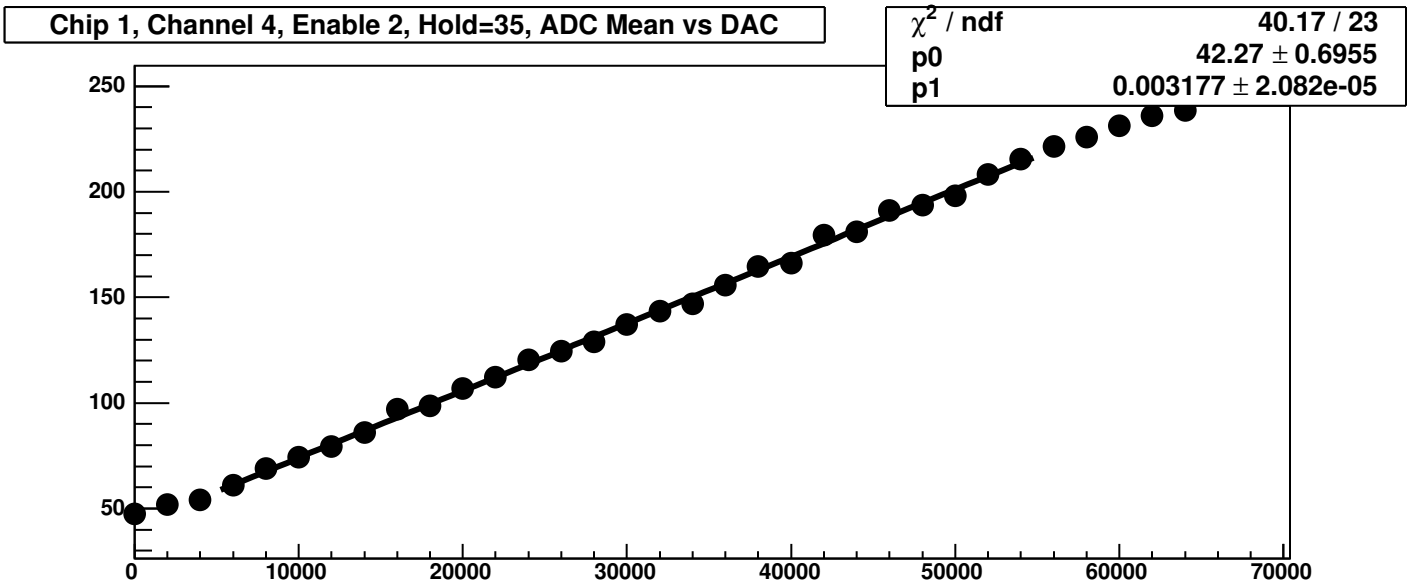

Chip 1, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

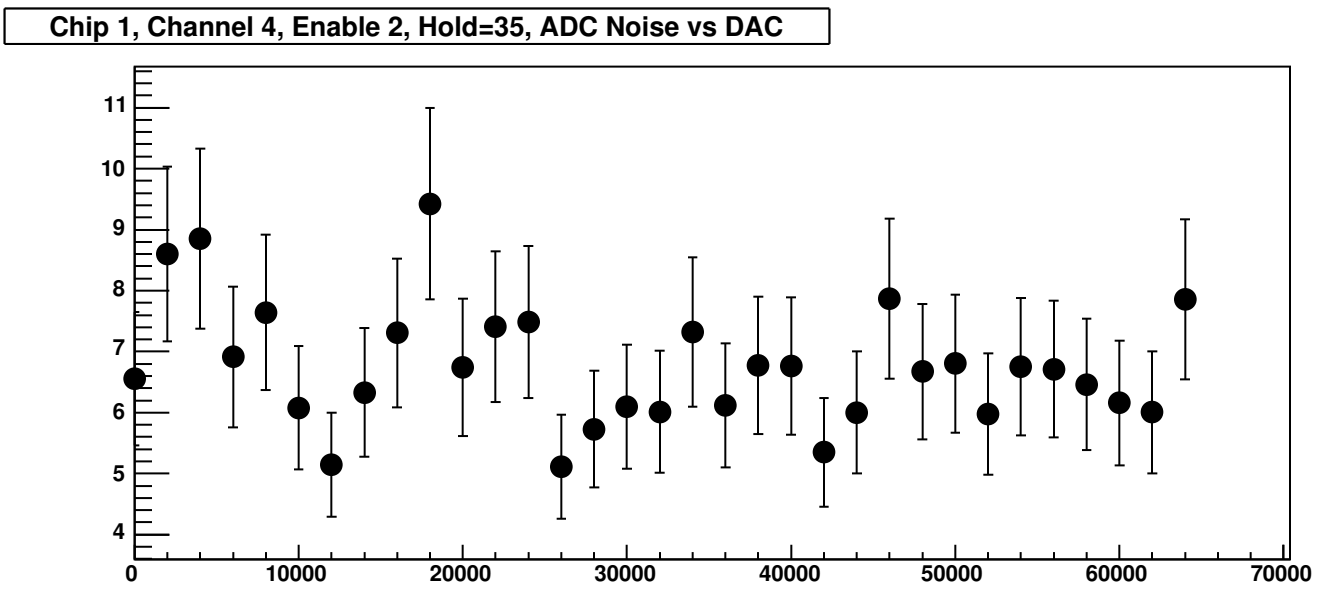

Chip 1, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

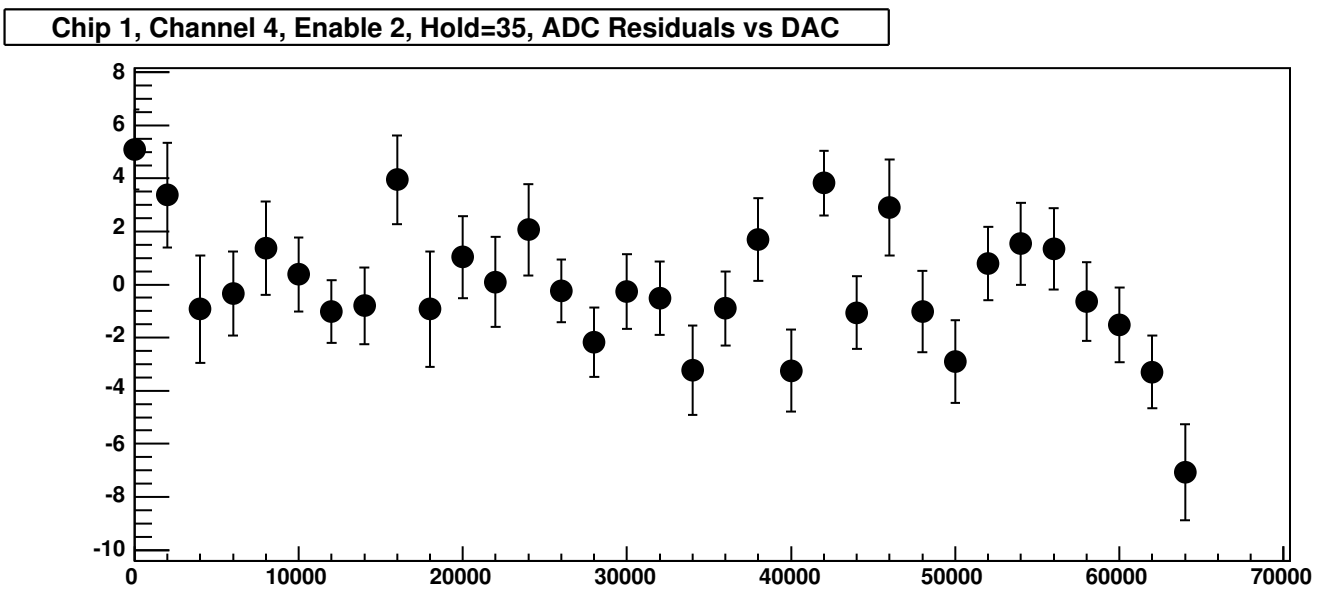

Chip 1, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 7063 / 23
p0 191.4 ± 0.8879
p1 0.5026 ± 4.048e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

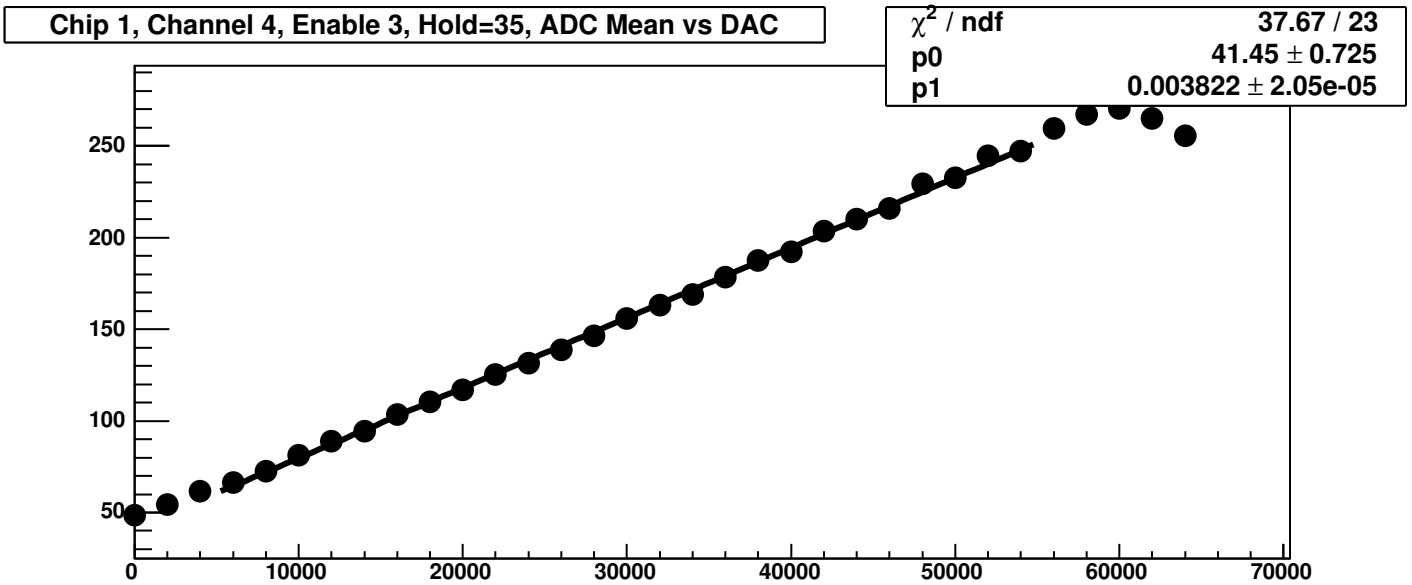

Chip 1, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000

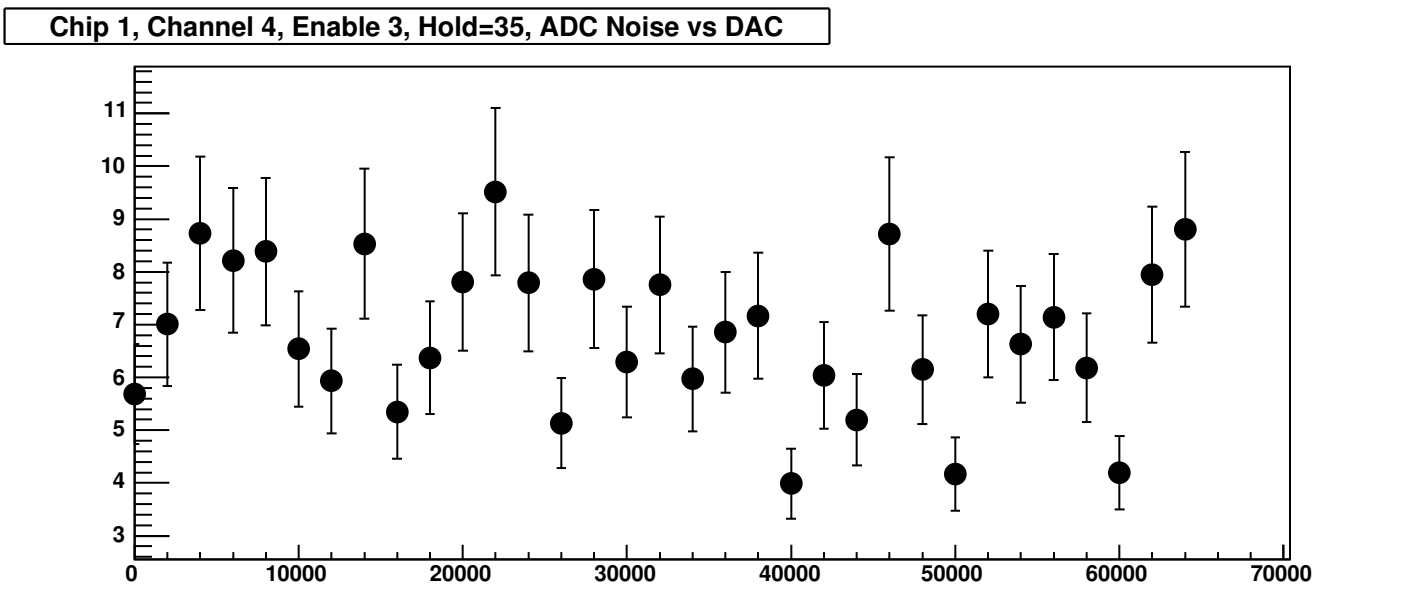

Chip 1, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
0
10000
20000
30000
40000
50000
60000
70000

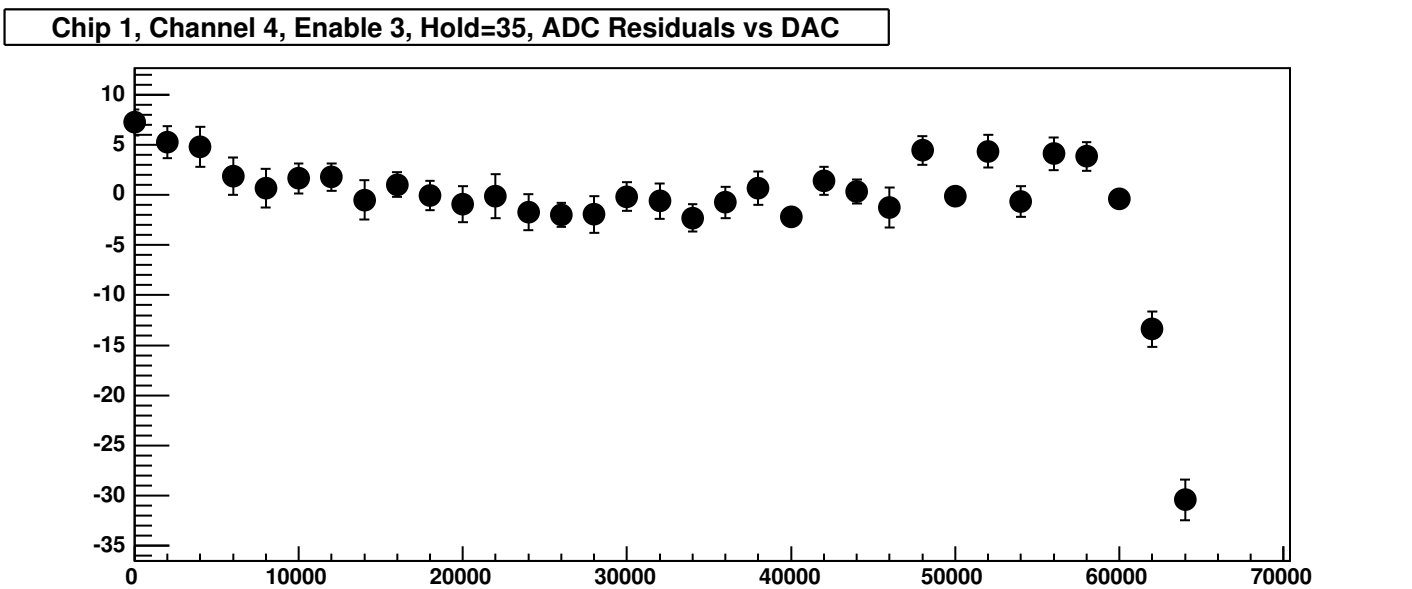

Chip 1, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 40.17 / 23
p0 42.27 ± 0.6955
p1 0.003177 ± 2.082e-05
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

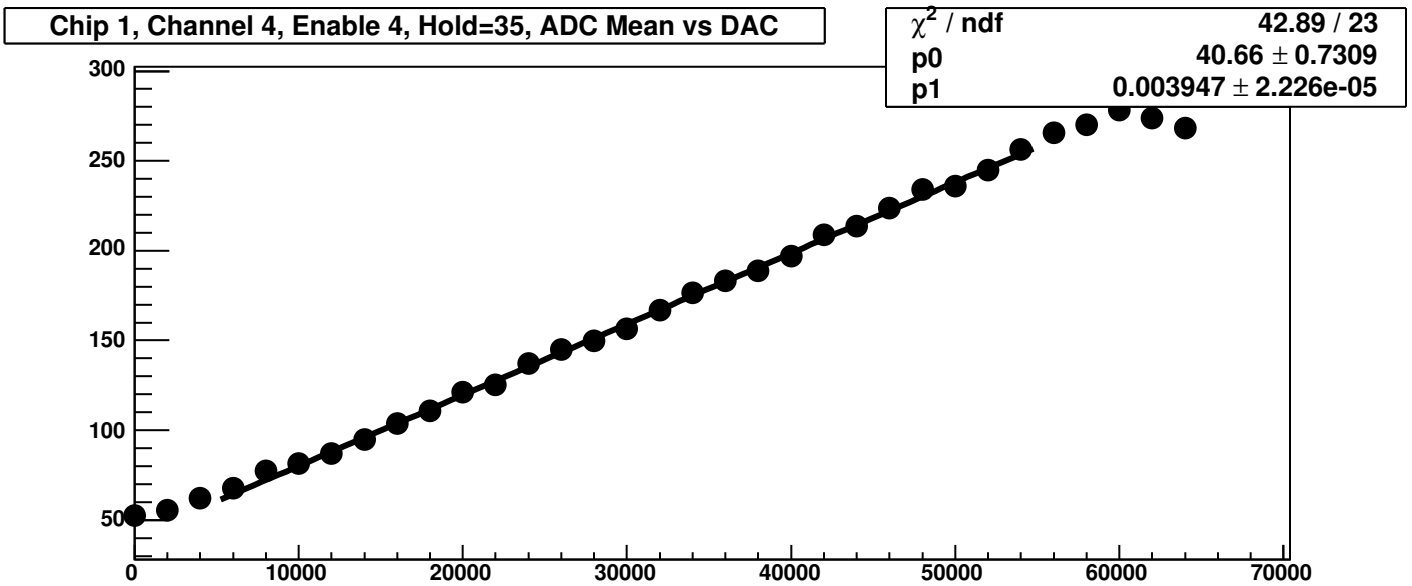

Chip 1, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

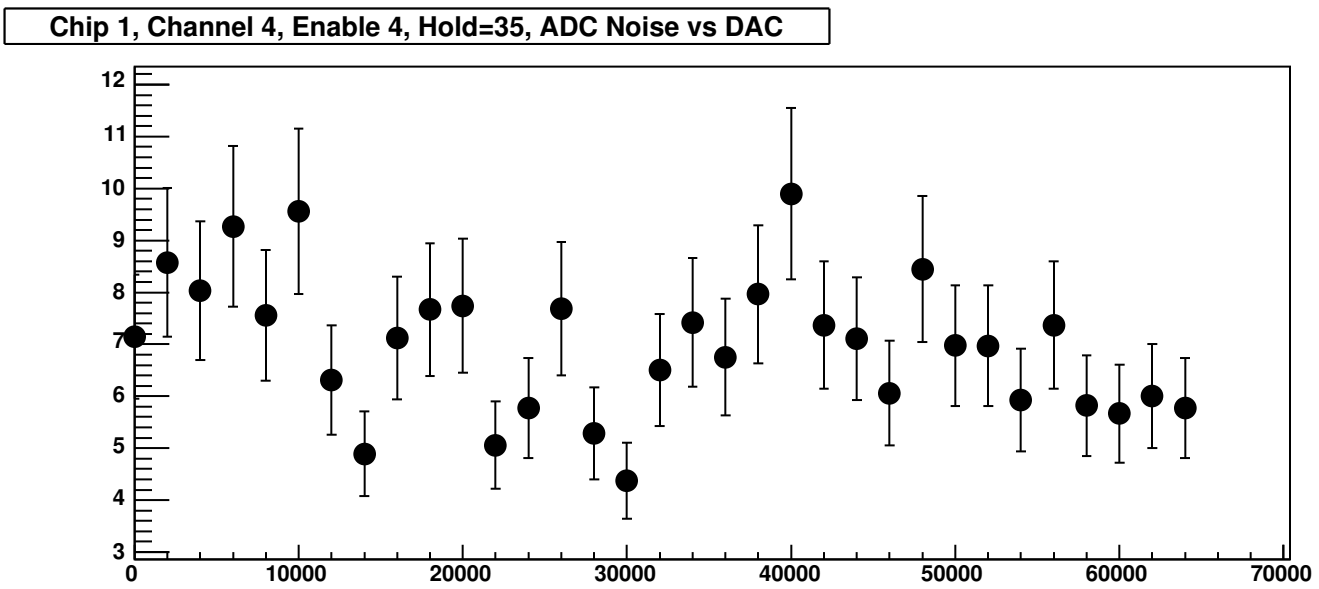

Chip 1, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
0
10000
20000
30000
40000
50000
60000
70000

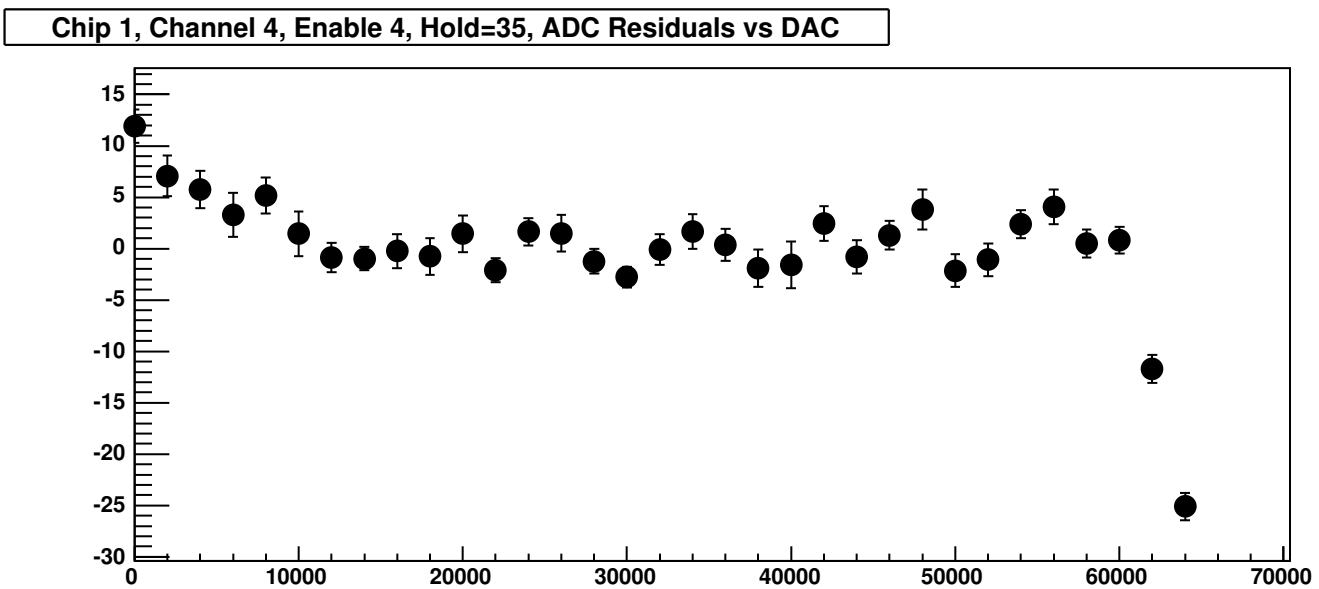

Chip 1, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 37.67 / 23
p0 41.45 ± 0.725
p1 0.003822 ± 2.05e-05
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

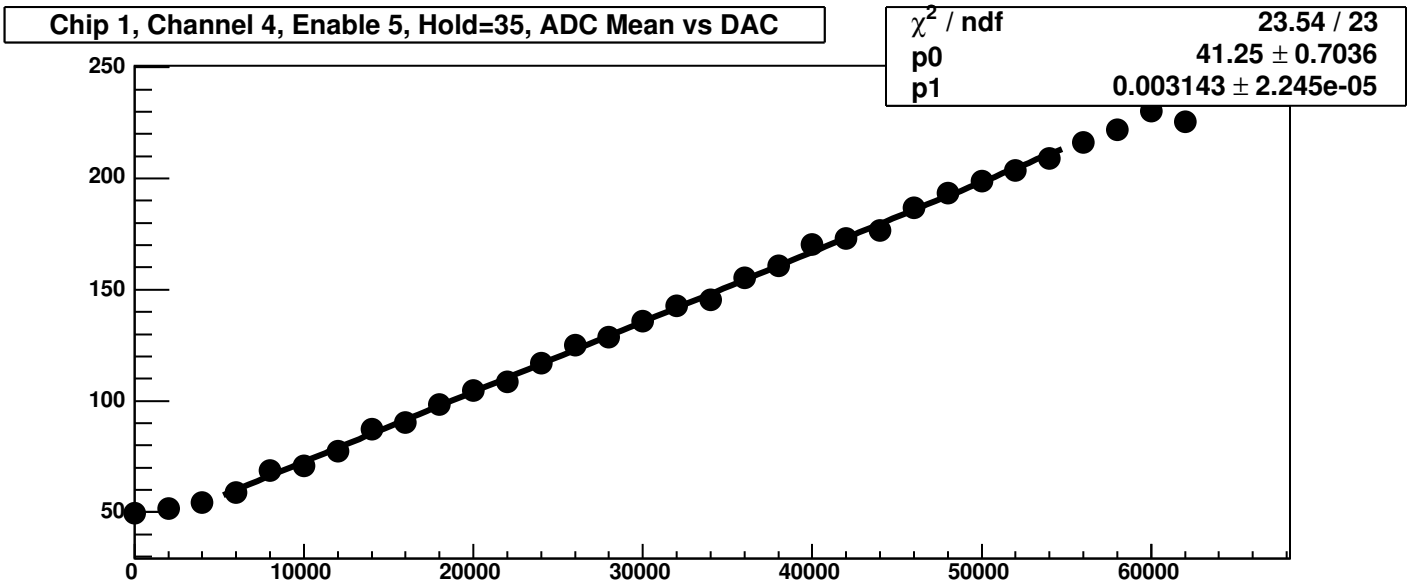

Chip 1, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

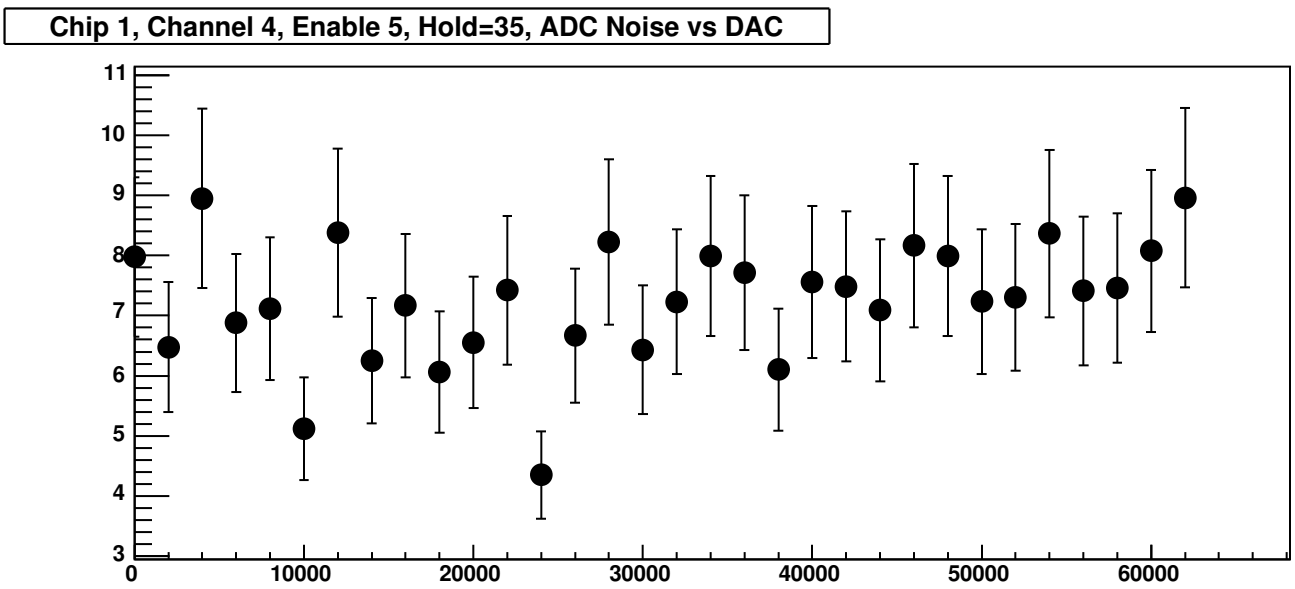

Chip 1, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
-35
0
10000
20000
30000
40000
50000
60000
70000

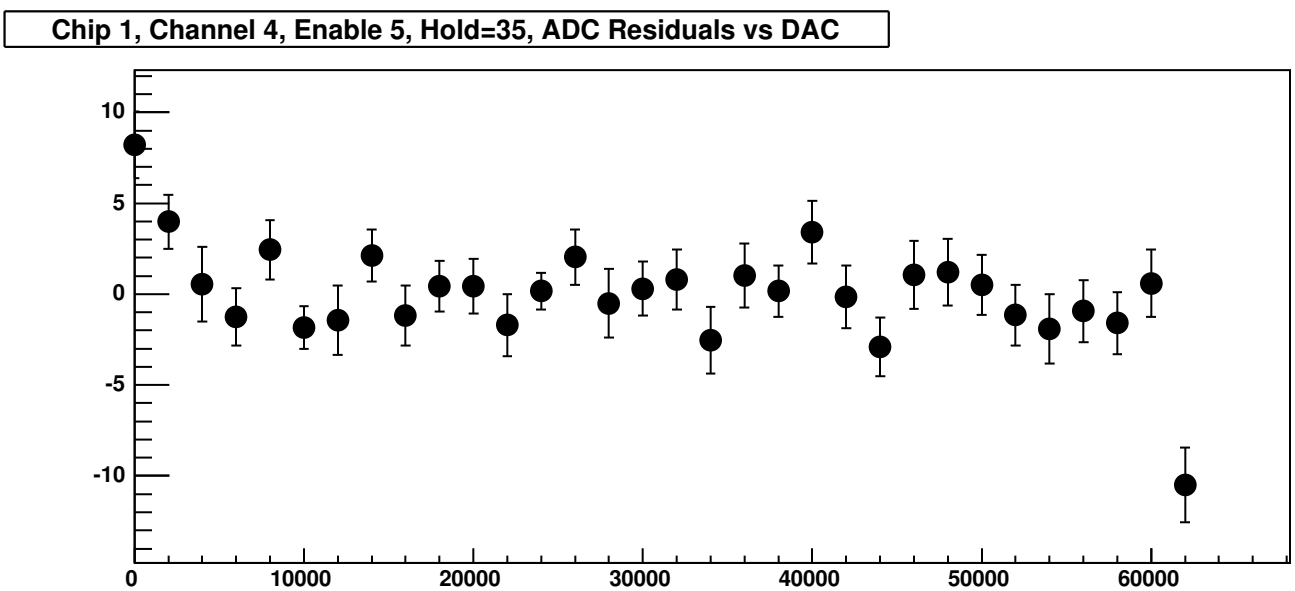

Chip 1, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 42.89 / 23
p0 40.66 ± 0.7309
p1 0.003947 ± 2.226e-05
300
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

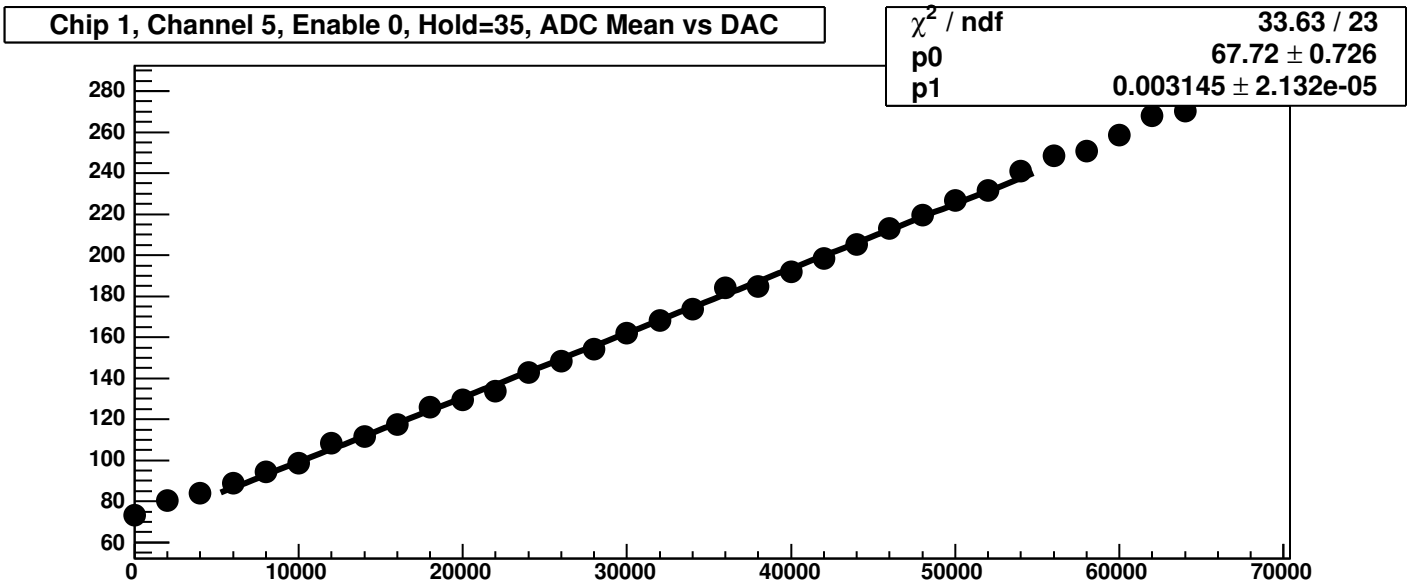

Chip 1, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

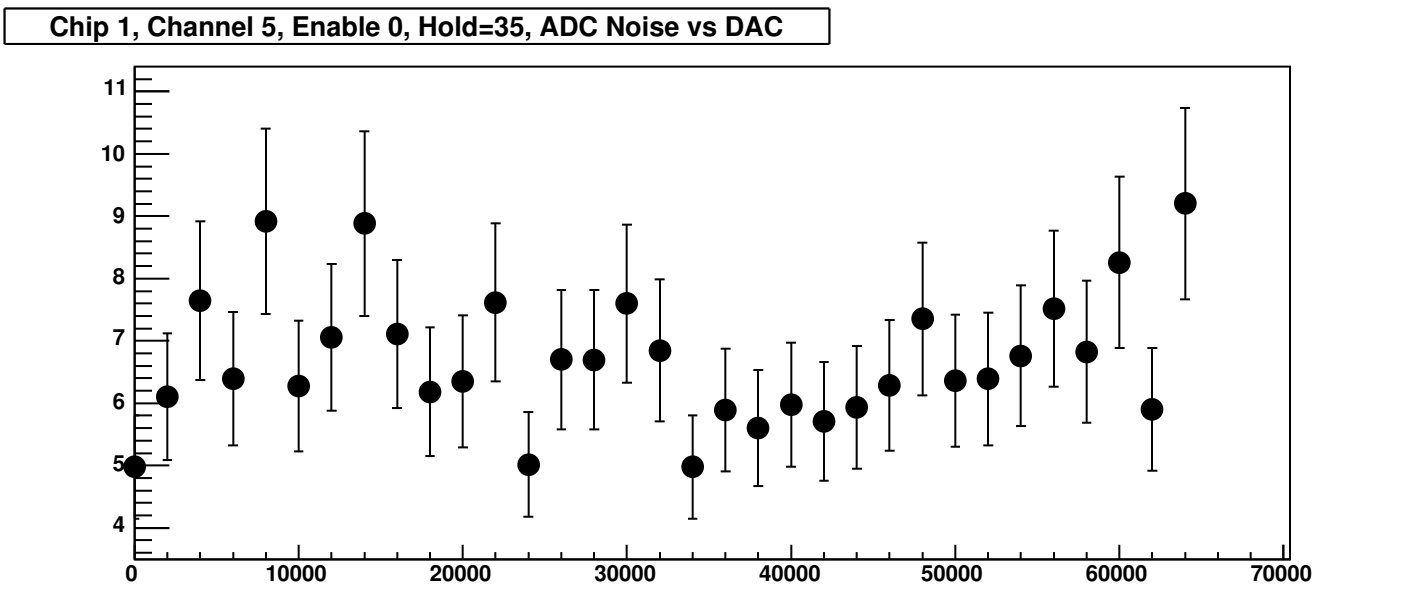

Chip 1, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
-25
-30
0
10000
20000
30000
40000
50000
60000
70000

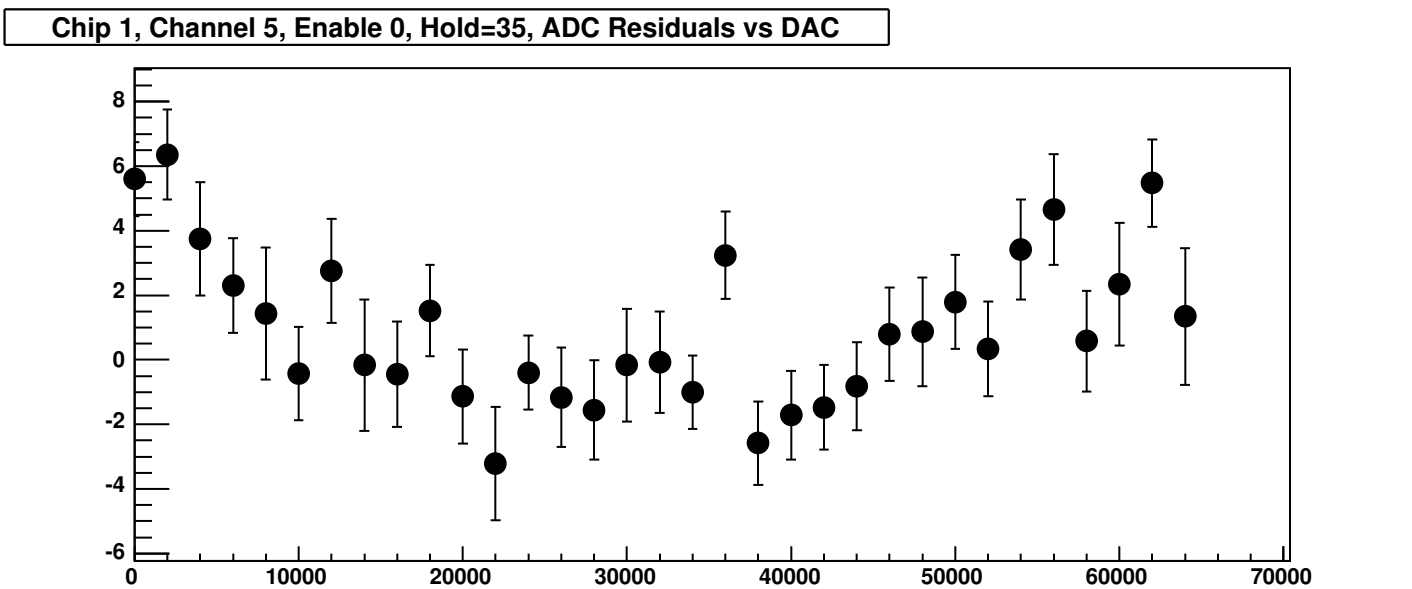

Chip 1, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 23.54 / 23
p0 41.25 ± 0.7036
p1 0.003143 ± 2.245e-05
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000

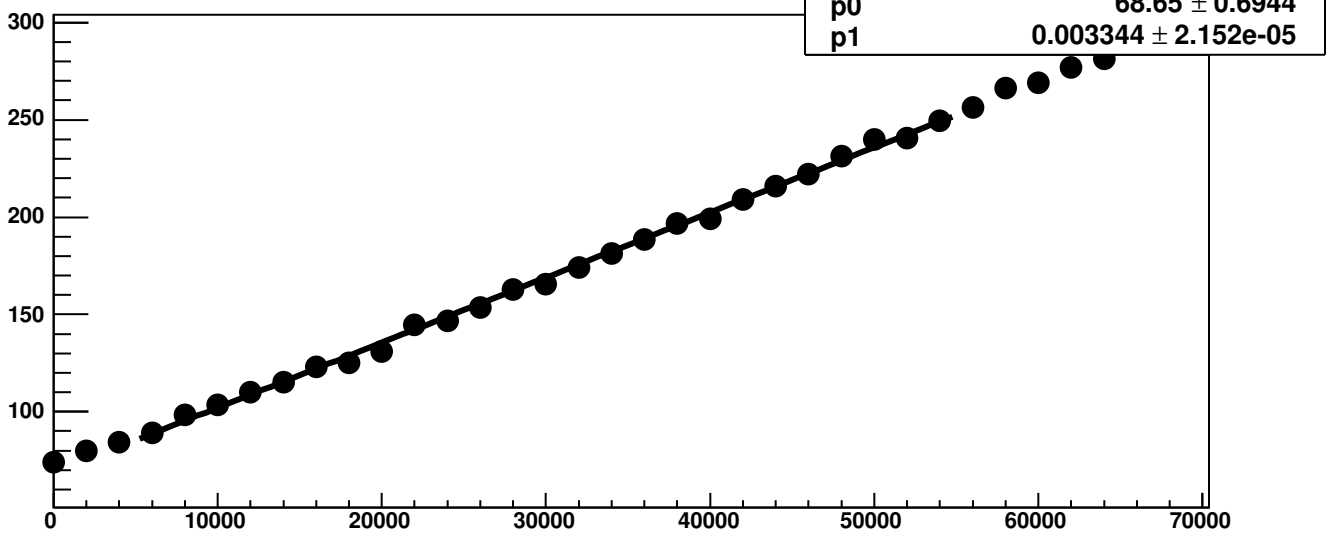

Chip 1, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 33.63 / 23
p0 67.72 ± 0.726
p1 0.003145 ± 2.132e-05
280
260
240
220
200
180
160
140
120
100
80
60
0
10000
20000
30000
40000
50000
60000
70000

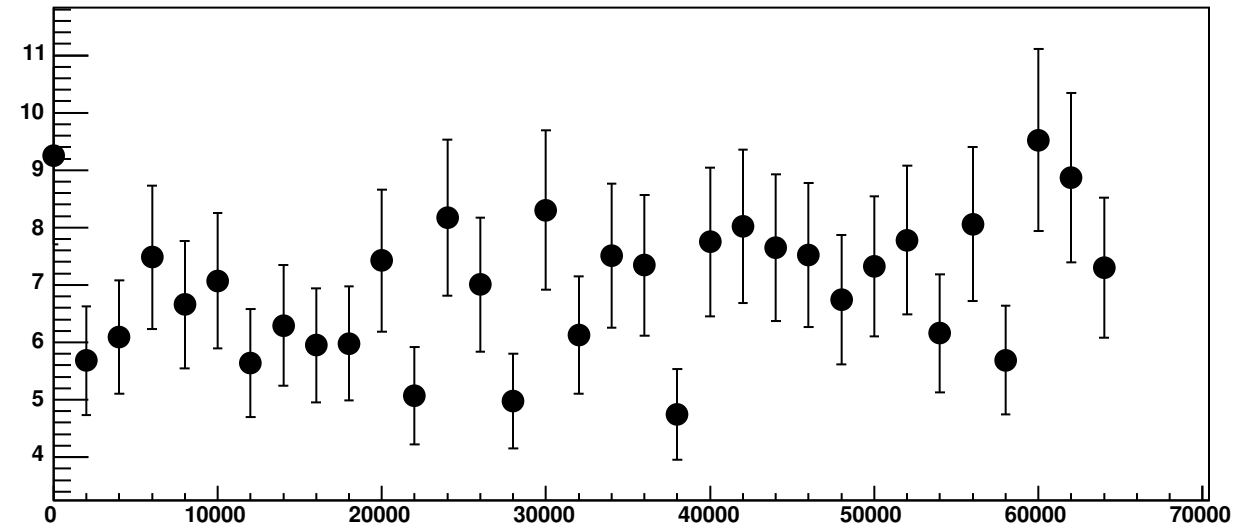

Chip 1, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

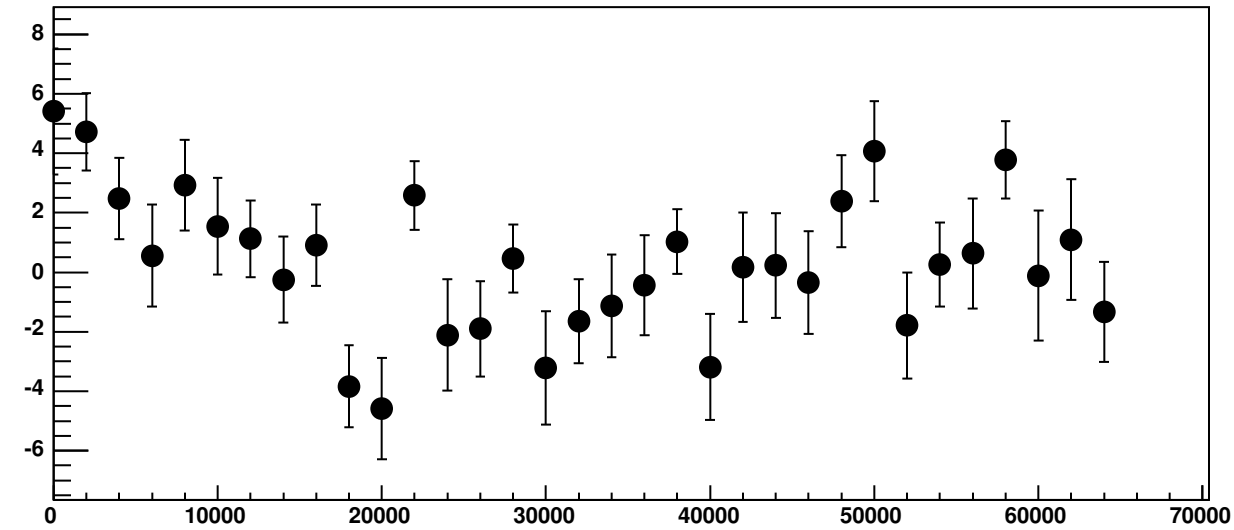
Chip 1, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC



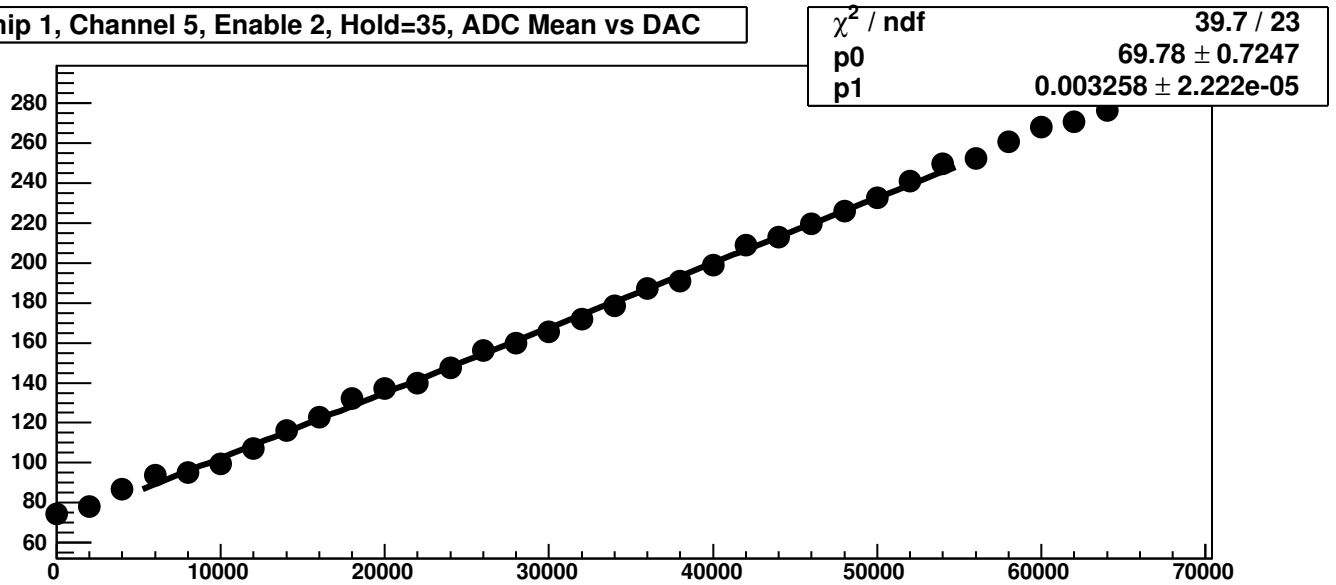
Chip 1, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC



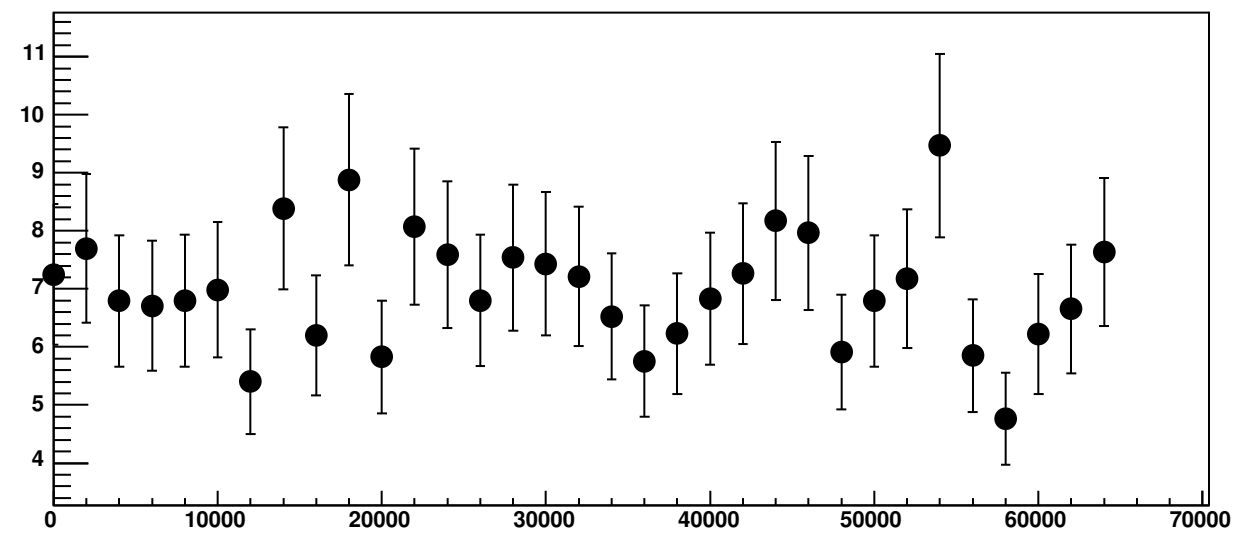
Chip 1, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC



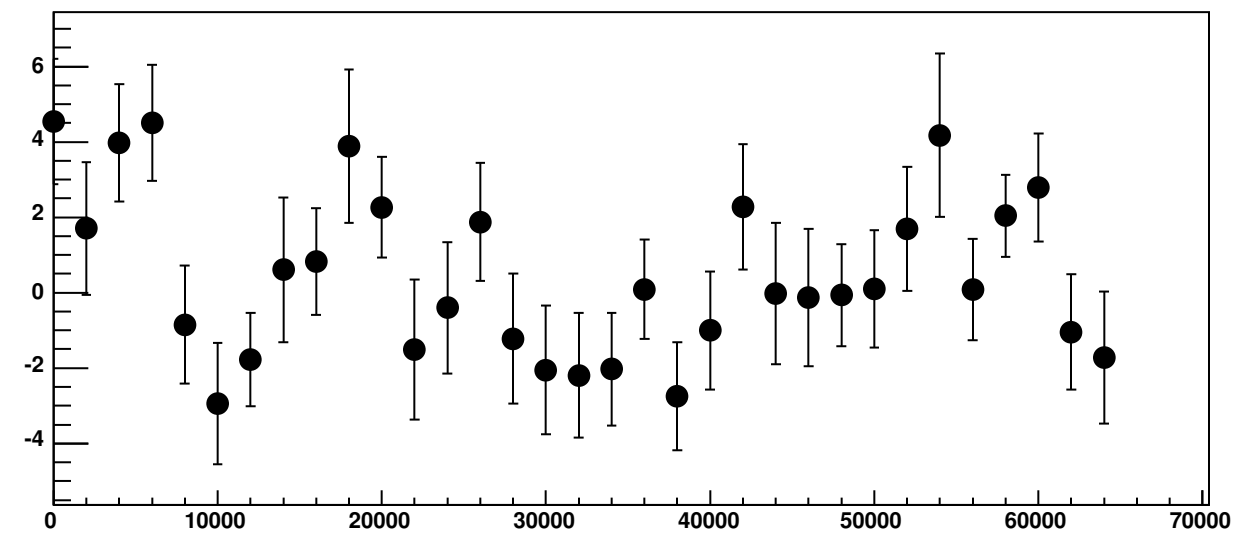
Chip 1, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC

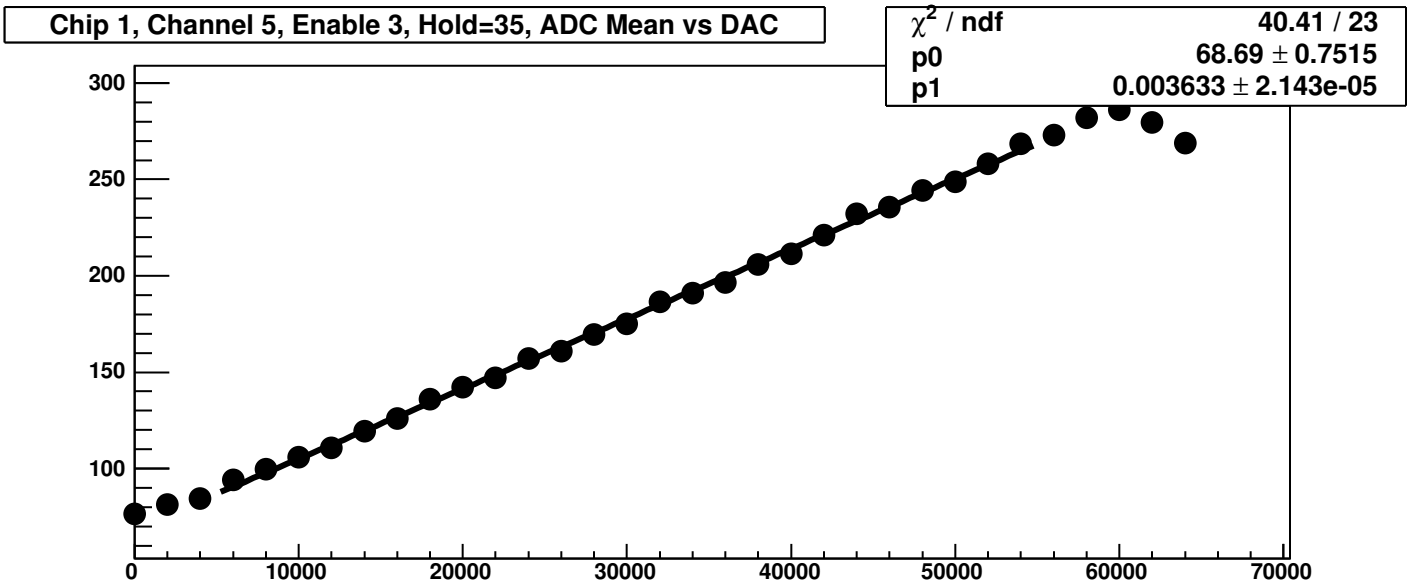


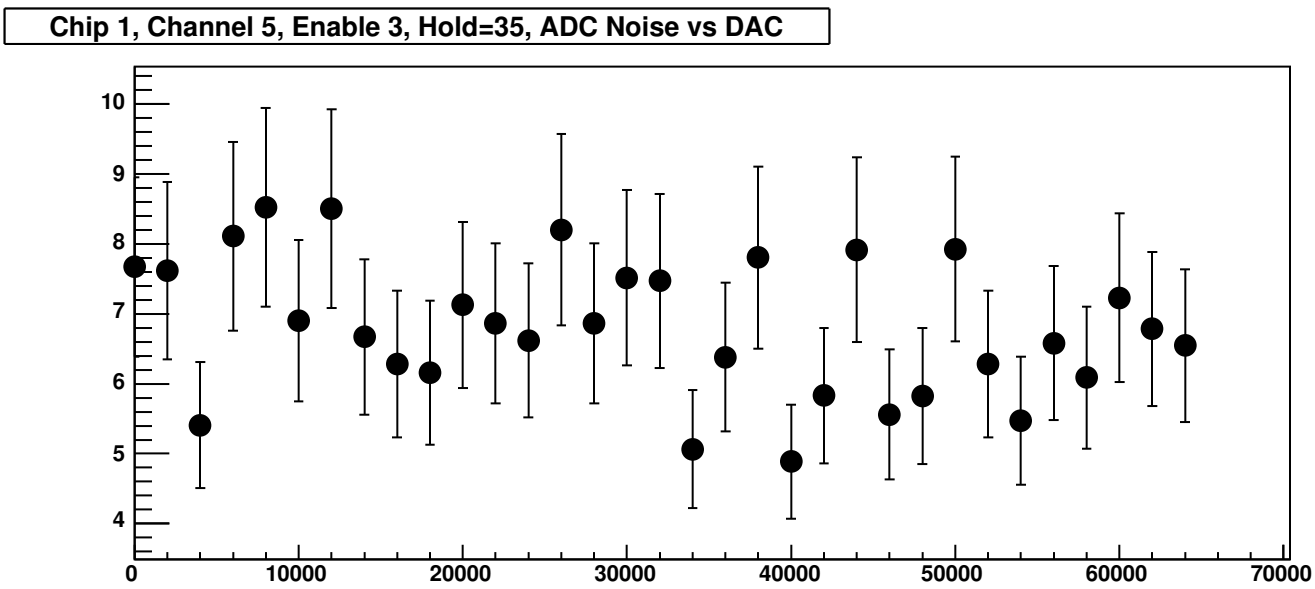
Chip 1, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC

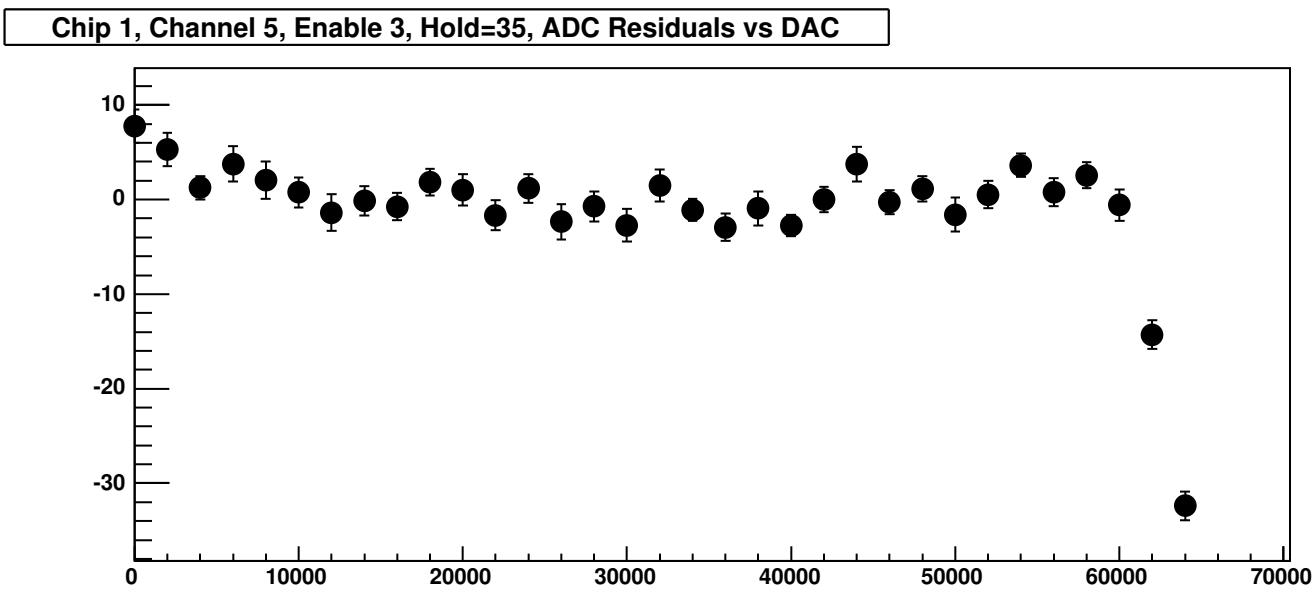


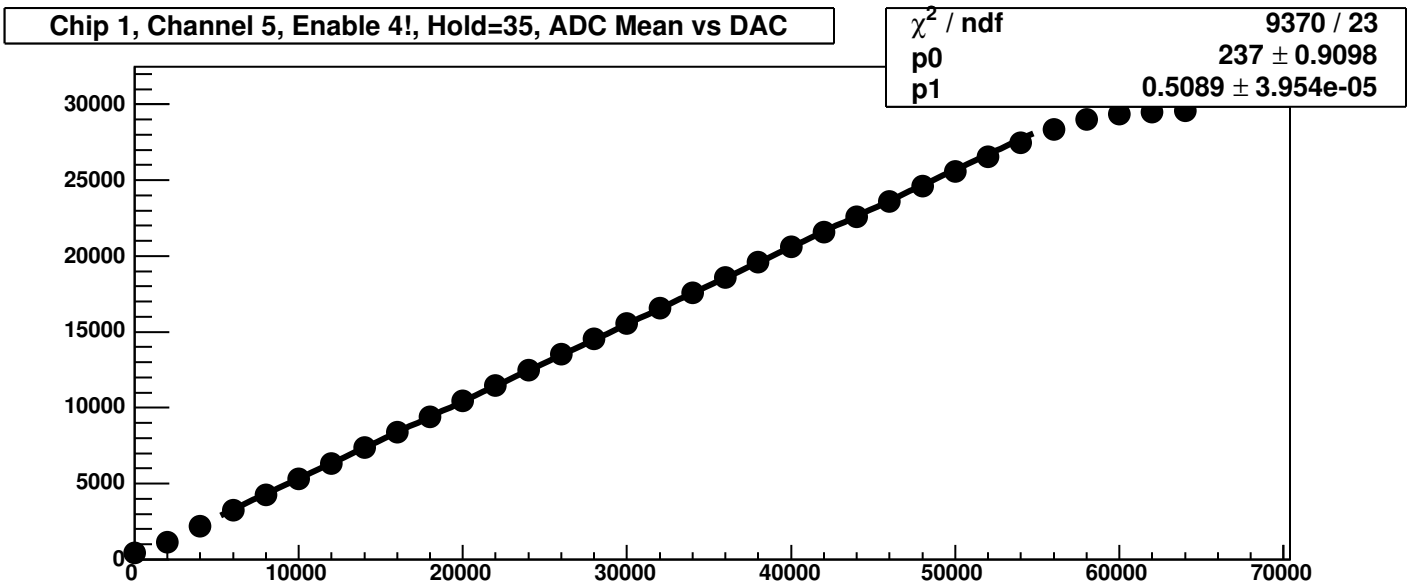
Chip 1, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC

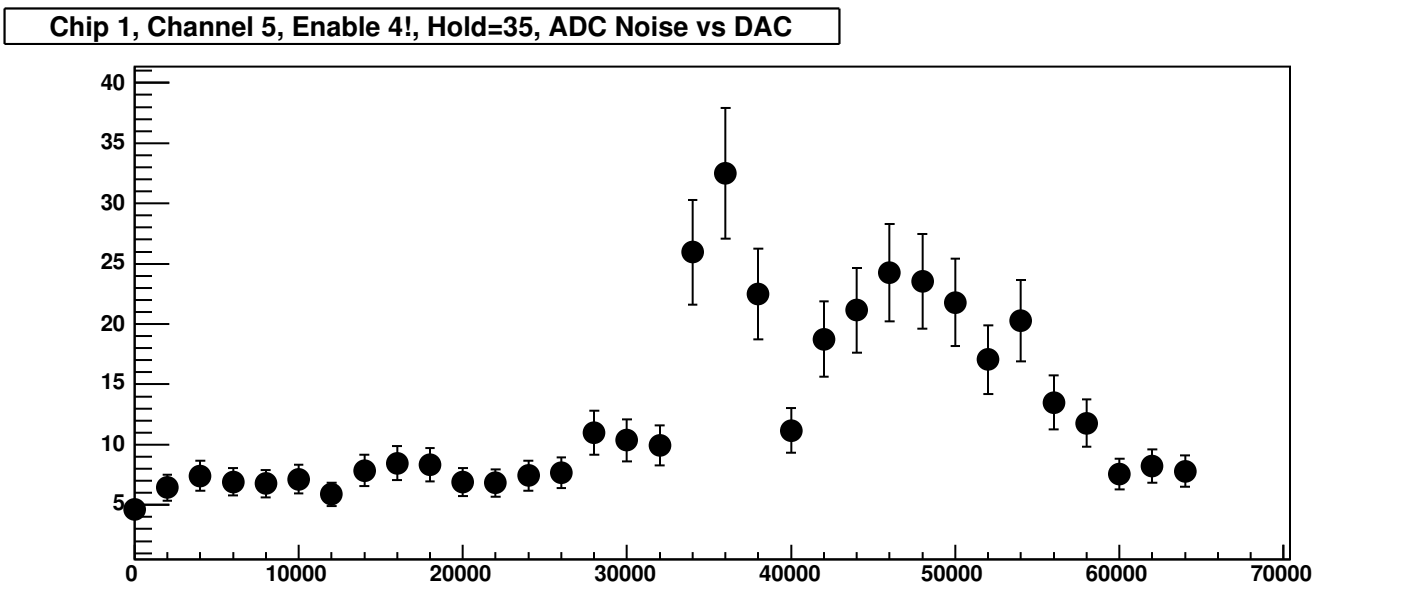


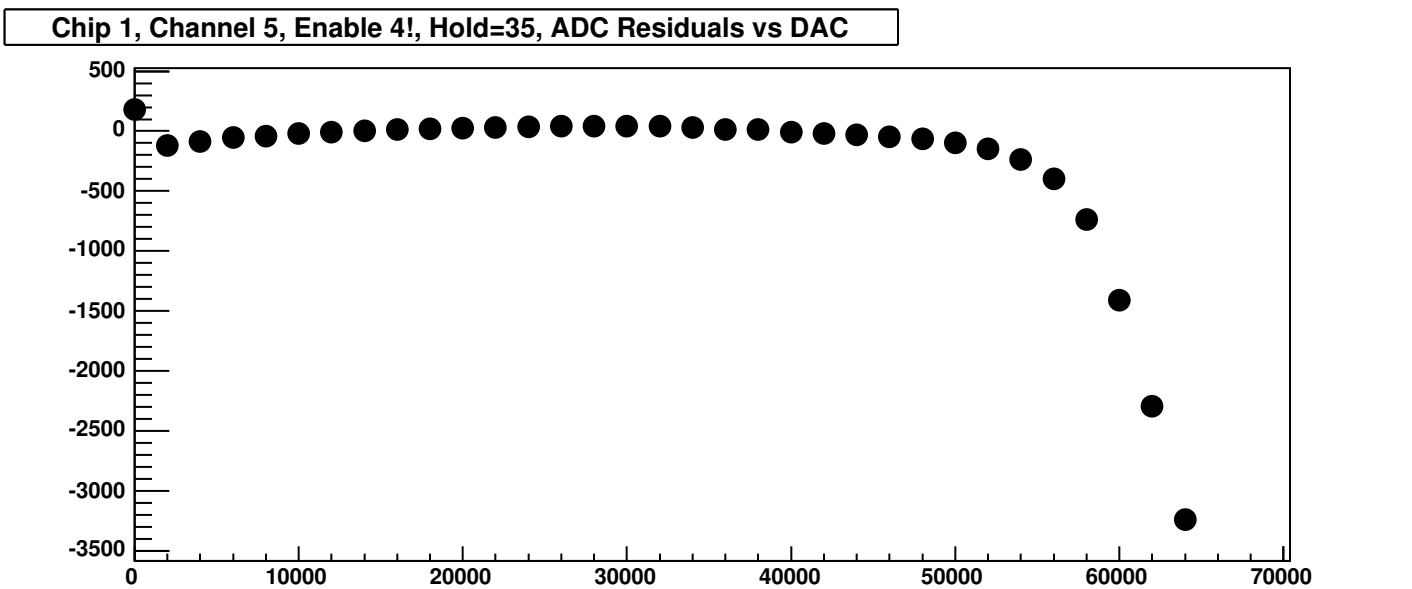

Chip 1, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 40.41 / 23
p0 68.69 ± 0.7515
p1 0.003633 ± 2.143e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

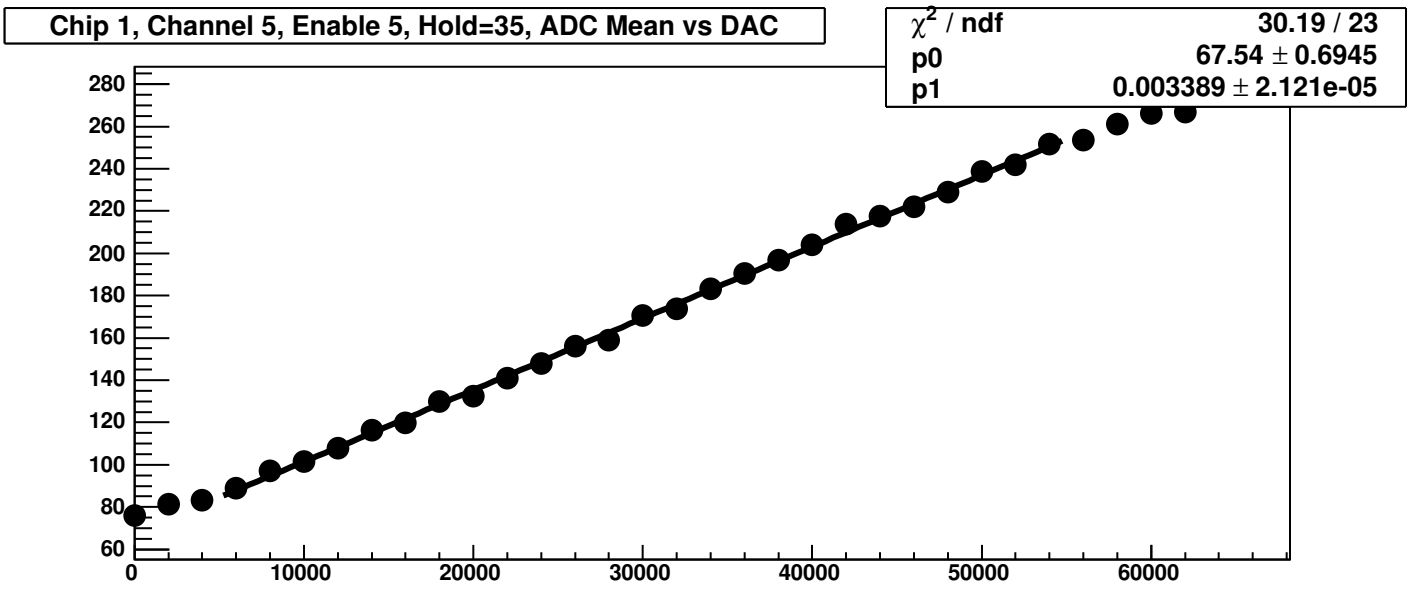

Chip 1, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

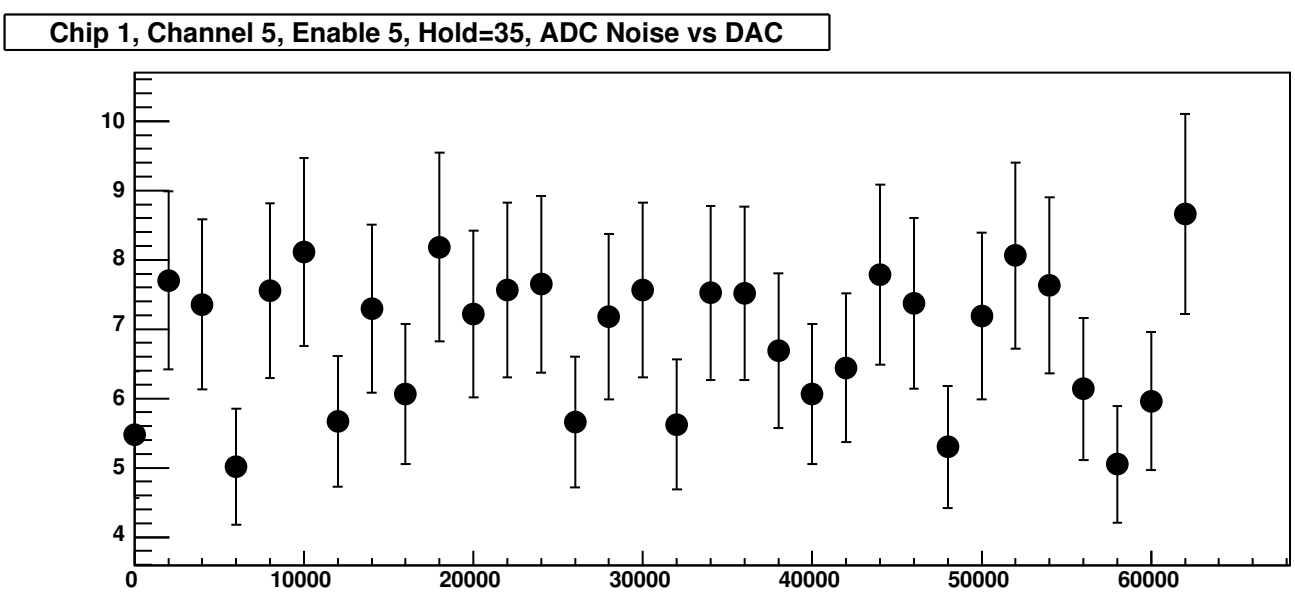

Chip 1, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
10000
20000
30000
40000
50000
60000
70000

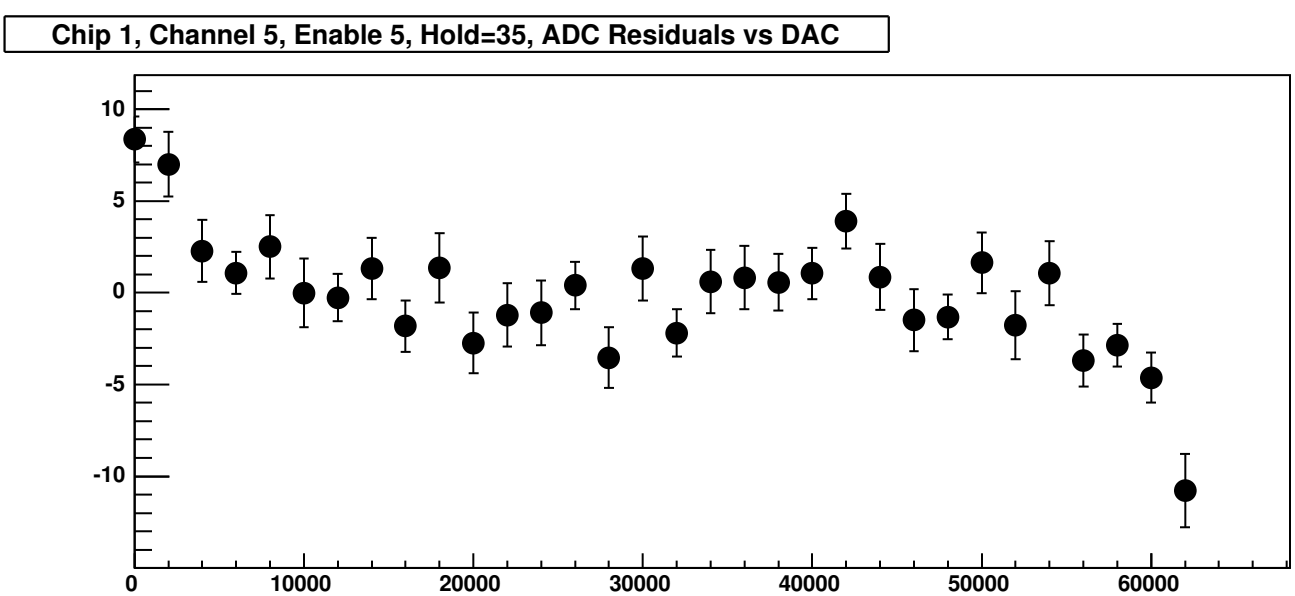

Chip 1, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 9370 / 23
p0 237 ± 0.9098
p1 0.5089 ± 3.954e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

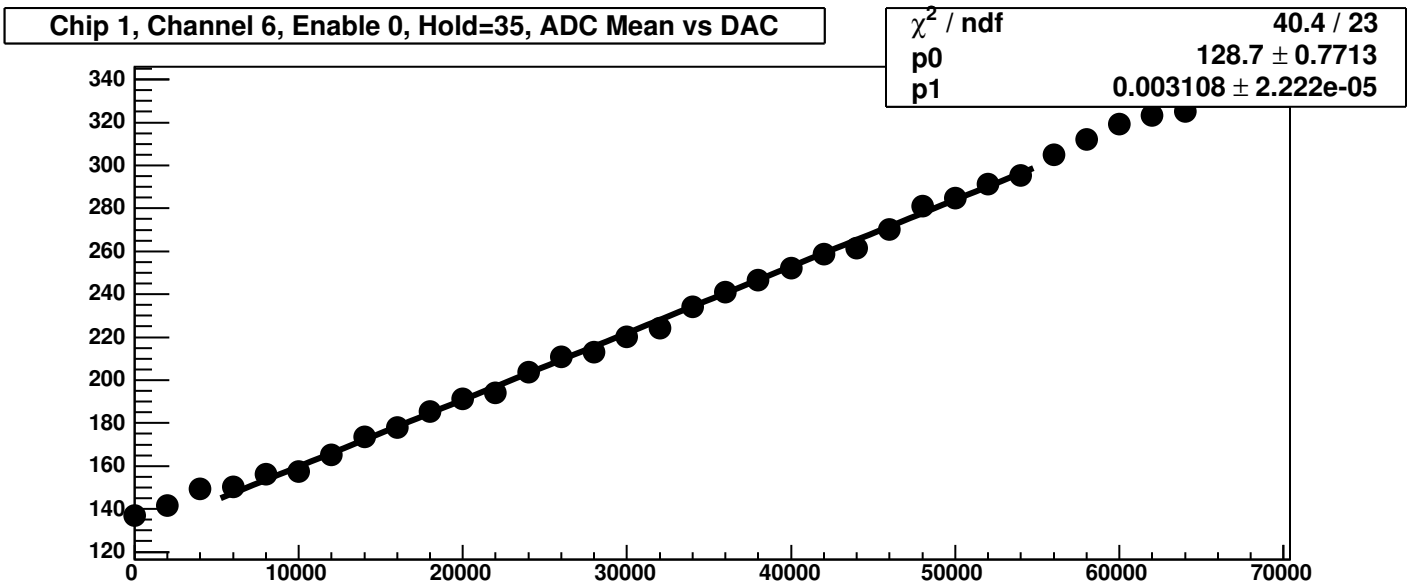

Chip 1, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000

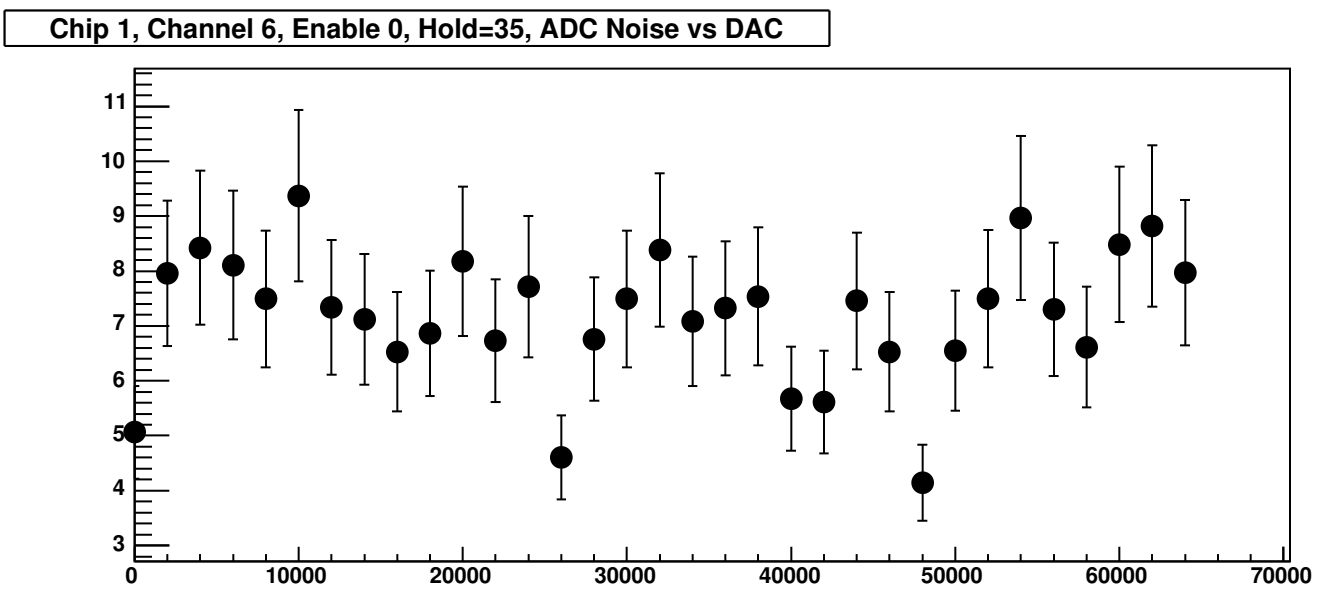

Chip 1, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
500
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
0
10000
20000
30000
40000
50000
60000
70000

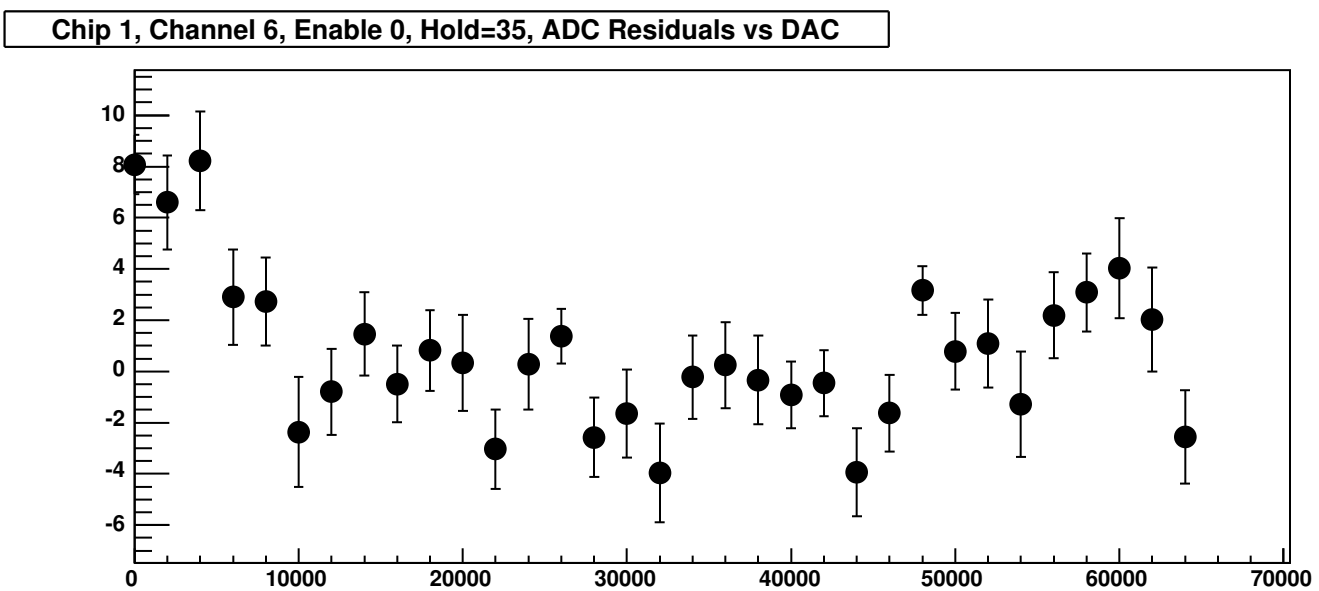

Chip 1, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 30.19 / 23
p0 67.54 ± 0.6945
p1 0.003389 ± 2.121e-05
280
260
240
220
200
180
160
140
120
100
80
60
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

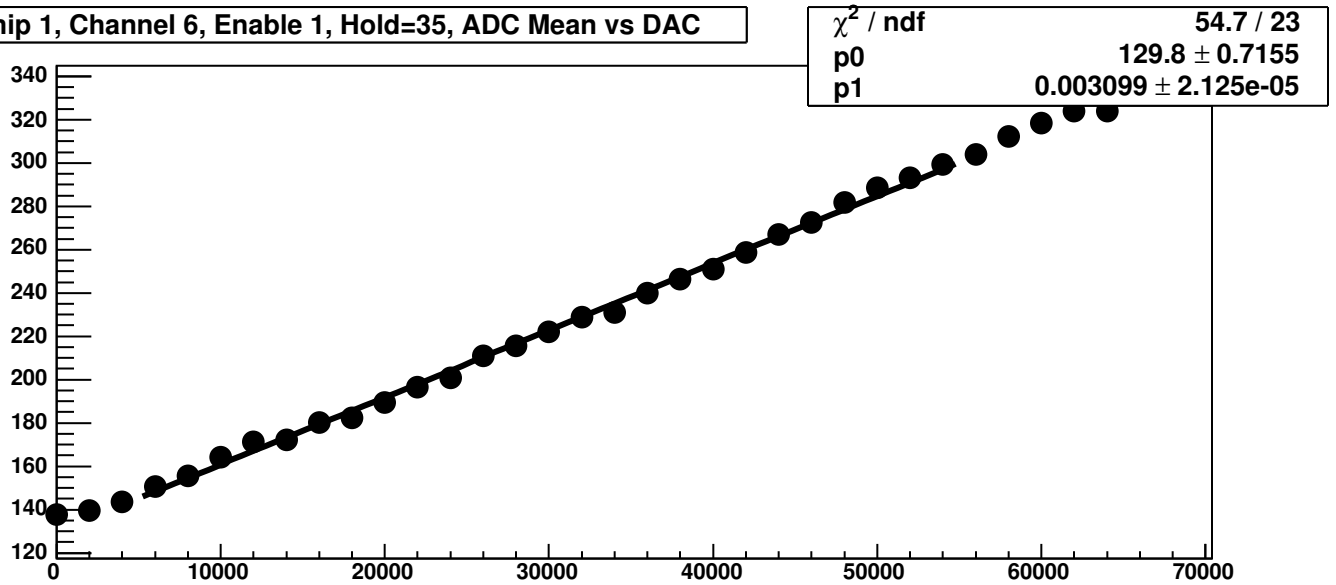

Chip 1, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000


Chip 1, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 40.4 / 23
p0 128.7 ± 0.7713
p1 0.003108 ± 2.222e-05
340
320
300
280
260
240
220
200
180
160
140
120
0
10000
20000
30000
40000
50000
60000
70000

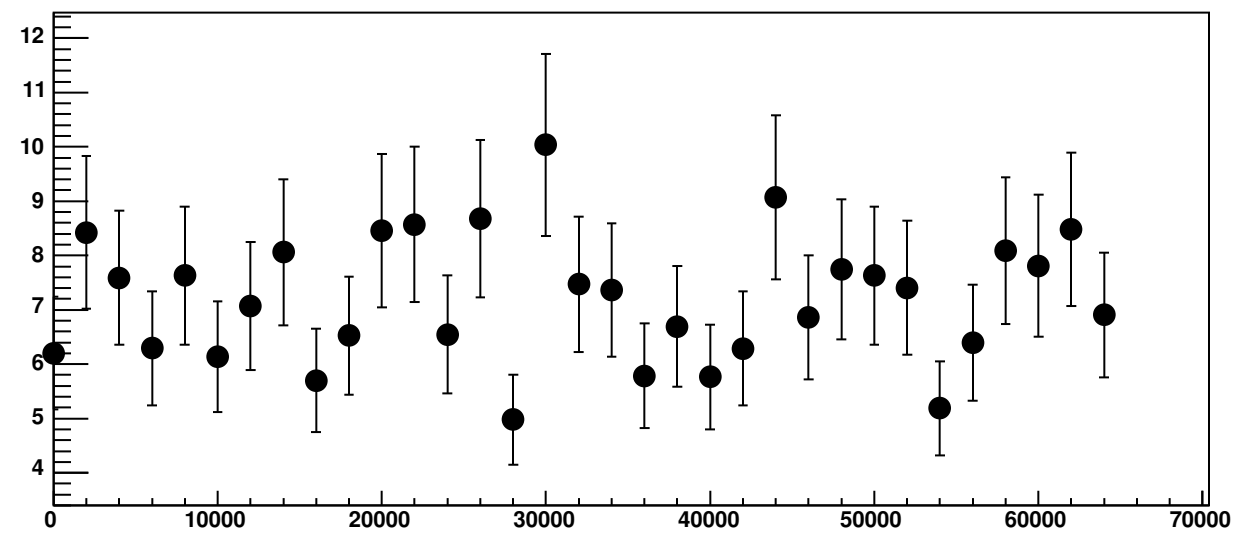

Chip 1, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

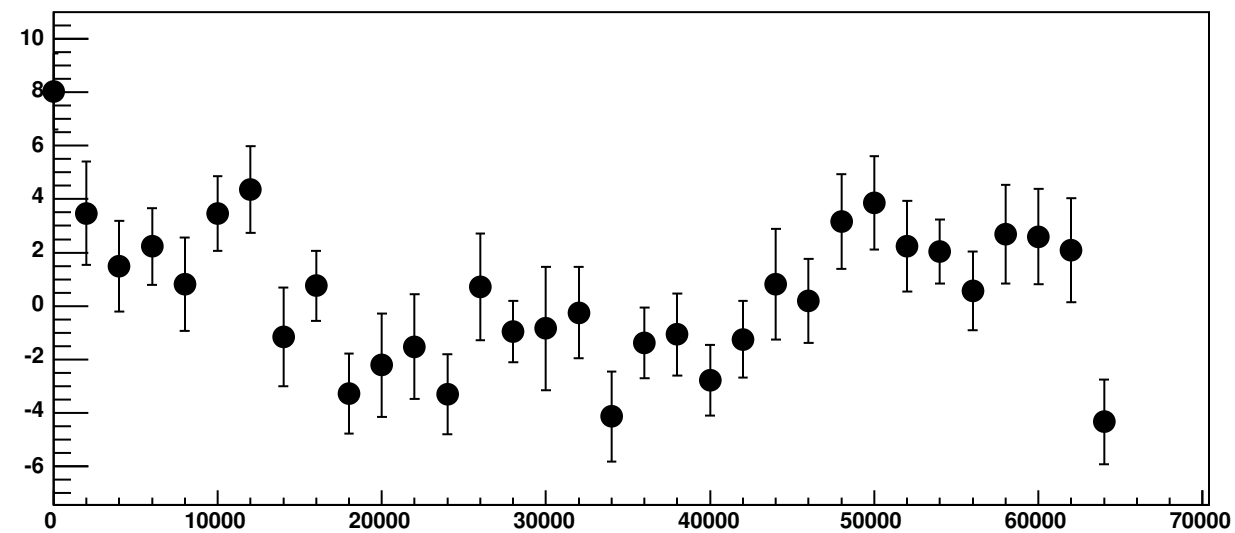
Chip 1, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC



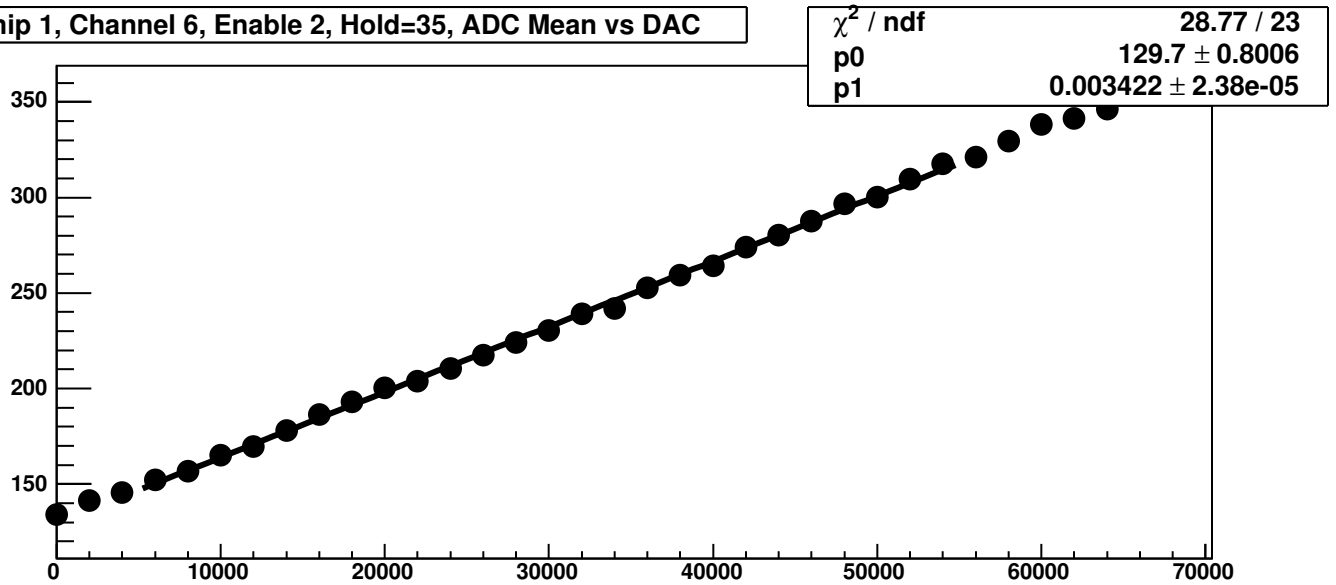
Chip 1, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC



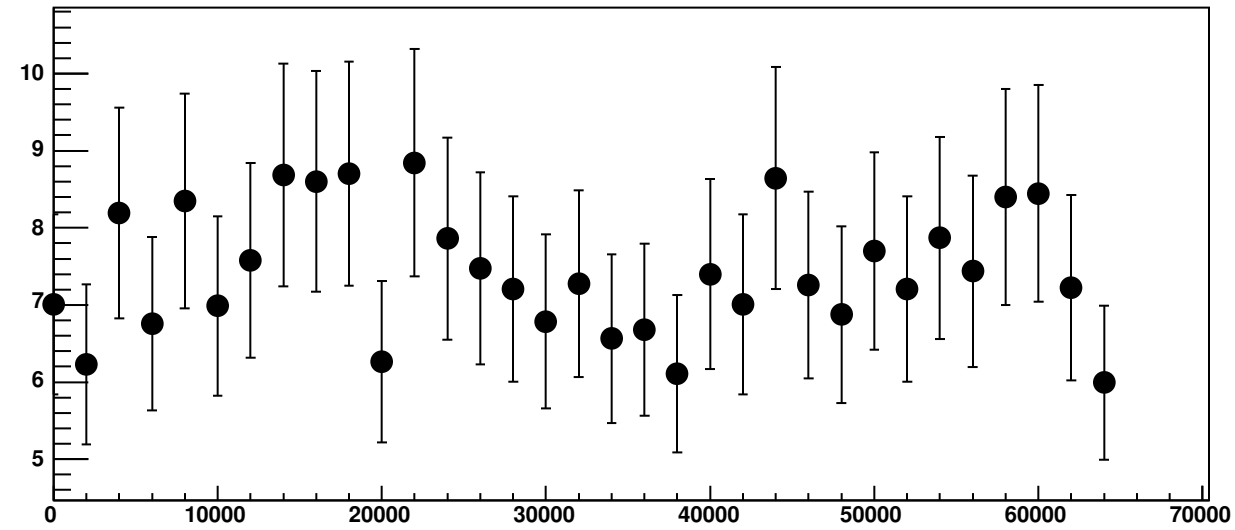
Chip 1, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC



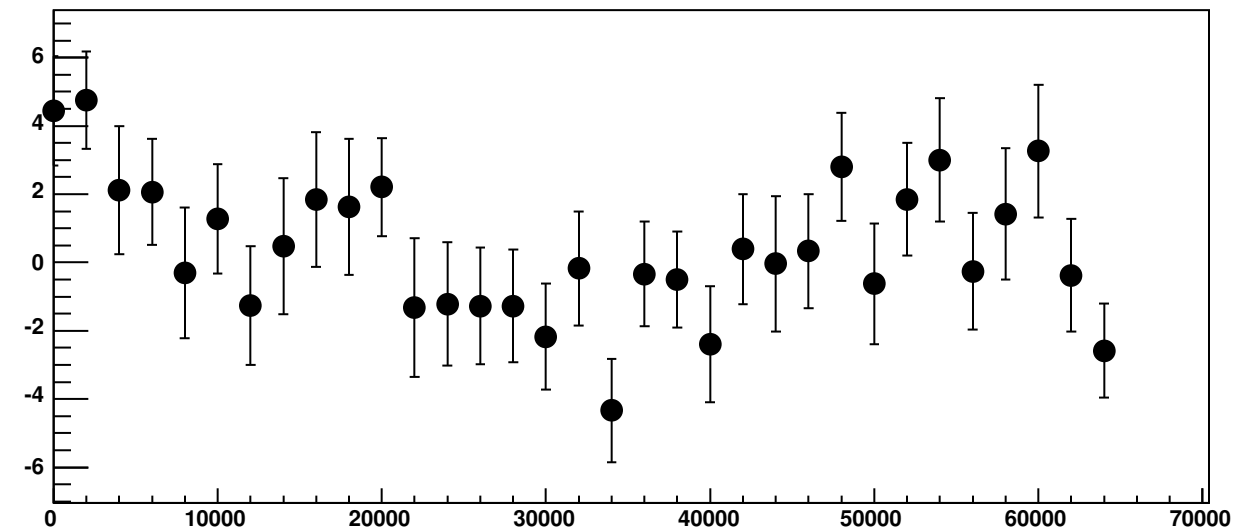
Chip 1, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC

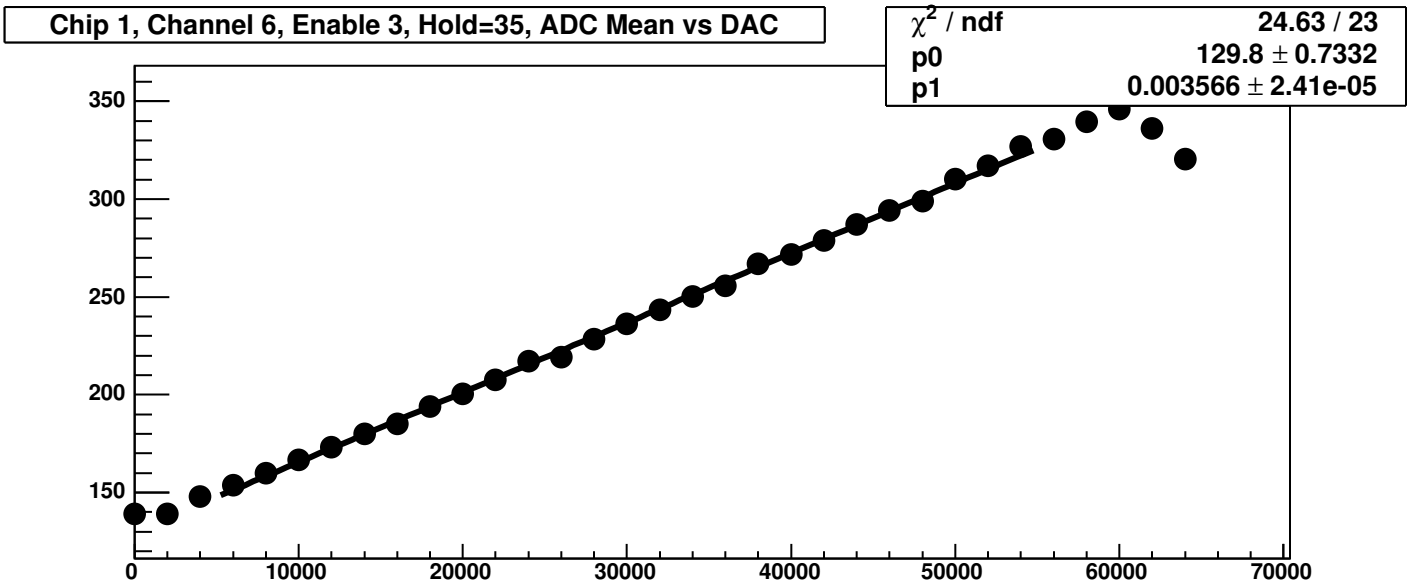


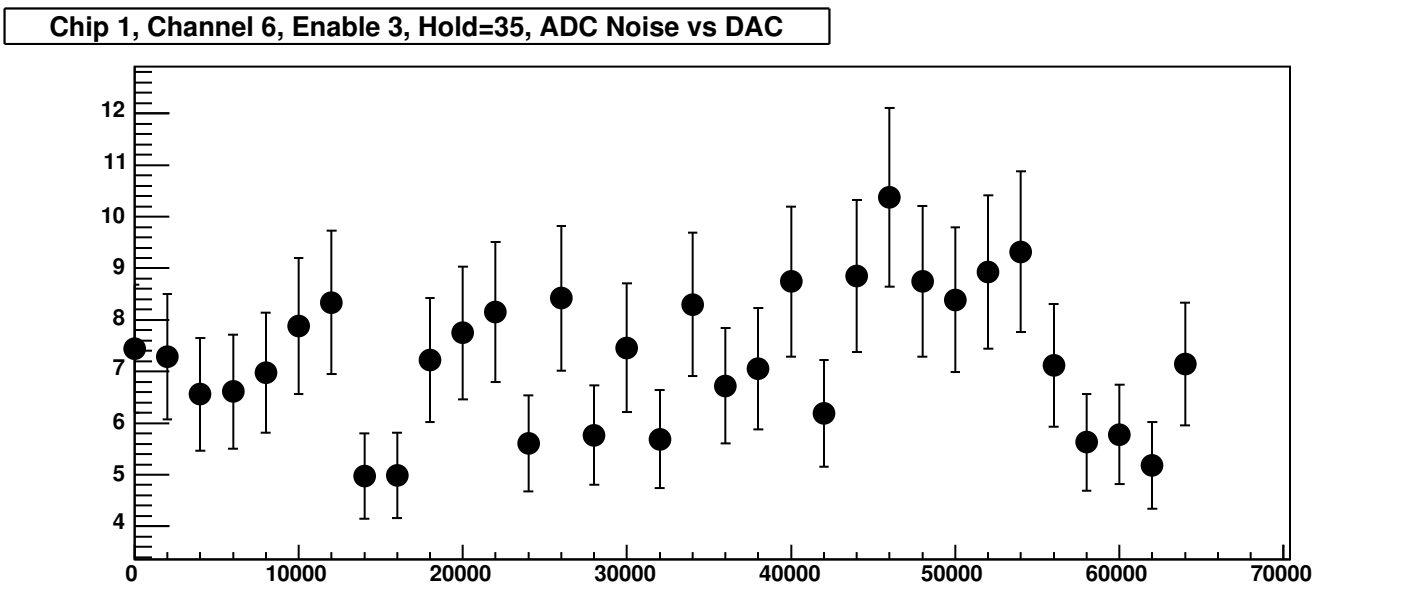
Chip 1, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC

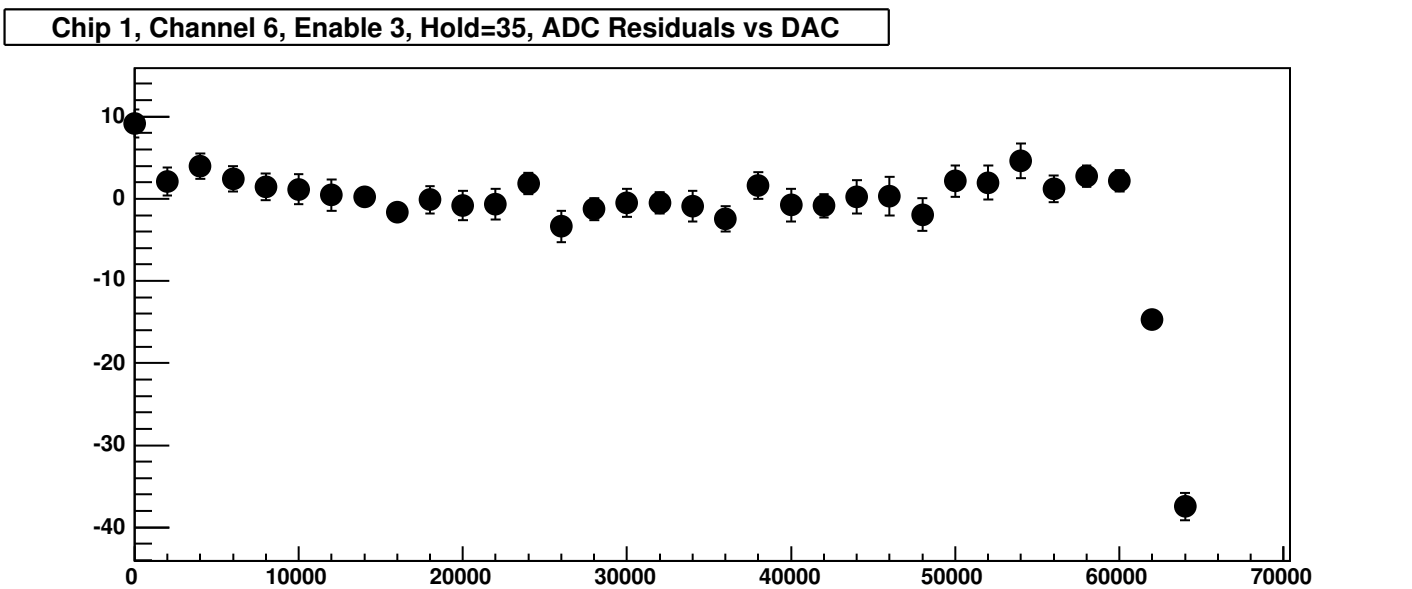


Chip 1, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC

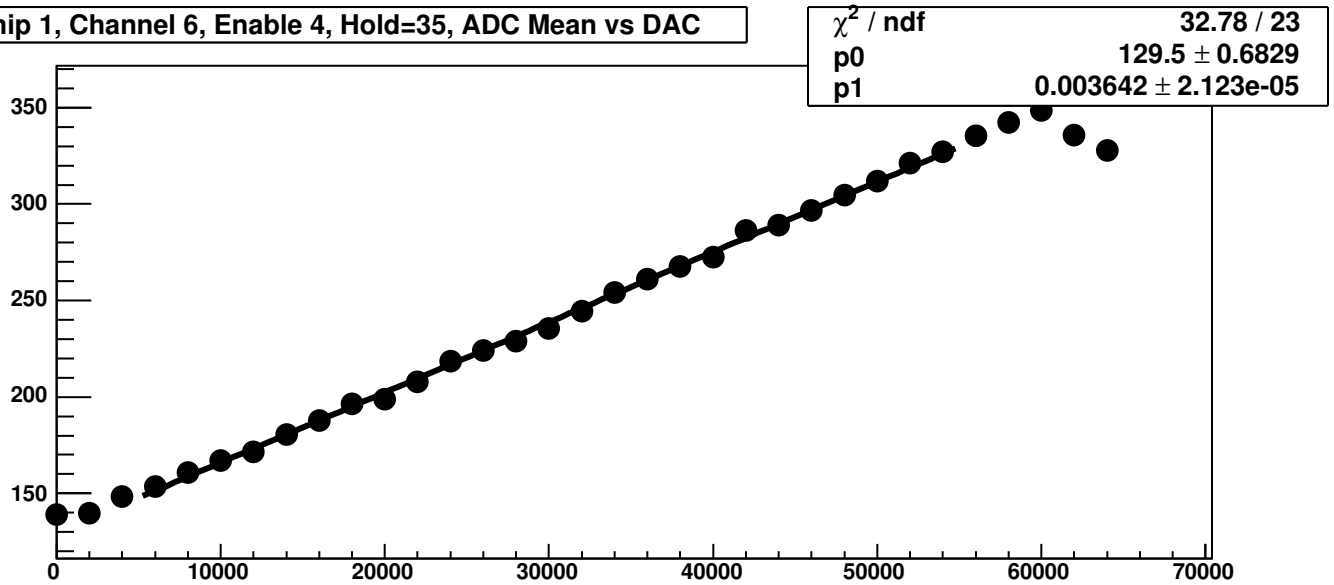



Chip 1, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.63 / 23
p0 129.8 ± 0.7332
p1 0.003566 ± 2.41e-05
350
300
250
200
150
0
10000
20000
30000
40000
50000
60000
70000

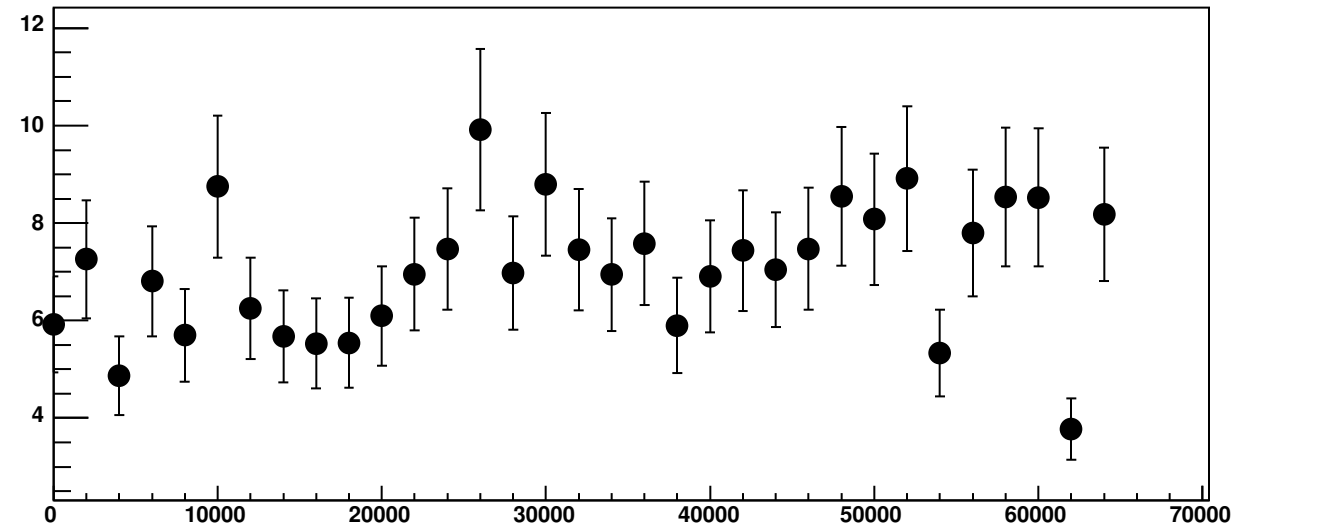

Chip 1, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
10000
20000
30000
40000
50000
60000
70000

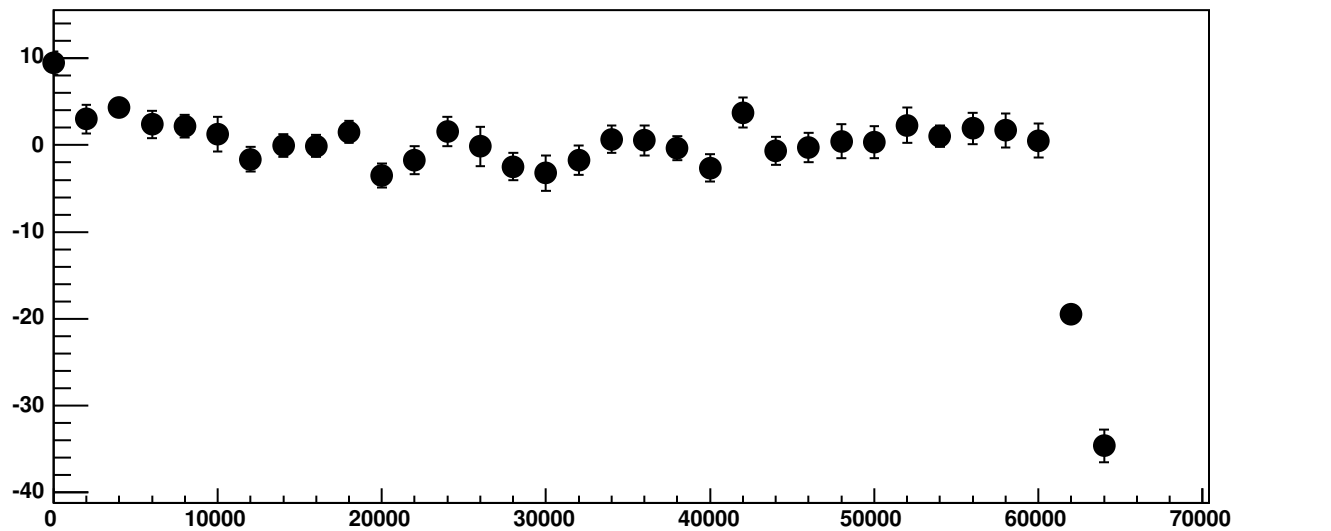
Chip 1, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

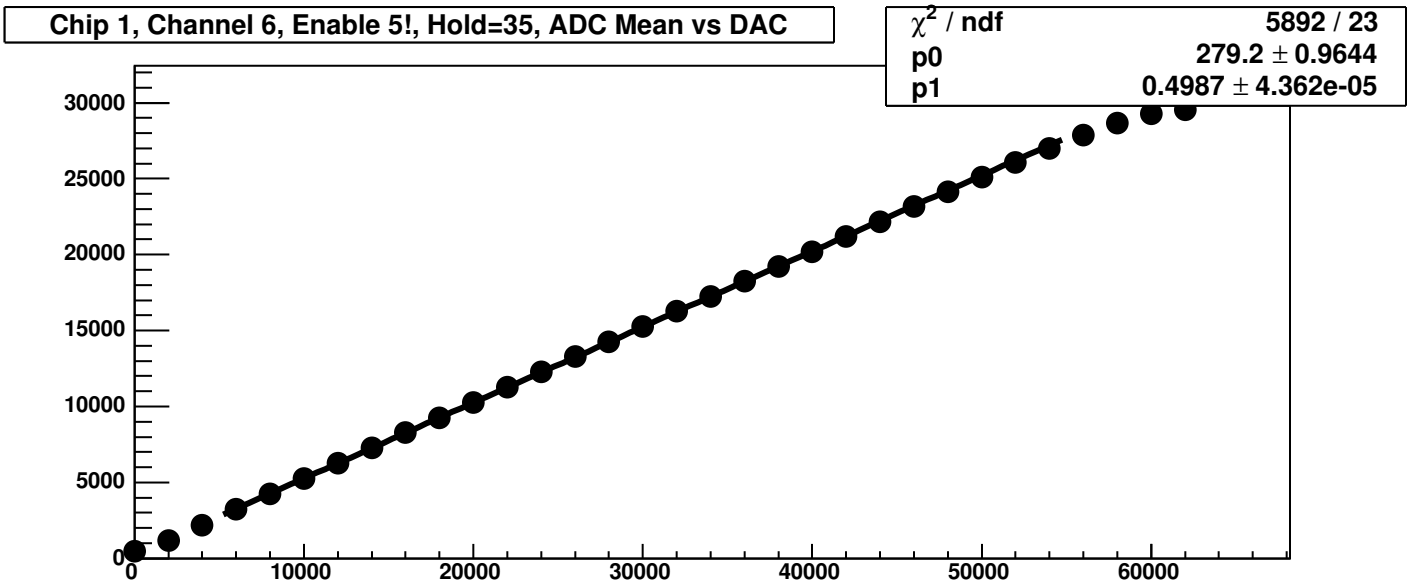


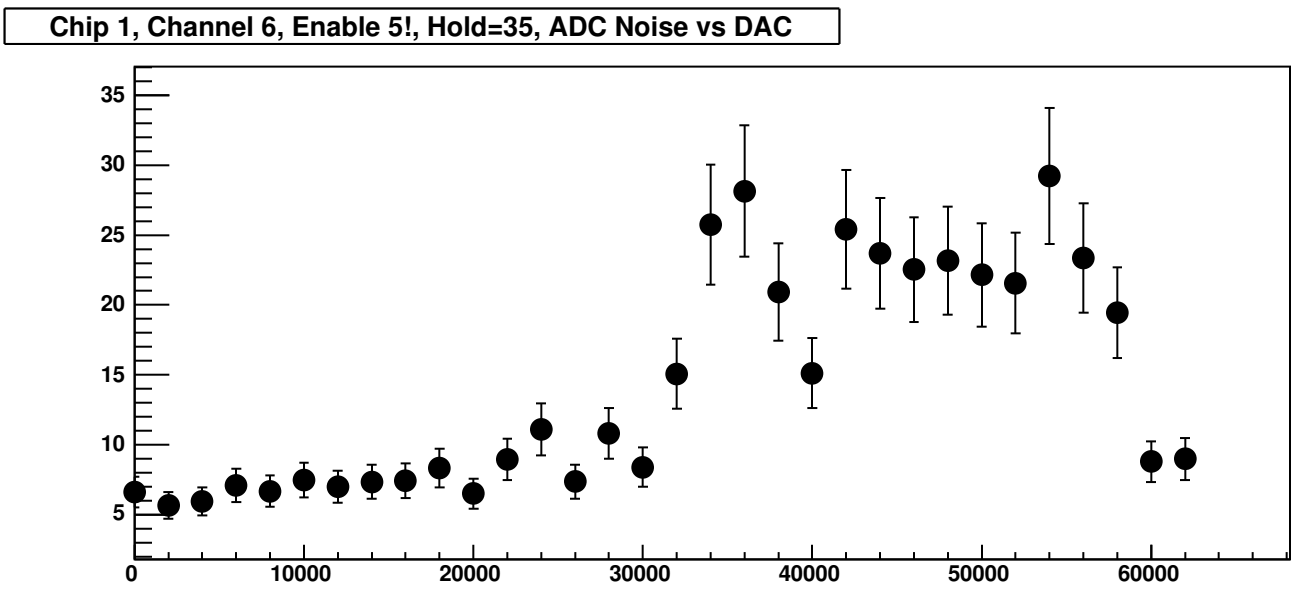
Chip 1, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

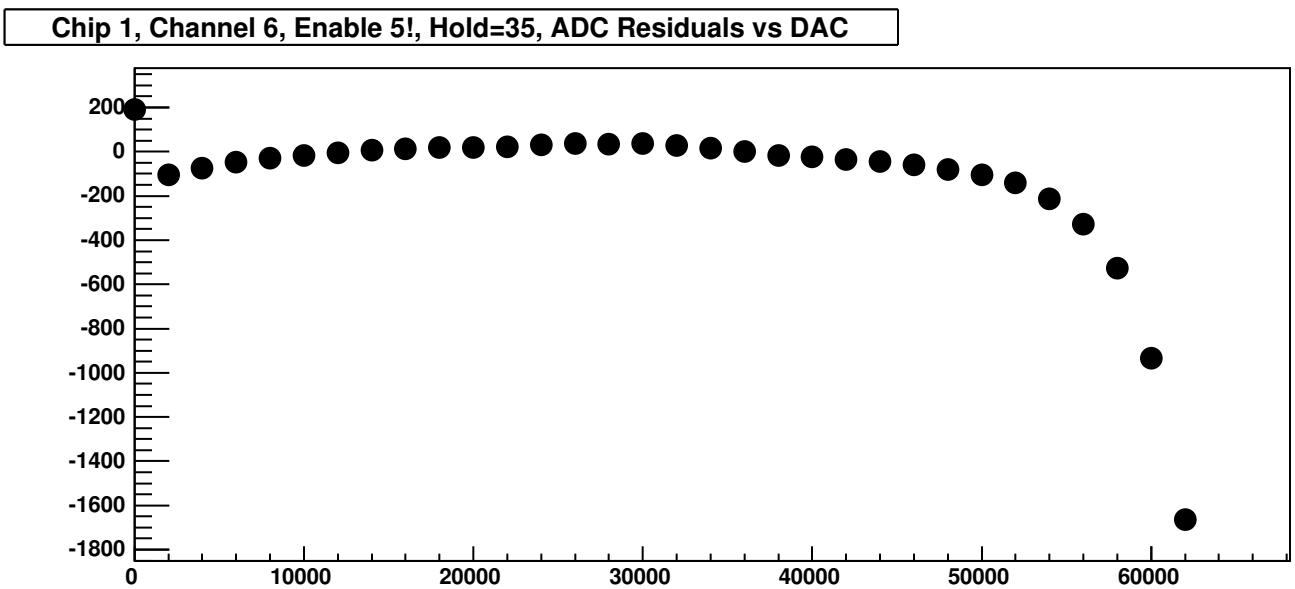


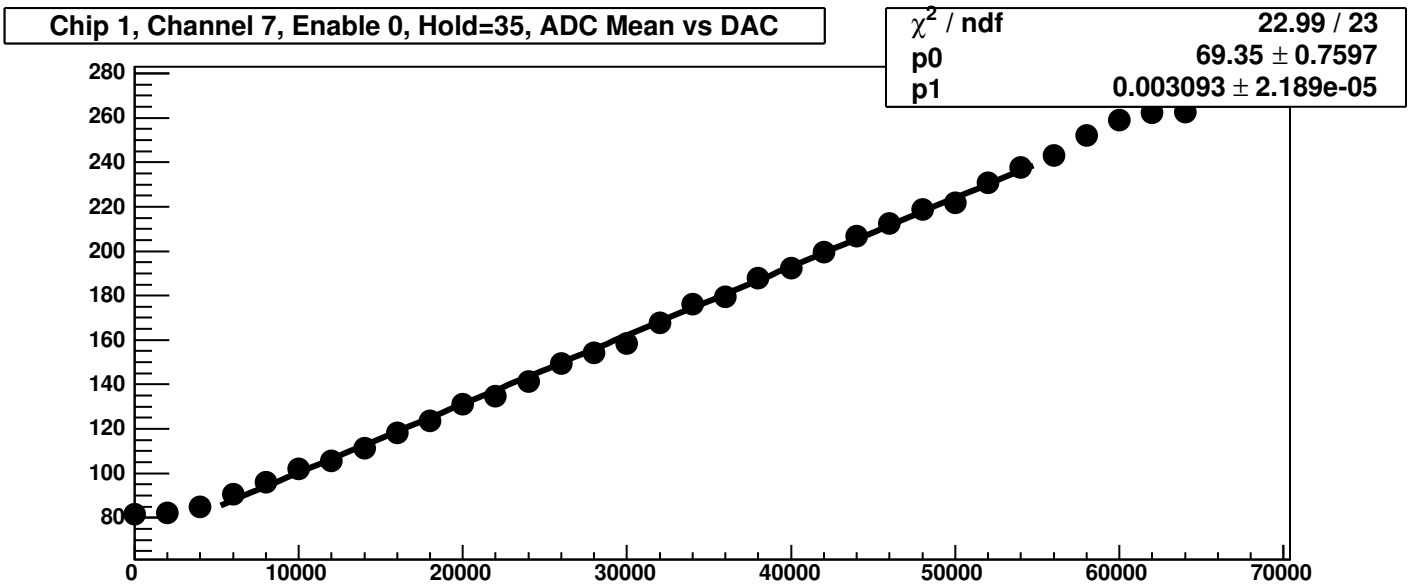
Chip 1, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

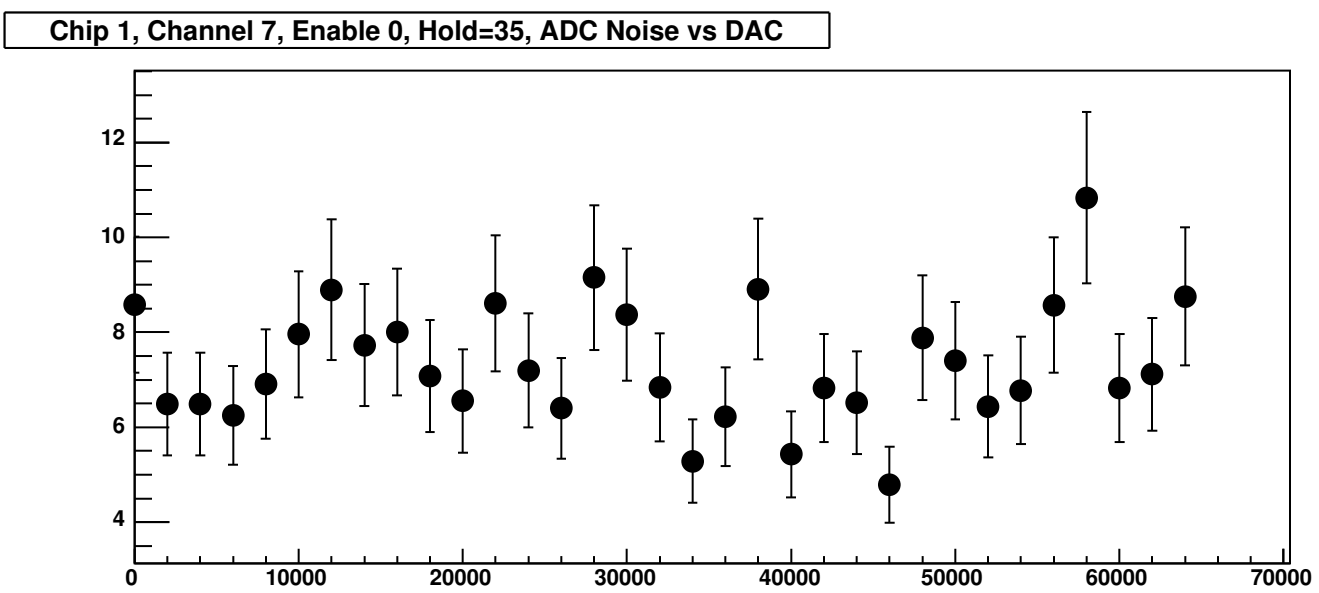


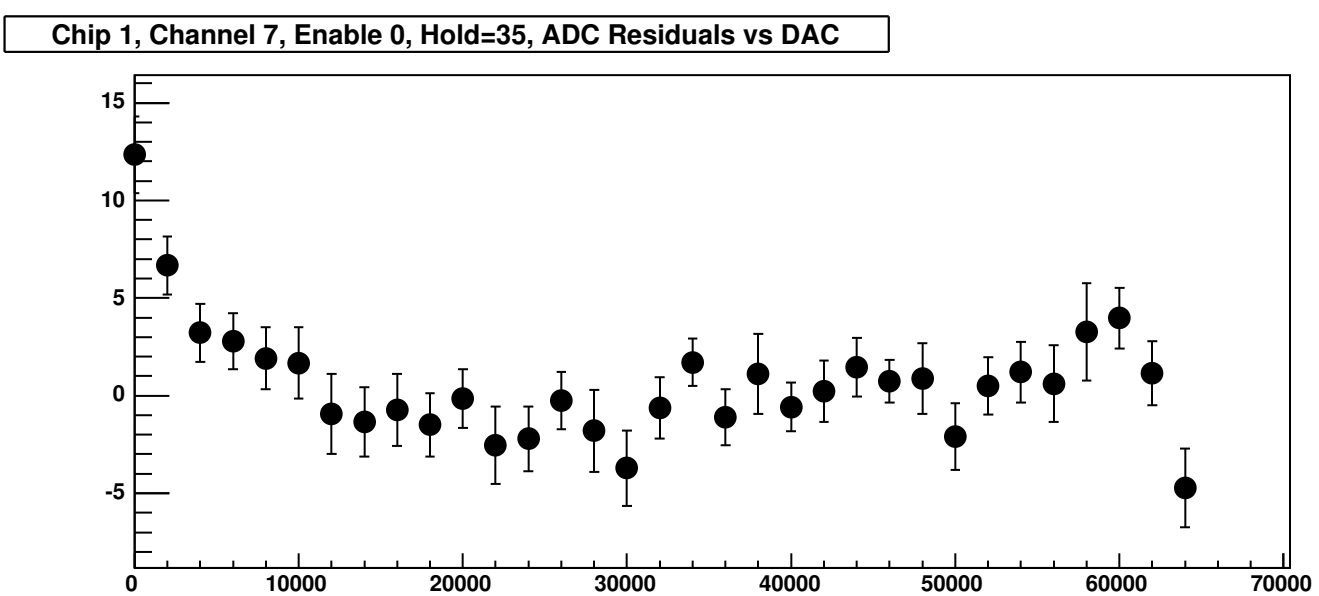

Chip 1, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 5892 / 23
p0 279.2 ± 0.9644
p1 0.4987 ± 4.362e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

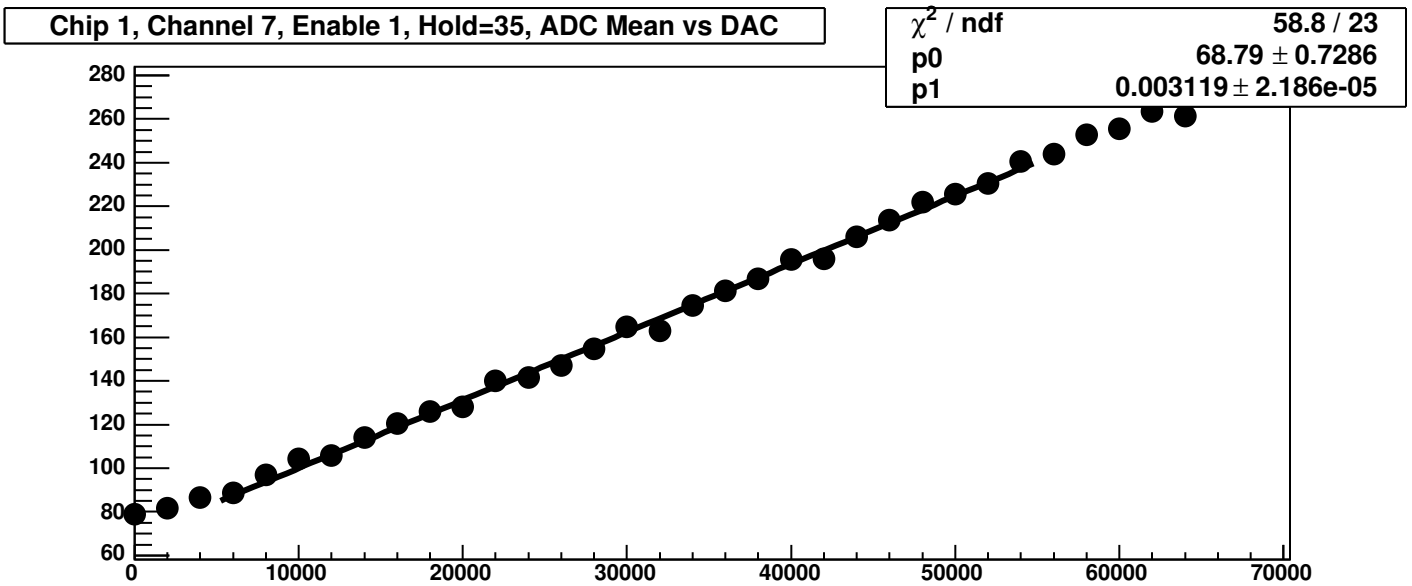

Chip 1, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000

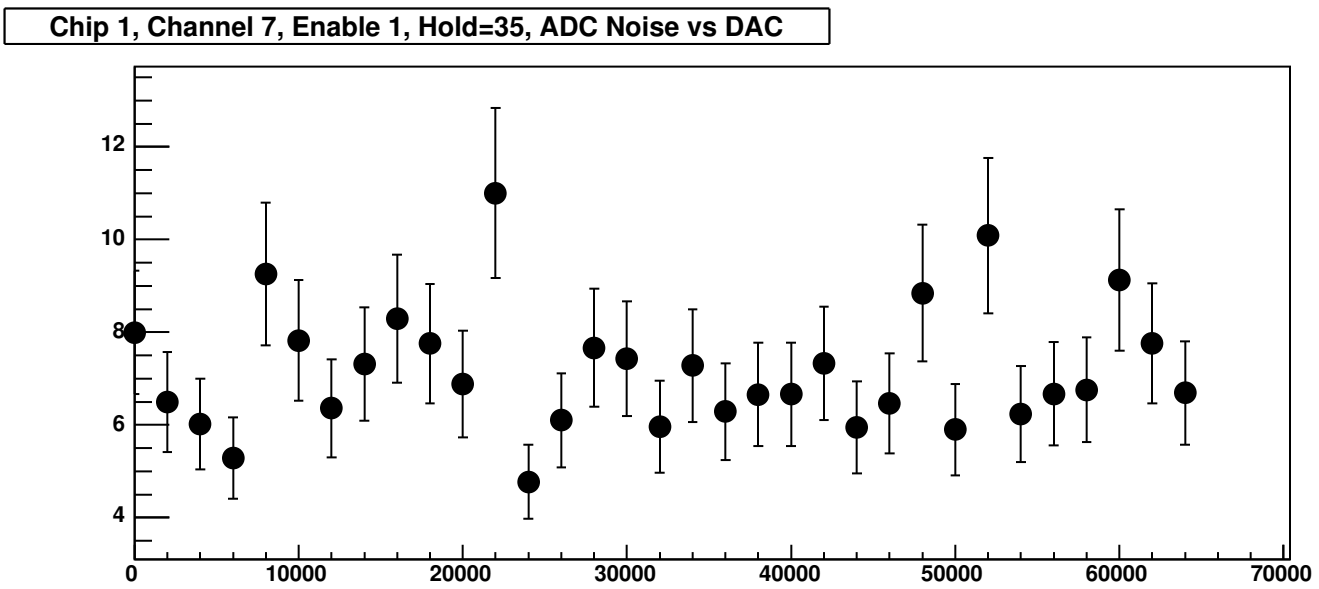

Chip 1, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
10000
20000
30000
40000
50000
60000

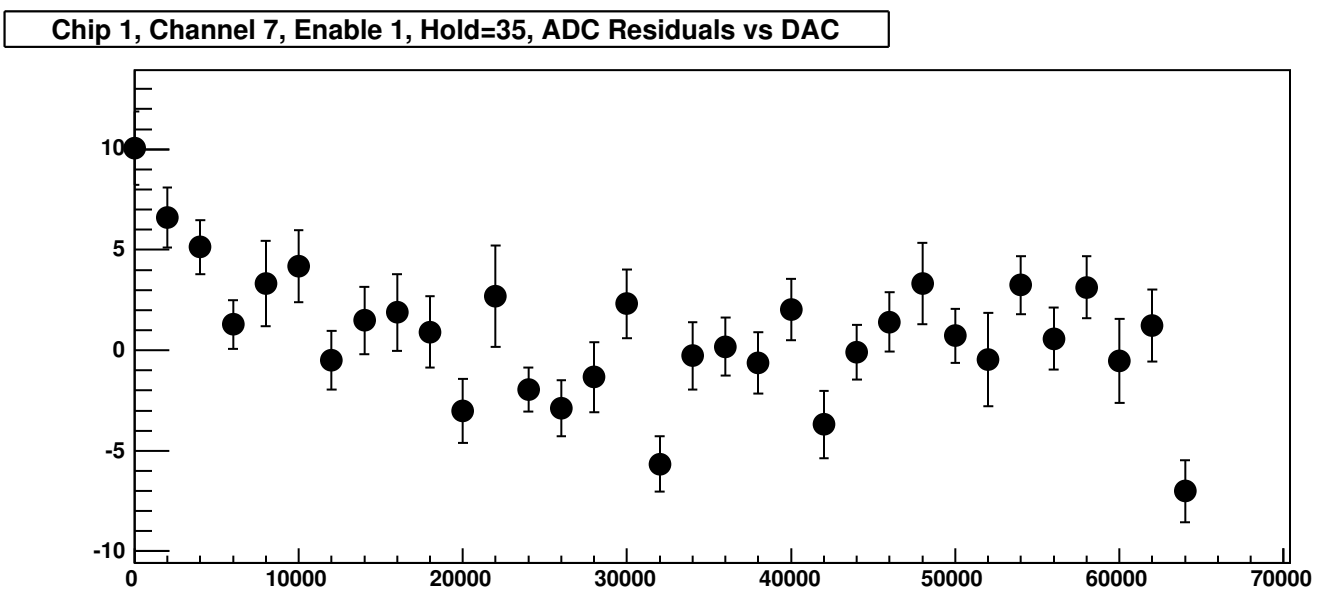

Chip 1, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 22.99 / 23
p0 69.35 ± 0.7597
p1 0.003093 ± 2.189e-05
280
260
240
220
200
180
160
140
120
100
80
0
10000
20000
30000
40000
50000
60000
70000

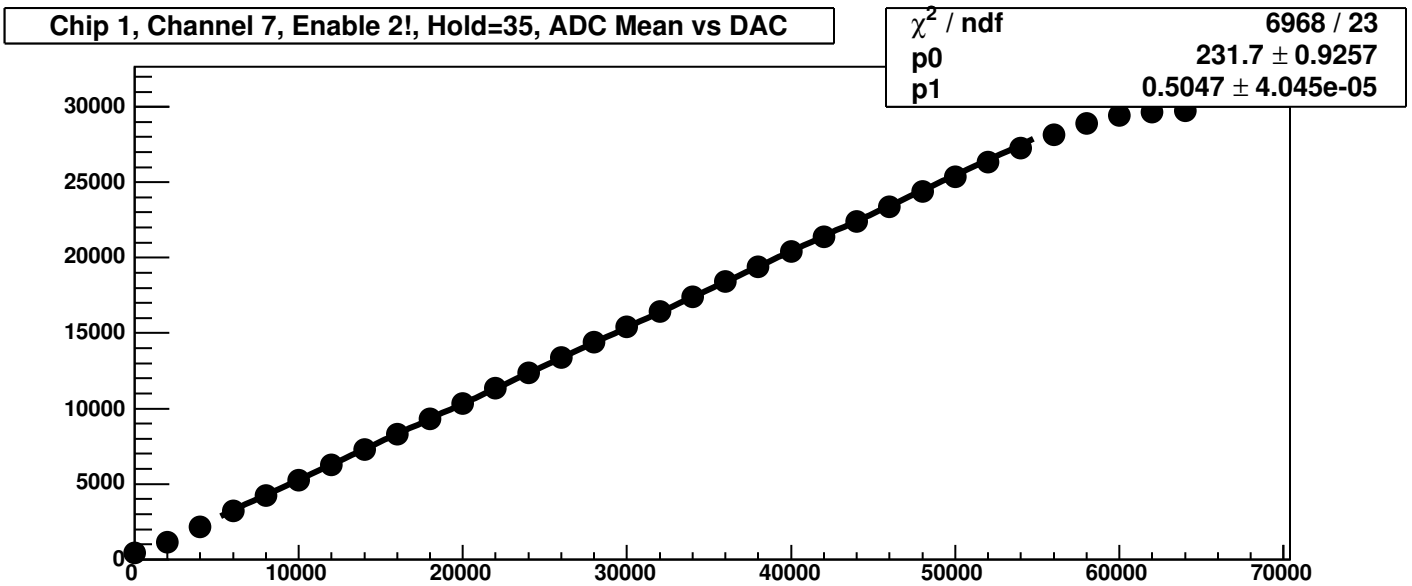

Chip 1, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

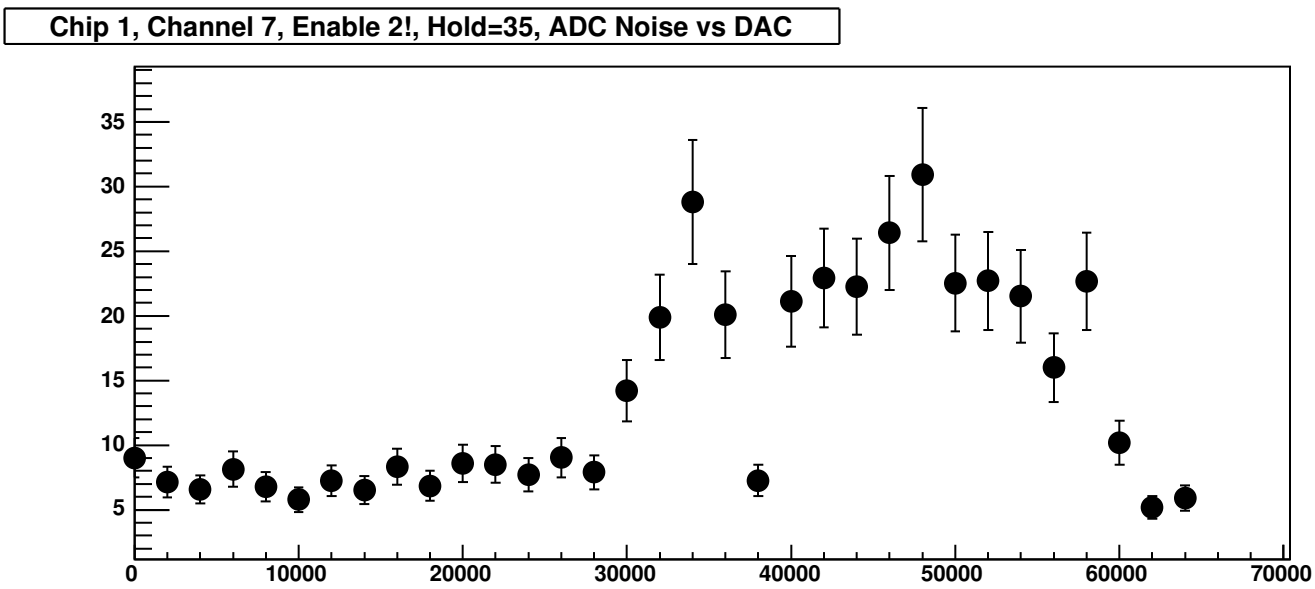

Chip 1, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

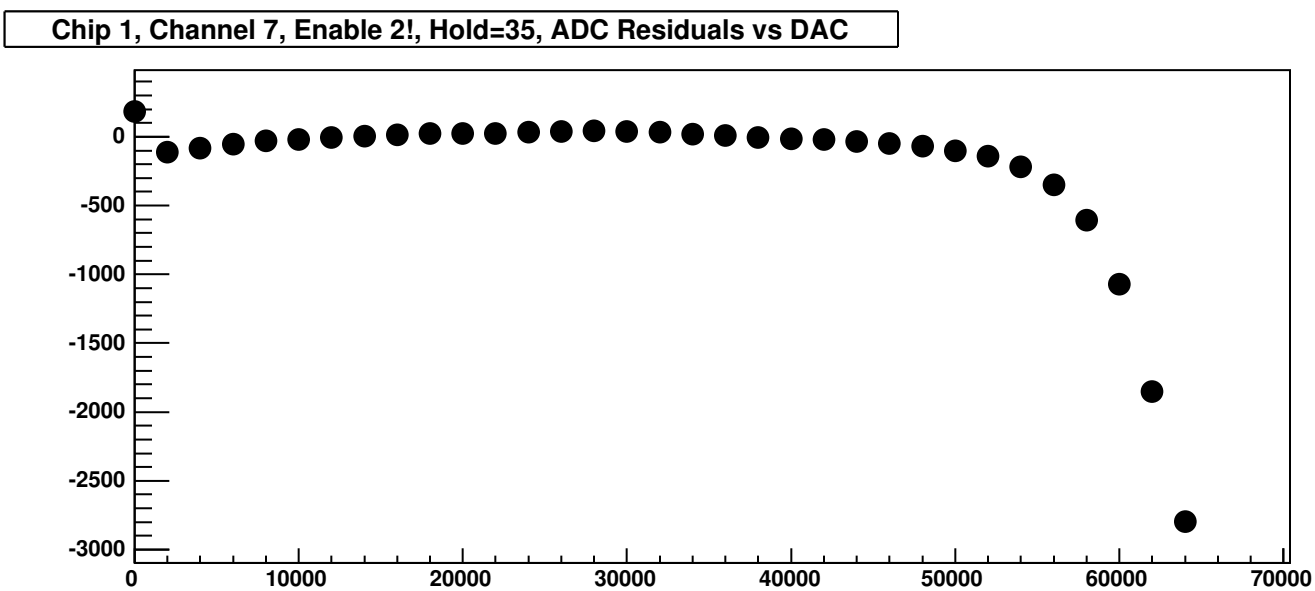

Chip 1, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 58.8 / 23
p0 68.79 ± 0.7286
p1 0.003119 ± 2.186e-05
280
260
240
220
200
180
160
140
120
100
80
60
0
10000
20000
30000
40000
50000
60000
70000

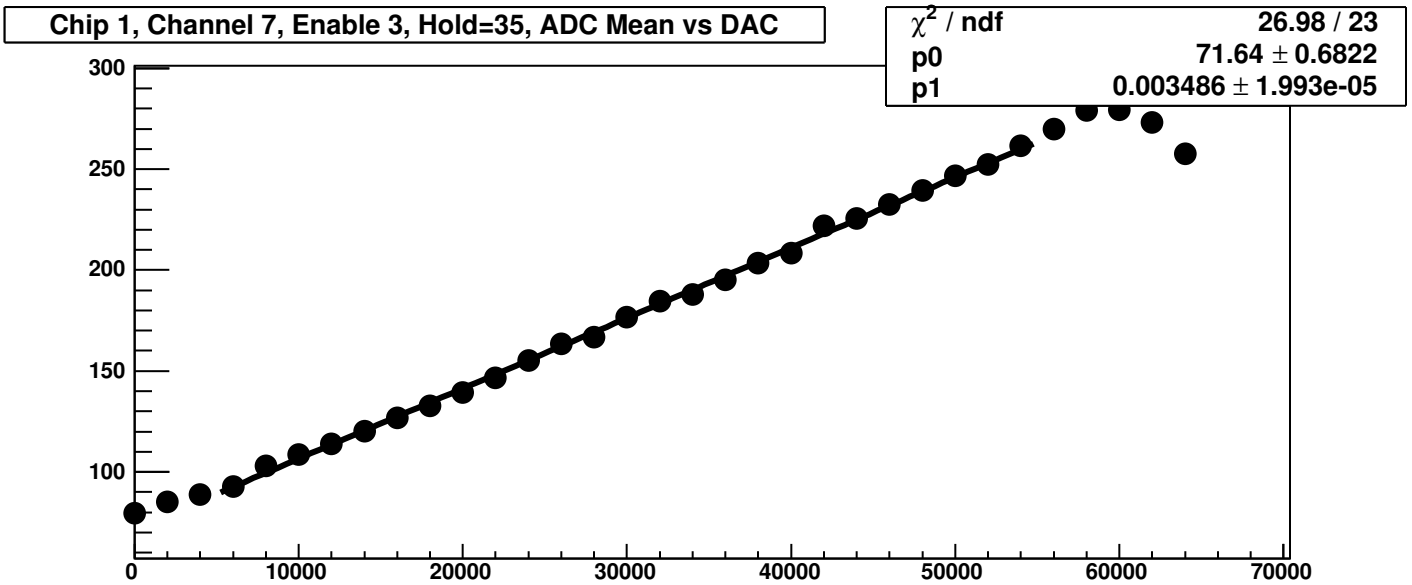

Chip 1, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

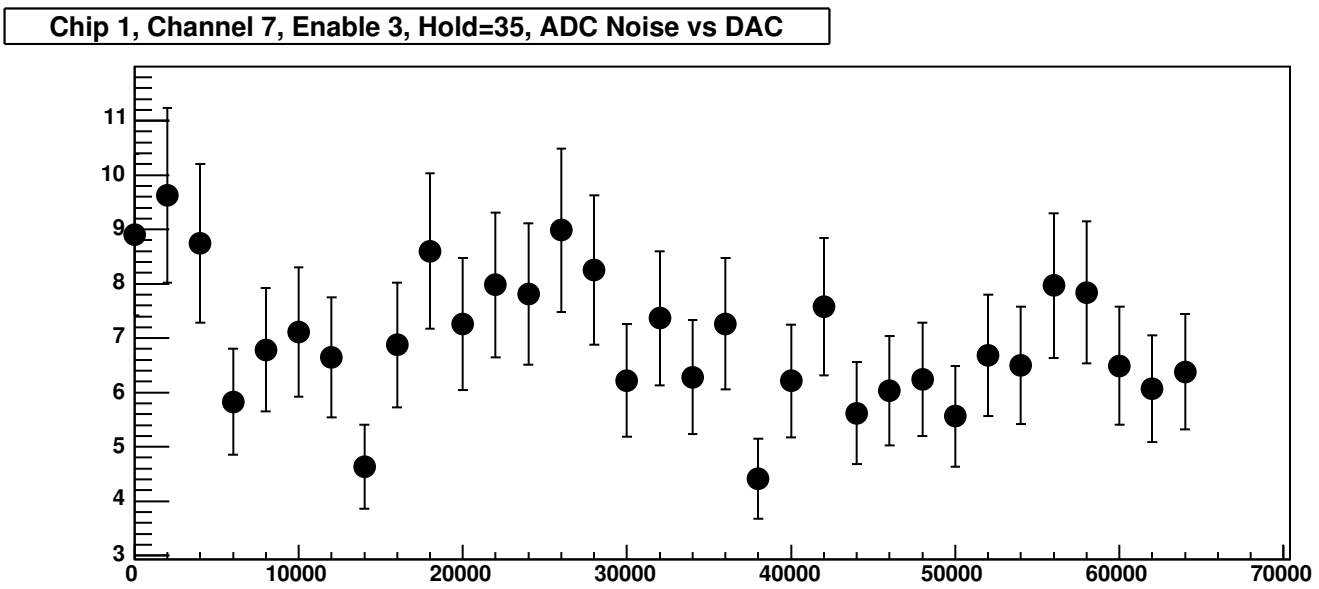

Chip 1, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

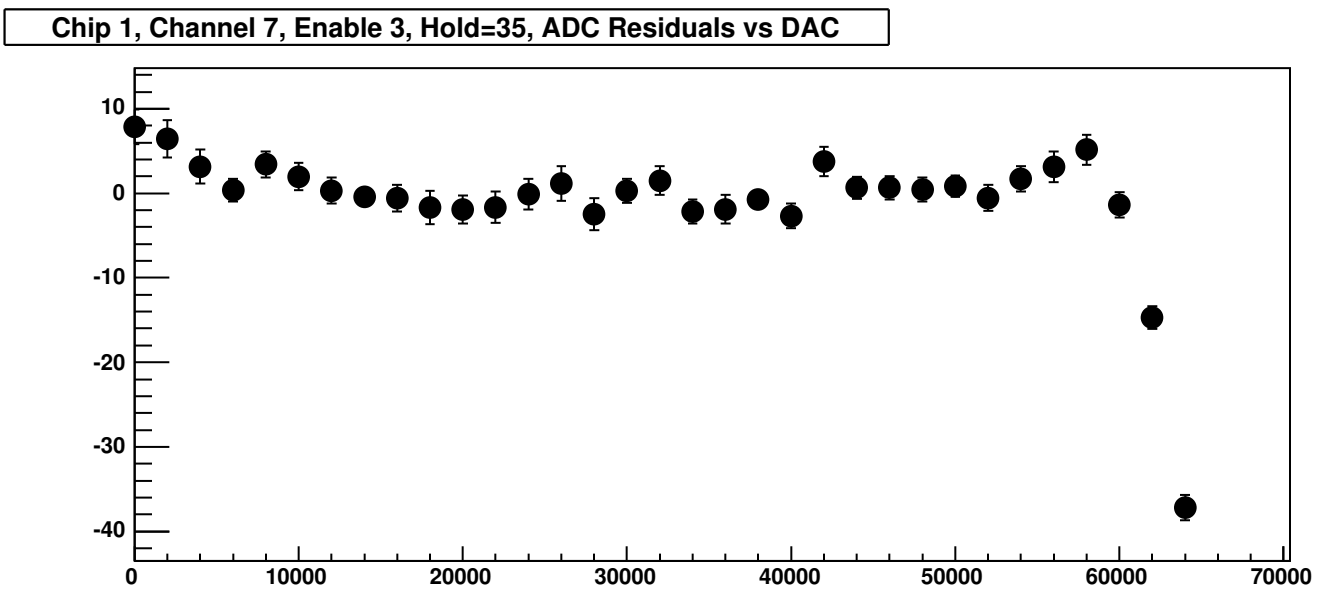

Chip 1, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 6968 / 23
p0 231.7 ± 0.9257
p1 0.5047 ± 4.045e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000

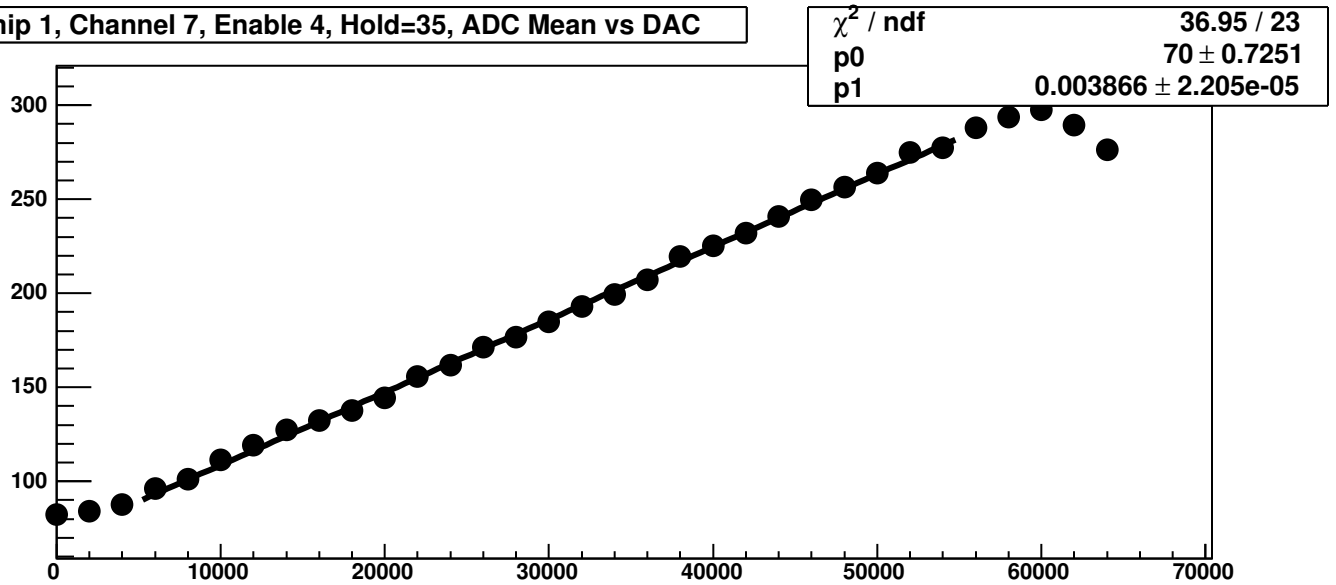

Chip 1, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 26.98 / 23
p0 71.64 ± 0.6822
p1 0.003486 ± 1.993e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

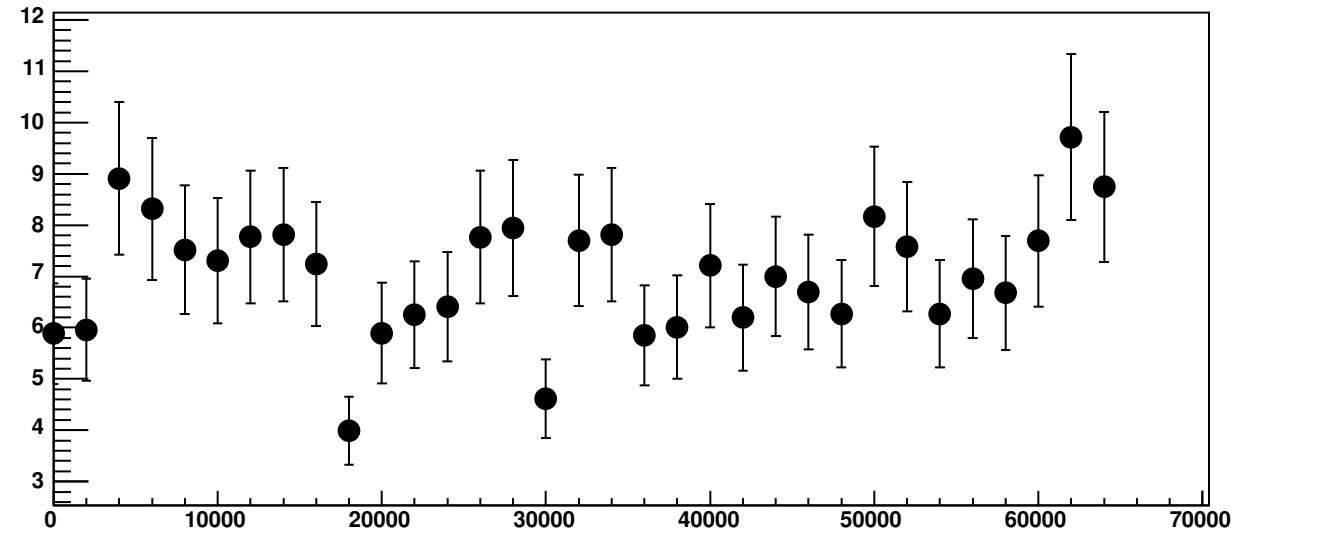

Chip 1, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
10000
20000
30000
40000
50000
60000
70000

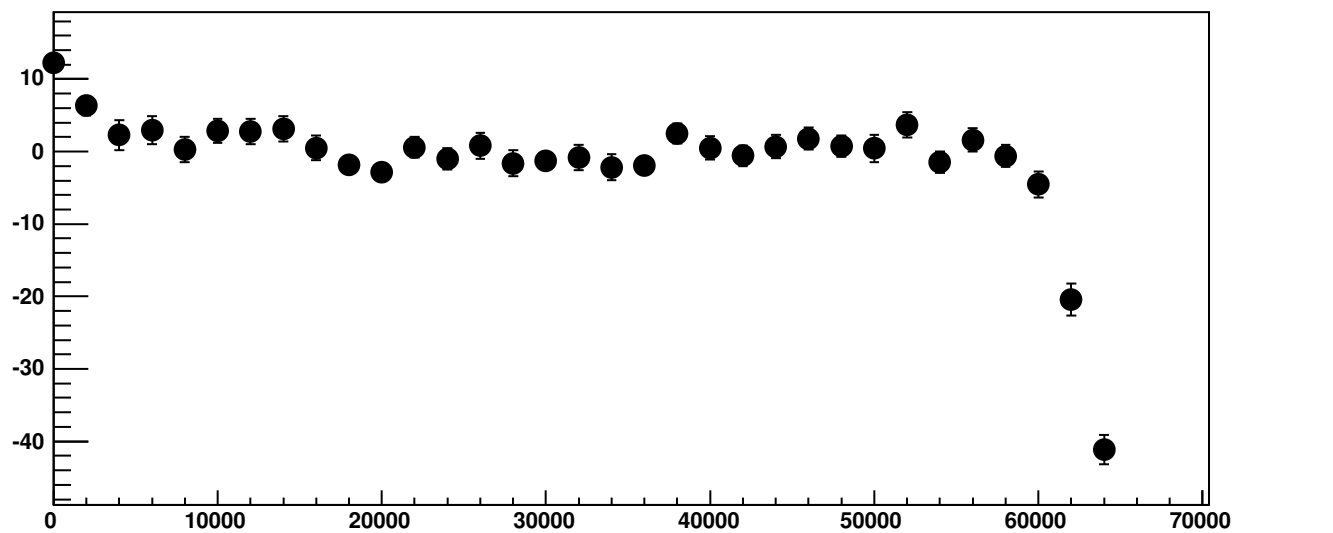
Chip 1, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

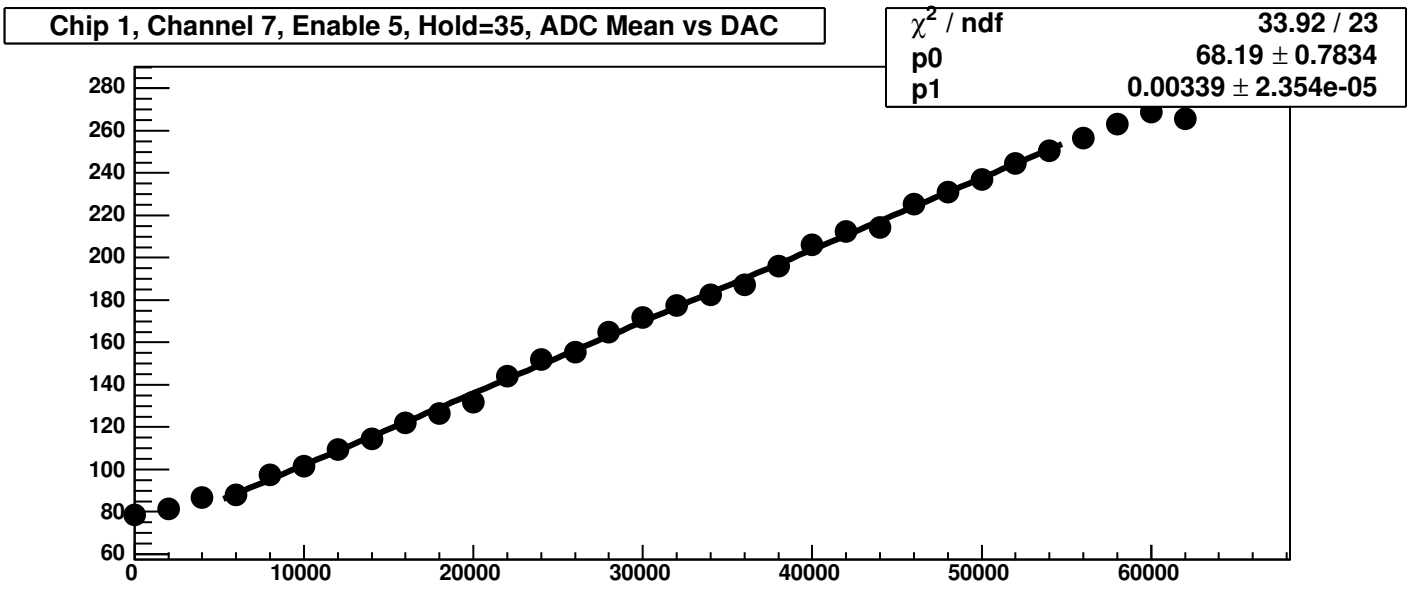


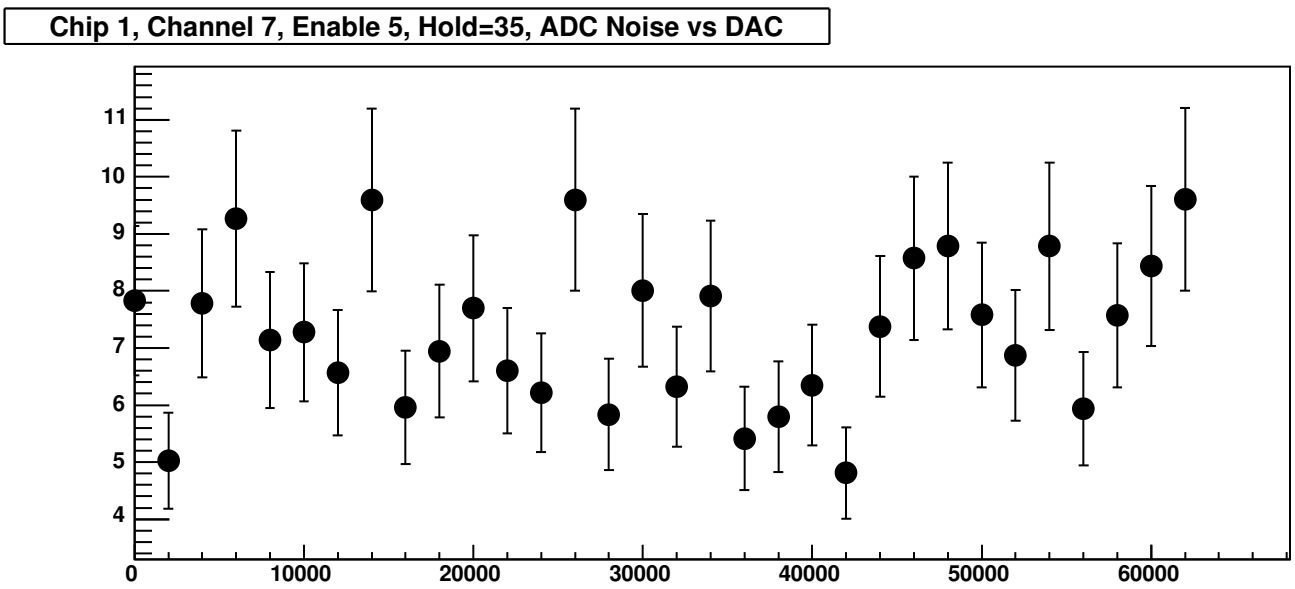
Chip 1, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

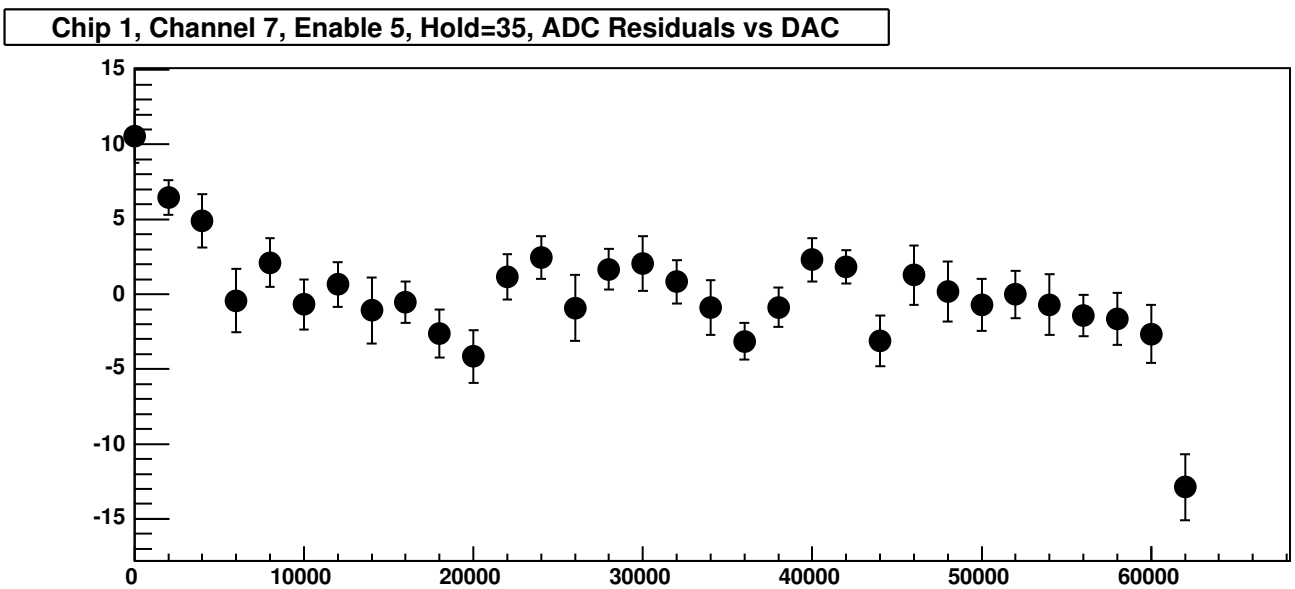


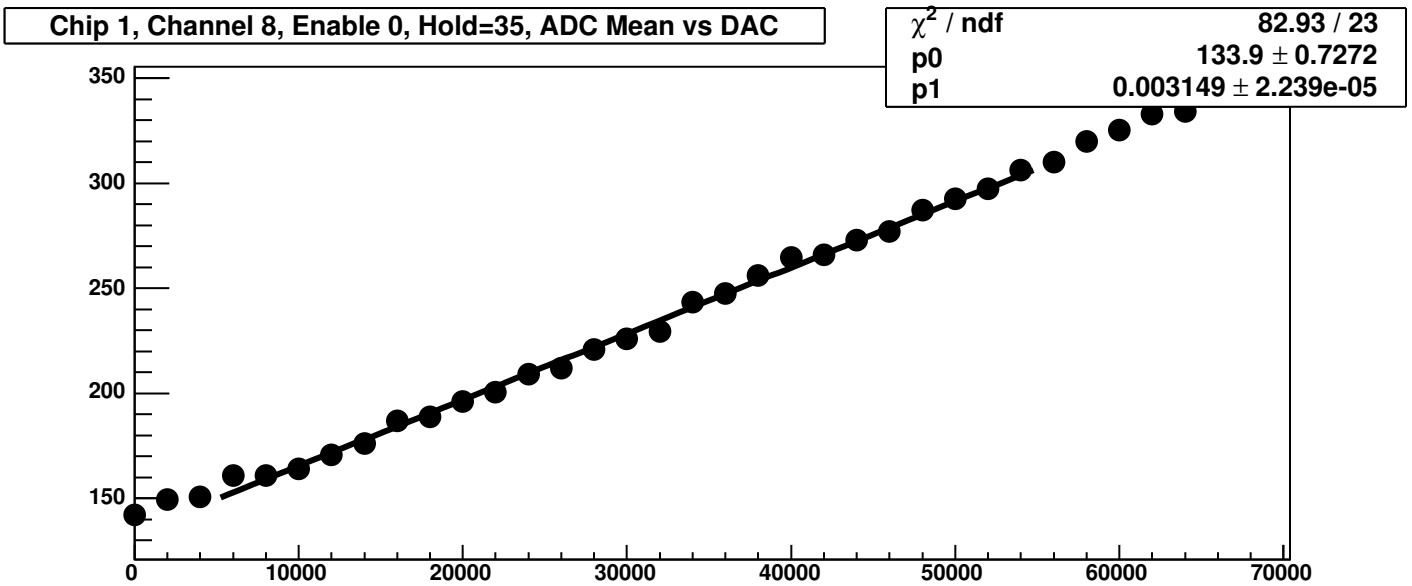
Chip 1, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

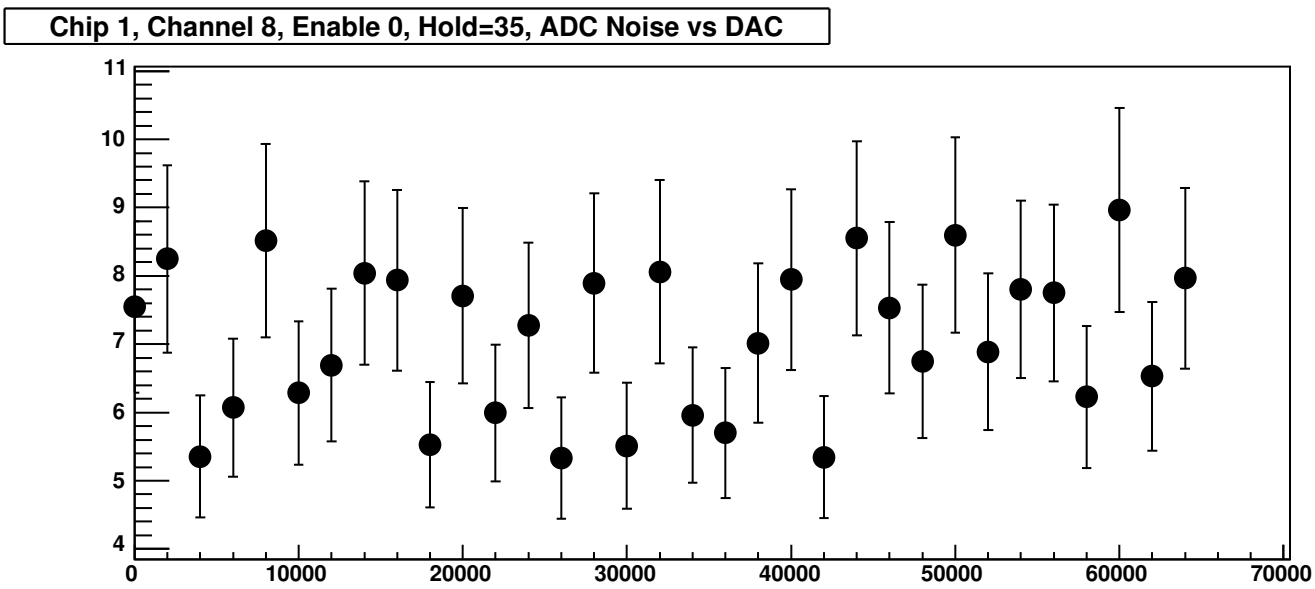


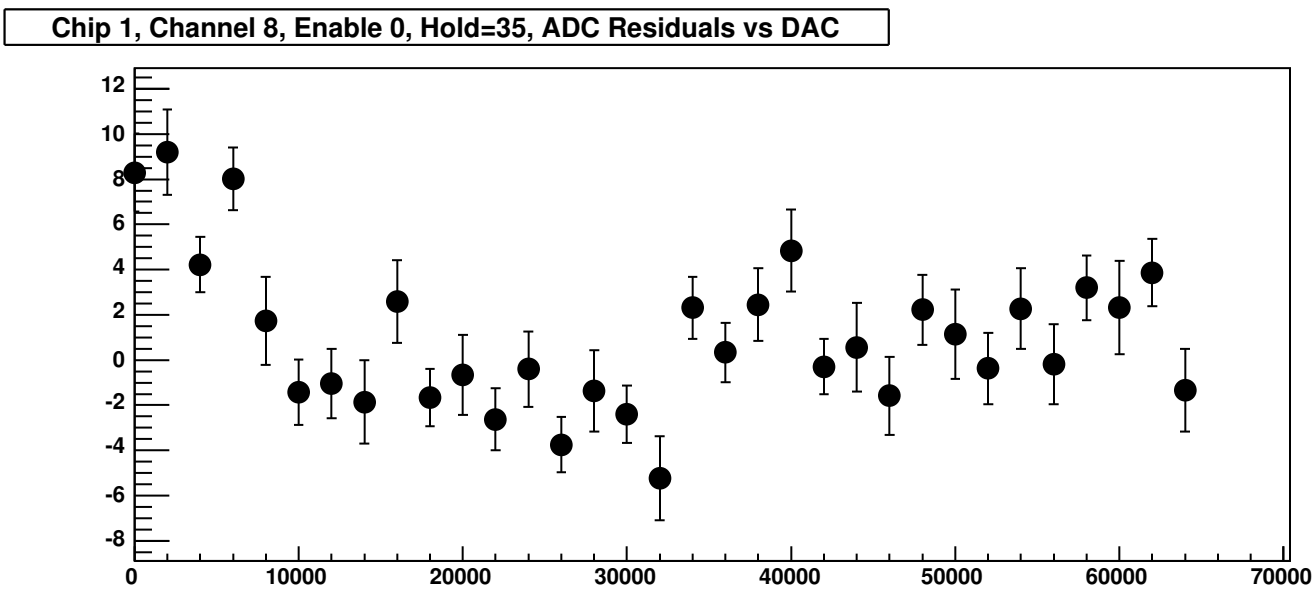

Chip 1, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 33.92 / 23
p0 68.19 ± 0.7834
p1 0.00339 ± 2.354e-05
280
260
240
220
200
180
160
140
120
100
80
60
0
10000
20000
30000
40000
50000
60000

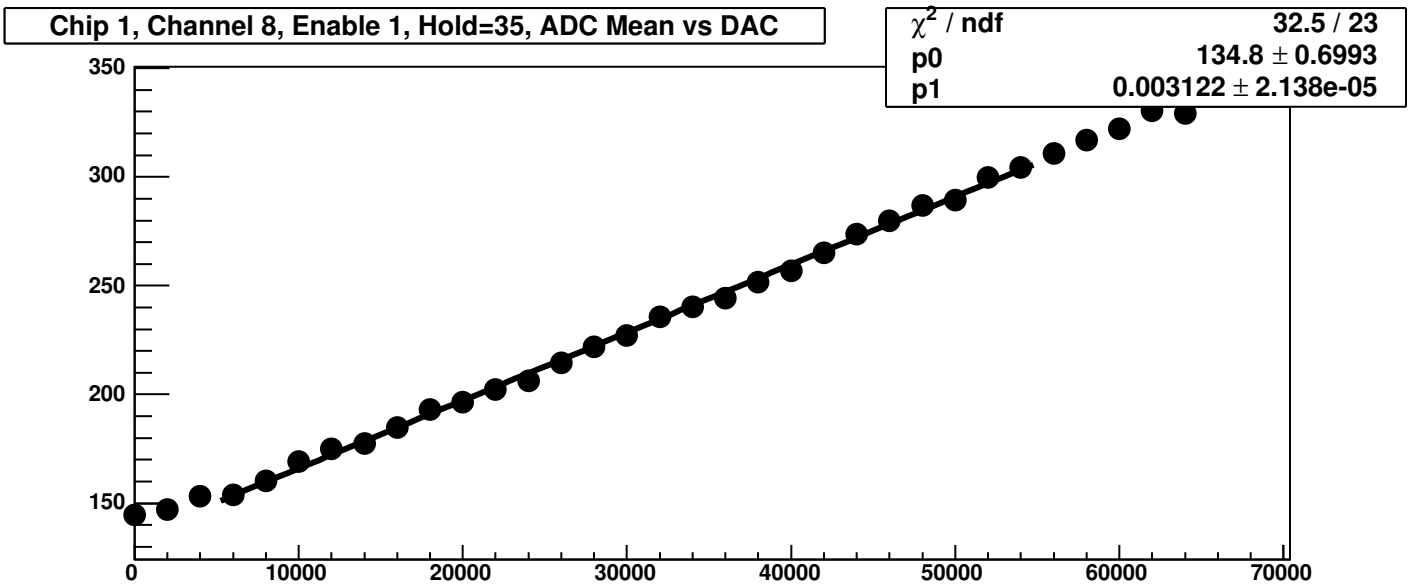

Chip 1, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

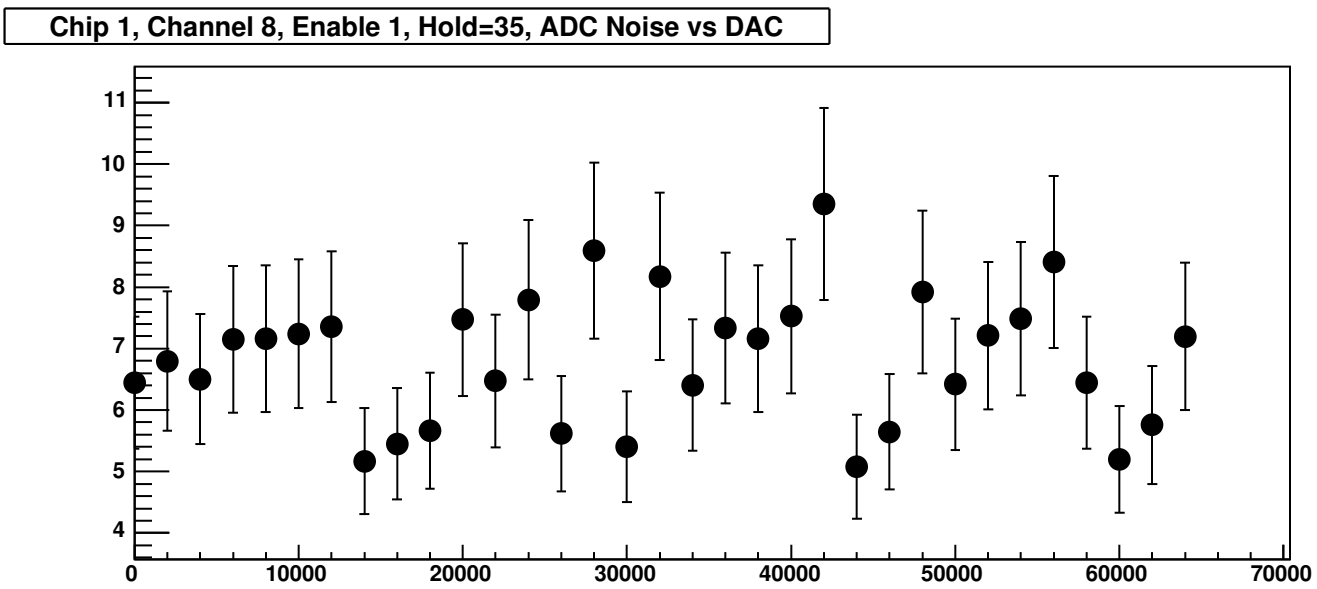

Chip 1, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000

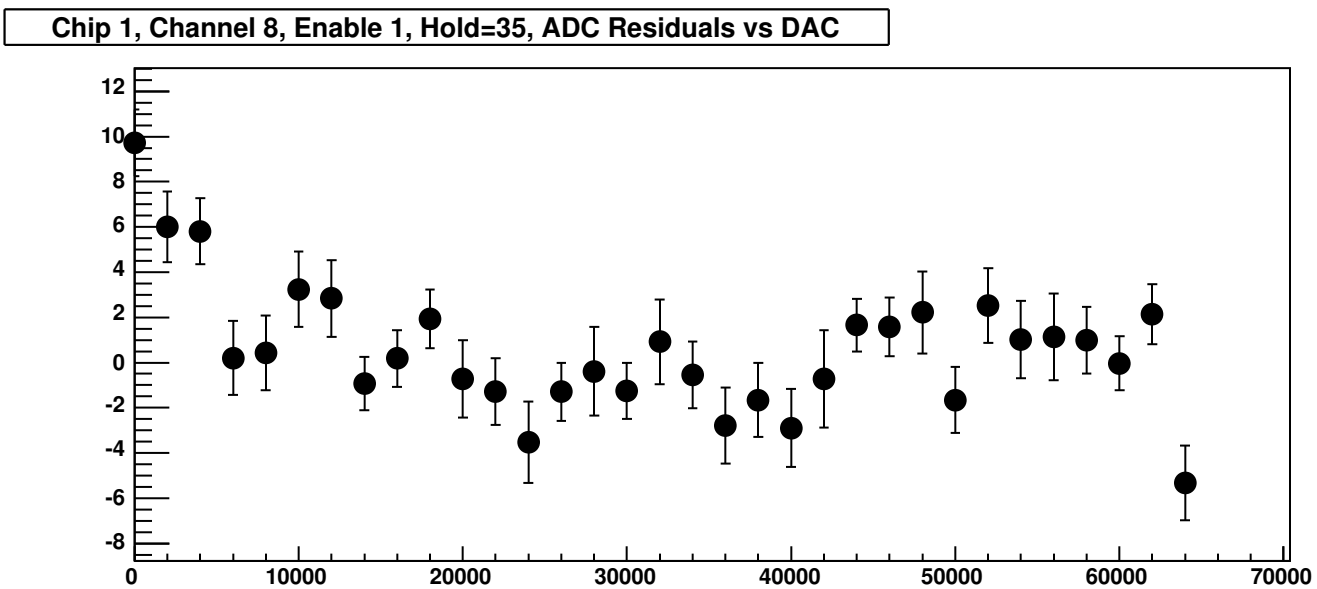

Chip 1, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 82.93 / 23
p0 133.9 ± 0.7272
p1 0.003149 ± 2.239e-05
350
300
250
200
150
0
10000
20000
30000
40000
50000
60000
70000

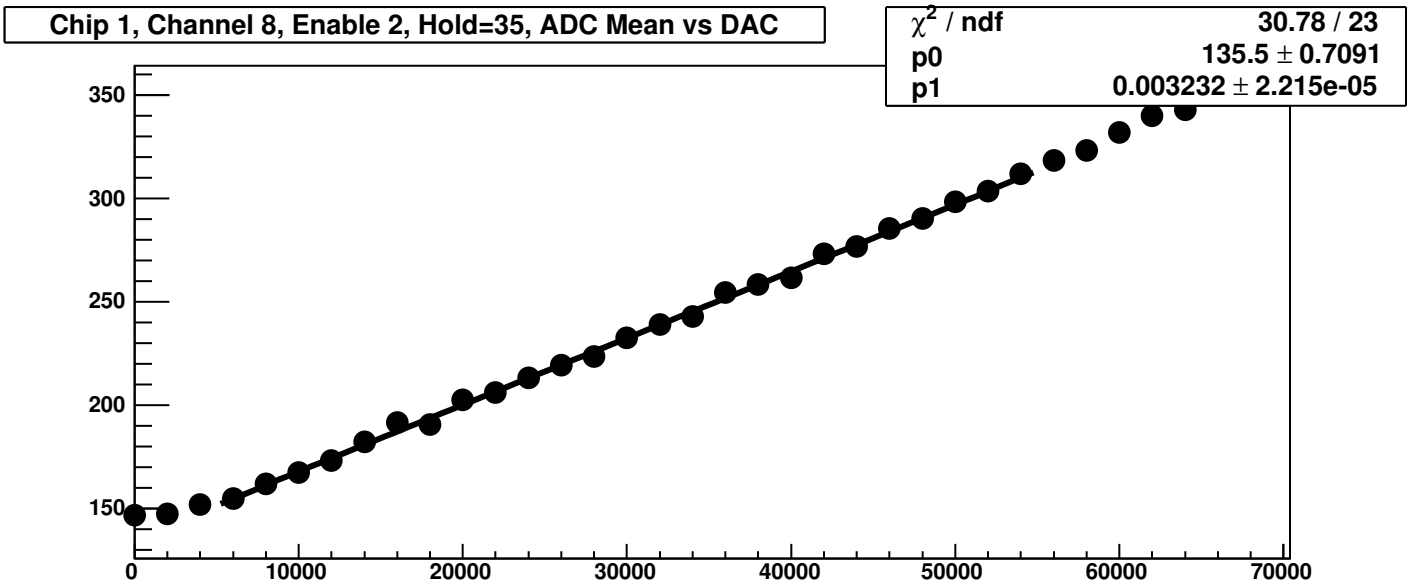

Chip 1, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

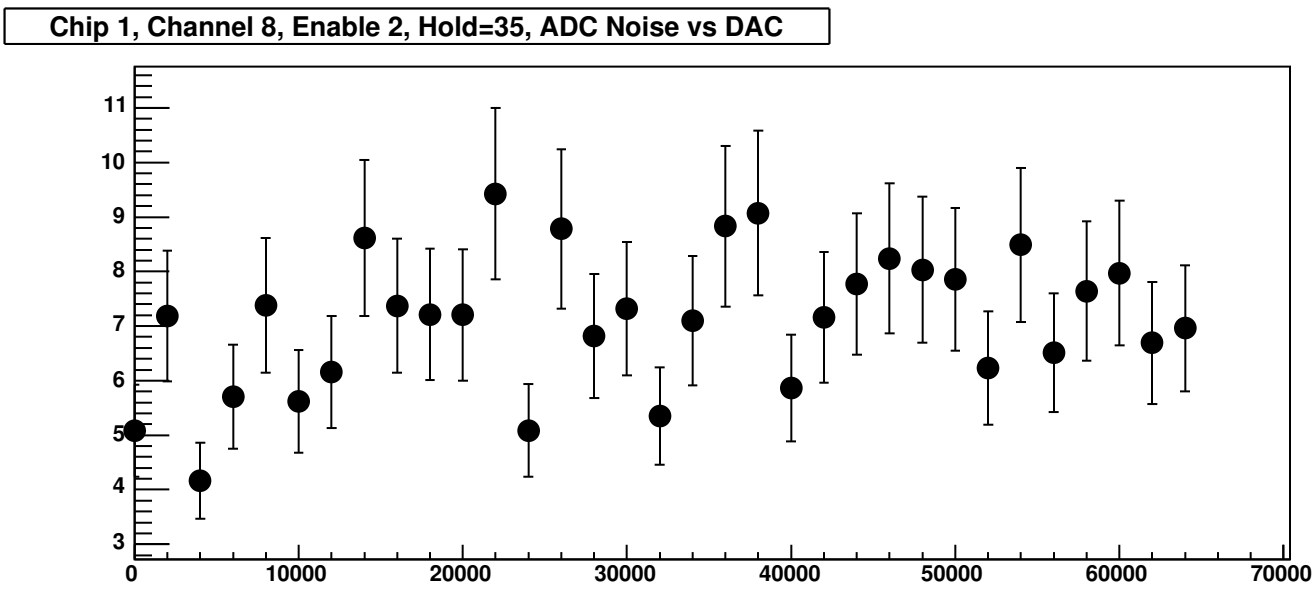

Chip 1, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

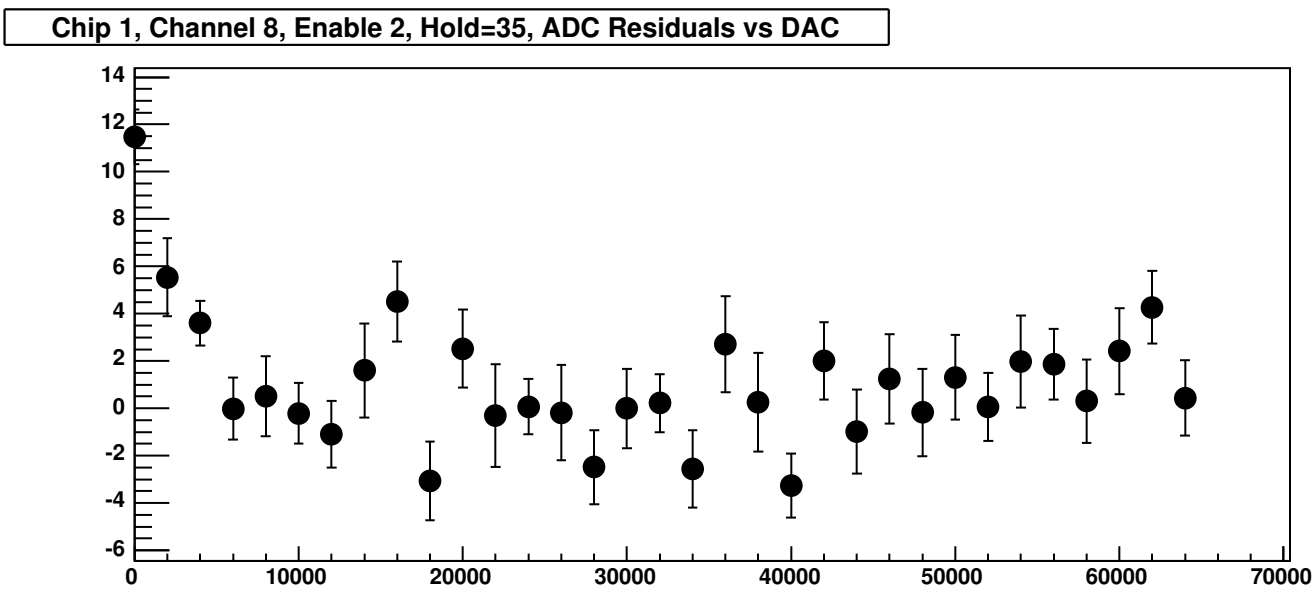

Chip 1, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.5 / 23
p0 134.8 ± 0.6993
p1 0.003122 ± 2.138e-05
350
300
250
200
150
0
10000
20000
30000
40000
50000
60000
70000

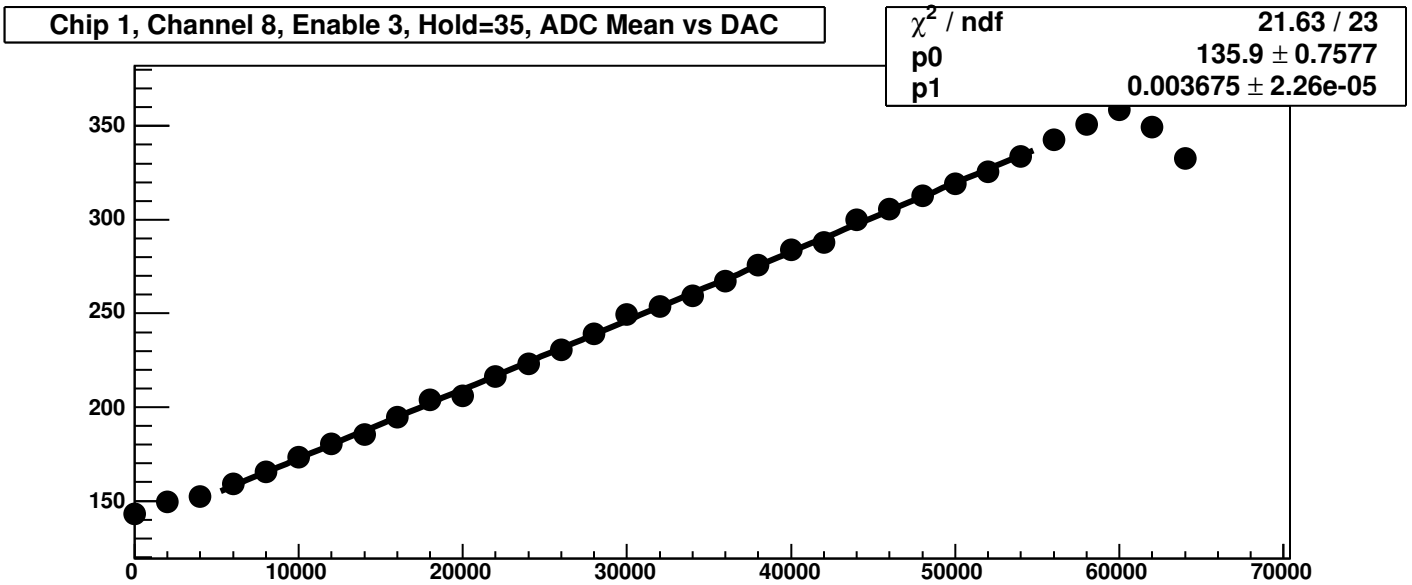

Chip 1, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

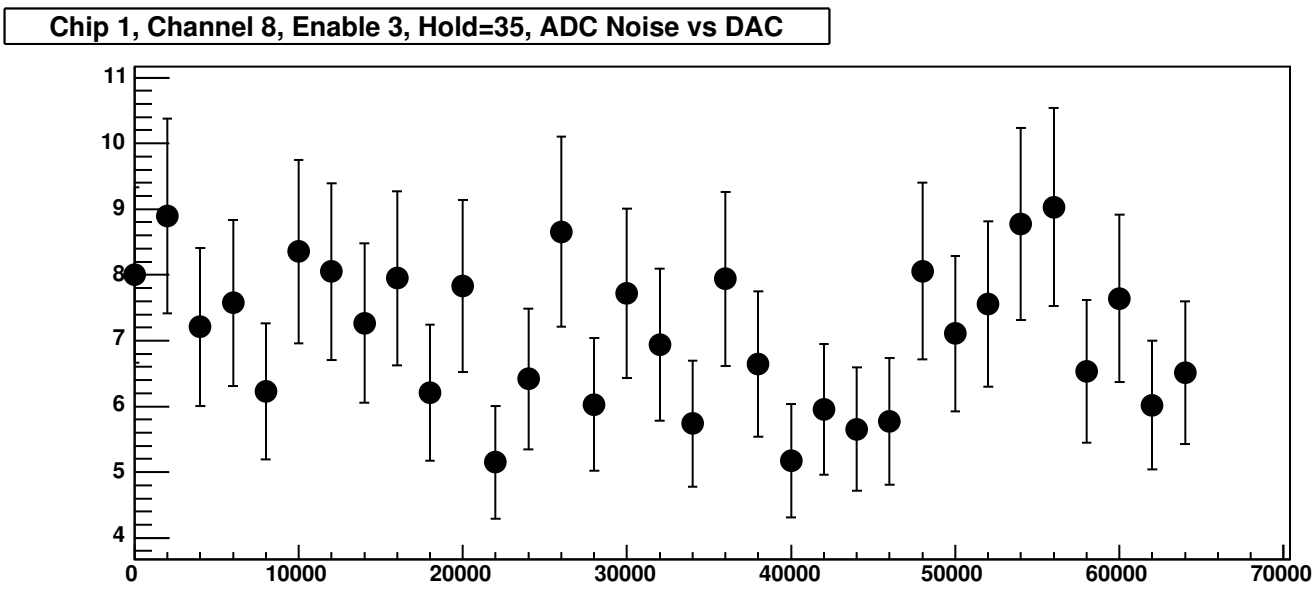

Chip 1, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

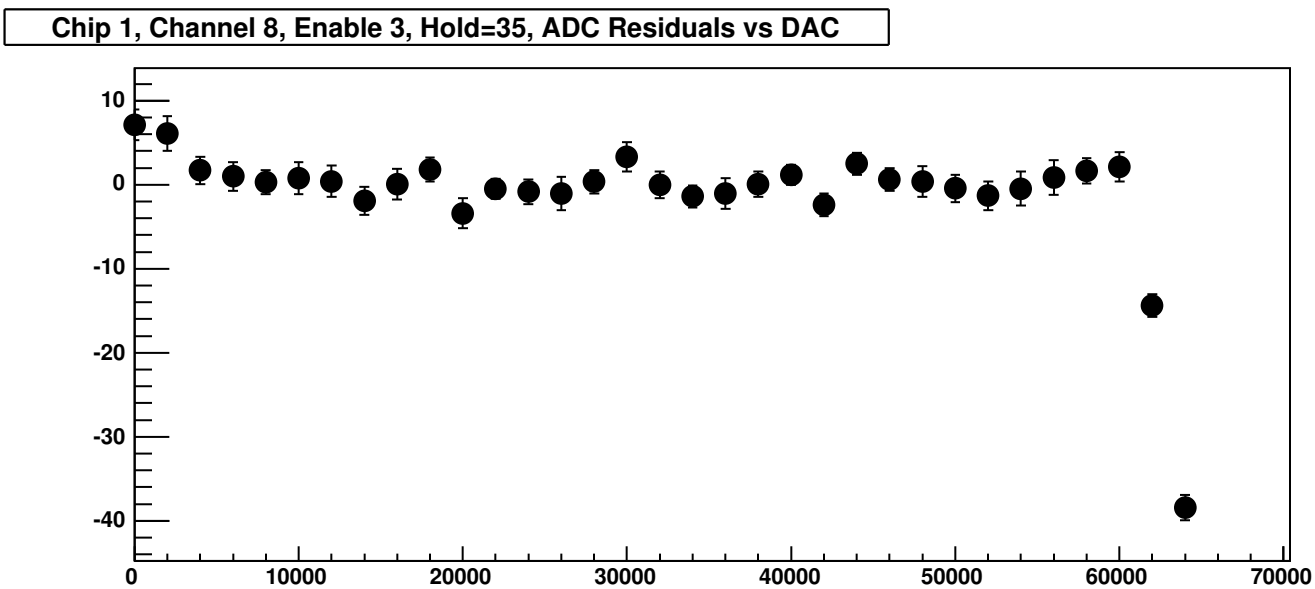

Chip 1, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 30.78 / 23
p0 135.5 ± 0.7091
p1 0.003232 ± 2.215e-05
350
300
250
200
150
0
10000
20000
30000
40000
50000
60000
70000

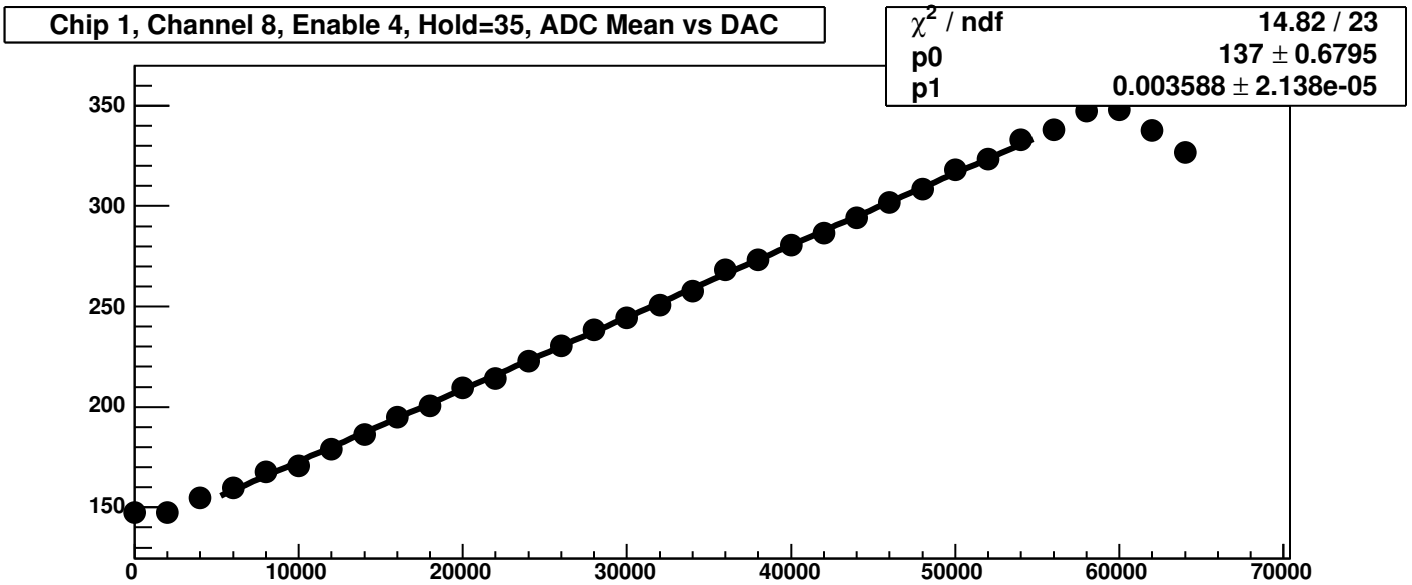

Chip 1, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

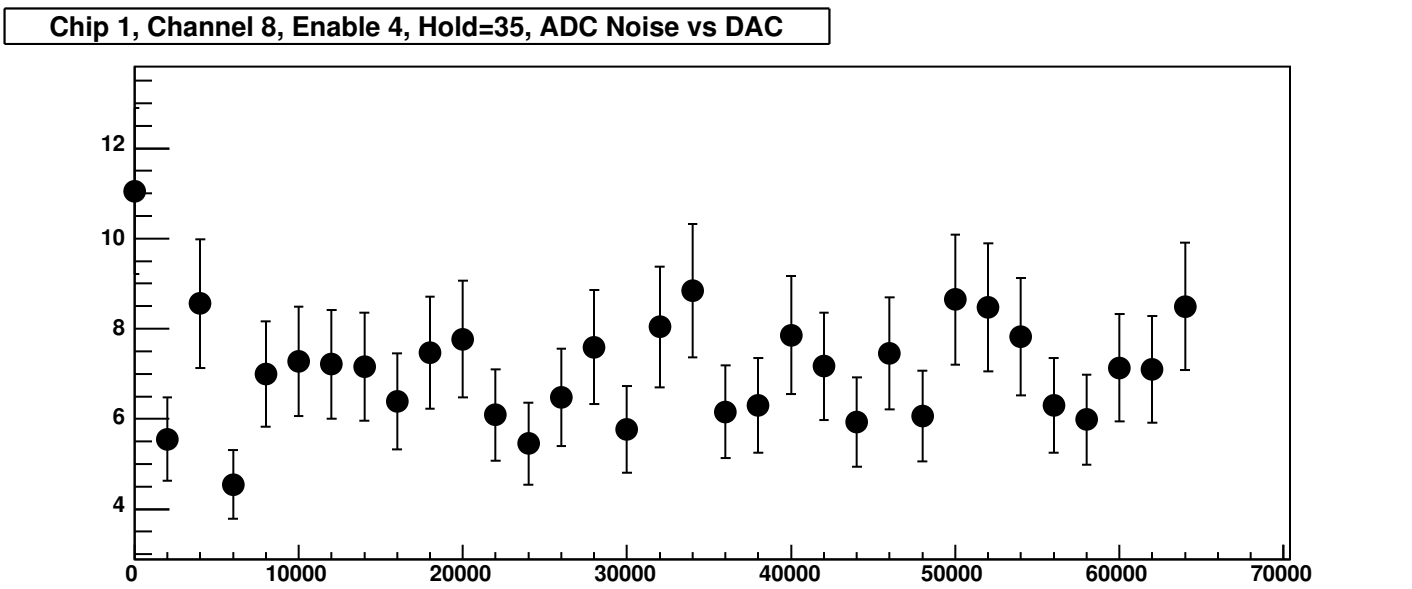

Chip 1, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

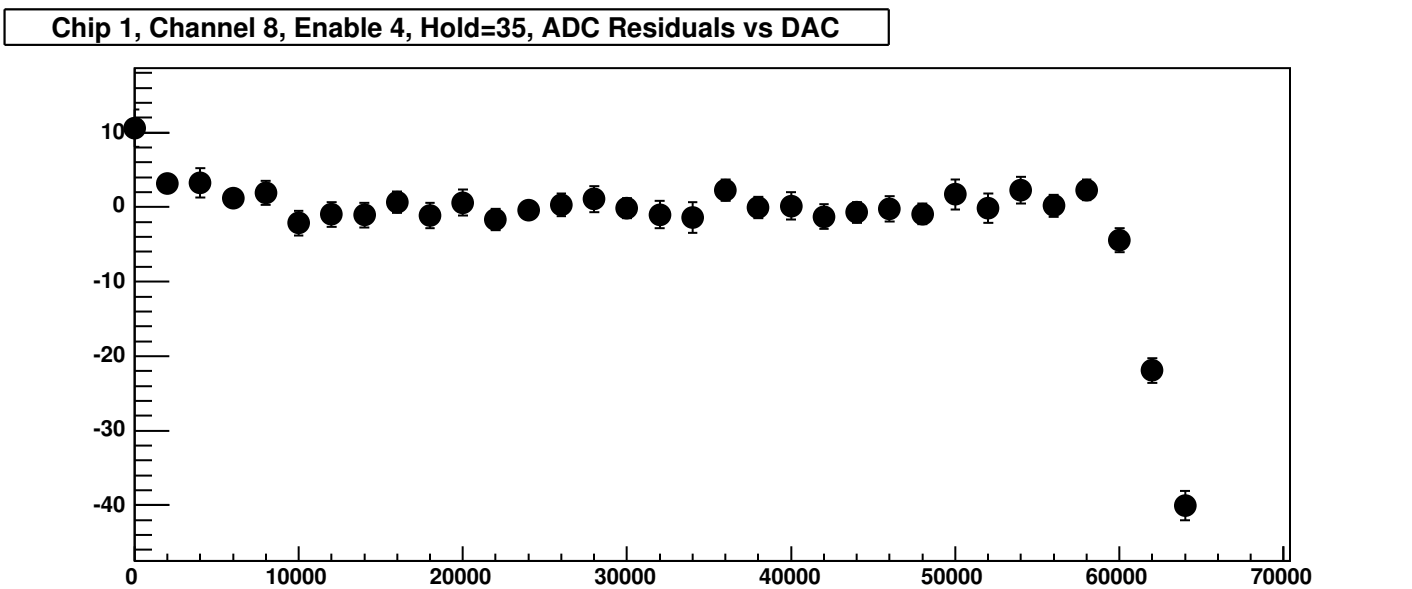

Chip 1, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.63 / 23
p0 135.9 ± 0.7577
p1 0.003675 ± 2.26e-05
350
300
250
200
150
0
10000
20000
30000
40000
50000
60000
70000

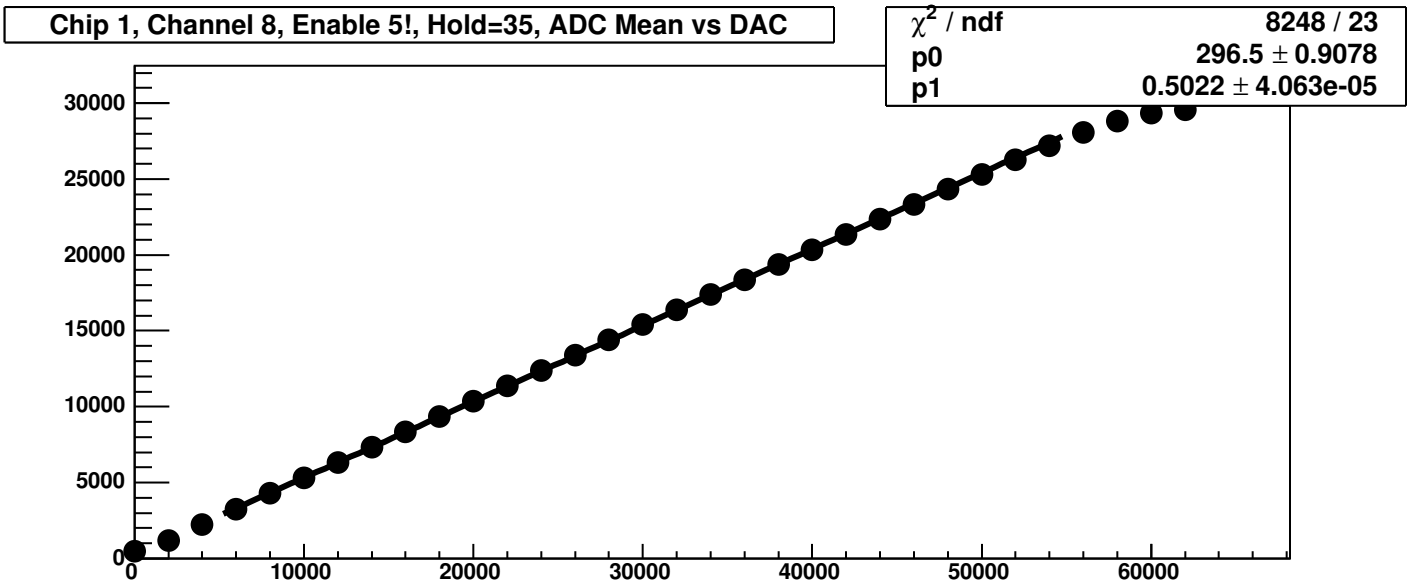

Chip 1, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

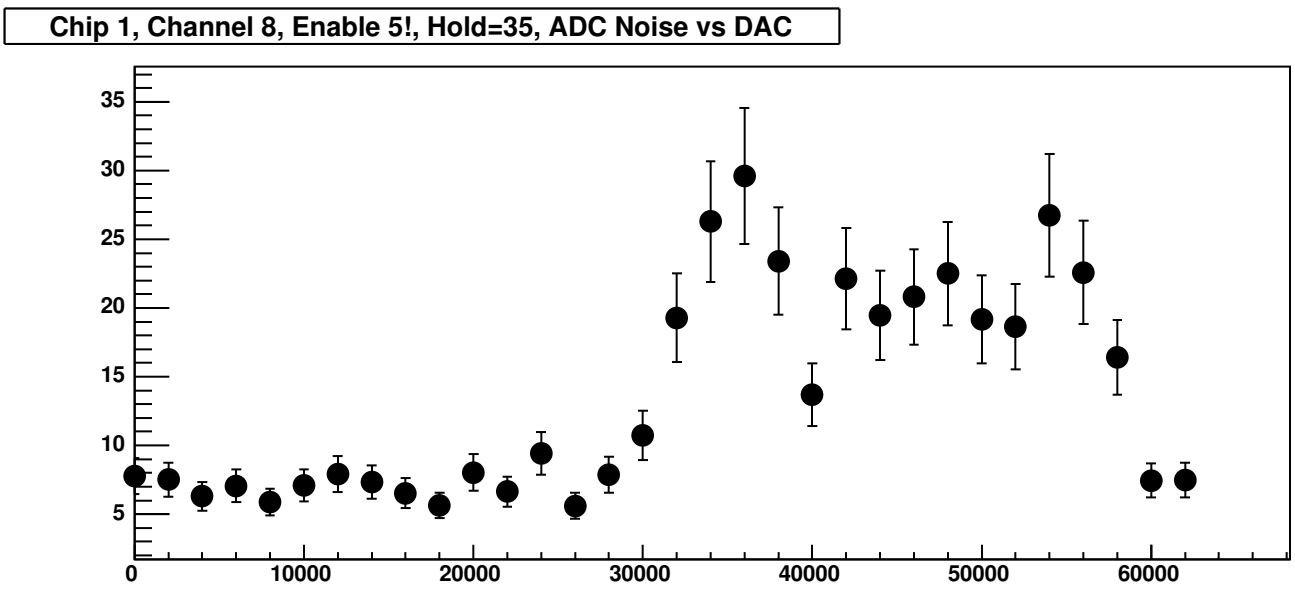

Chip 1, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
10000
20000
30000
40000
50000
60000
70000

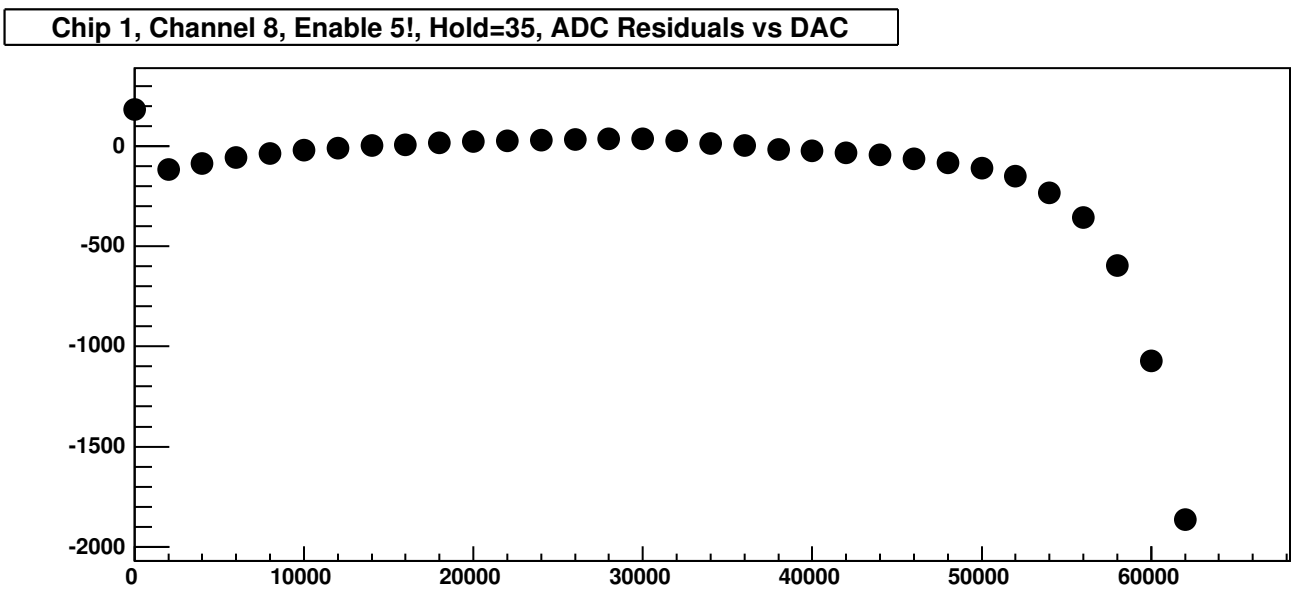

Chip 1, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 14.82 / 23
p0 137 ± 0.6795
p1 0.003588 ± 2.138e-05
350
300
250
200
150
0
10000
20000
30000
40000
50000
60000
70000

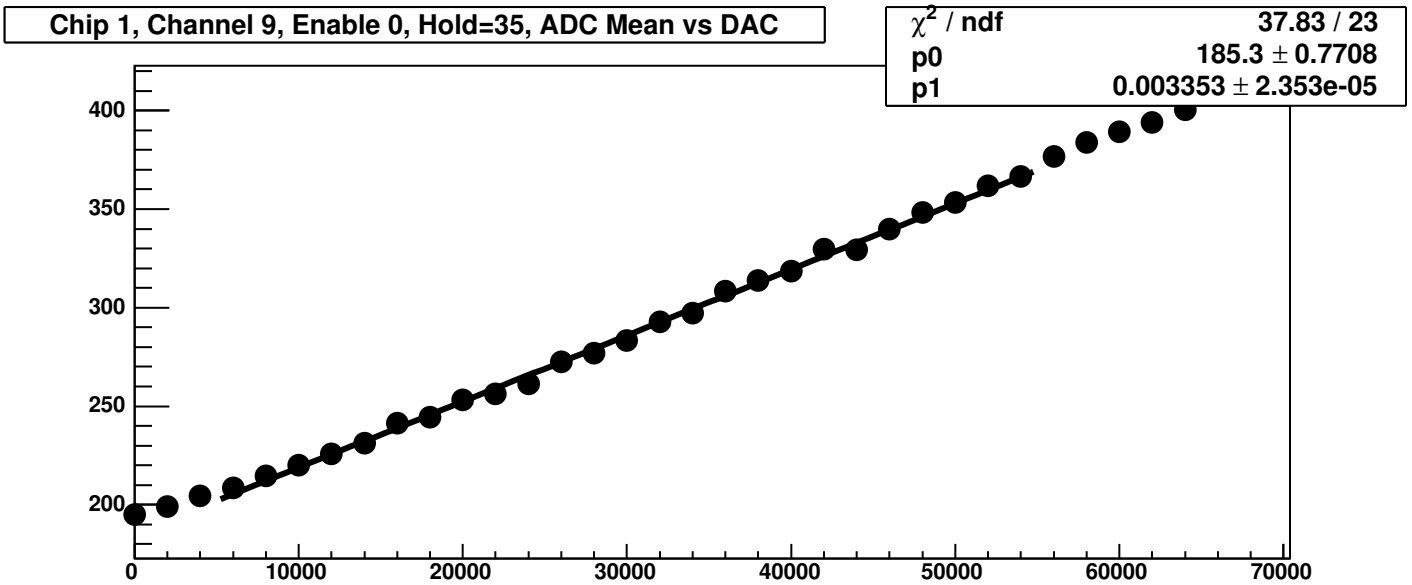

Chip 1, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

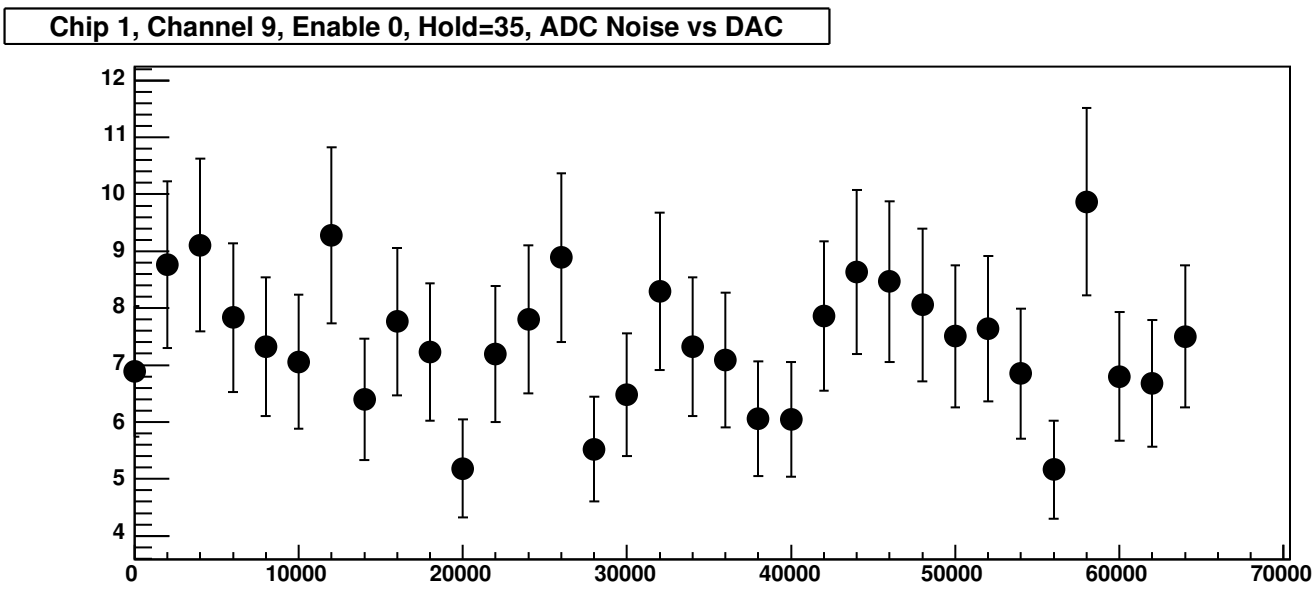

Chip 1, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
10000
20000
30000
40000
50000
60000
70000

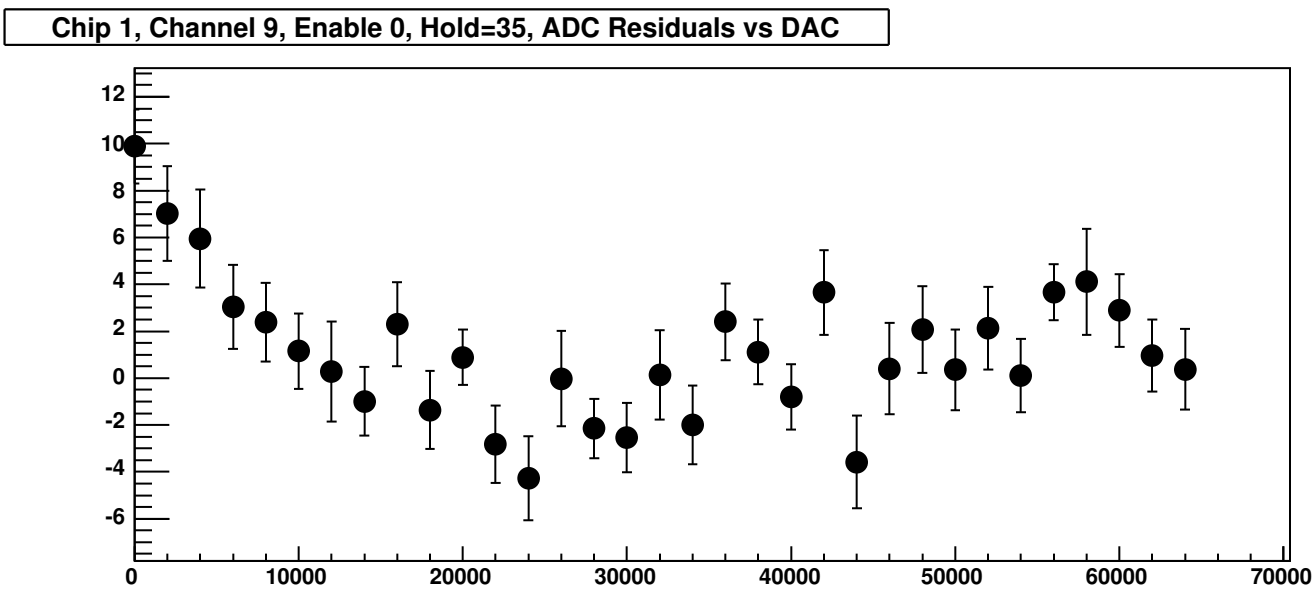

Chip 1, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 8248 / 23
p0 296.5 ± 0.9078
p1 0.5022 ± 4.063e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

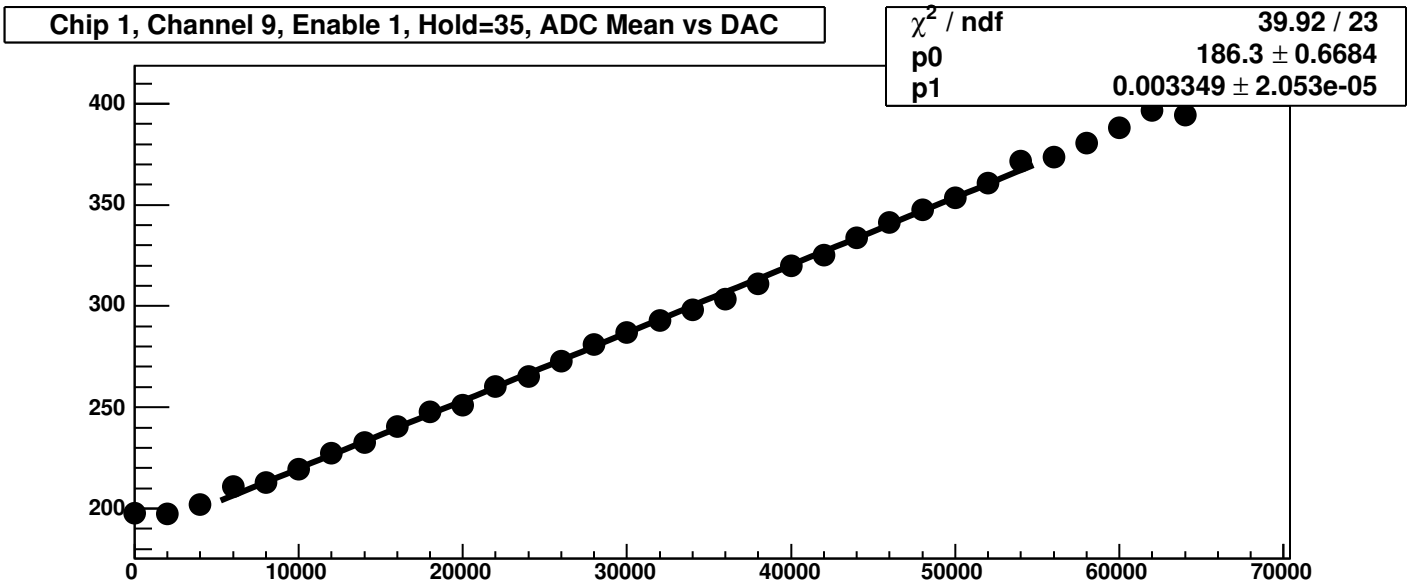

Chip 1, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000

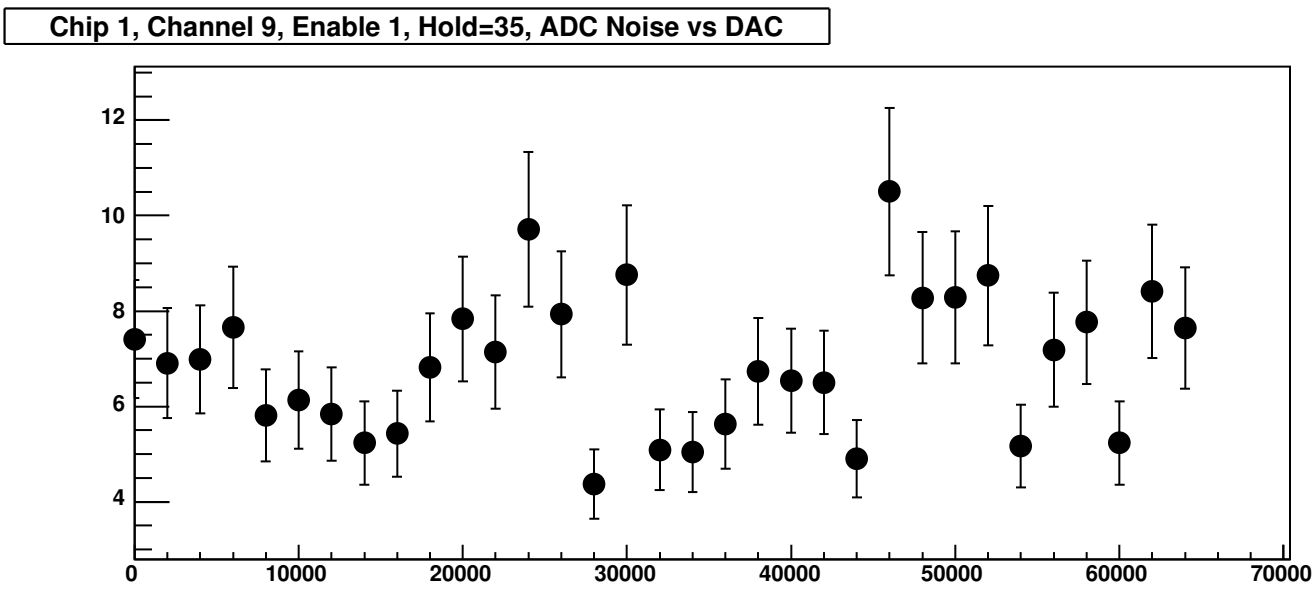

Chip 1, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000

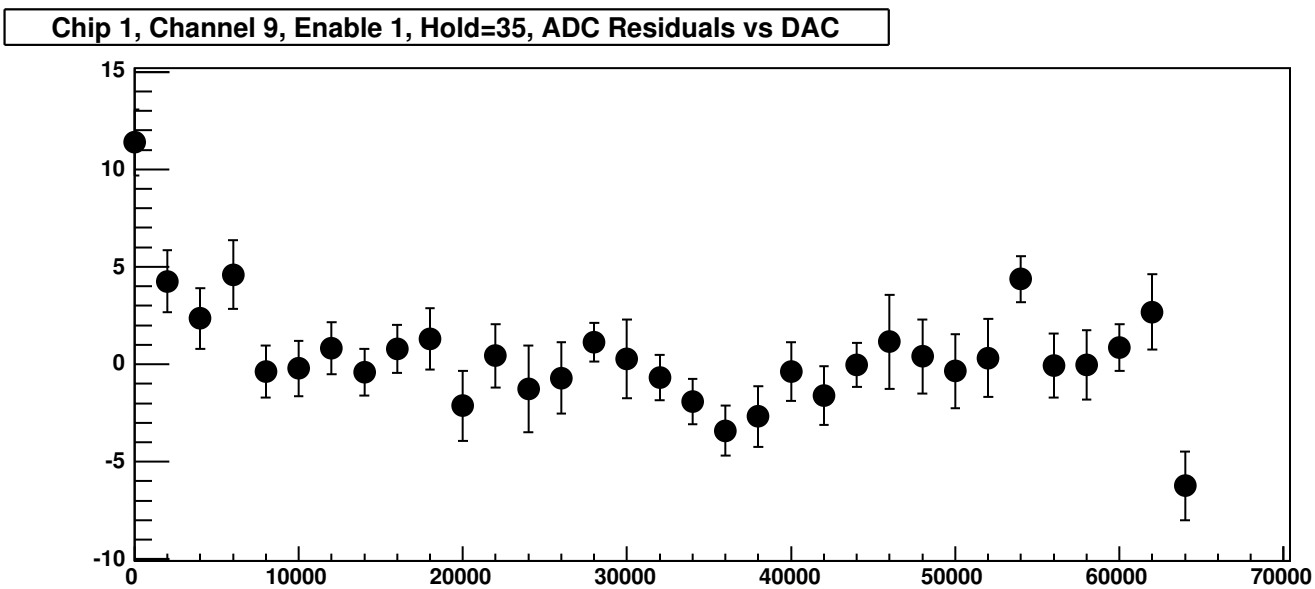

Chip 1, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 37.83 / 23
p0 185.3 ± 0.7708
p1 0.003353 ± 2.353e-05
400
350
300
250
200
0
10000
20000
30000
40000
50000
60000
70000

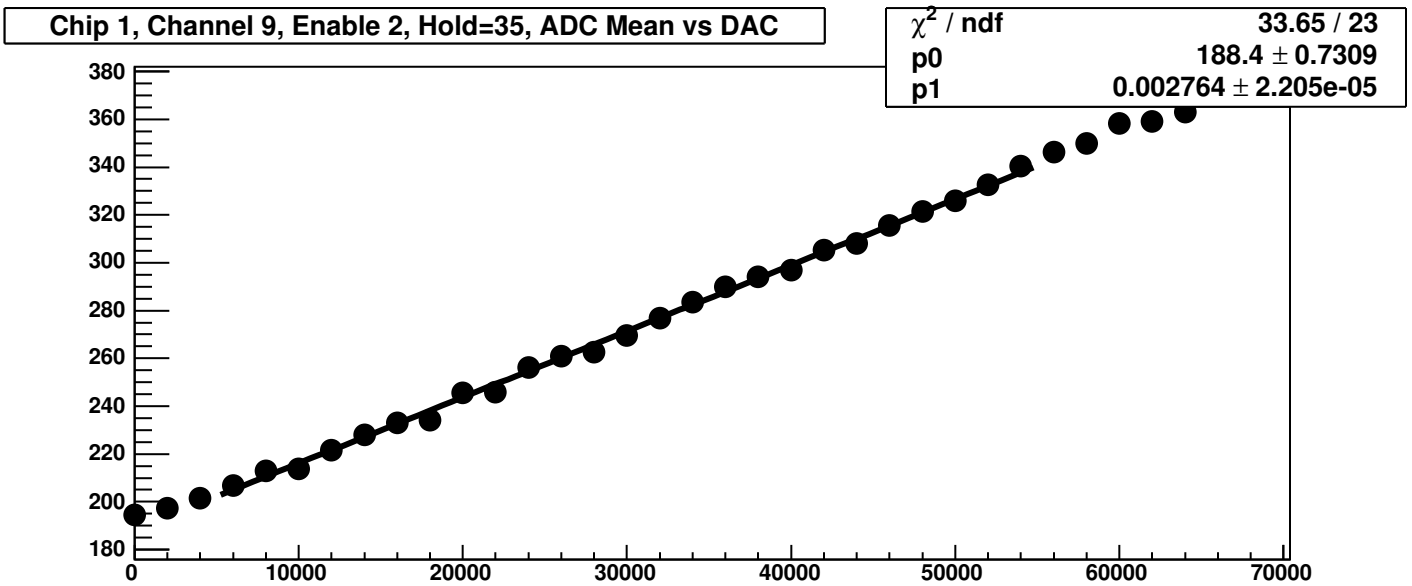

Chip 1, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

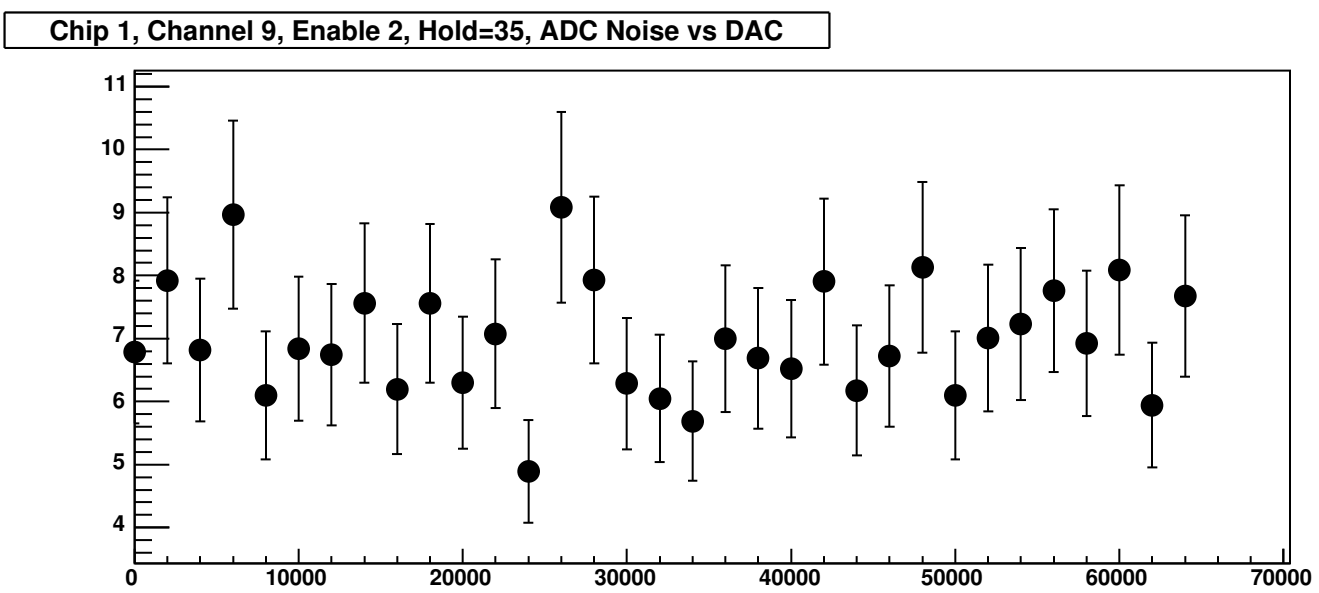

Chip 1, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

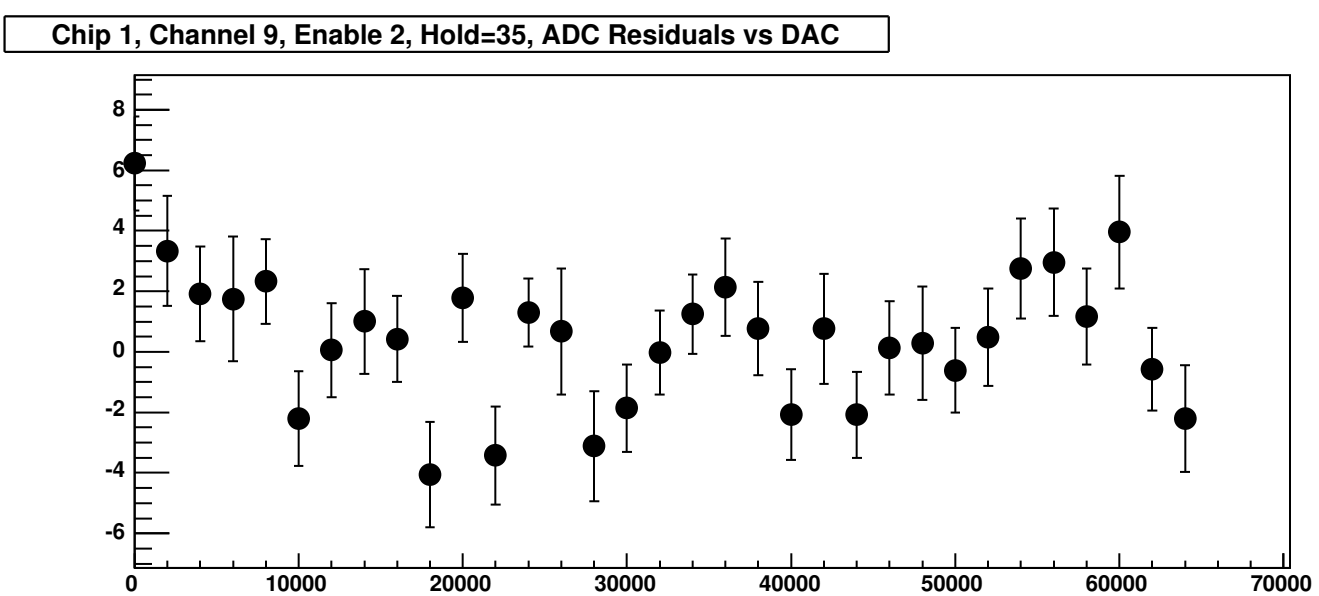

Chip 1, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 39.92 / 23
p0 186.3 ± 0.6684
p1 0.003349 ± 2.053e-05
400
350
300
250
200
0
10000
20000
30000
40000
50000
60000
70000

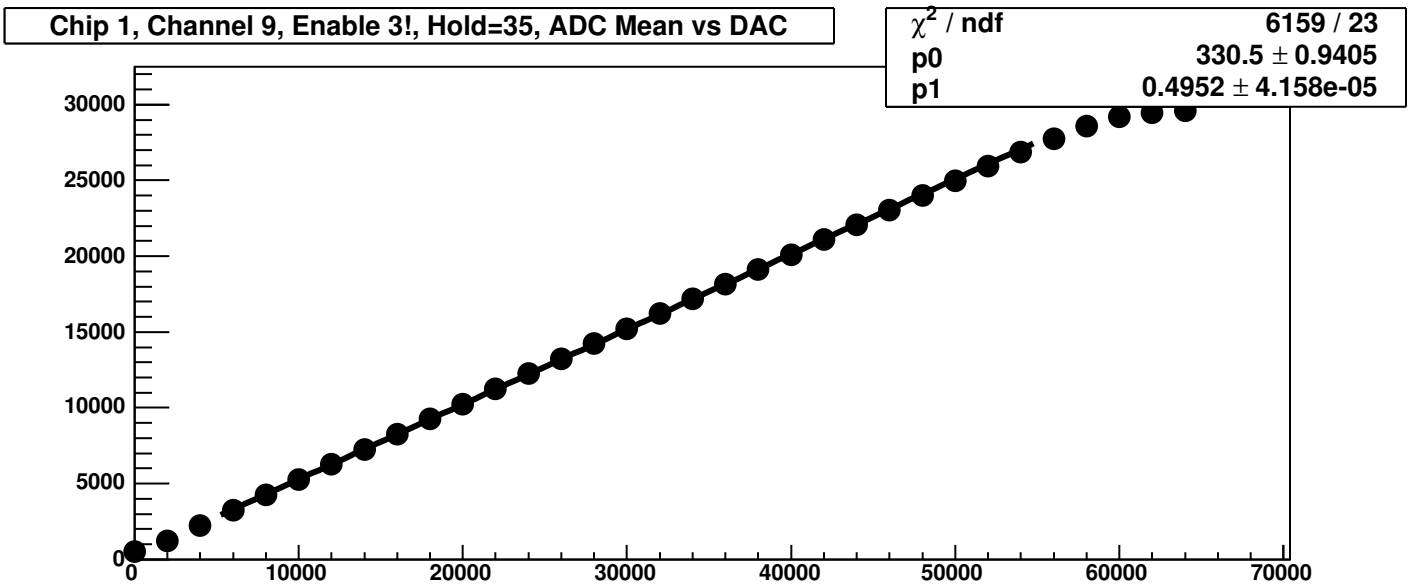

Chip 1, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

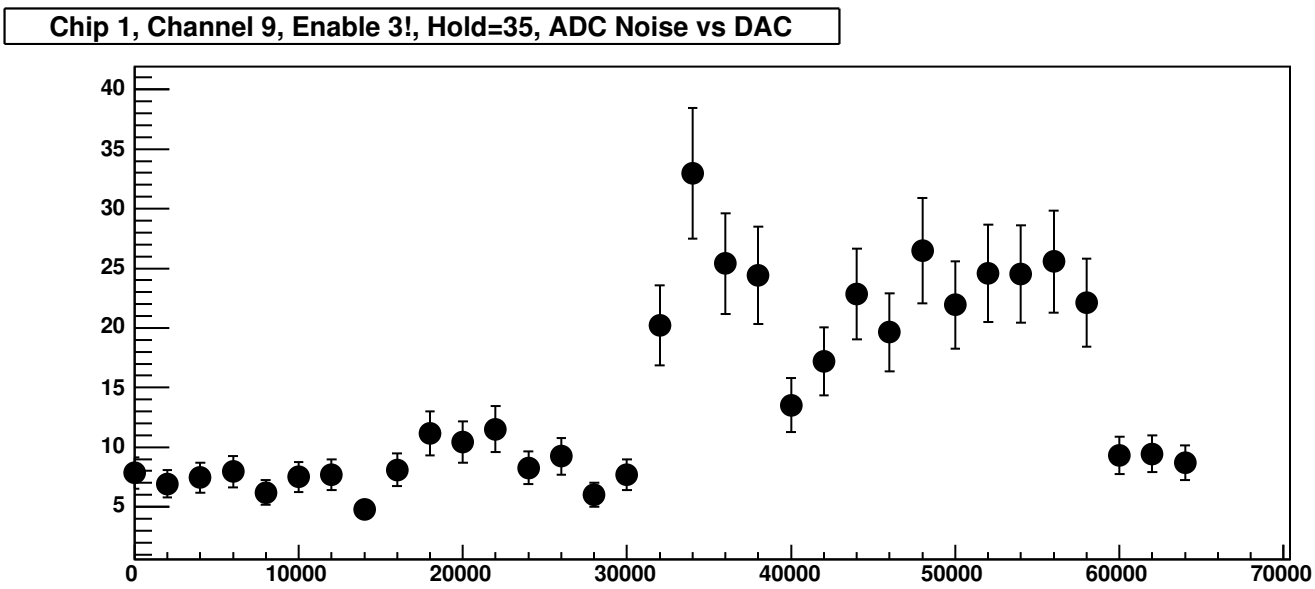

Chip 1, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

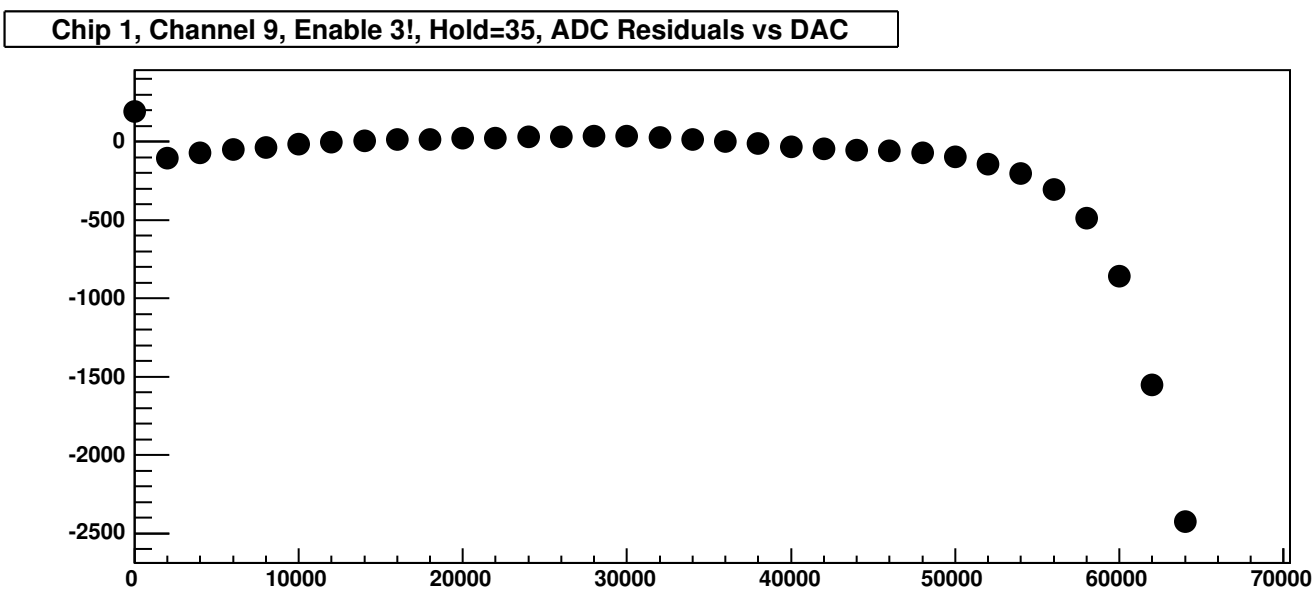

Chip 1, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 33.65 / 23
p0 188.4 ± 0.7309
p1 0.002764 ± 2.205e-05
380
360
340
320
300
280
260
240
220
200
180
0
10000
20000
30000
40000
50000
60000
70000

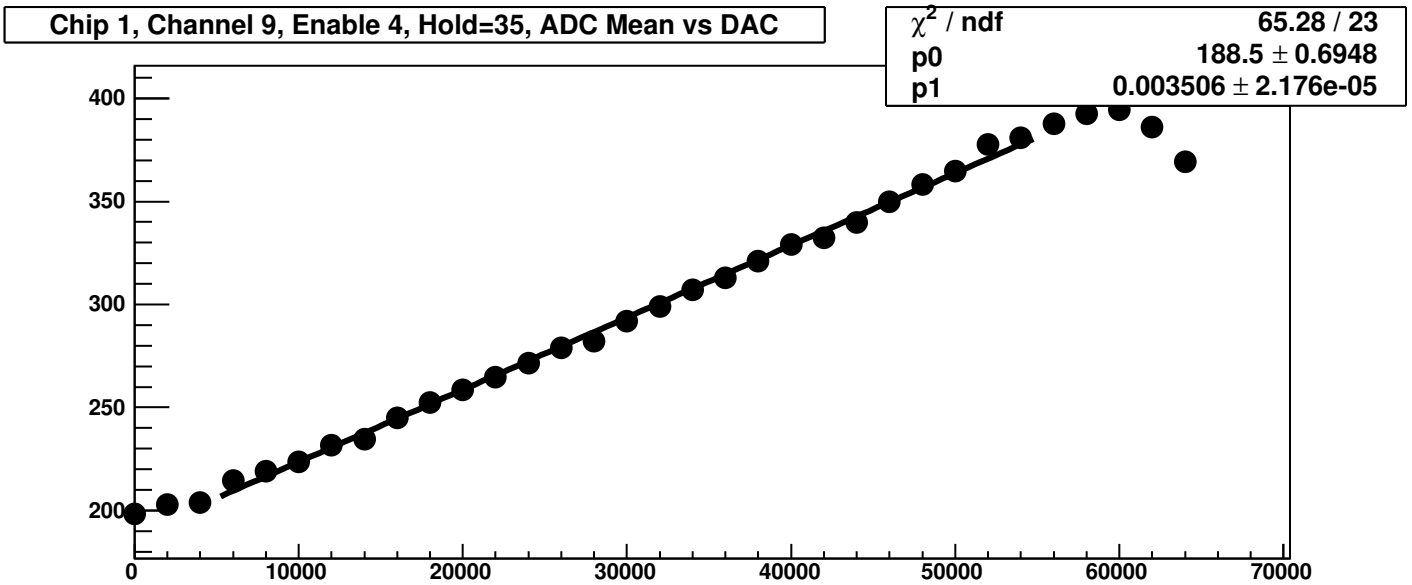

Chip 1, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

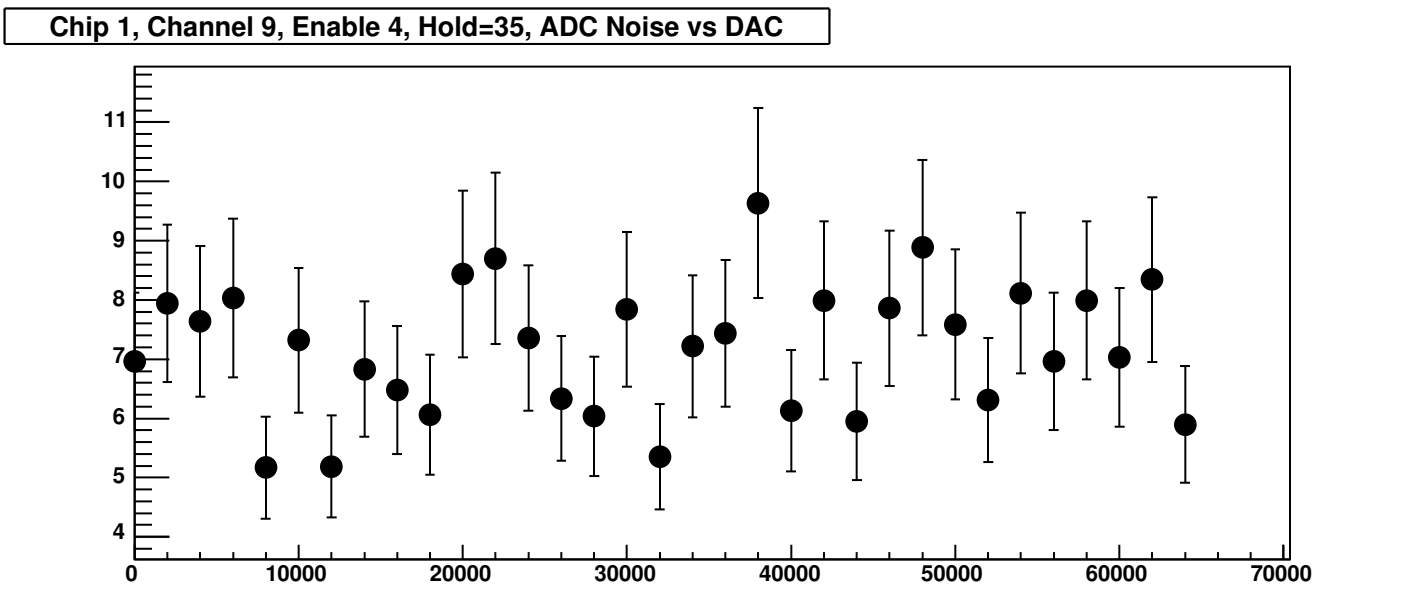

Chip 1, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

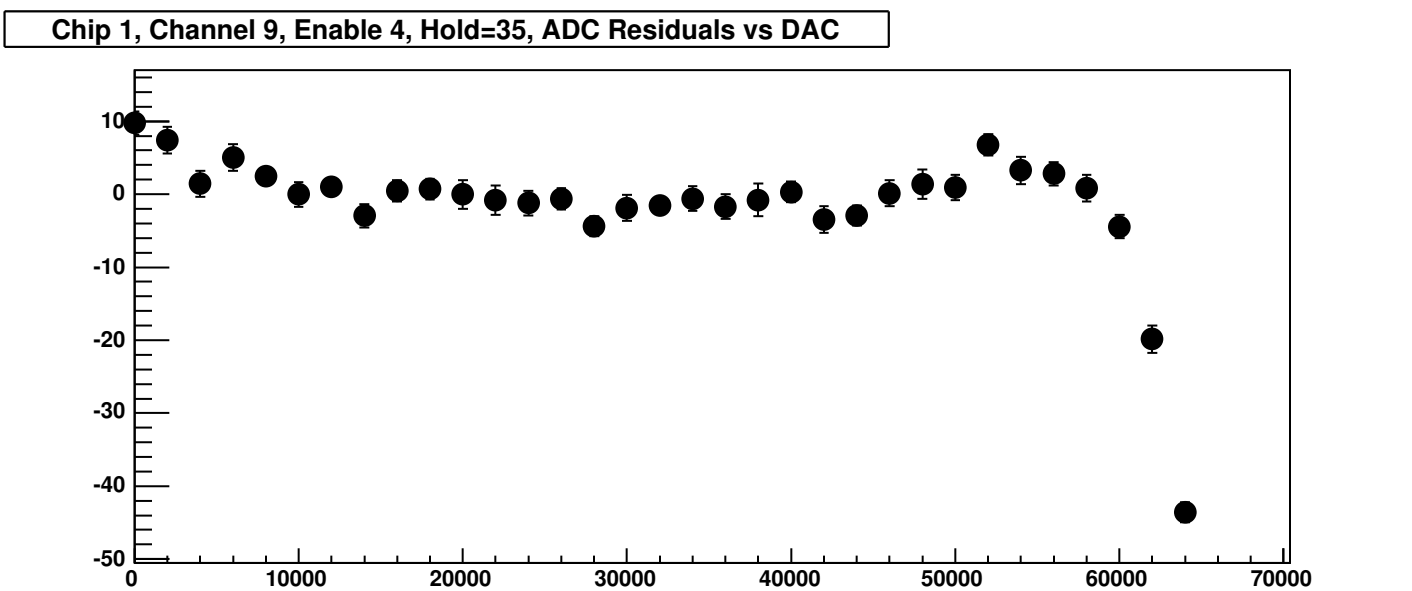

Chip 1, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 6159 / 23
p0 330.5 ± 0.9405
p1 0.4952 ± 4.158e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

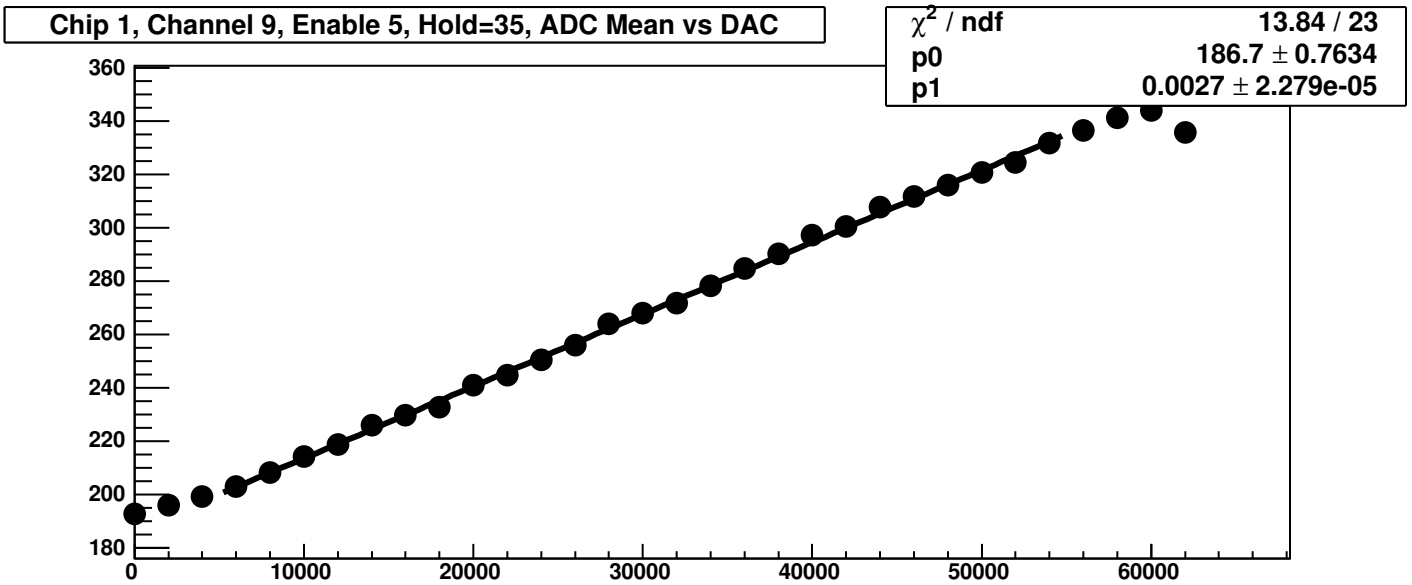

Chip 1, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000

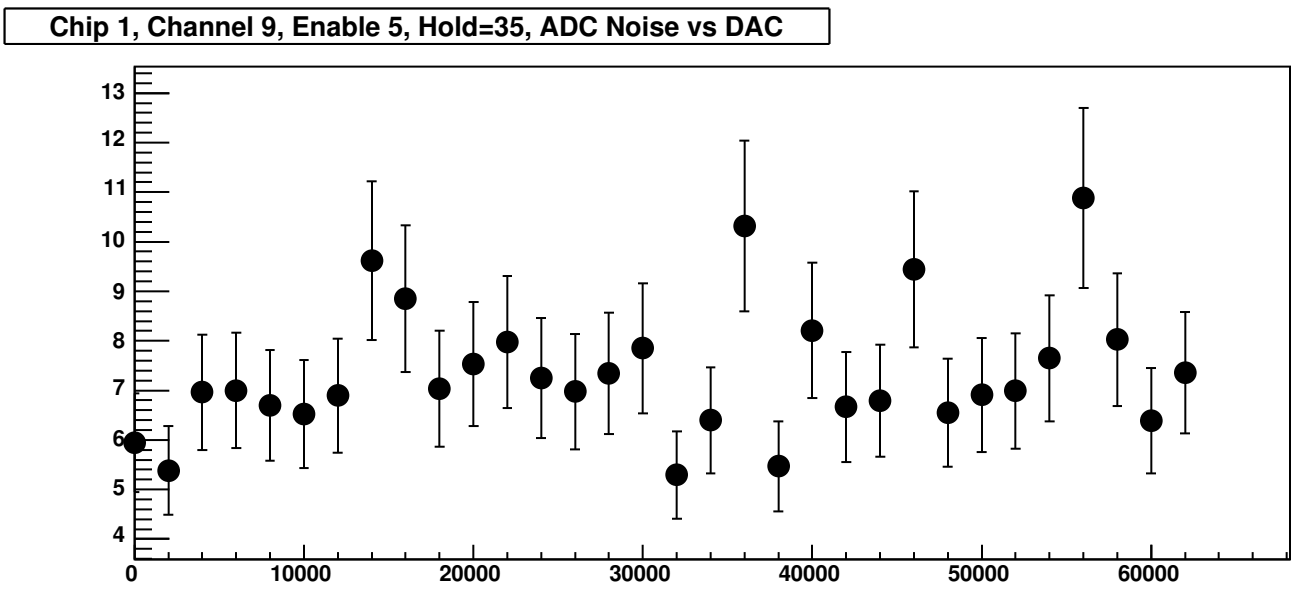

Chip 1, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
0
10000
20000
30000
40000
50000
60000
70000

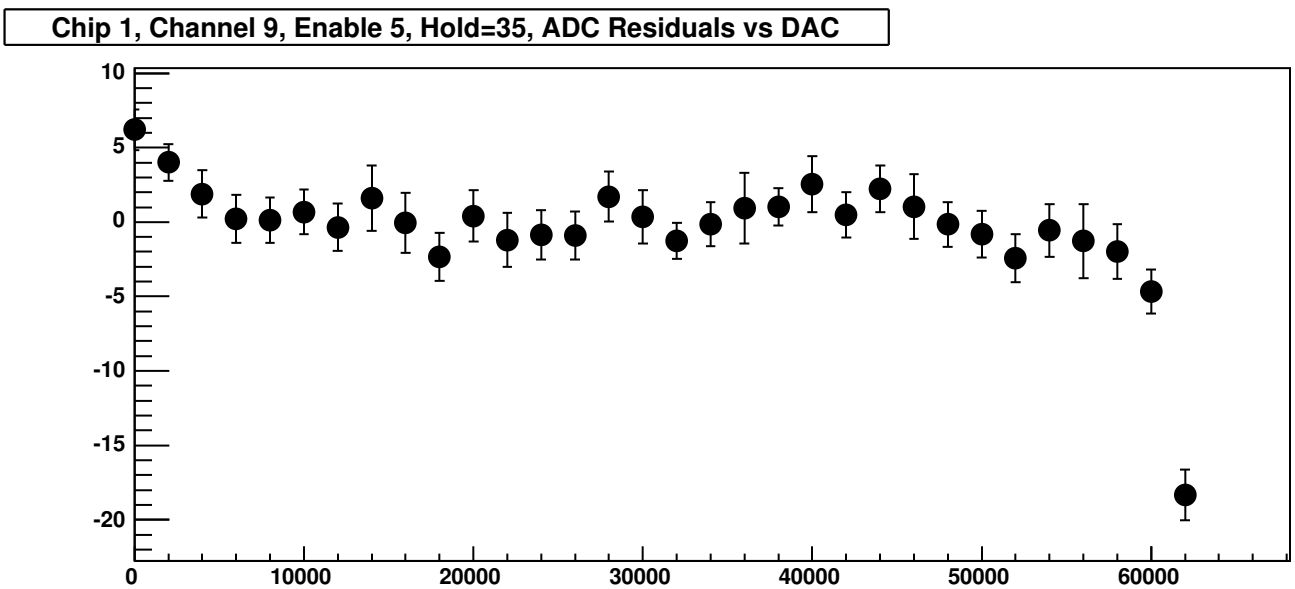

Chip 1, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 65.28 / 23
p0 188.5 ± 0.6948
p1 0.003506 ± 2.176e-05
400
350
300
250
200
0
10000
20000
30000
40000
50000
60000
70000

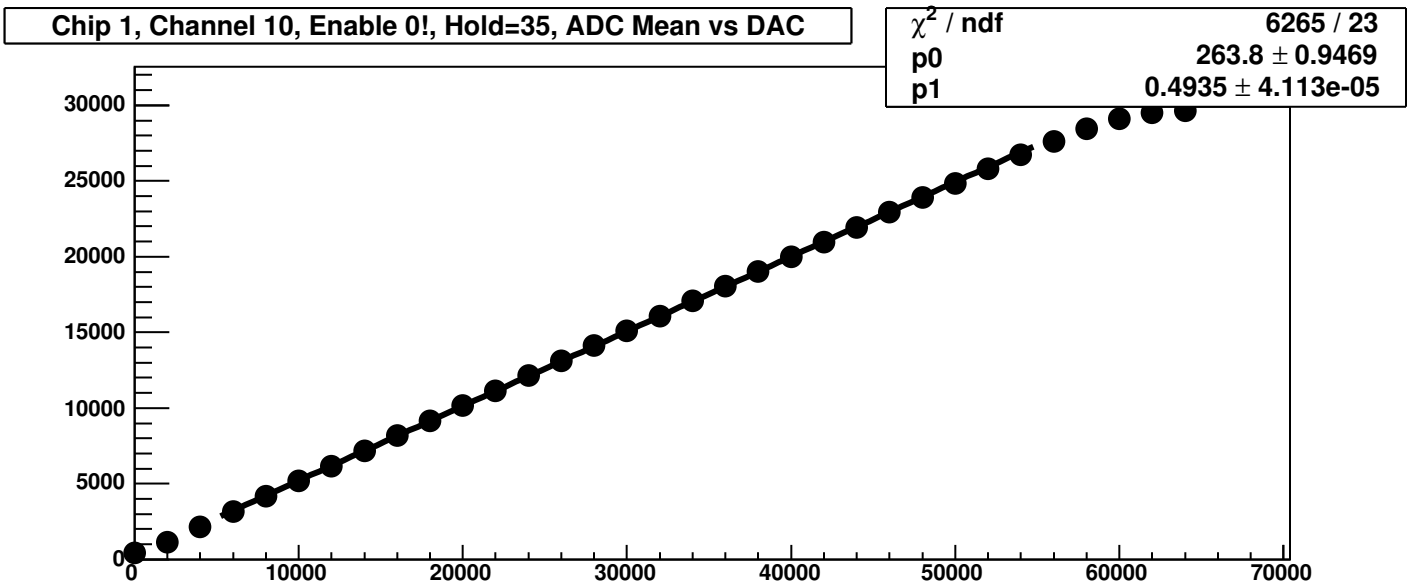

Chip 1, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

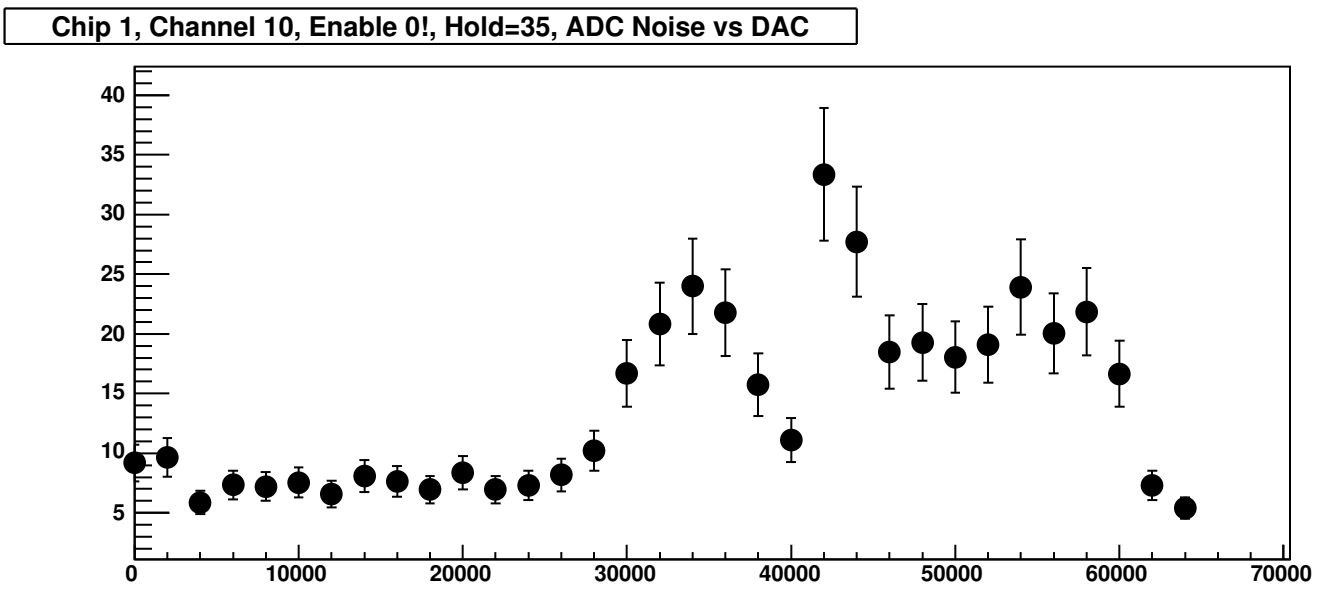

Chip 1, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
-50
0
10000
20000
30000
40000
50000
60000
70000

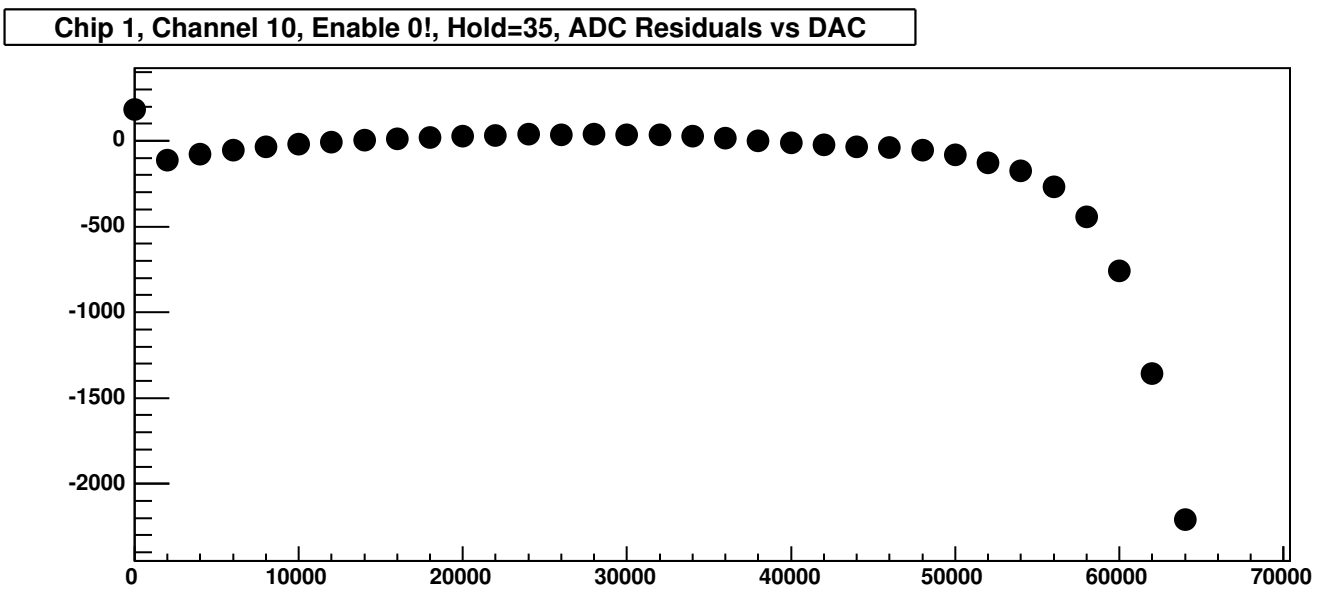

Chip 1, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 13.84 / 23
p0 186.7 ± 0.7634
p1 0.0027 ± 2.279e-05
360
340
320
300
280
260
240
220
200
180
0
10000
20000
30000
40000
50000
60000

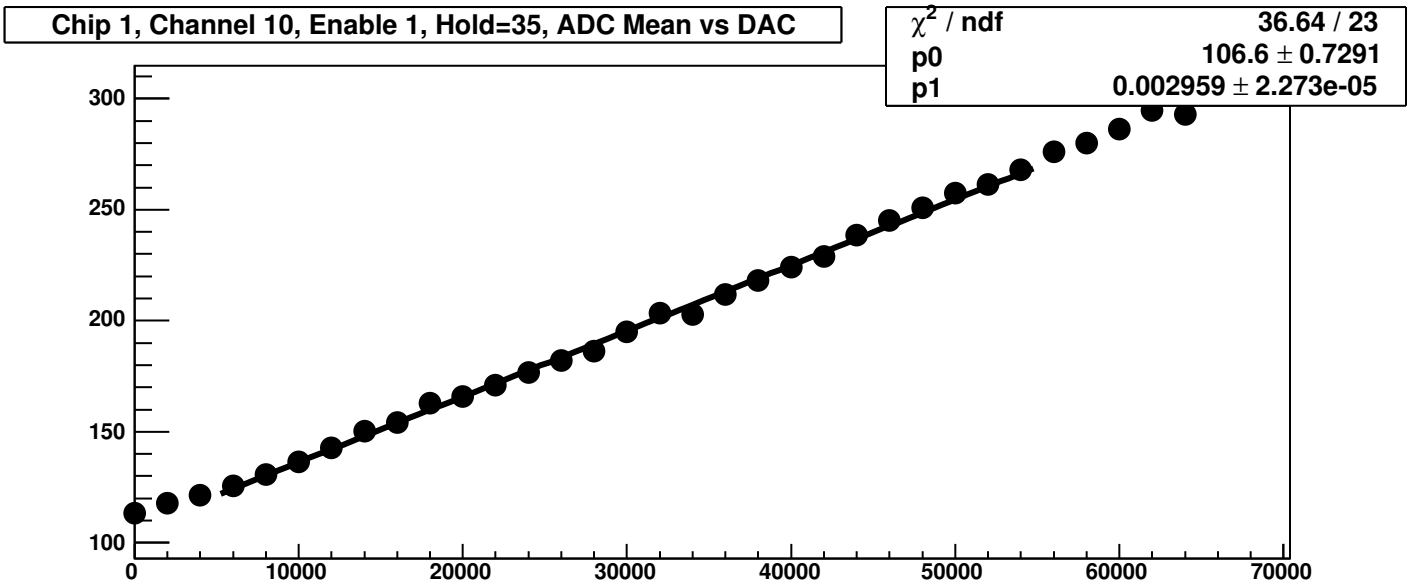

Chip 1, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

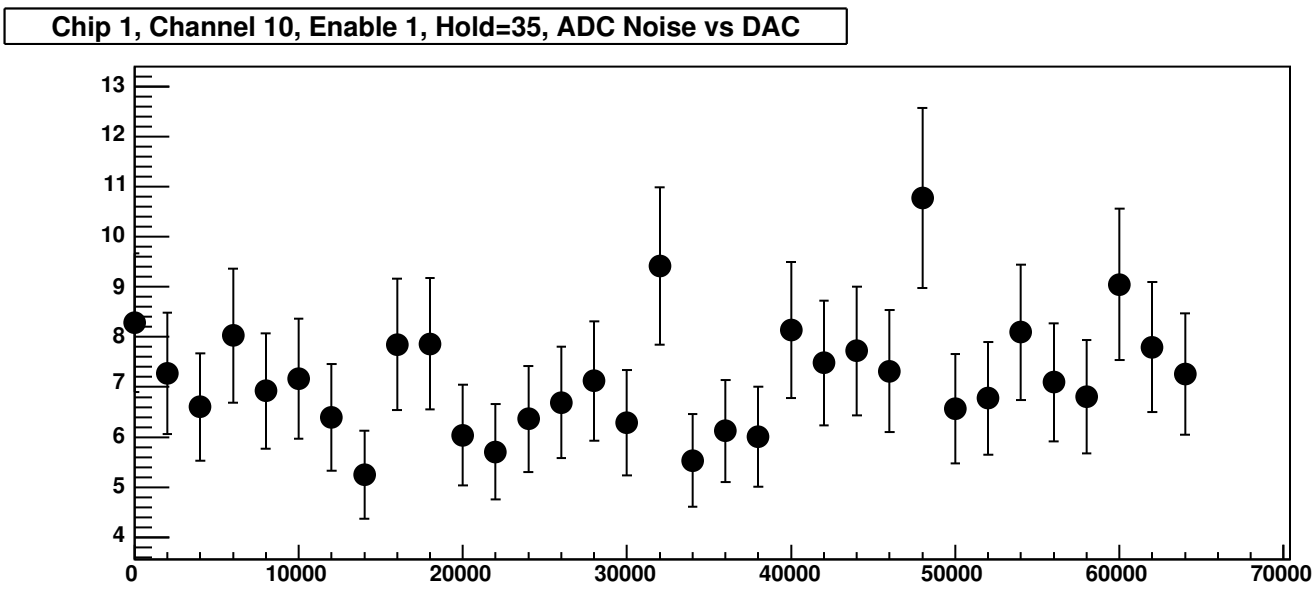

Chip 1, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
0
10000
20000
30000
40000
50000
60000

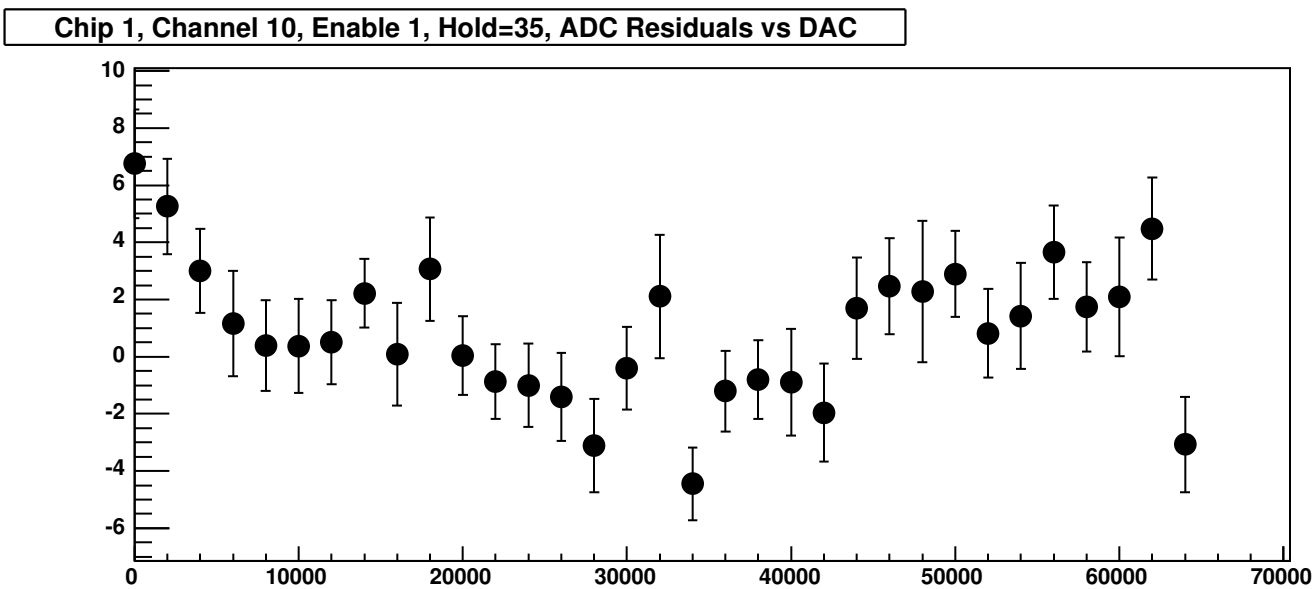

Chip 1, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 6265 / 23
p0 263.8 ± 0.9469
p1 0.4935 ± 4.113e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000

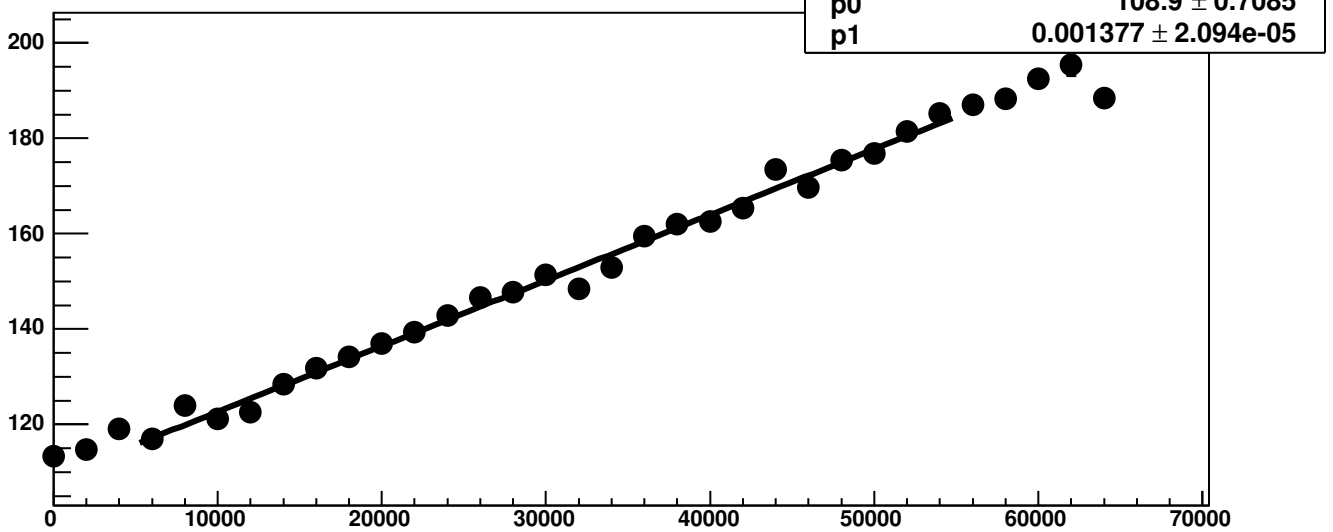

Chip 1, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 36.64 / 23
p0 106.6 ± 0.7291
p1 0.002959 ± 2.273e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

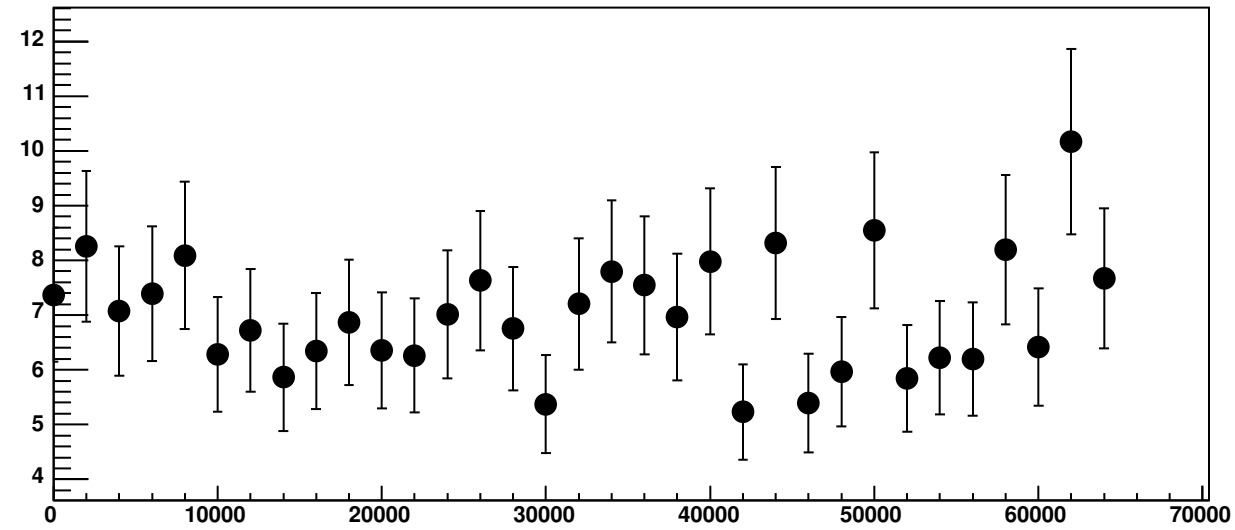

Chip 1, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

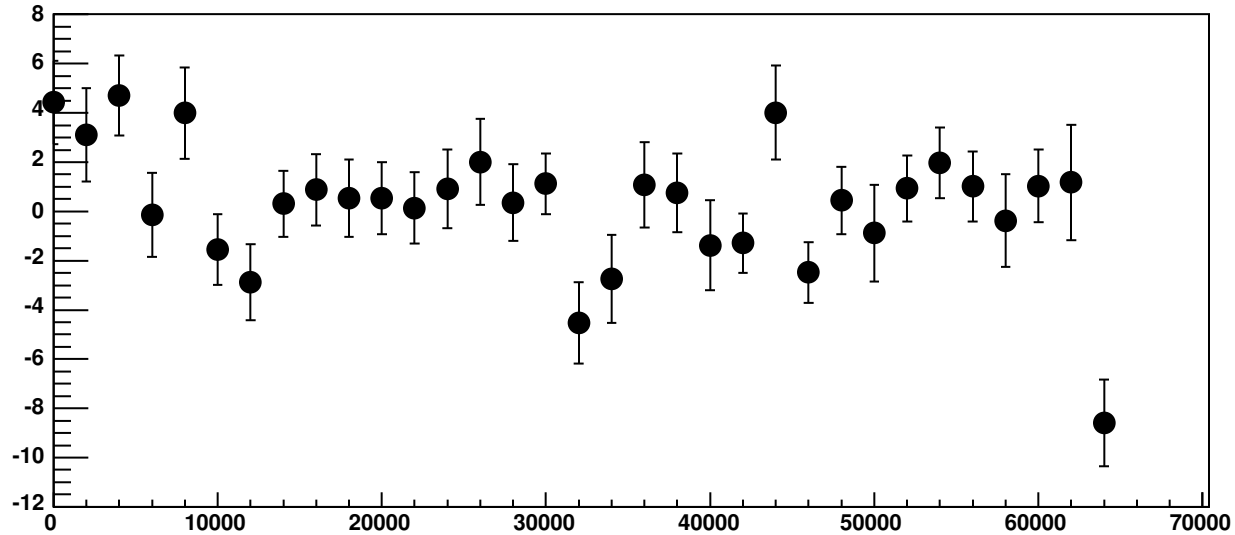
Chip 1, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC

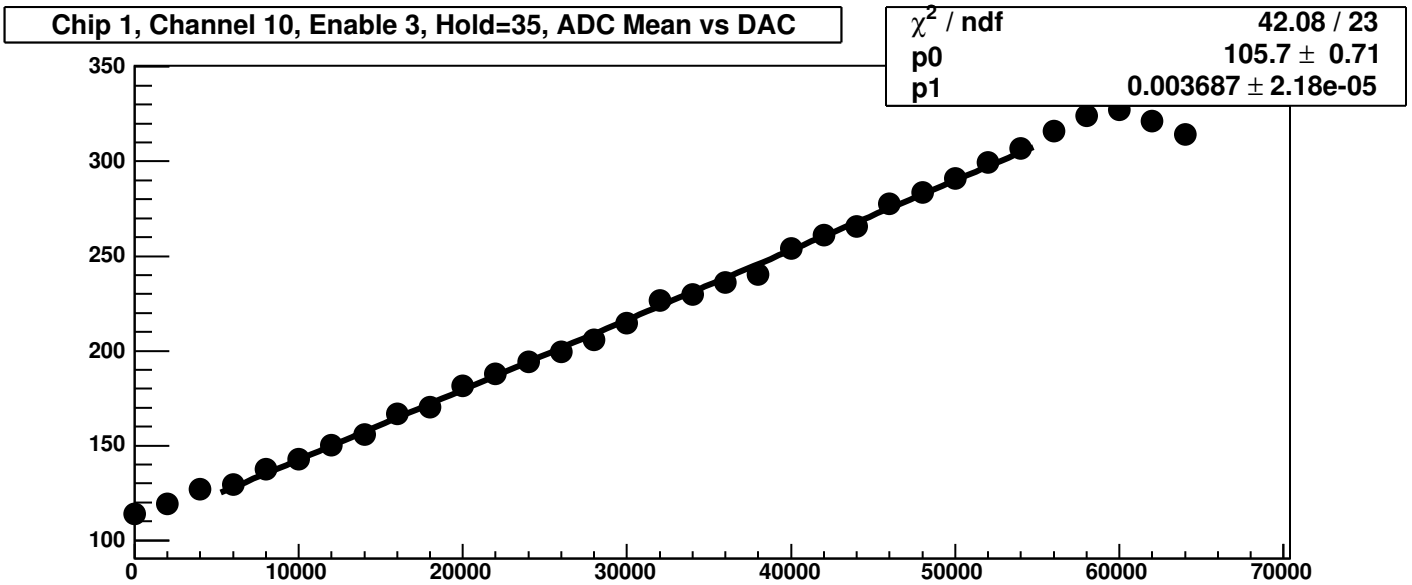


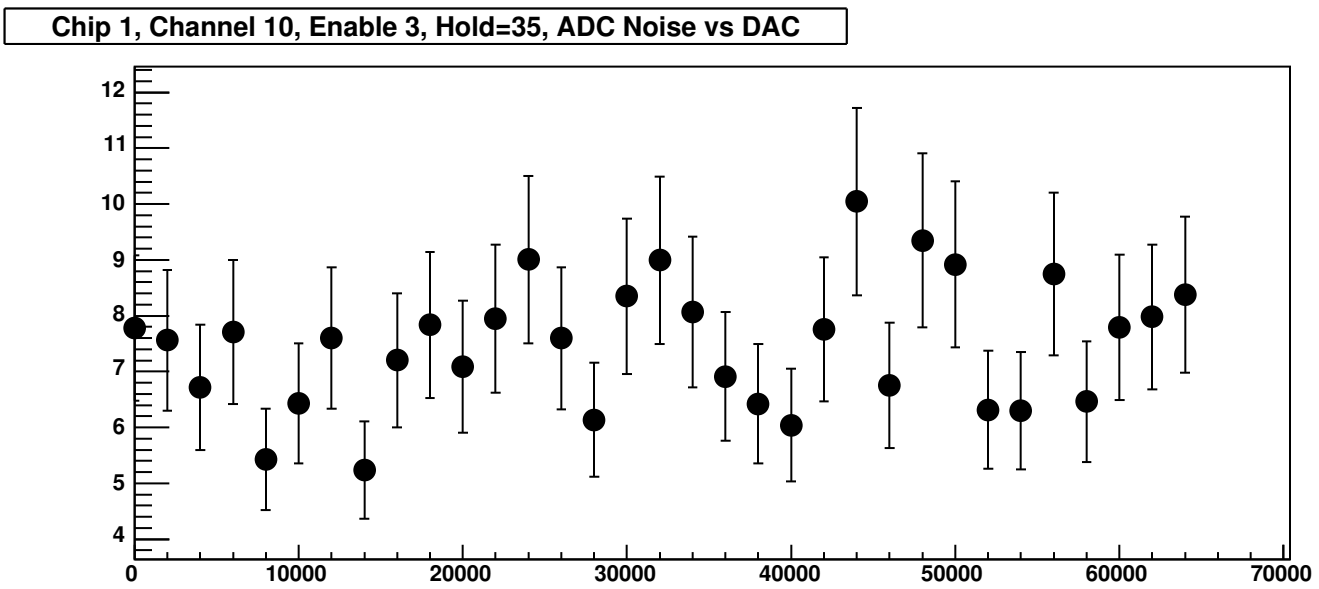
Chip 1, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC

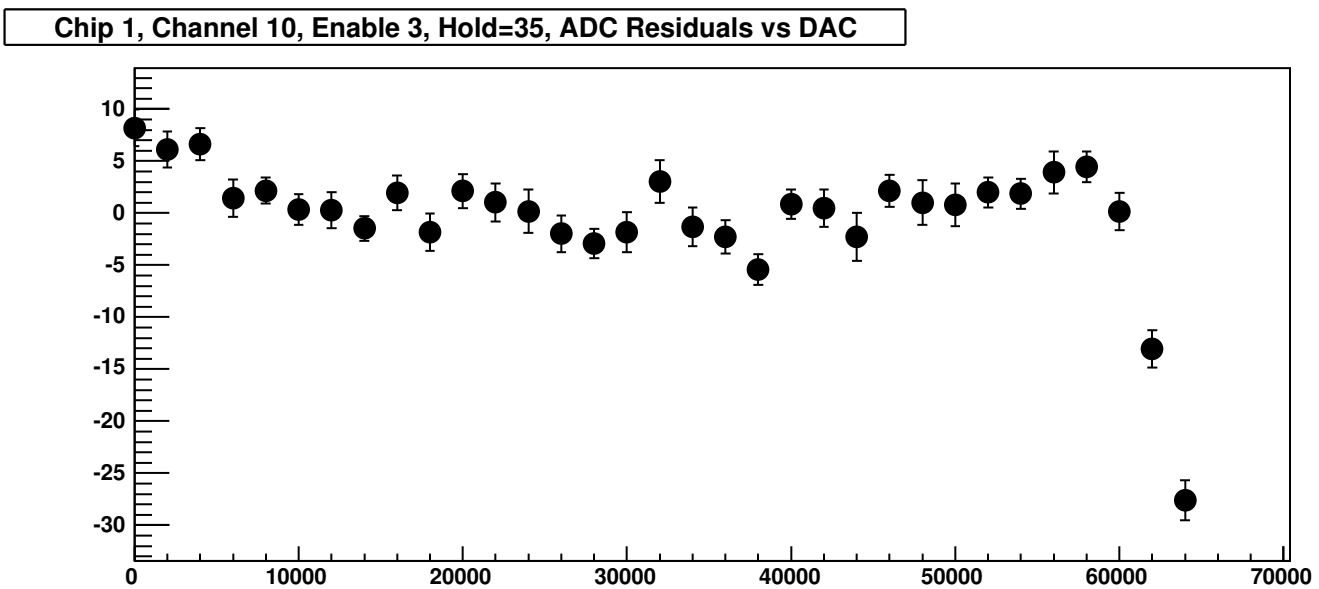


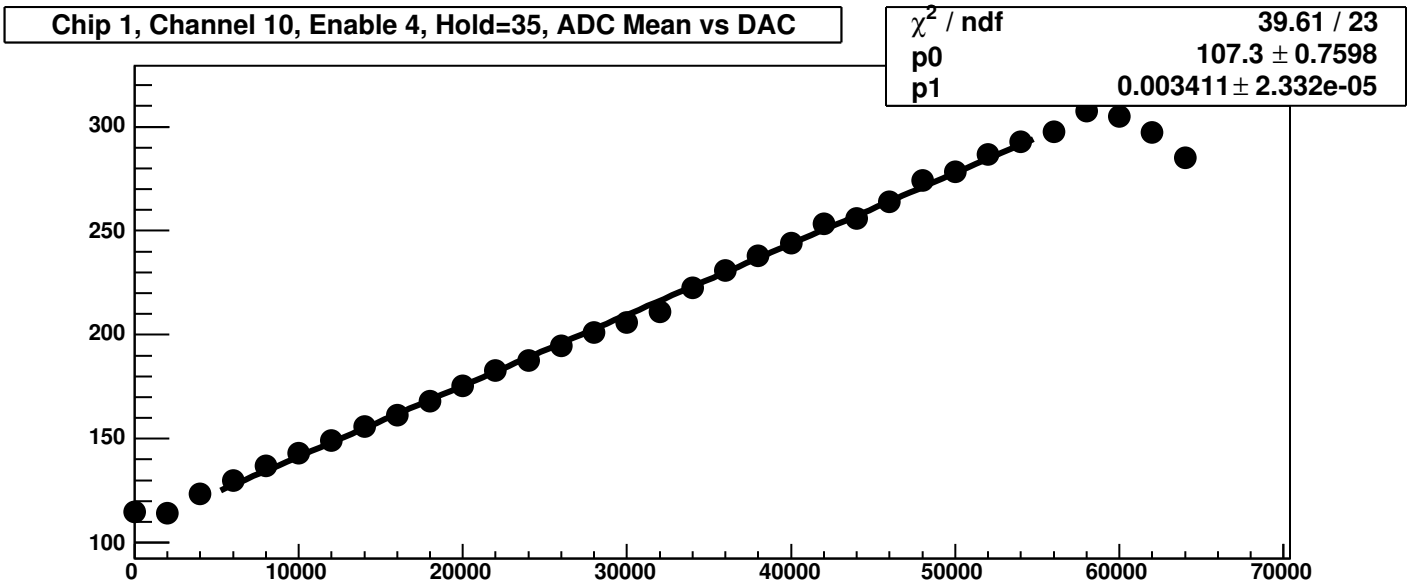
Chip 1, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC

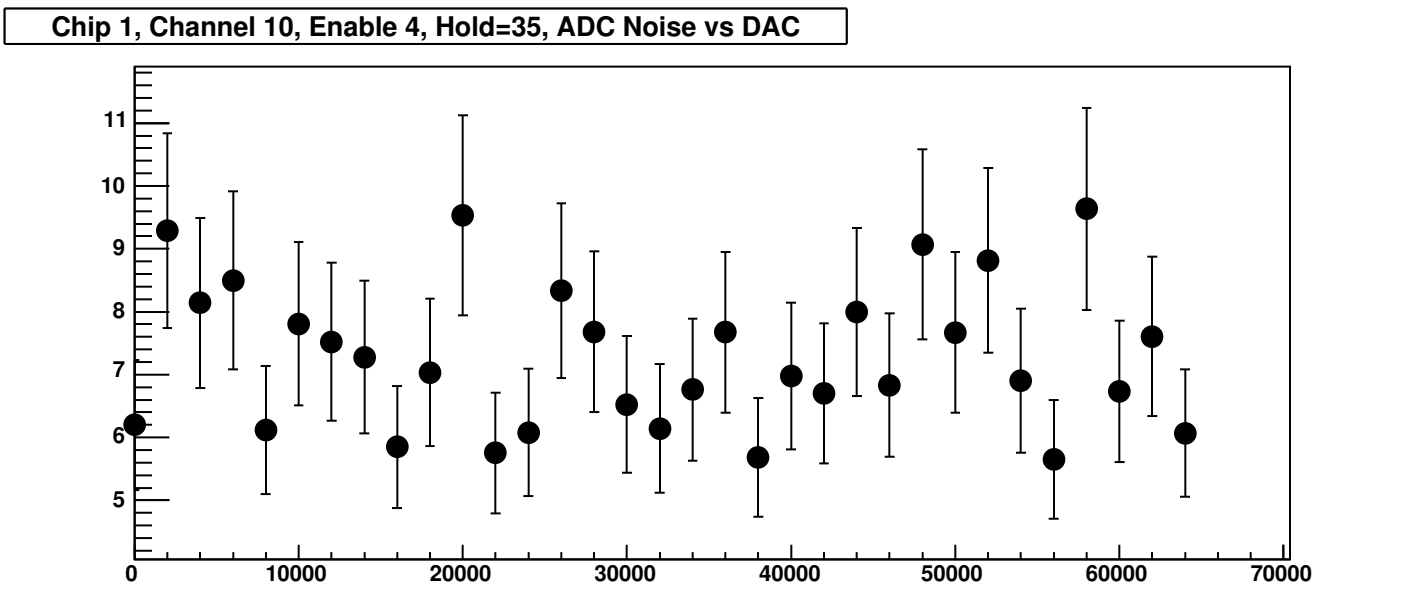


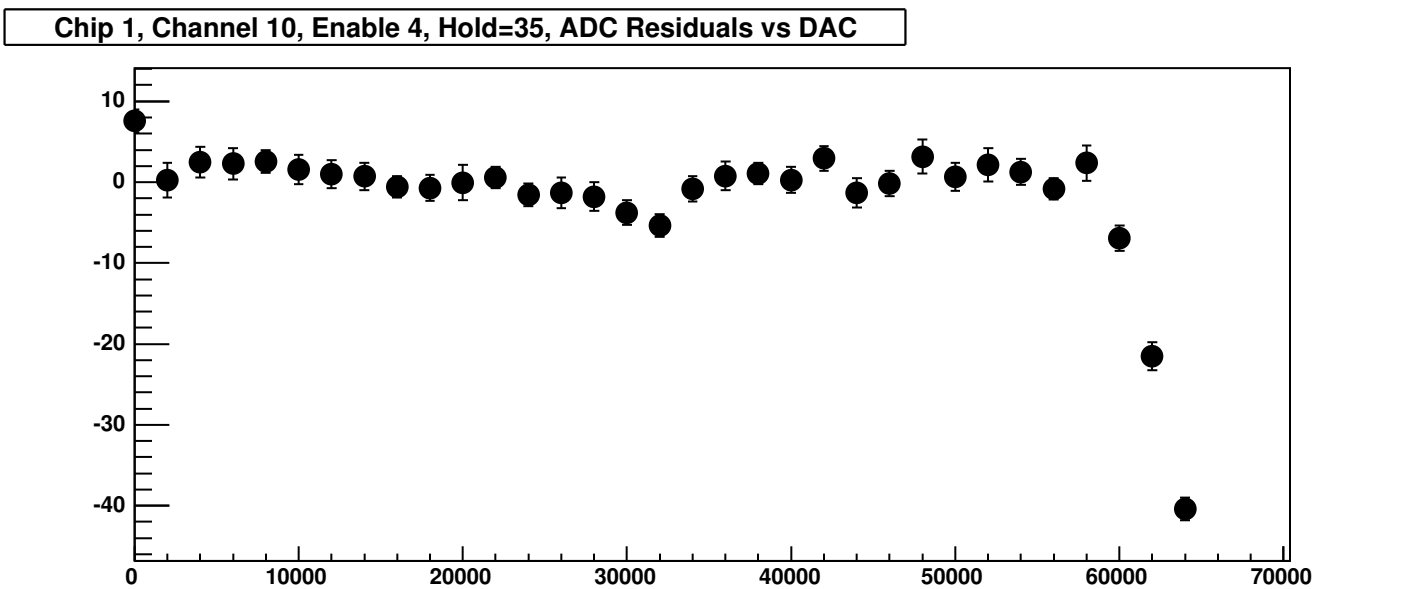

Chip 1, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 42.08 / 23
p0 105.7 ± 0.71
p1 0.003687 ± 2.18e-05
350
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

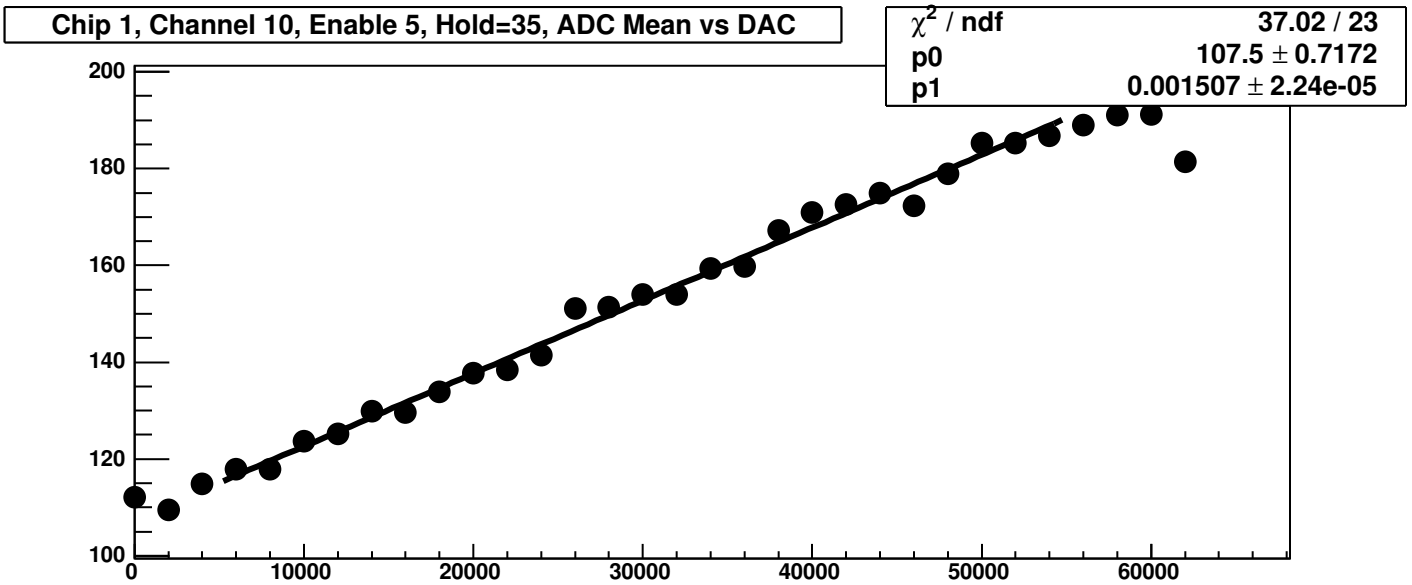

Chip 1, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

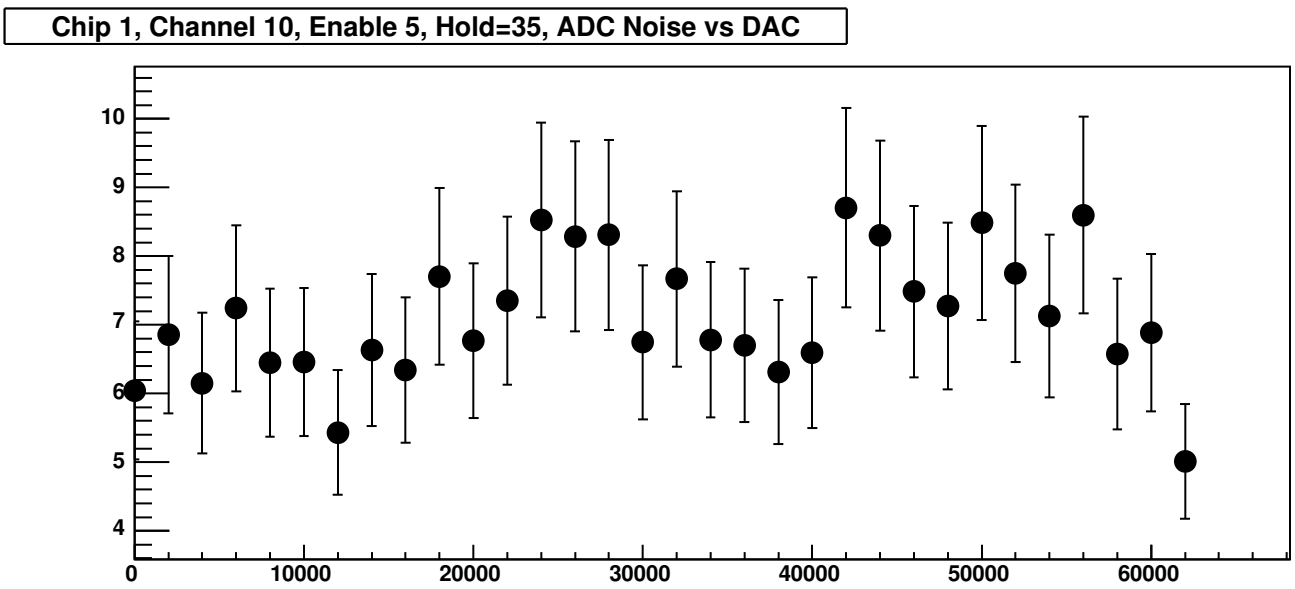

Chip 1, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
0
10000
20000
30000
40000
50000
60000
70000

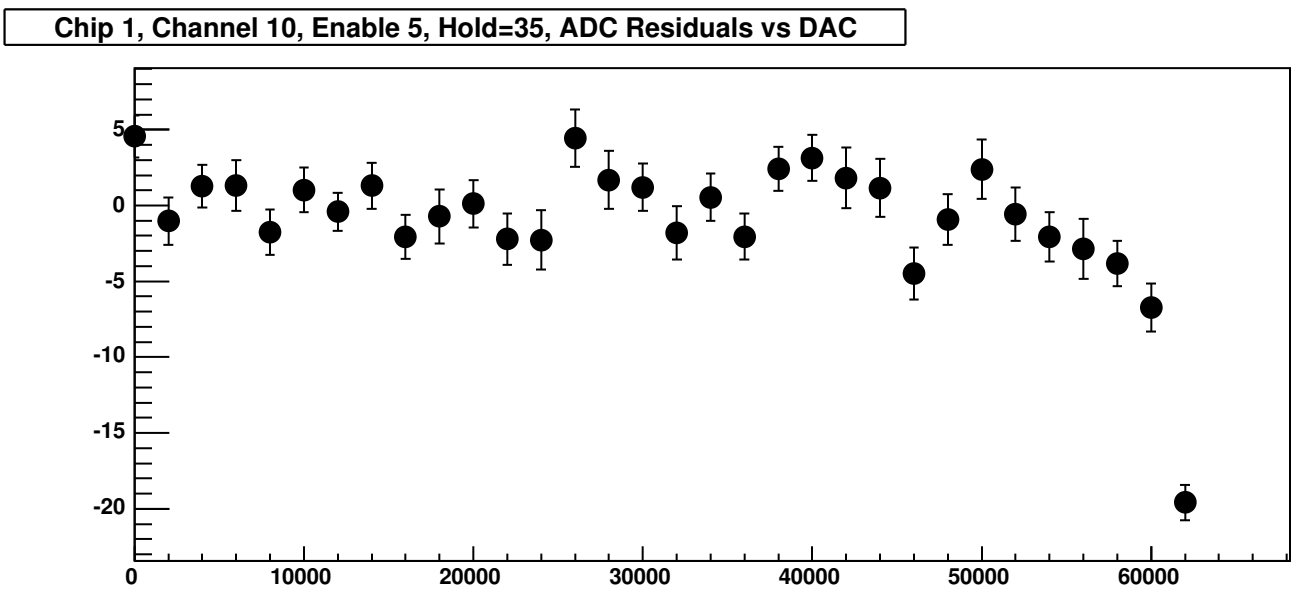

Chip 1, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 39.61 / 23
p0 107.3 ± 0.7598
p1 0.003411 ± 2.332e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

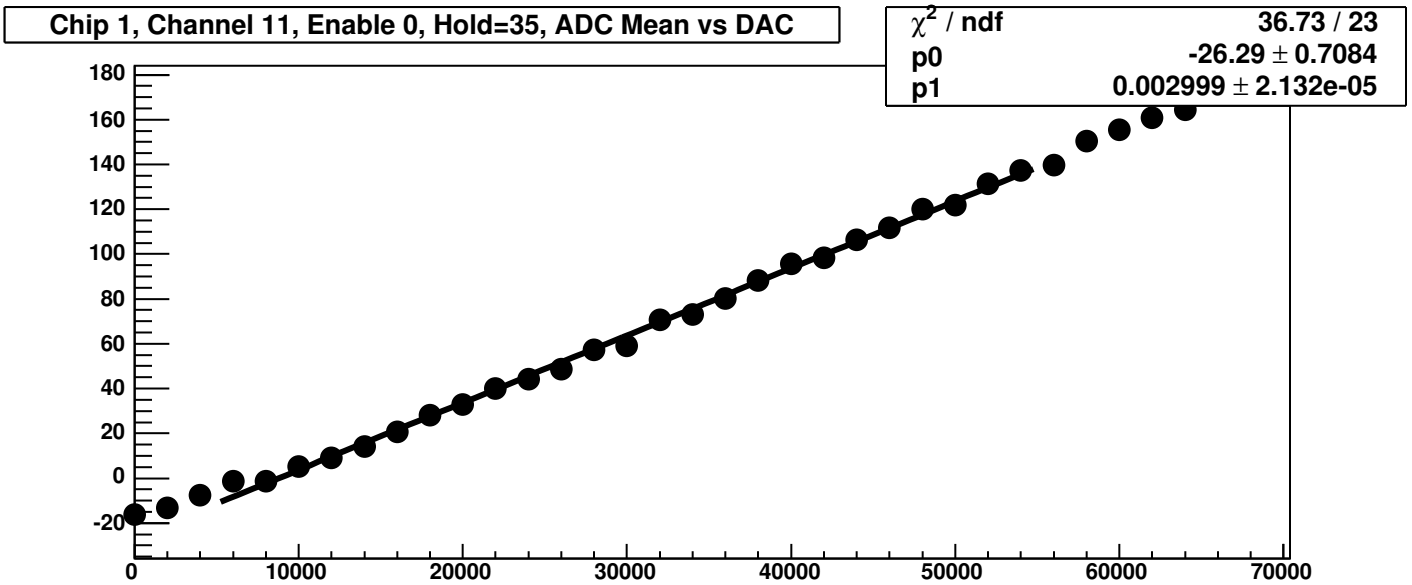

Chip 1, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000

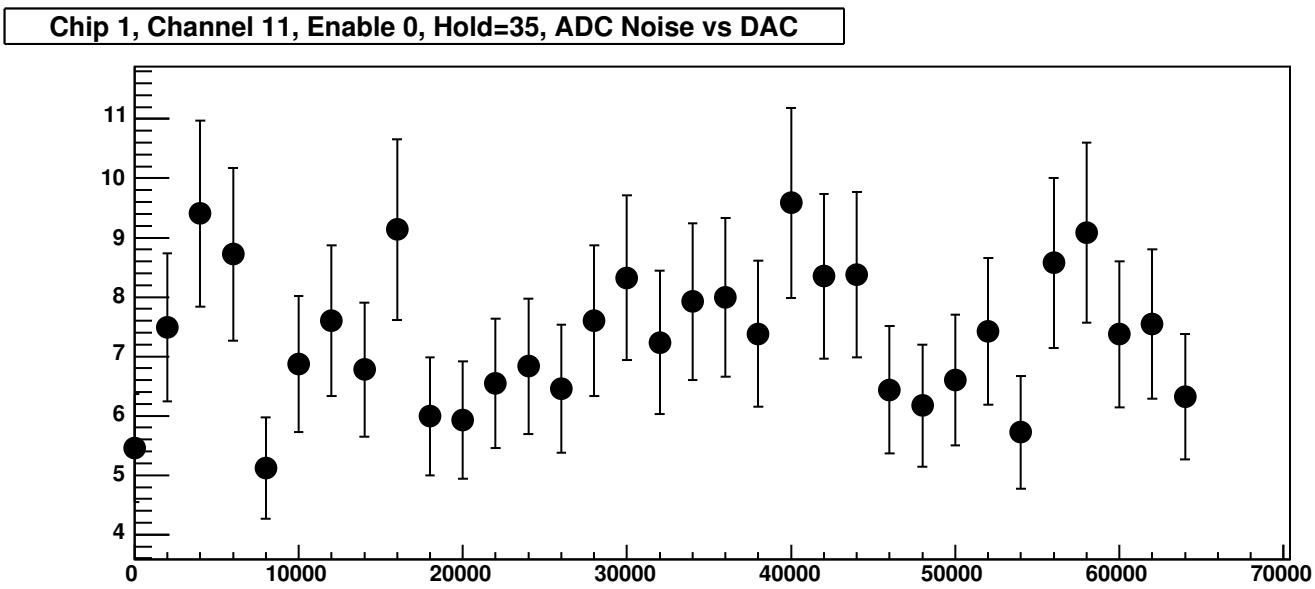

Chip 1, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
10000
20000
30000
40000
50000
60000
70000

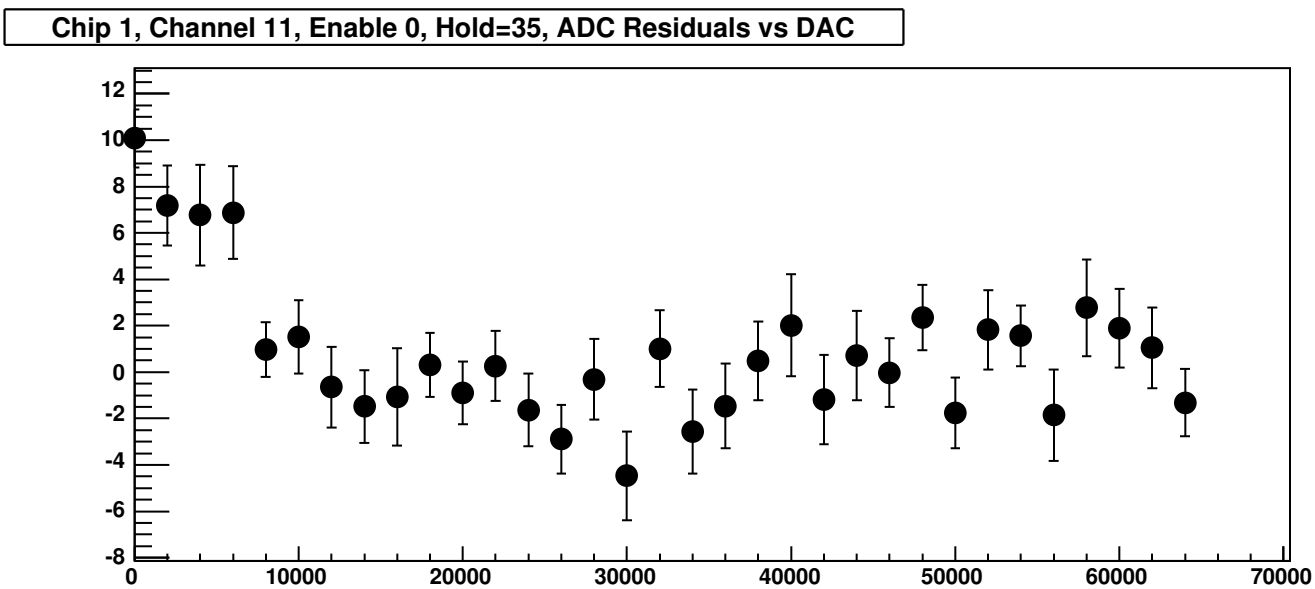

Chip 1, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 37.02 / 23
p0 107.5 ± 0.7172
p1 0.001507 ± 2.24e-05
200
180
160
140
120
100
0
10000
20000
30000
40000
50000
60000

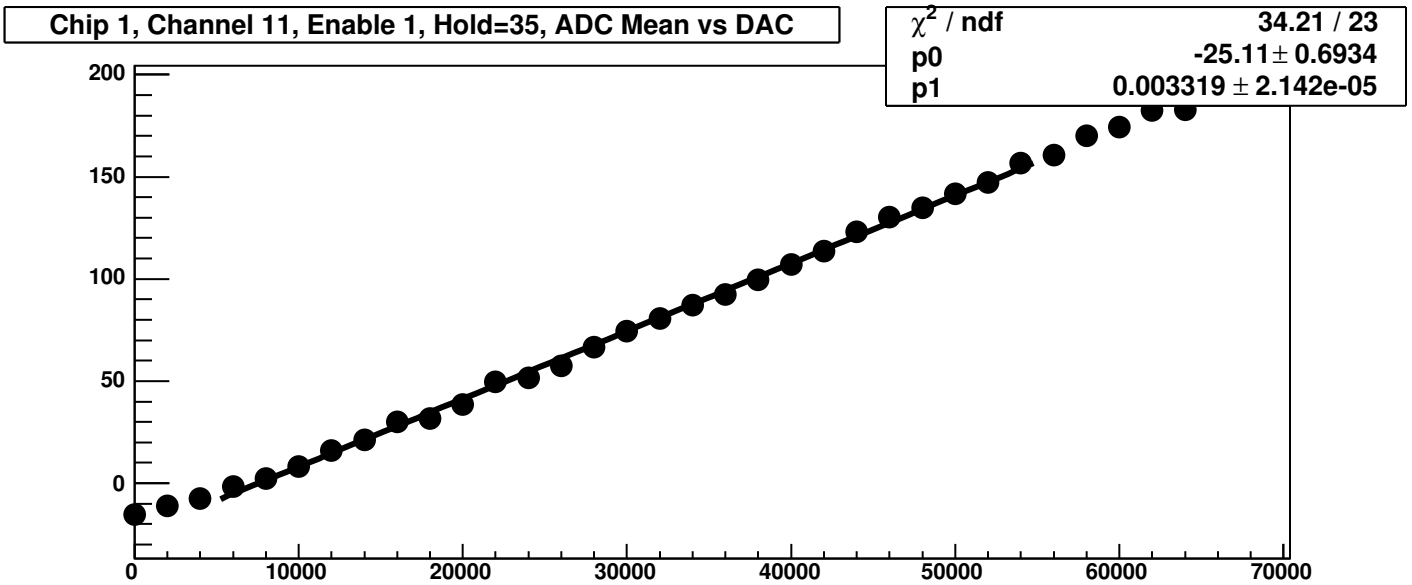

Chip 1, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

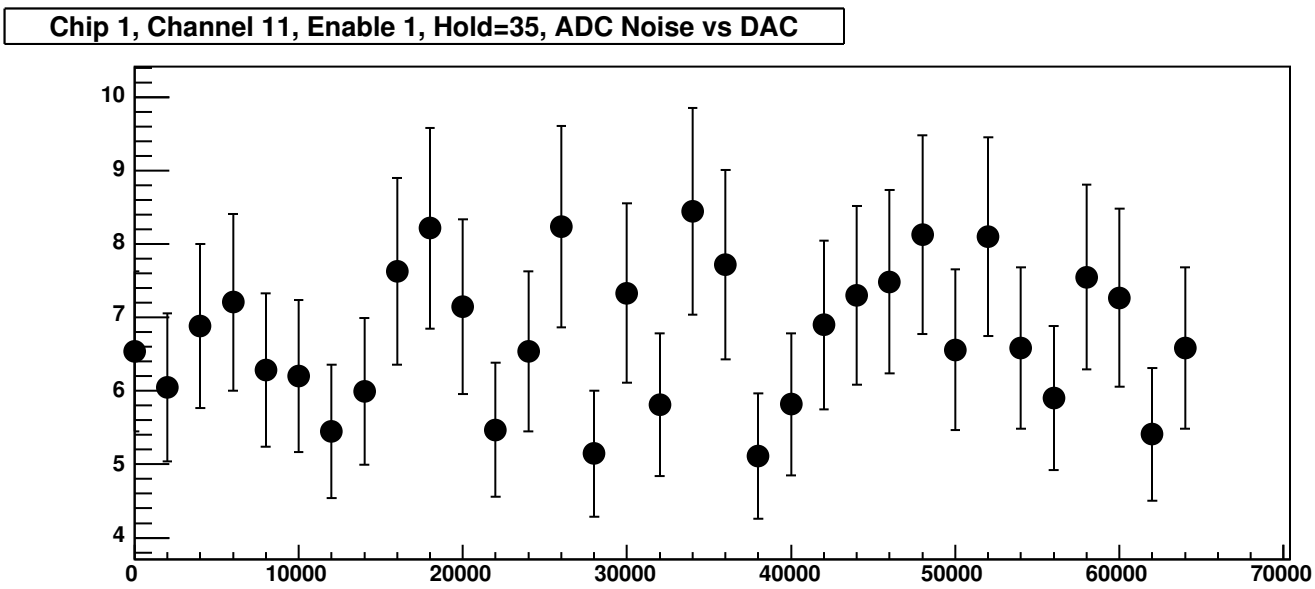

Chip 1, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
0
10000
20000
30000
40000
50000
60000

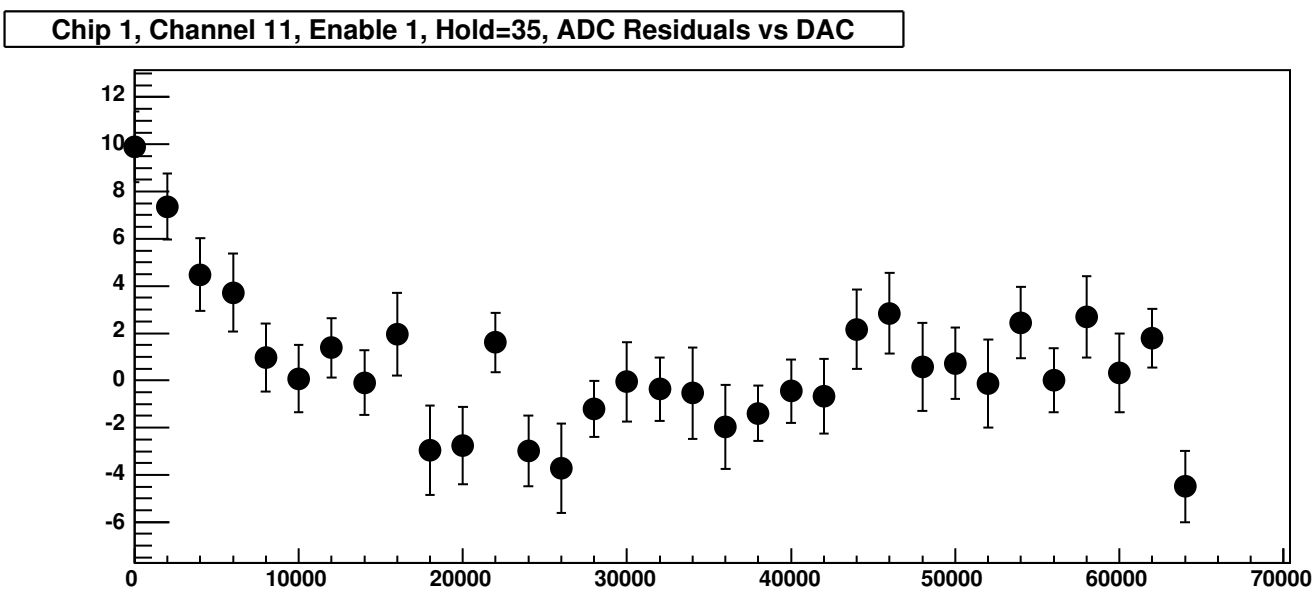

Chip 1, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 36.73 / 23
p0 -26.29 ± 0.7084
p1 0.002999 ± 2.132e-05
180
160
140
120
100
80
60
40
20
0
-20
0
10000
20000
30000
40000
50000
60000
70000

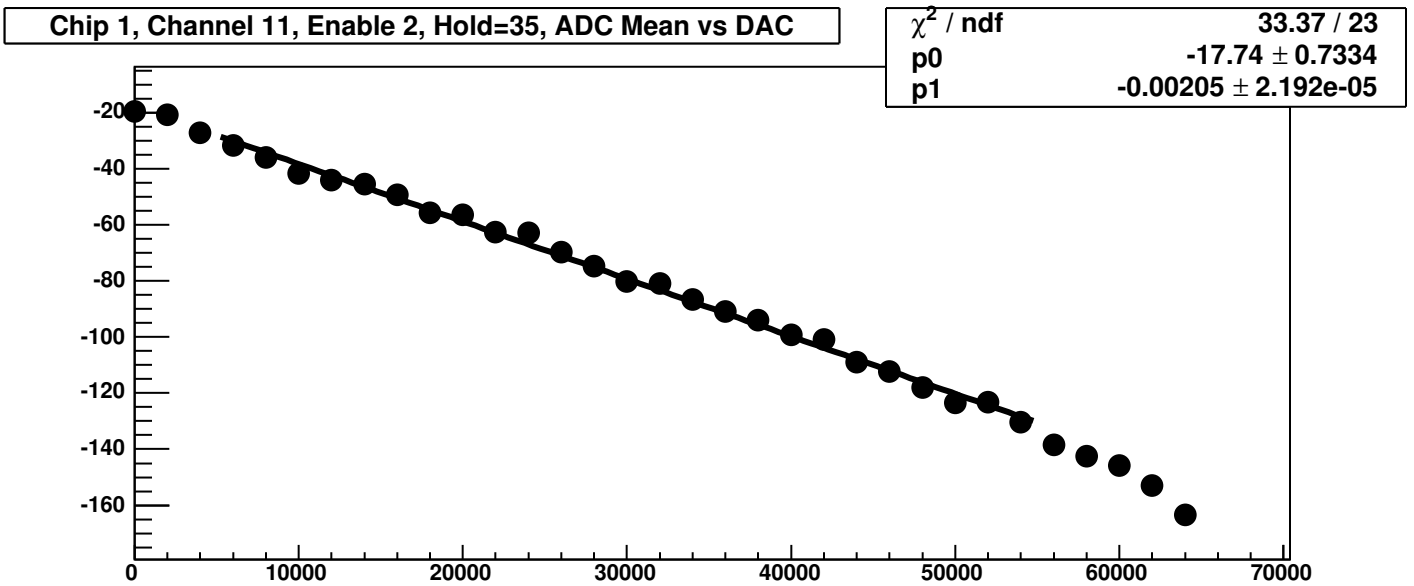

Chip 1, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

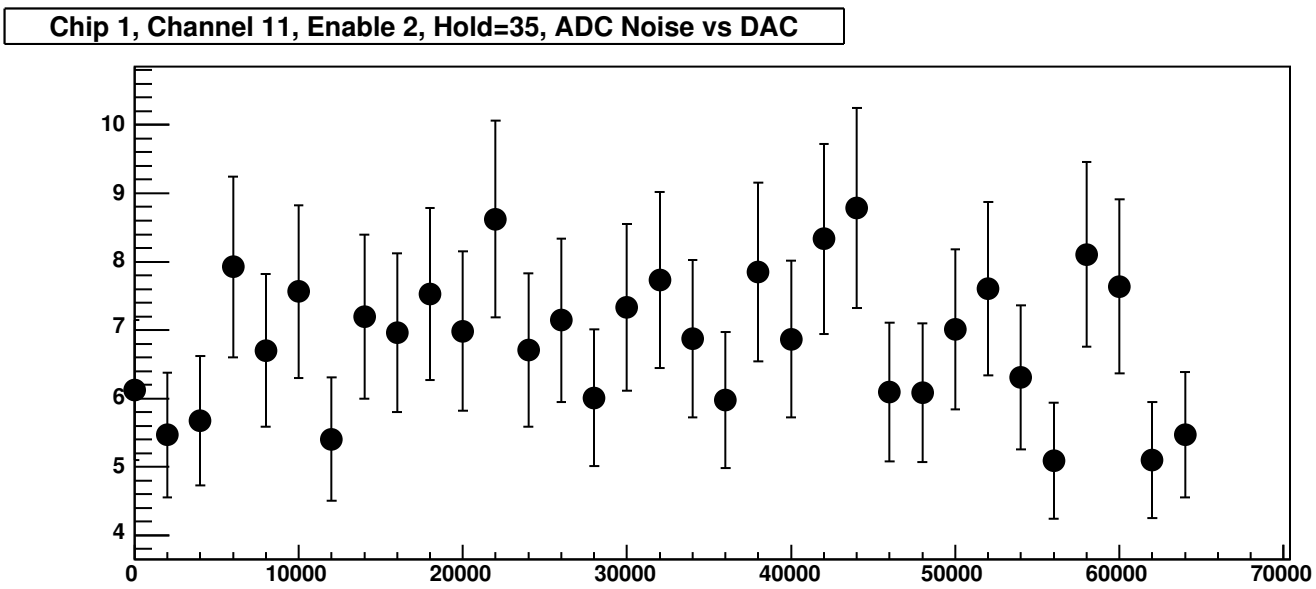

Chip 1, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

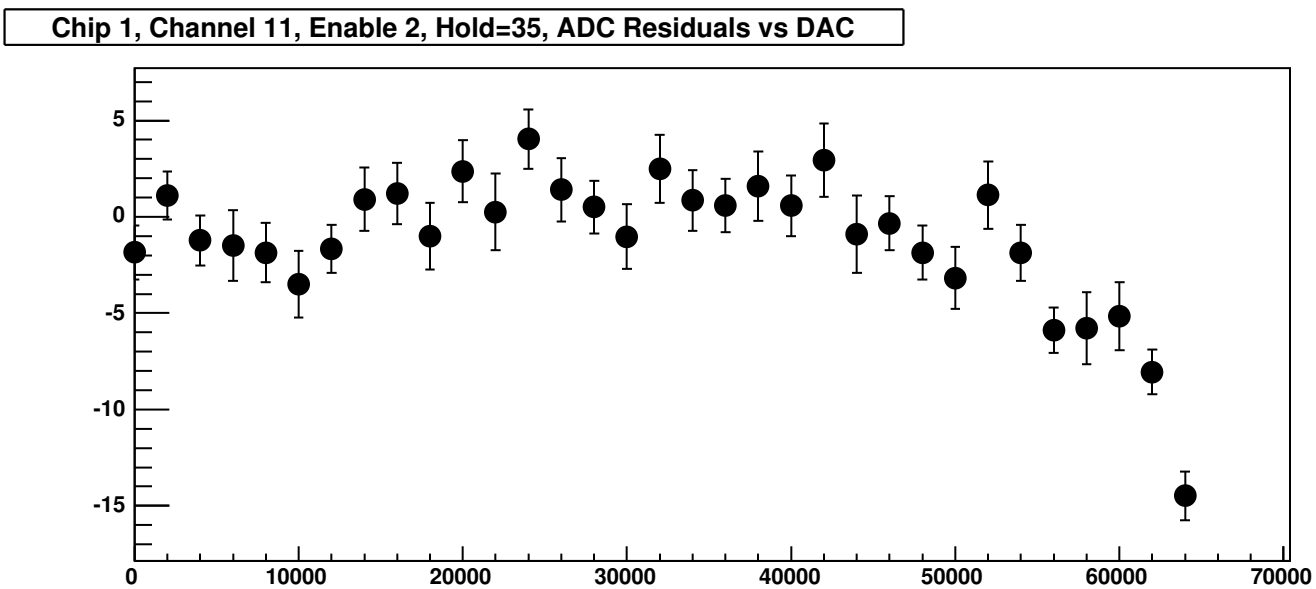

Chip 1, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 34.21 / 23
p0 -25.11 ± 0.6934
p1 0.003319 ± 2.142e-05
200
150
100
50
0
0
10000
20000
30000
40000
50000
60000
70000

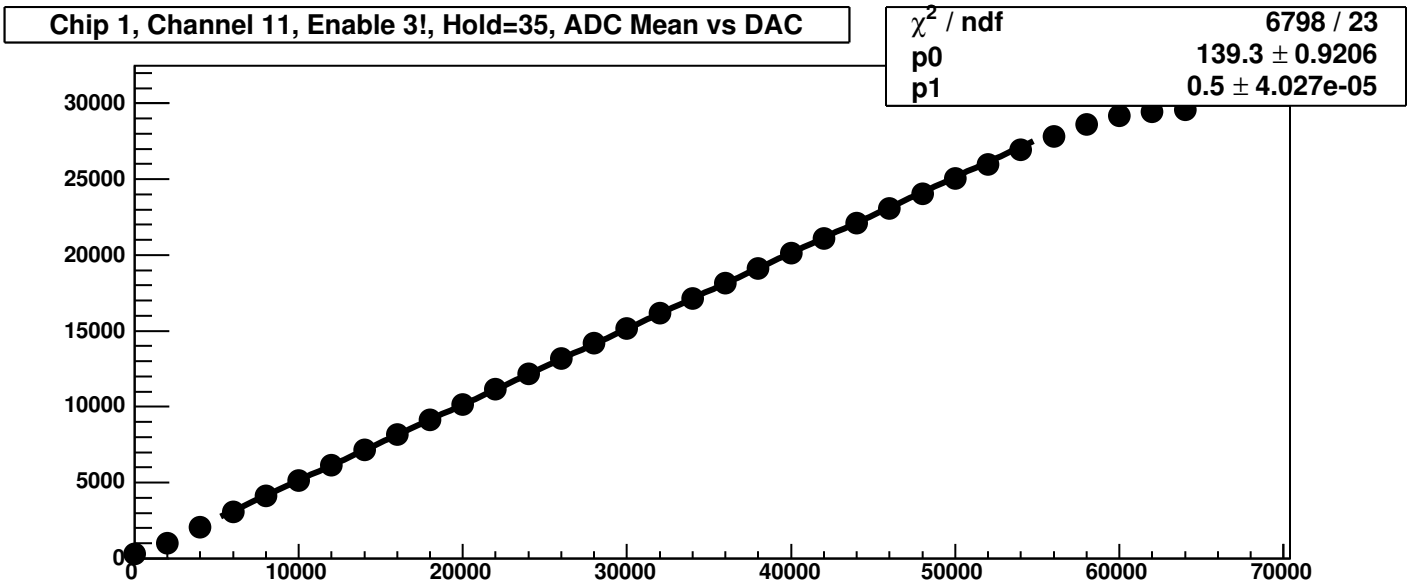

Chip 1, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

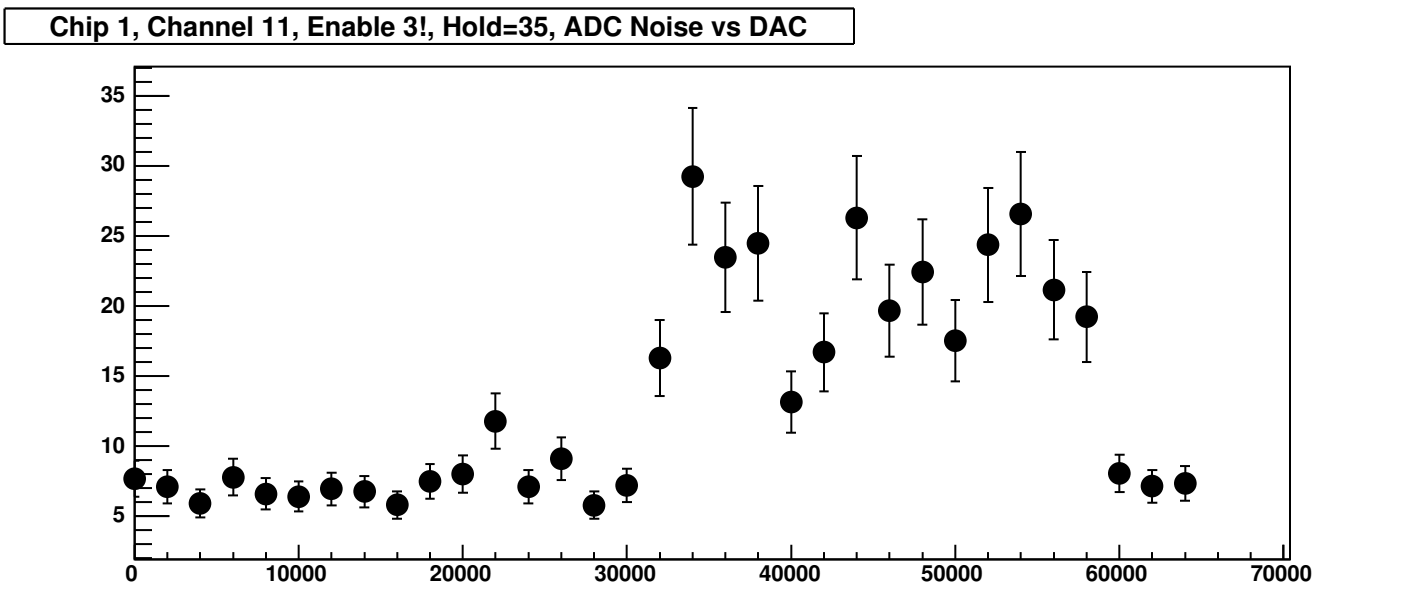

Chip 1, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

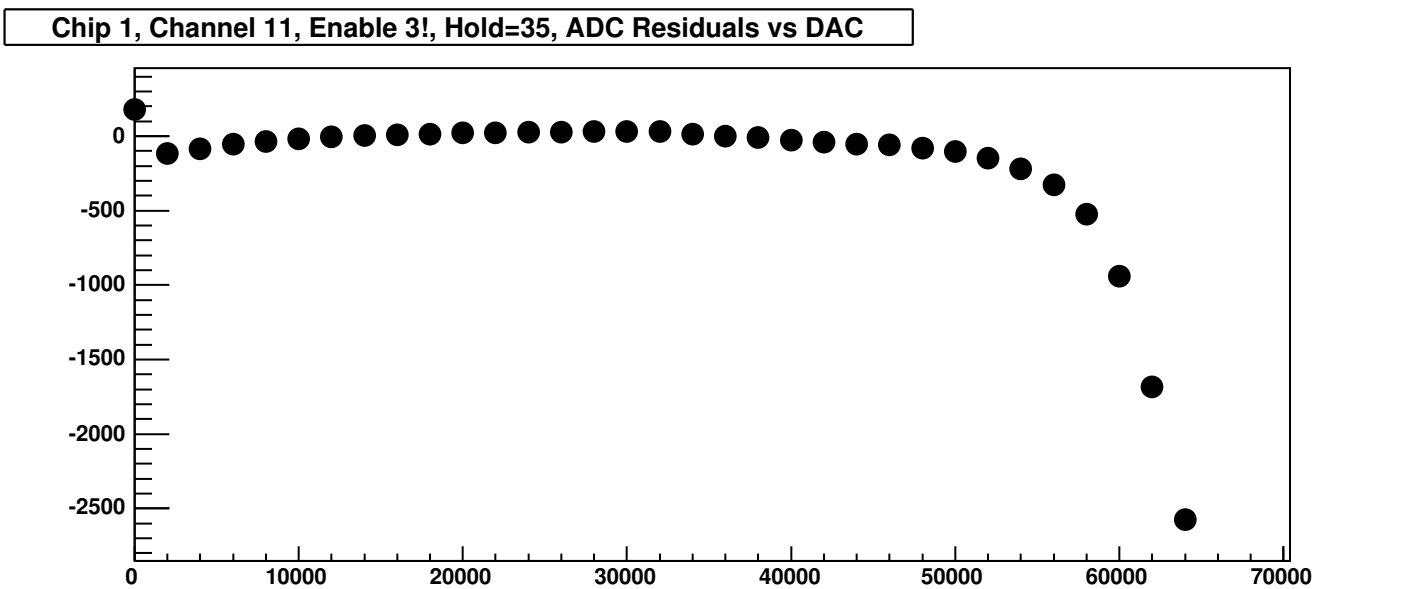

Chip 1, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 33.37 / 23
p0 -17.74 ± 0.7334
p1 -0.00205 ± 2.192e-05
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

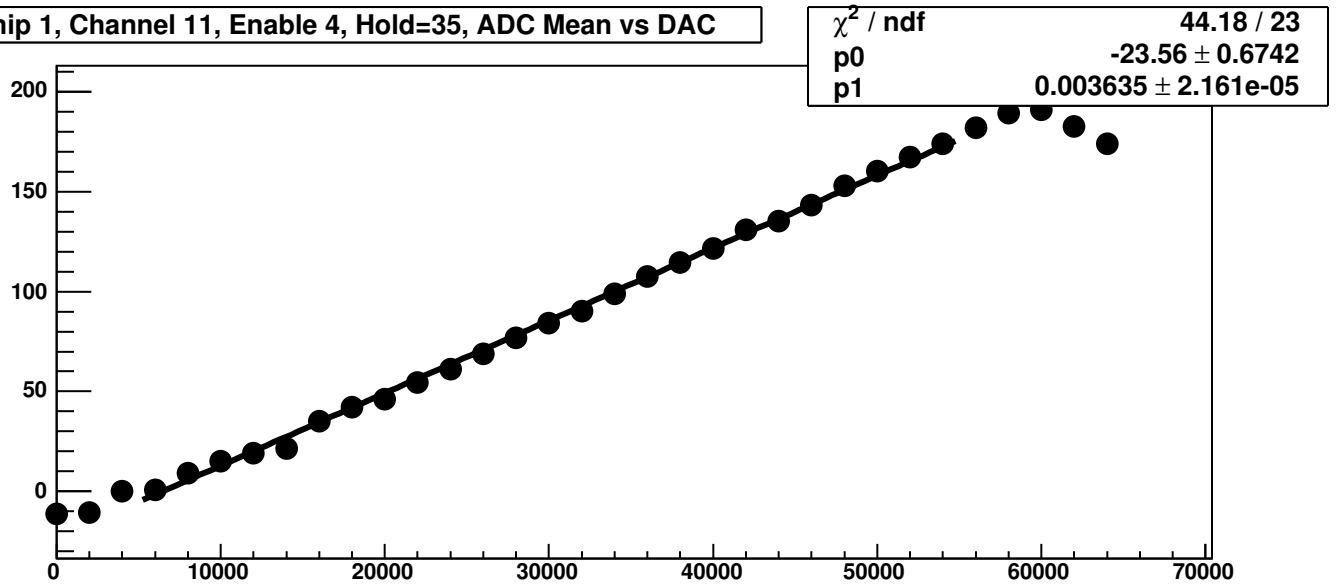

Chip 1, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 6798 / 23
p0 139.3 ± 0.9206
p1 0.5 ± 4.027e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

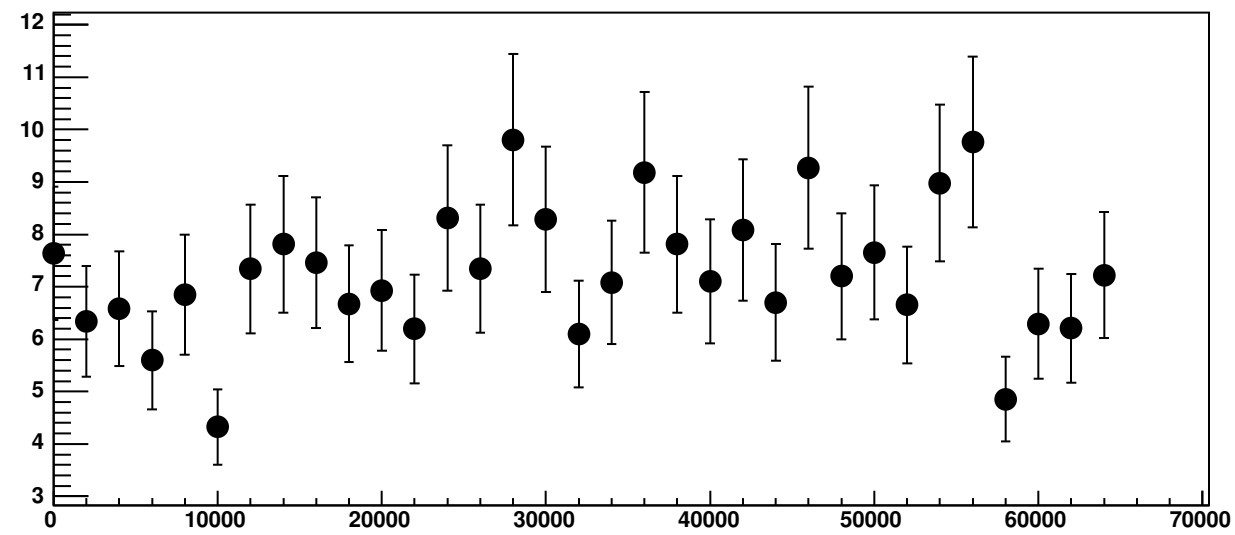

Chip 1, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
0
10000
20000
30000
40000
50000
60000
70000

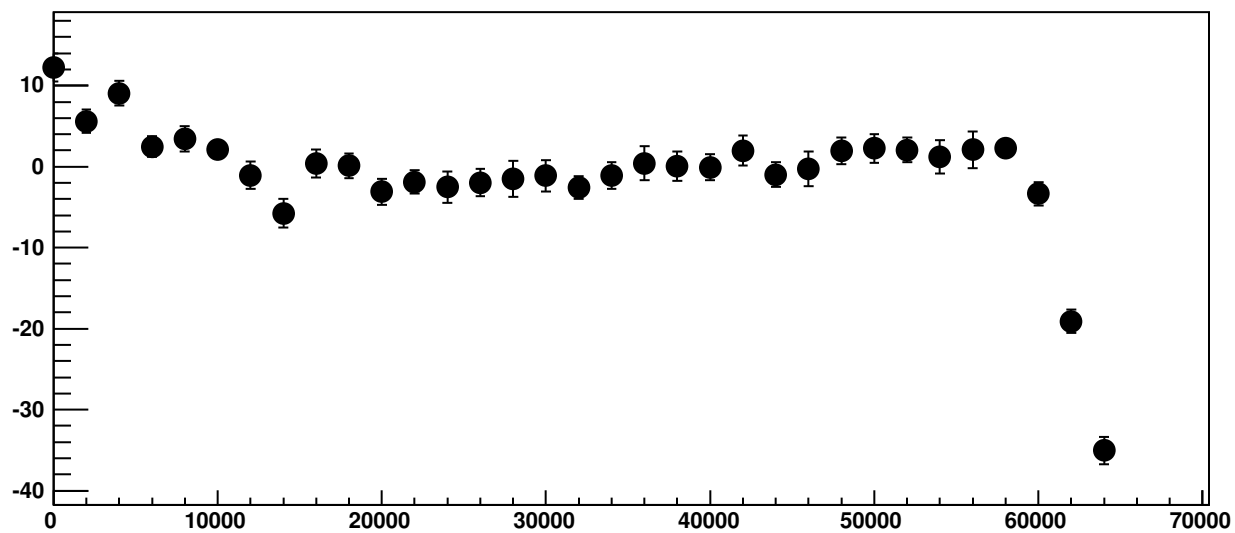
Chip 1, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

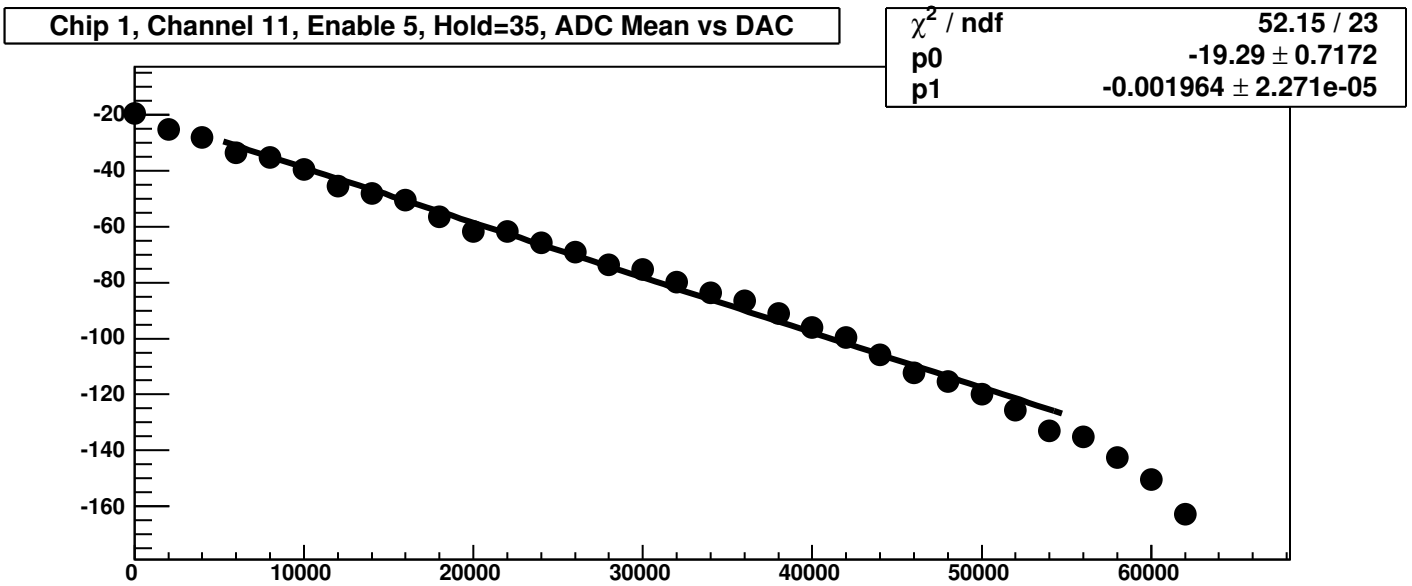


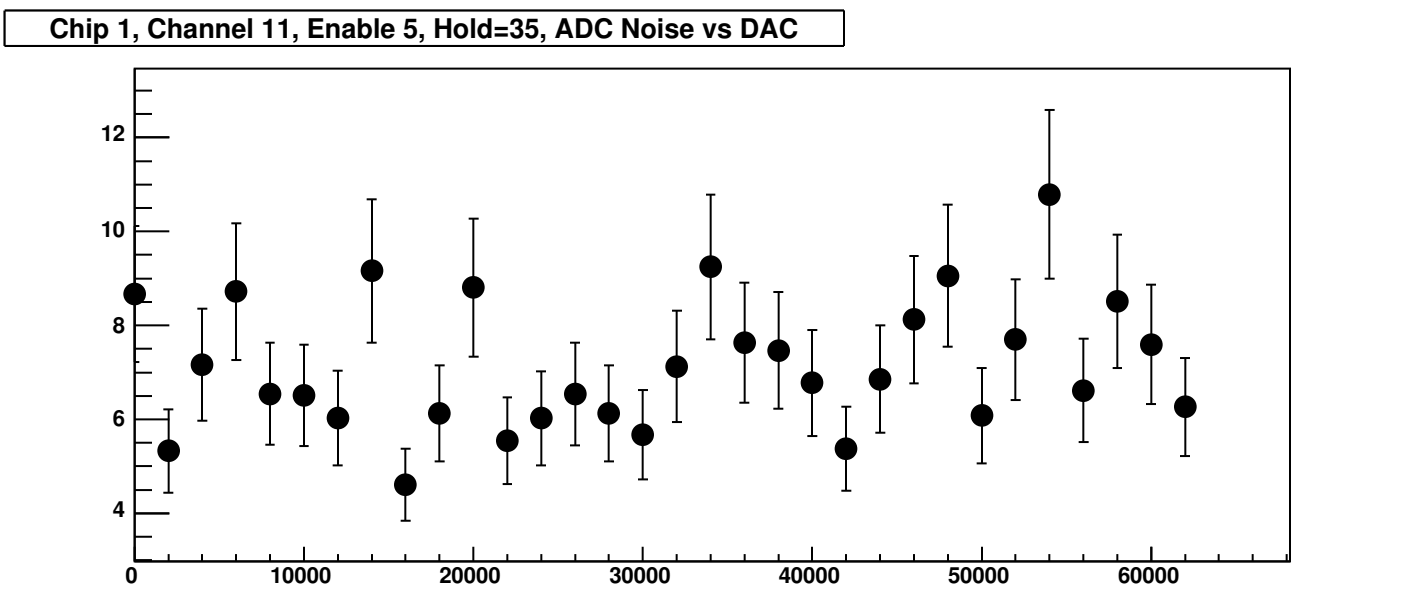
Chip 1, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

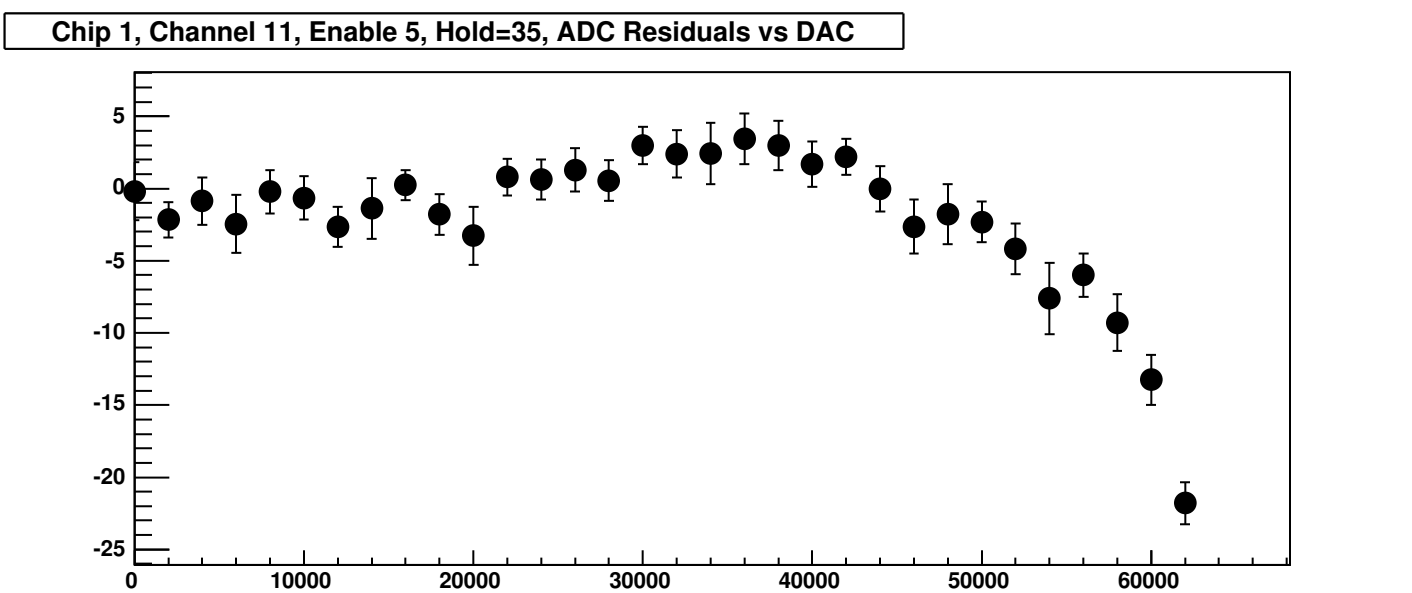


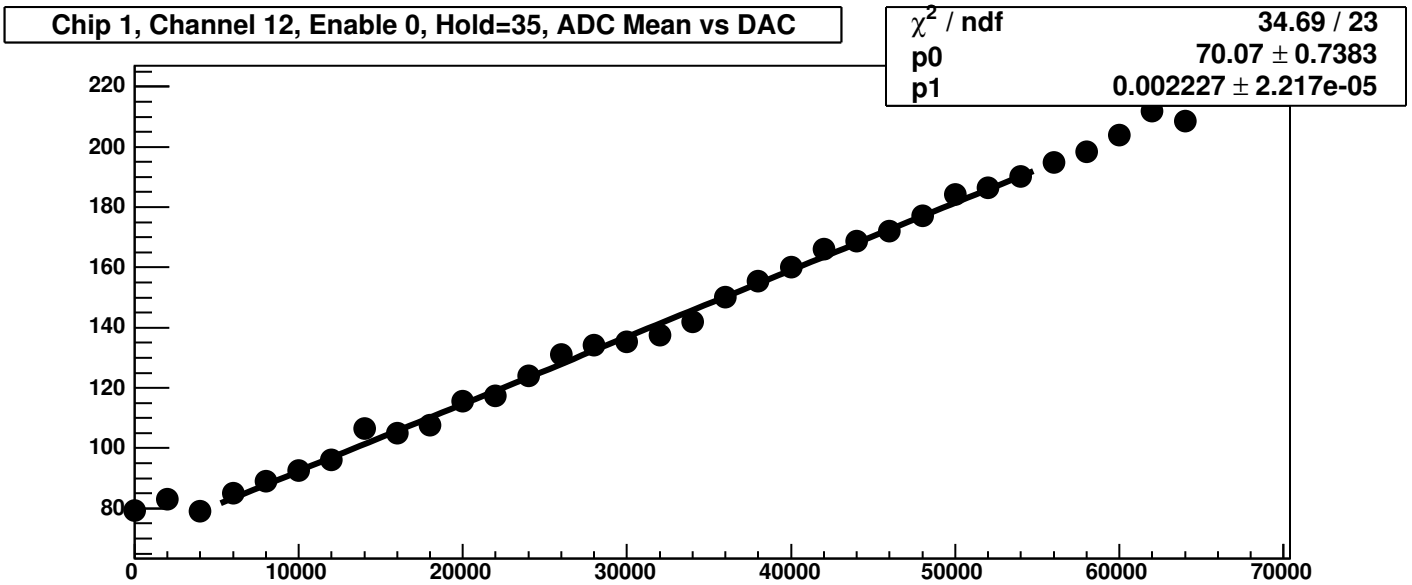
Chip 1, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

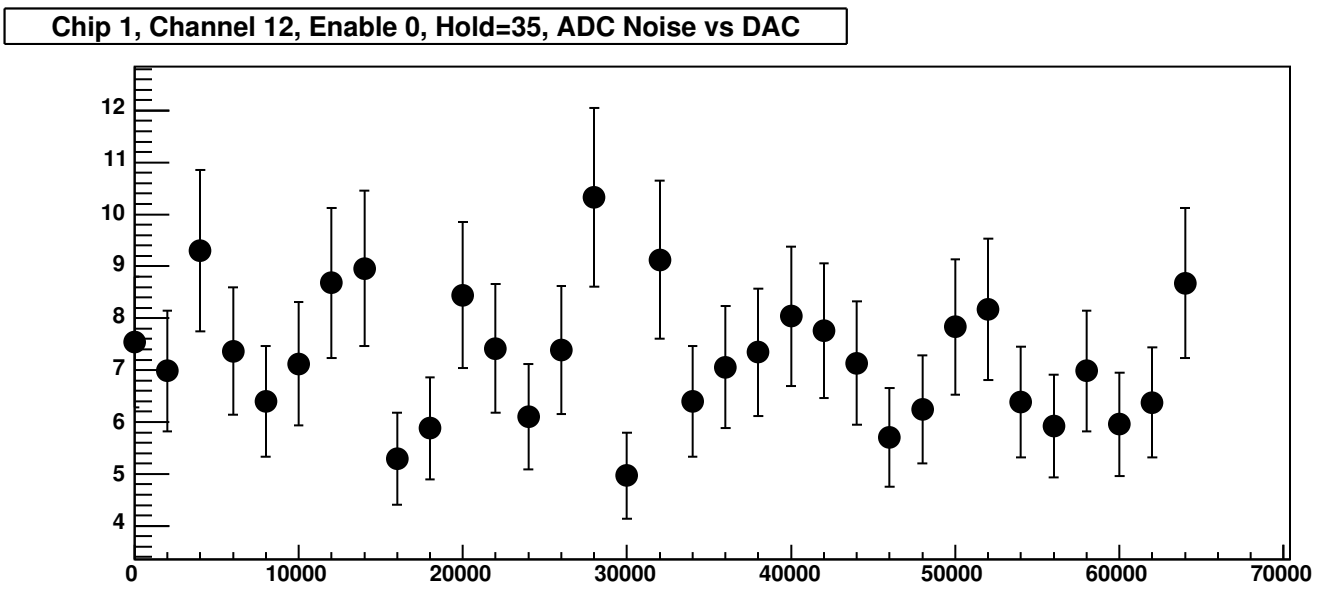


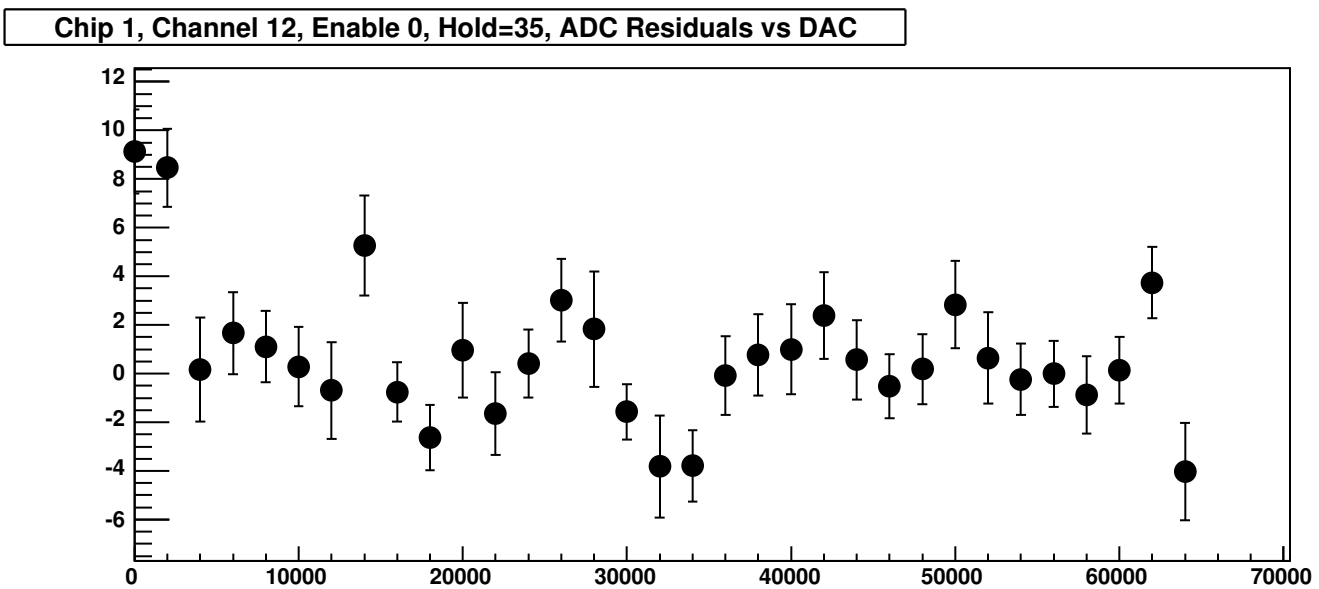

Chip 1, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 52.15 / 23
p0 -19.29 ± 0.7172
p1 -0.001964 ± 2.271e-05
-20
-40
-60
-80
-100
-120
-140
-160
0
10000
20000
30000
40000
50000
60000

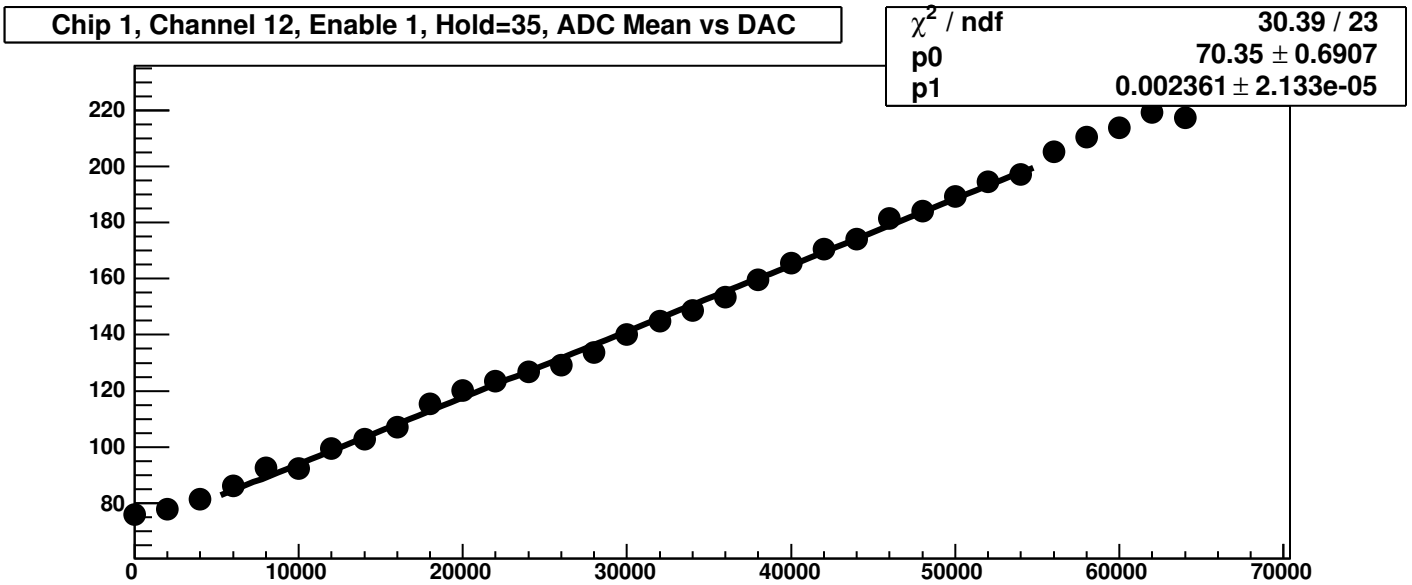

Chip 1, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000

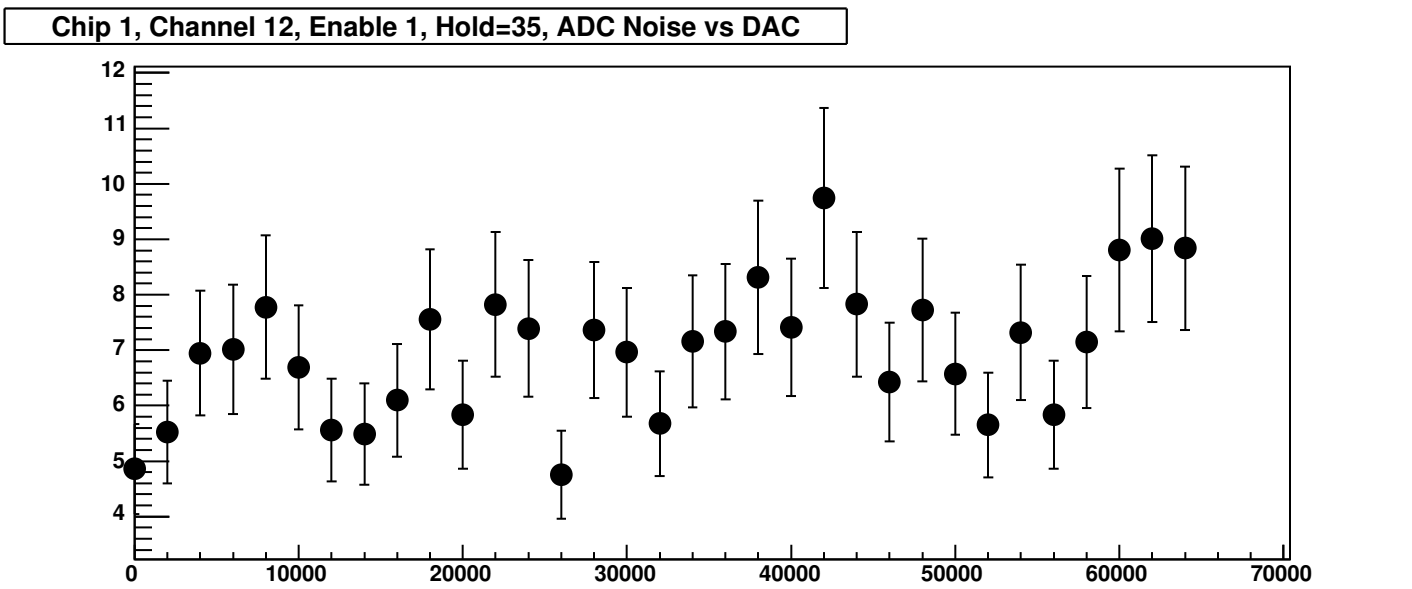

Chip 1, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
0
10000
20000
30000
40000
50000
60000

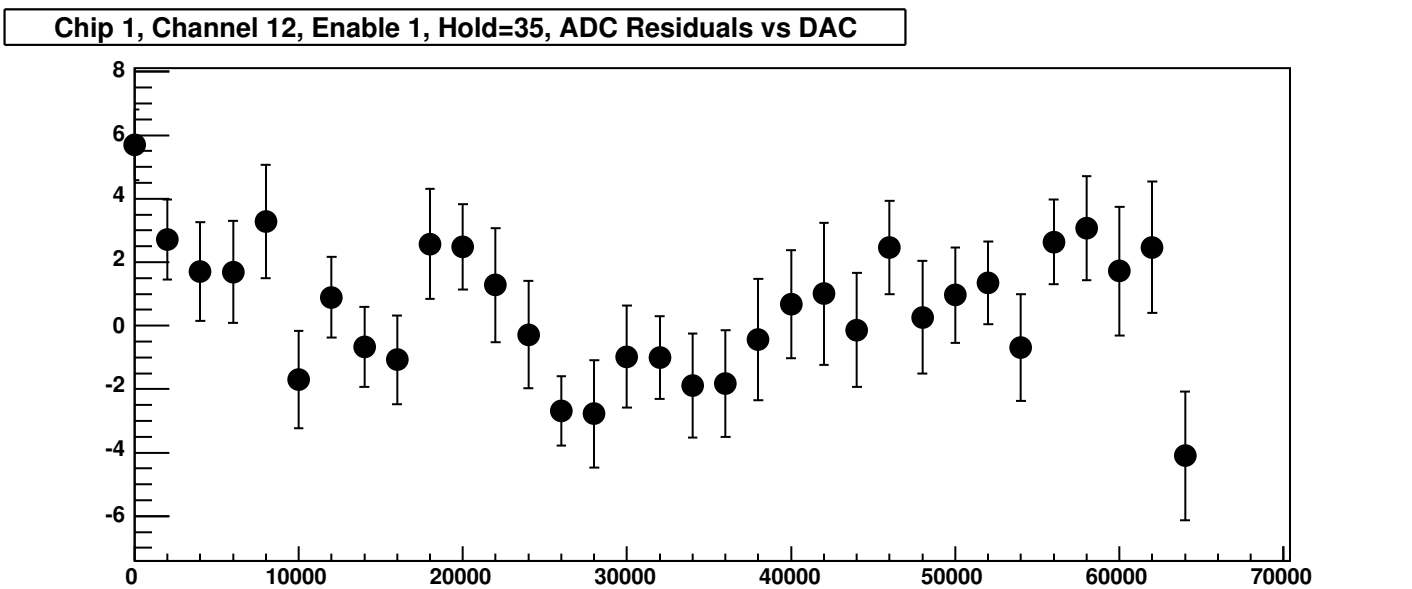

Chip 1, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 34.69 / 23
p0 70.07 ± 0.7383
p1 0.002227 ± 2.217e-05
220
200
180
160
140
120
100
80
0
10000
20000
30000
40000
50000
60000
70000

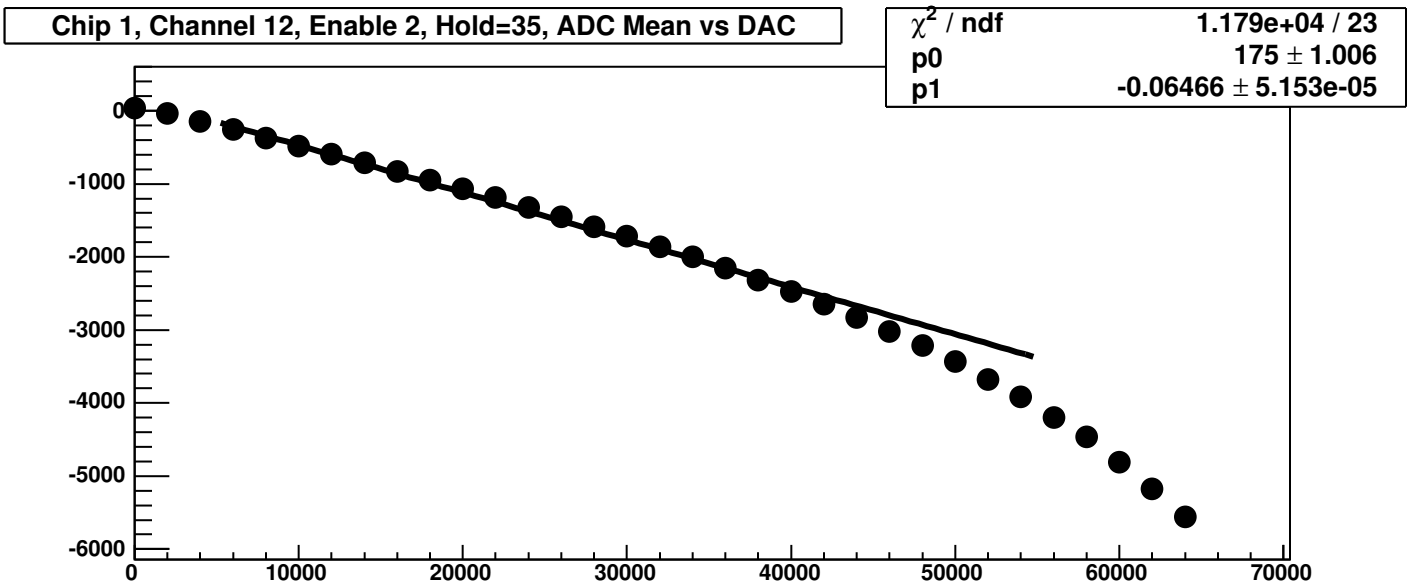

Chip 1, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

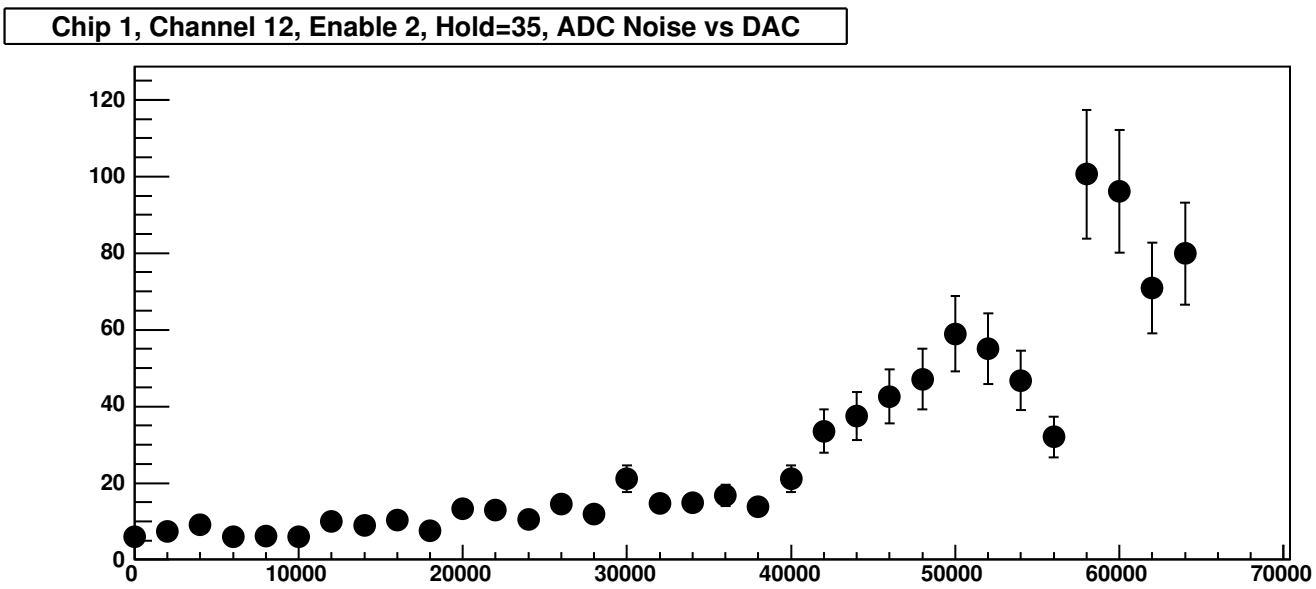

Chip 1, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

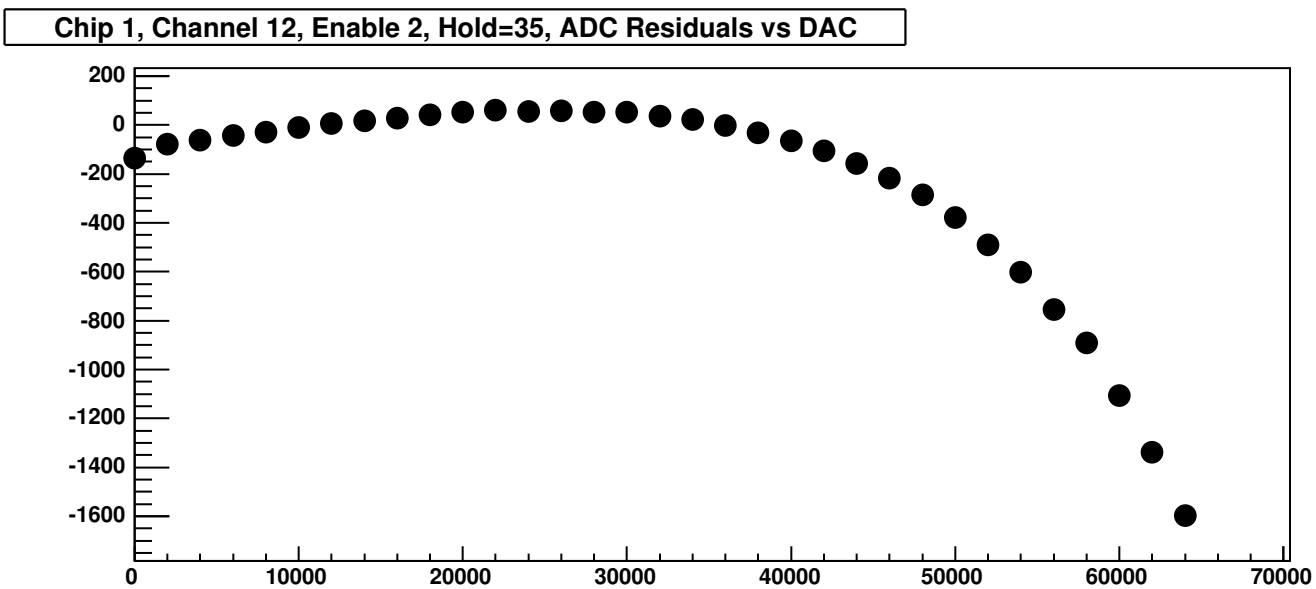

Chip 1, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 30.39 / 23
p0 70.35 ± 0.6907
p1 0.002361 ± 2.133e-05
220
200
180
160
140
120
100
80
0
10000
20000
30000
40000
50000
60000
70000

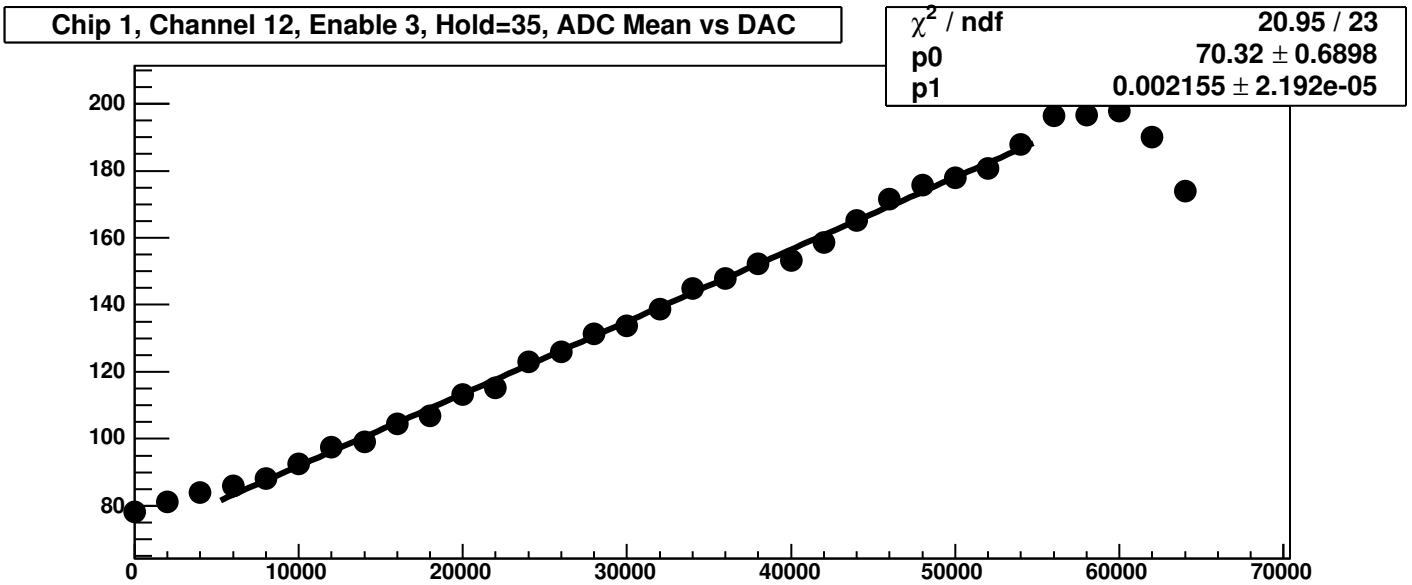

Chip 1, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

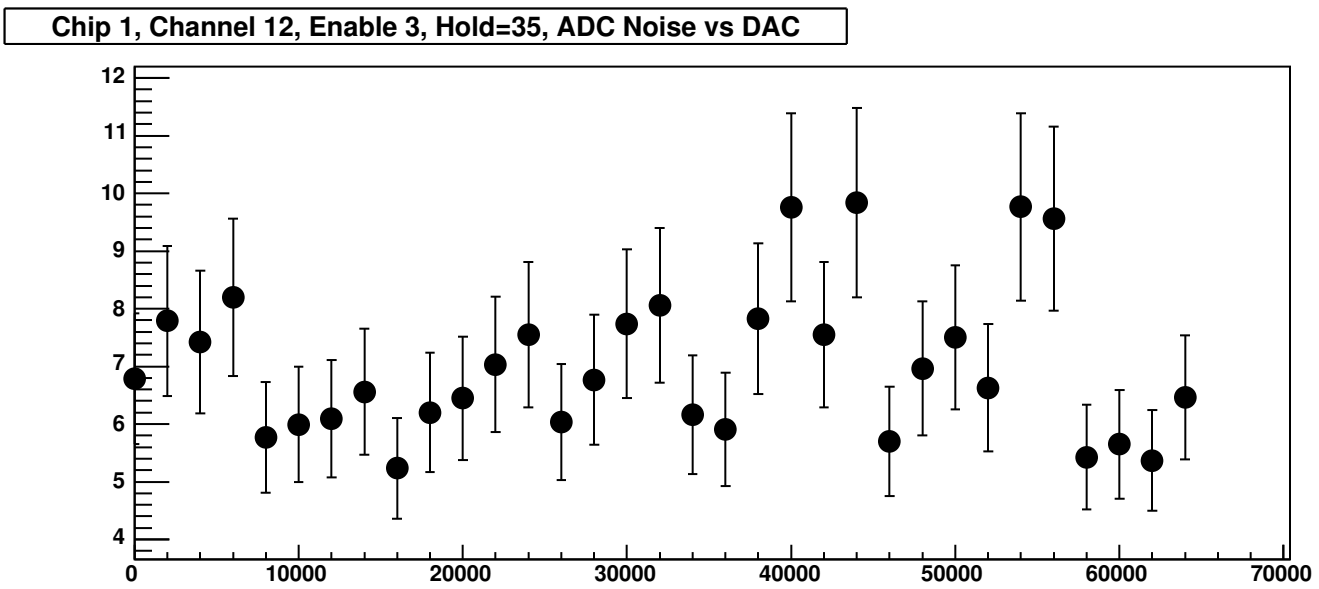

Chip 1, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

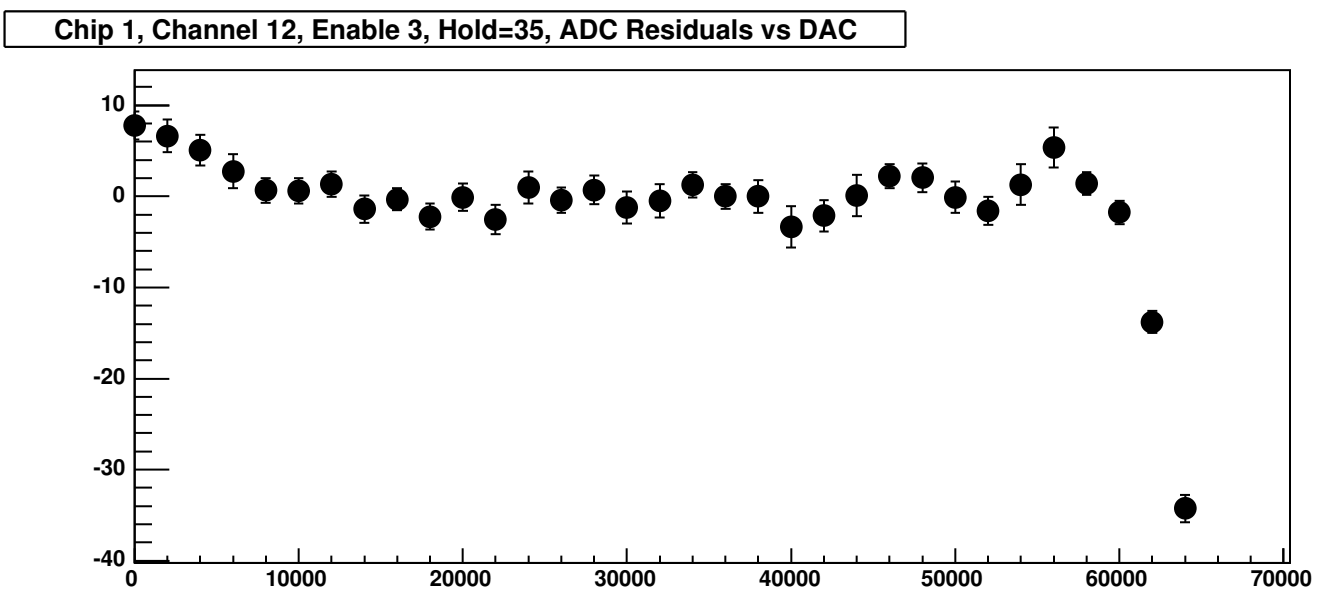

Chip 1, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1.179e+04 / 23
p0 175 ± 1.006
p1 -0.06466 ± 5.153e-05
0
-1000
-2000
-3000
-4000
-5000
-6000
0
10000
20000
30000
40000
50000
60000
70000

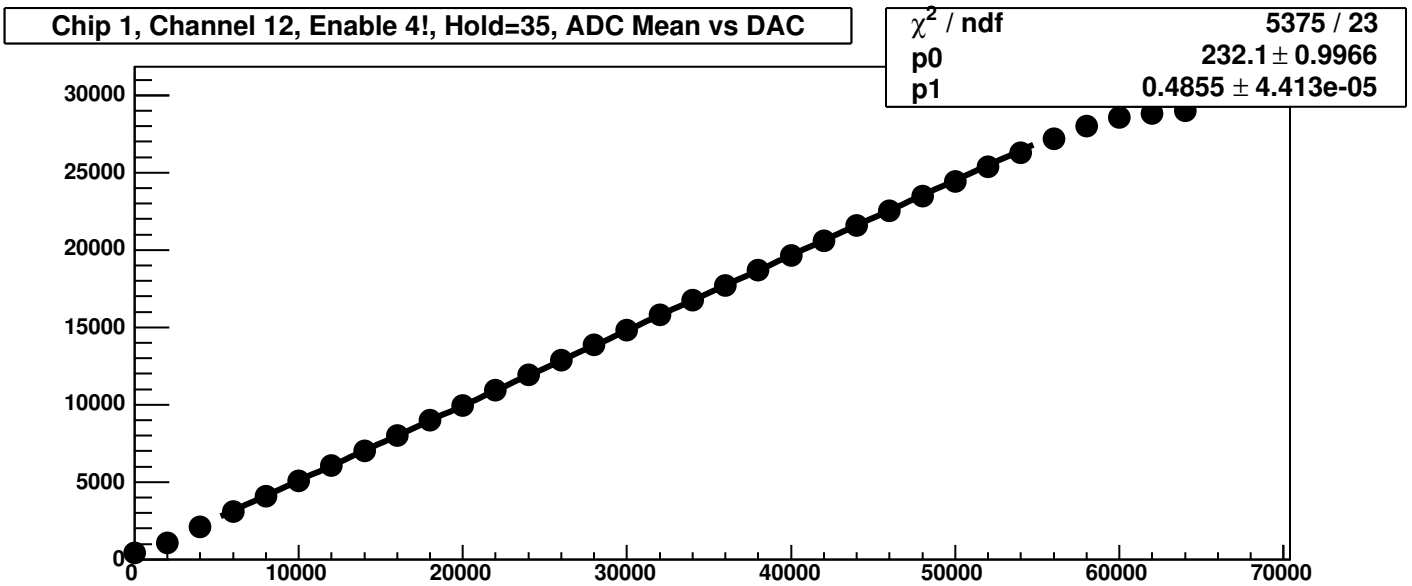

Chip 1, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000
70000

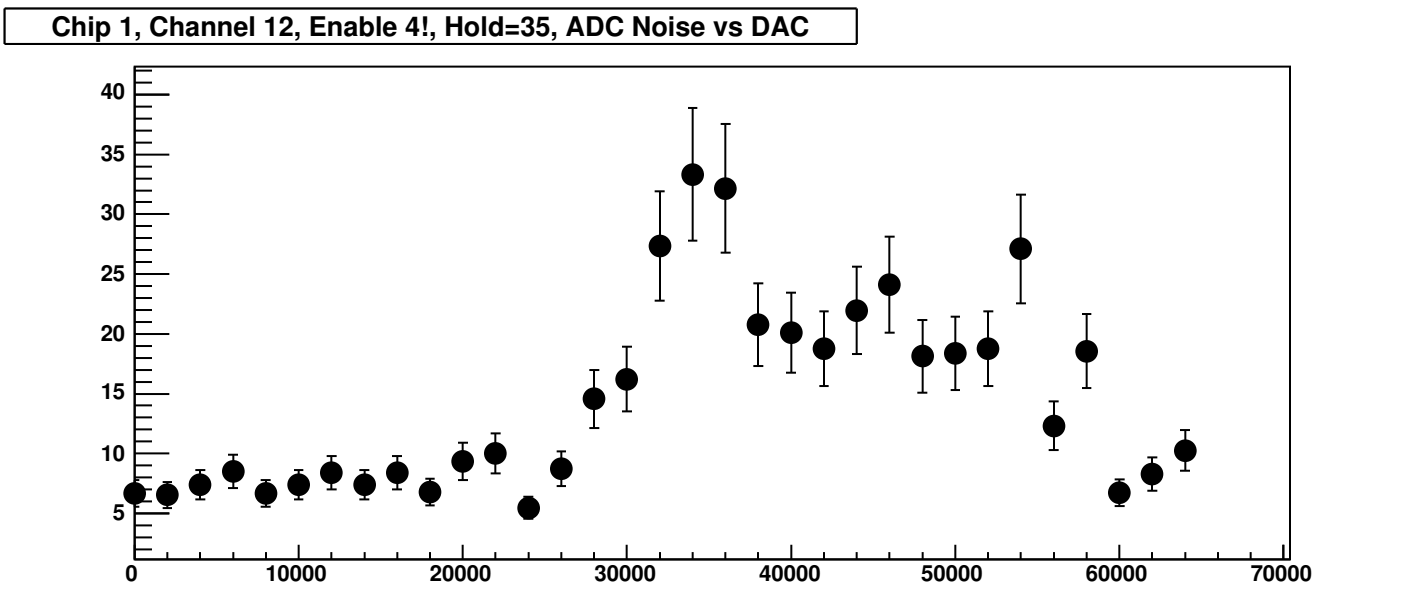

Chip 1, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
0
10000
20000
30000
40000
50000
60000
70000

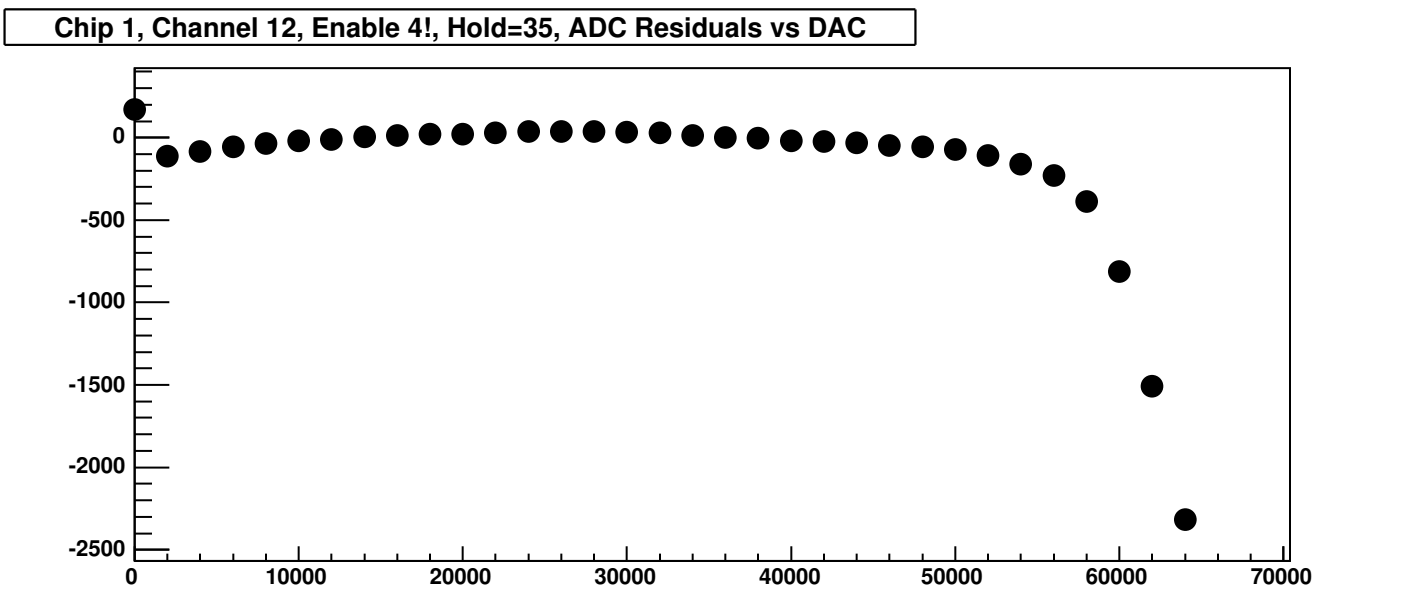

Chip 1, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 20.95 / 23
p0 70.32 ± 0.6898
p1 0.002155 ± 2.192e-05
200
180
160
140
120
100
80
0
10000
20000
30000
40000
50000
60000
70000

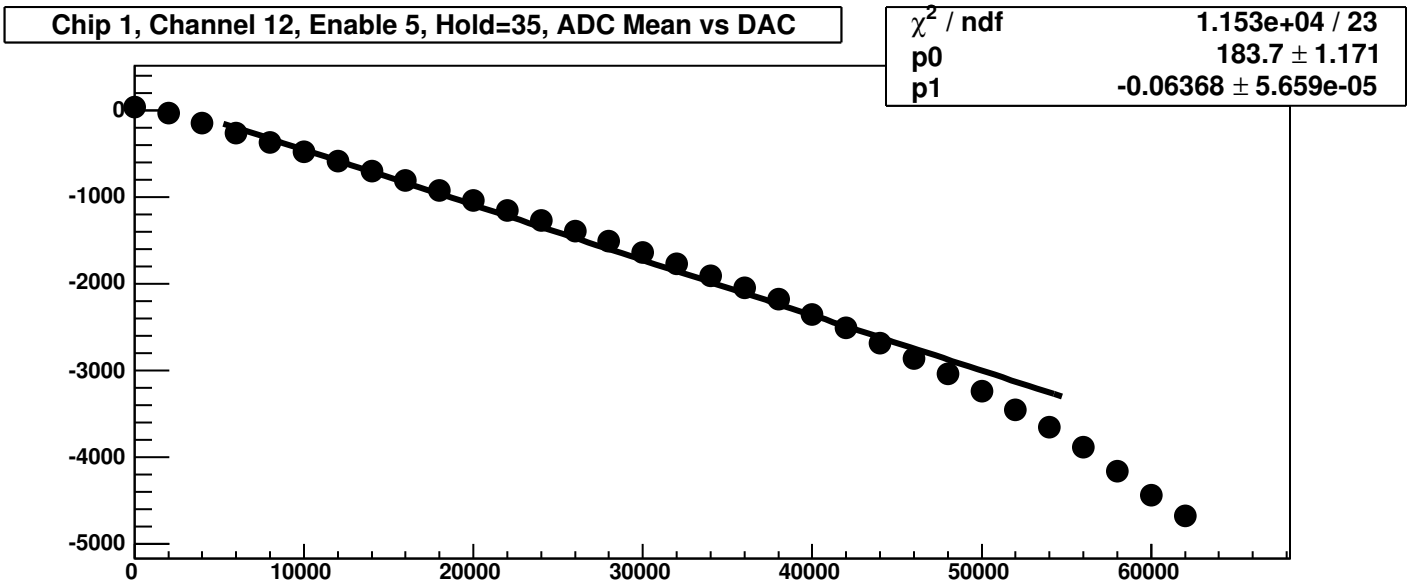

Chip 1, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

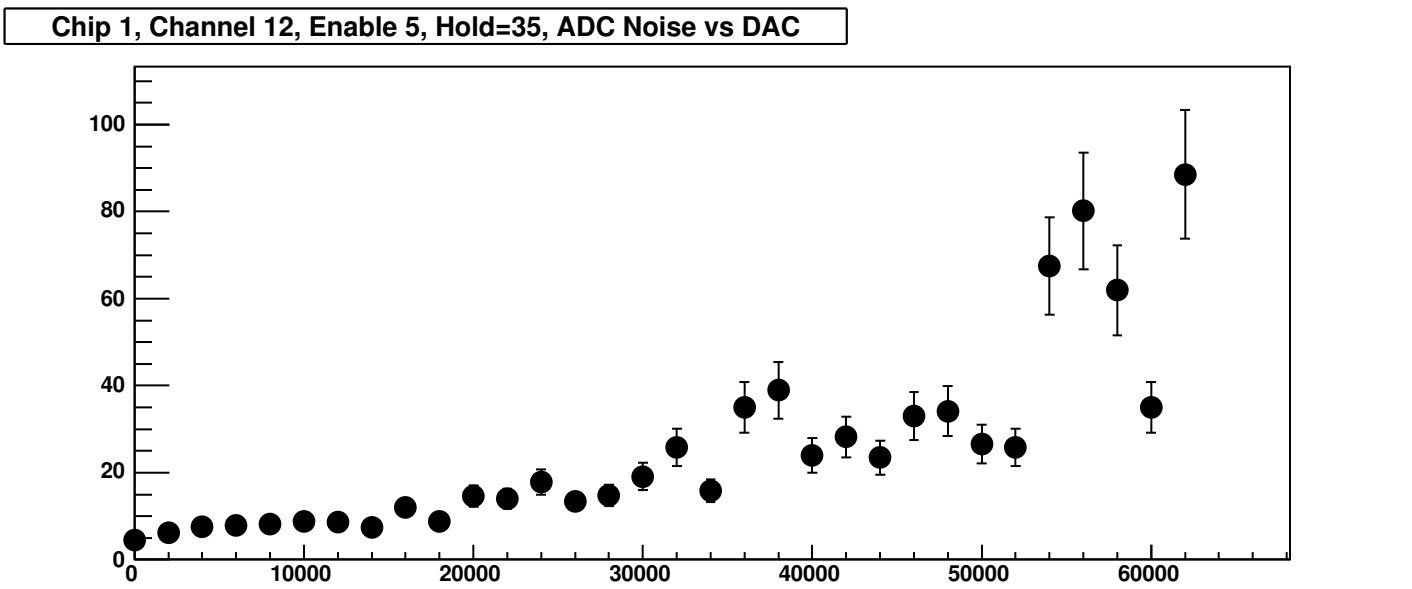

Chip 1, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
10000
20000
30000
40000
50000
60000
70000

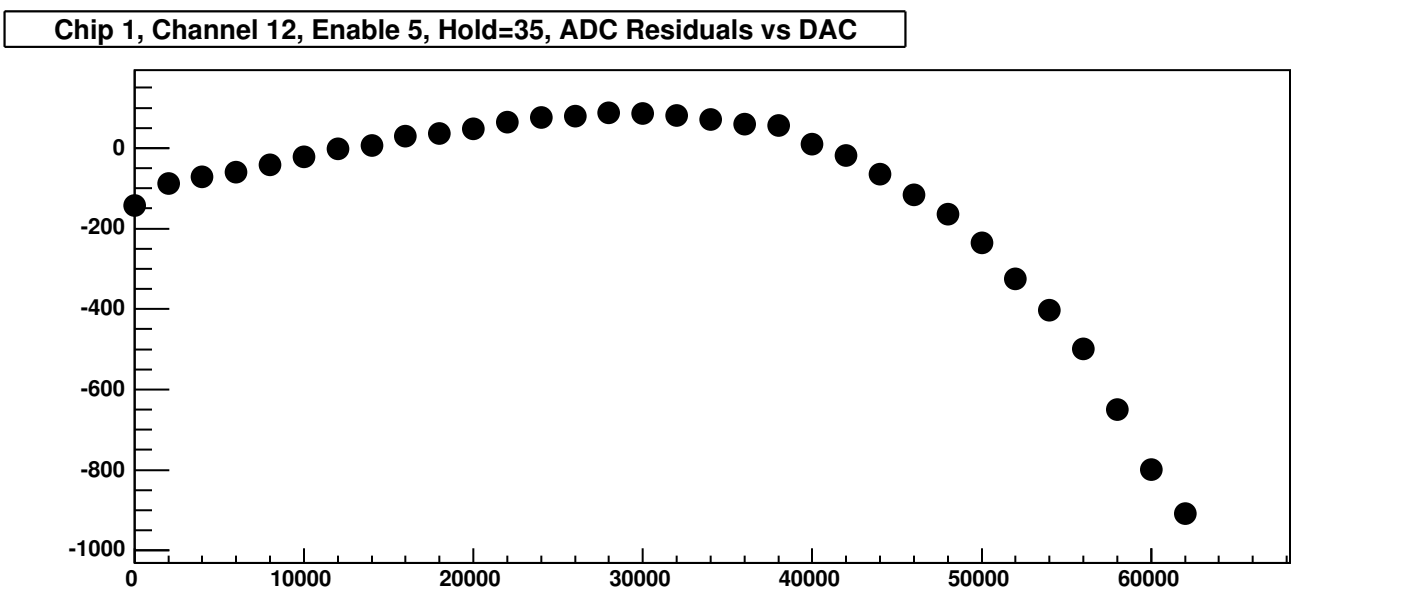

Chip 1, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 5375 / 23
p0 232.1 ± 0.9966
p1 0.4855 ± 4.413e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

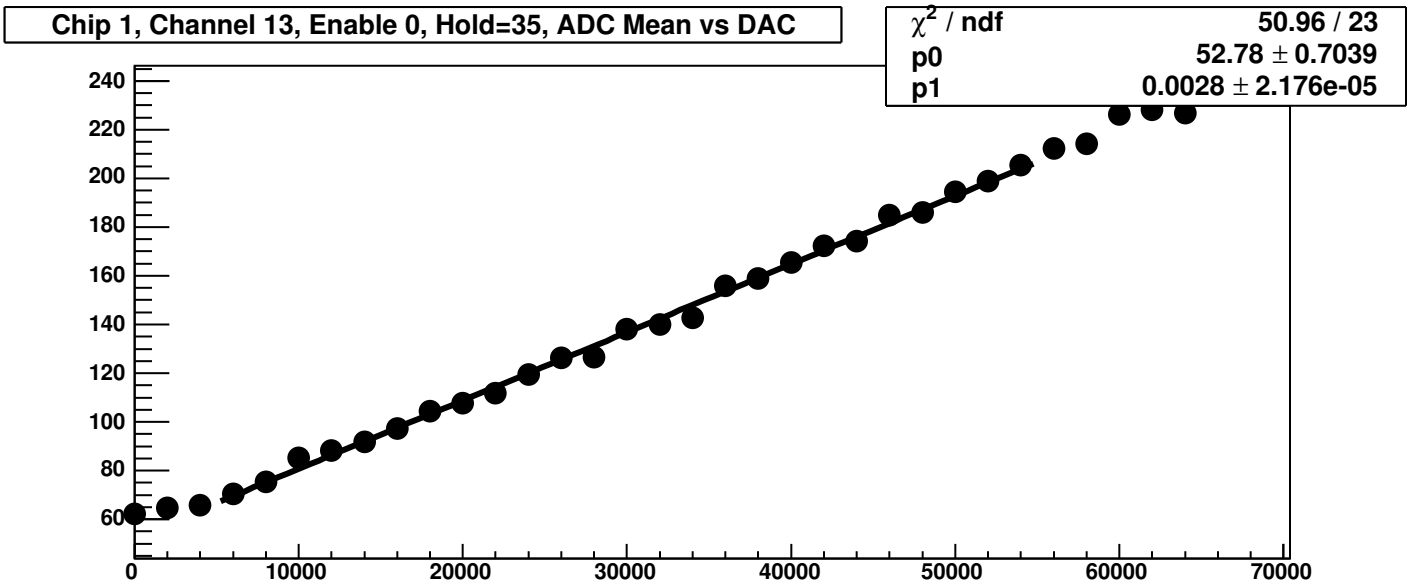

Chip 1, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000

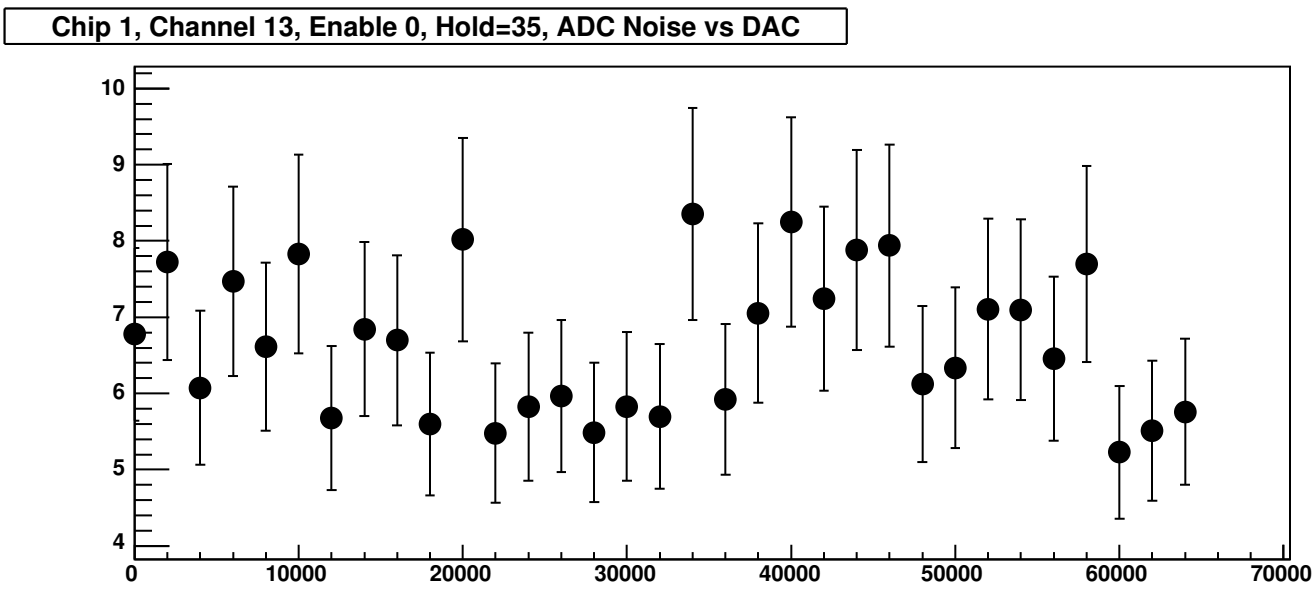

Chip 1, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
0
10000
20000
30000
40000
50000
60000
70000

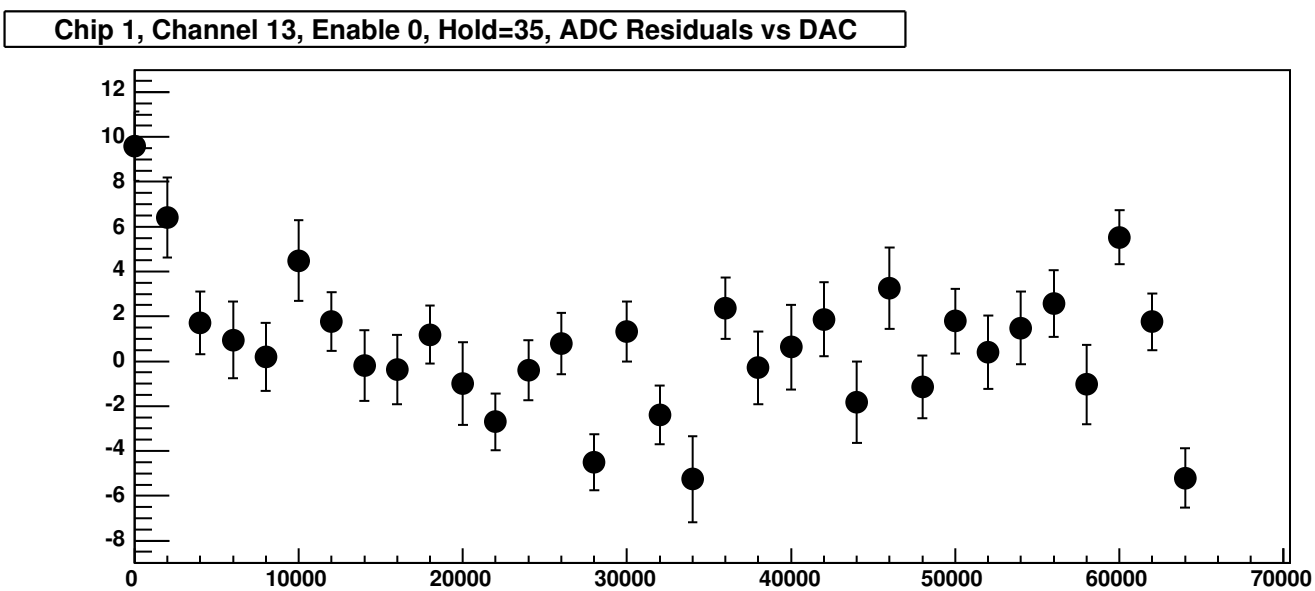

Chip 1, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.153e+04 / 23
p0 183.7 ± 1.171
p1 -0.06368 ± 5.659e-05
0
-1000
-2000
-3000
-4000
-5000
0
10000
20000
30000
40000
50000
60000

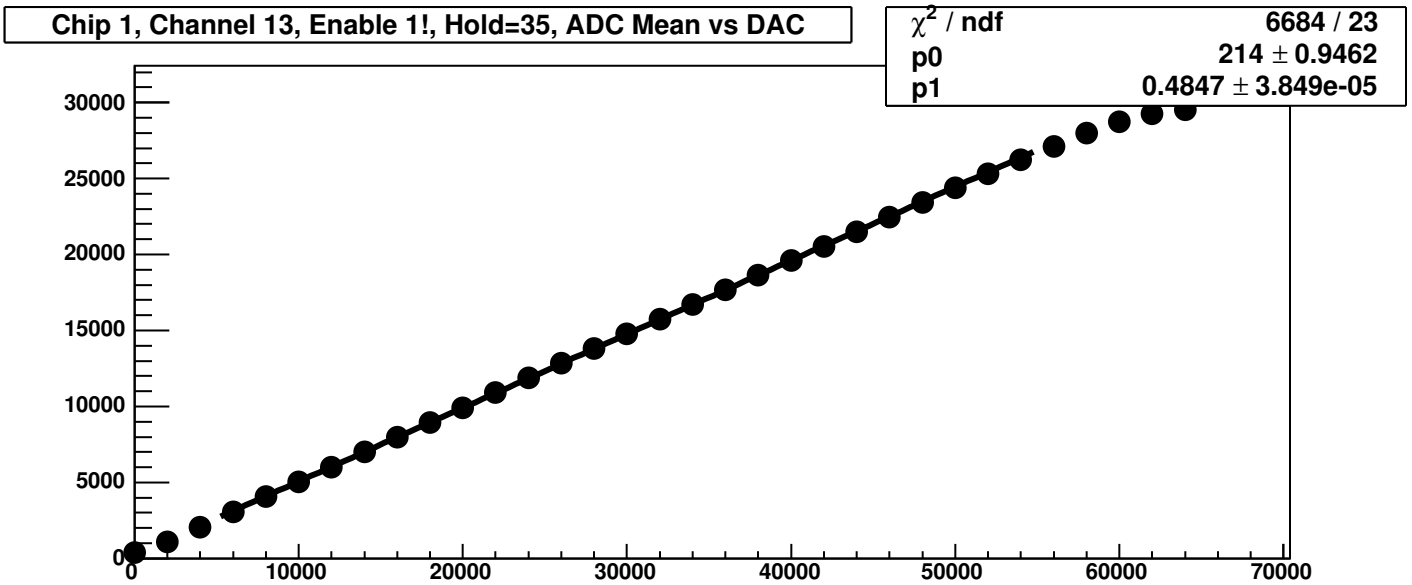

Chip 1, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

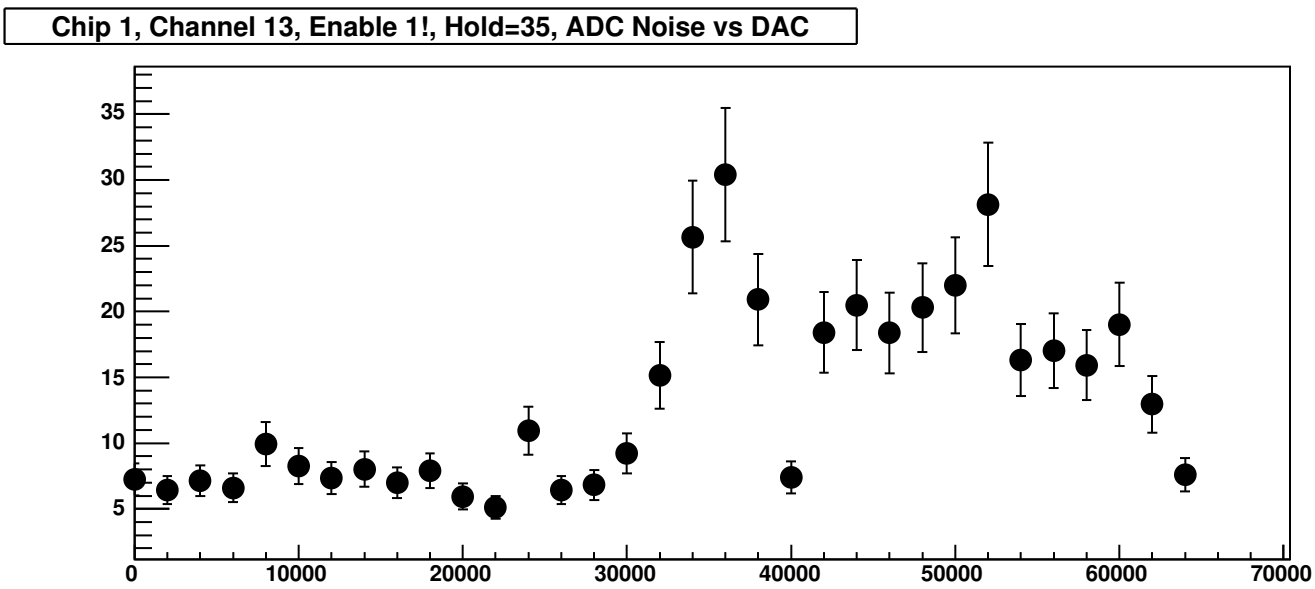

Chip 1, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
0
-200
-400
-600
-800
-1000
0
10000
20000
30000
40000
50000
60000

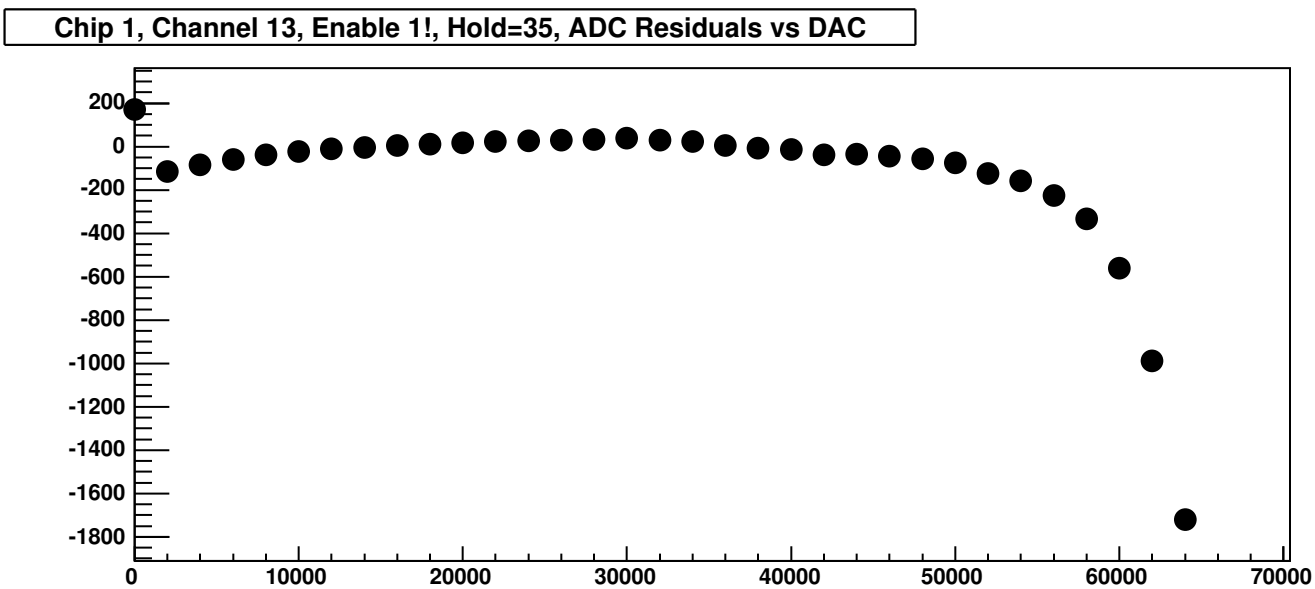

Chip 1, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 50.96 / 23
p0 52.78 ± 0.7039
p1 0.0028 ± 2.176e-05
240
220
200
180
160
140
120
100
80
60
0
10000
20000
30000
40000
50000
60000
70000

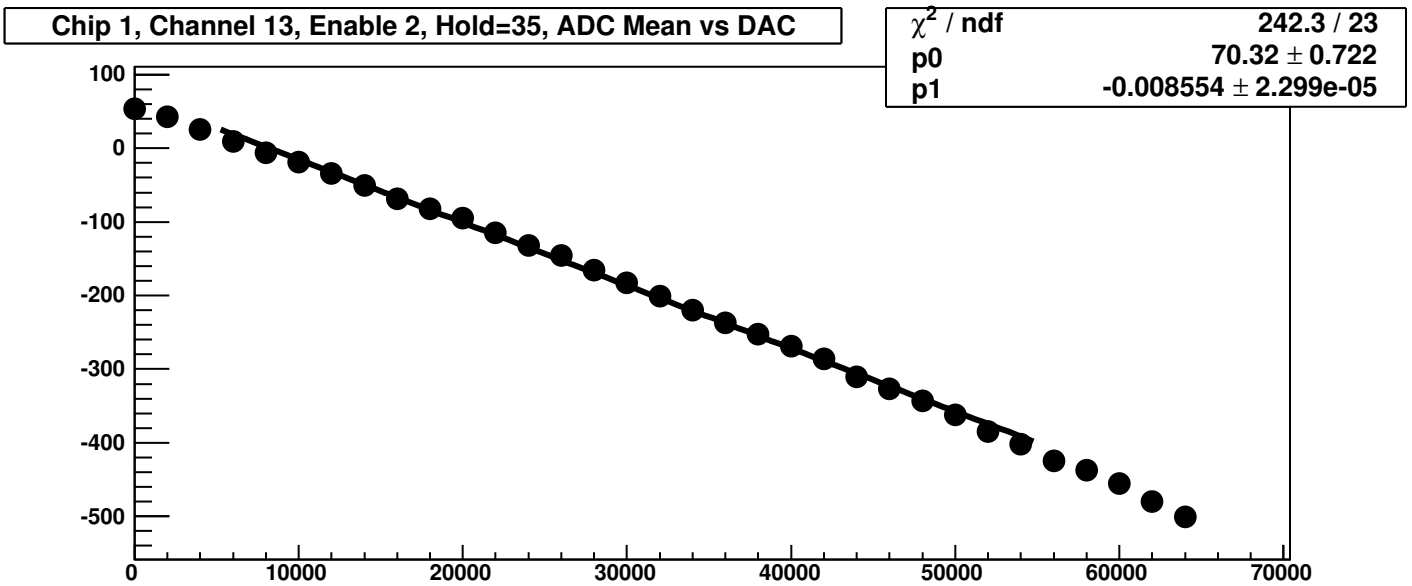

Chip 1, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

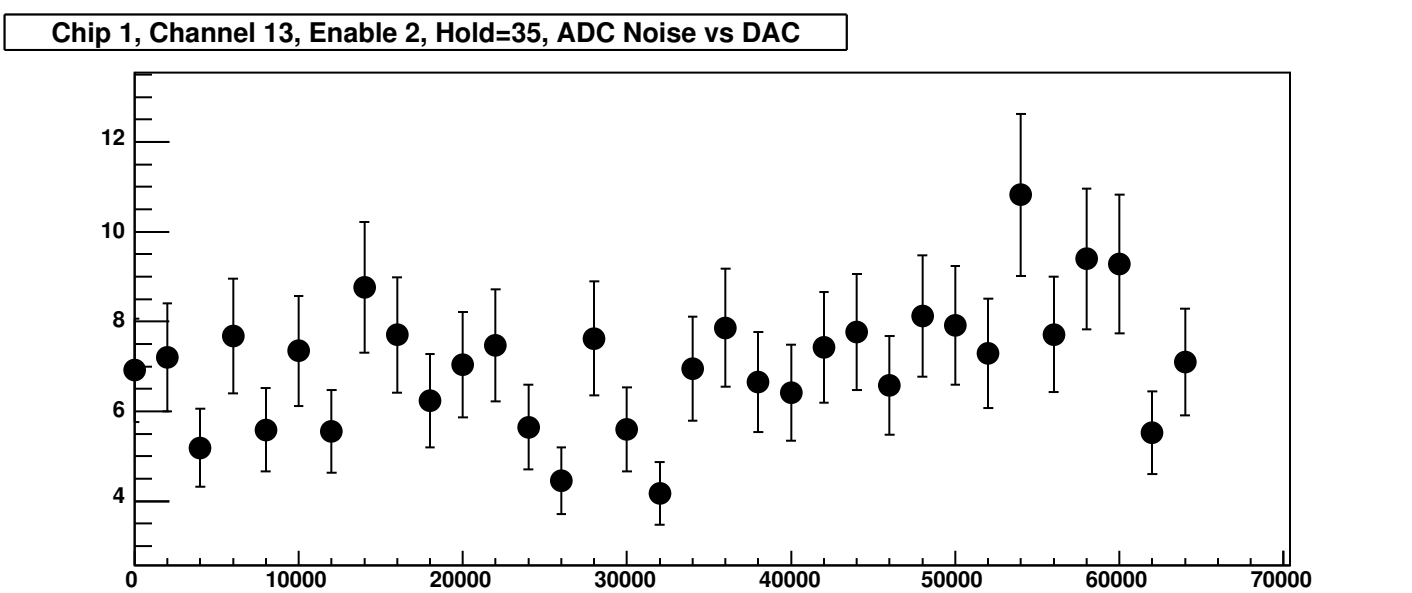

Chip 1, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

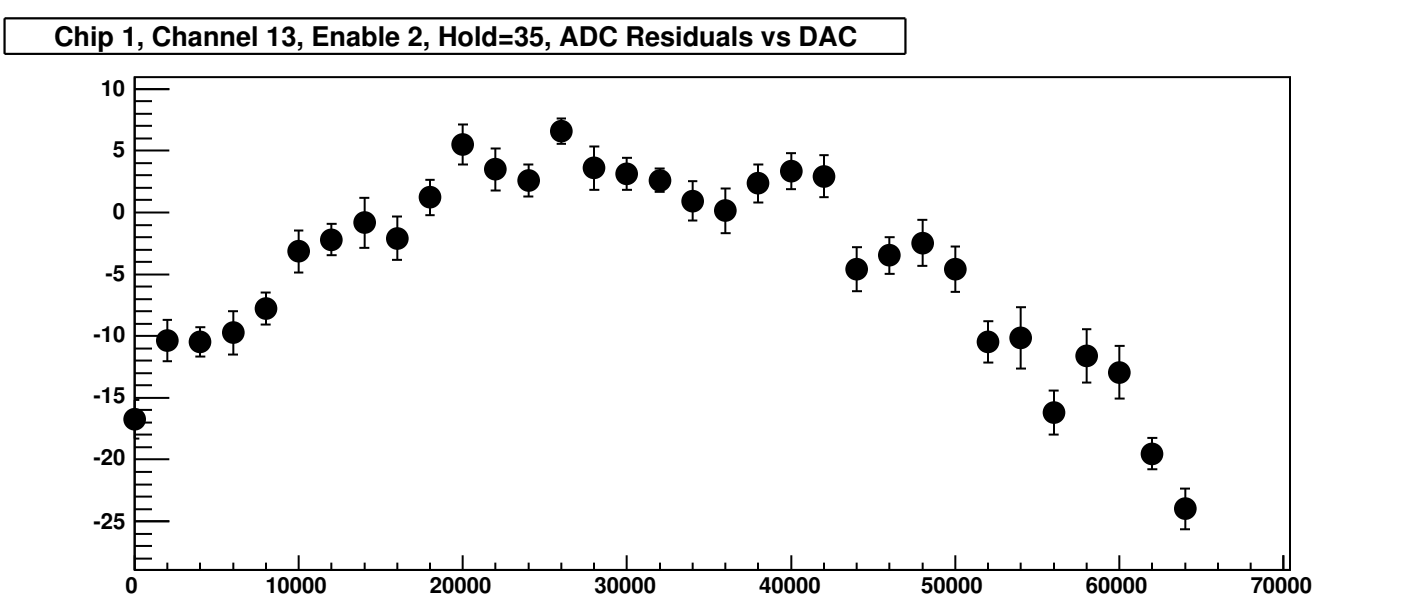

Chip 1, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 6684 / 23
p0 214 ± 0.9462
p1 0.4847 ± 3.849e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

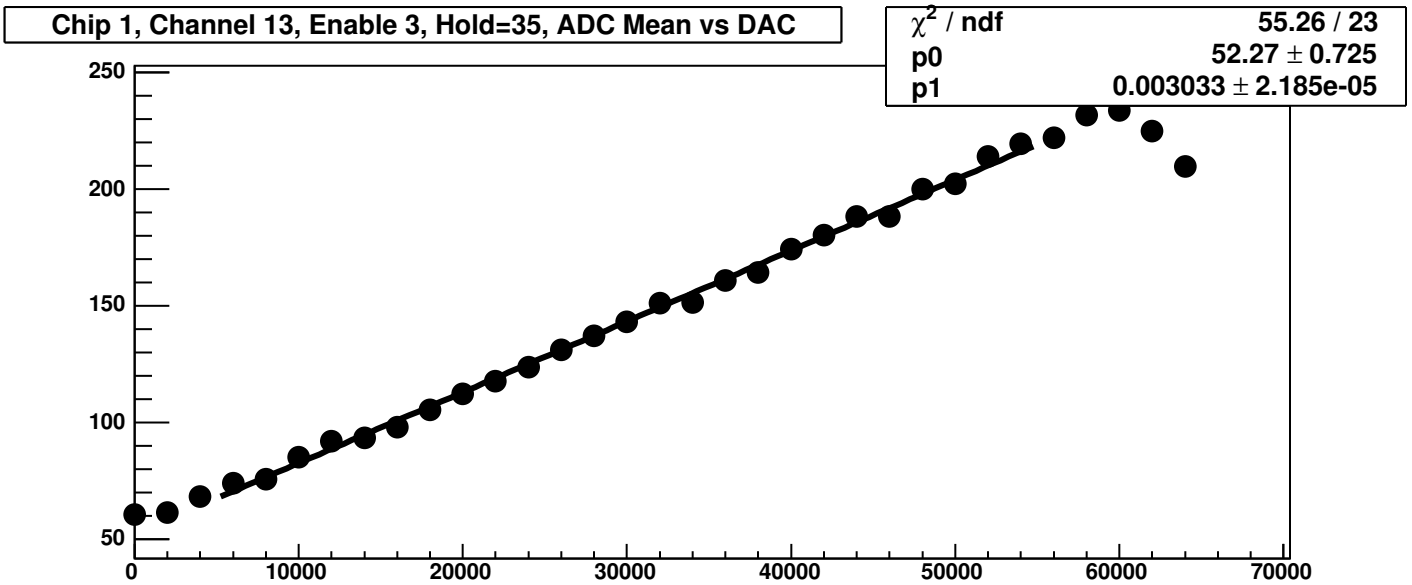

Chip 1, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000

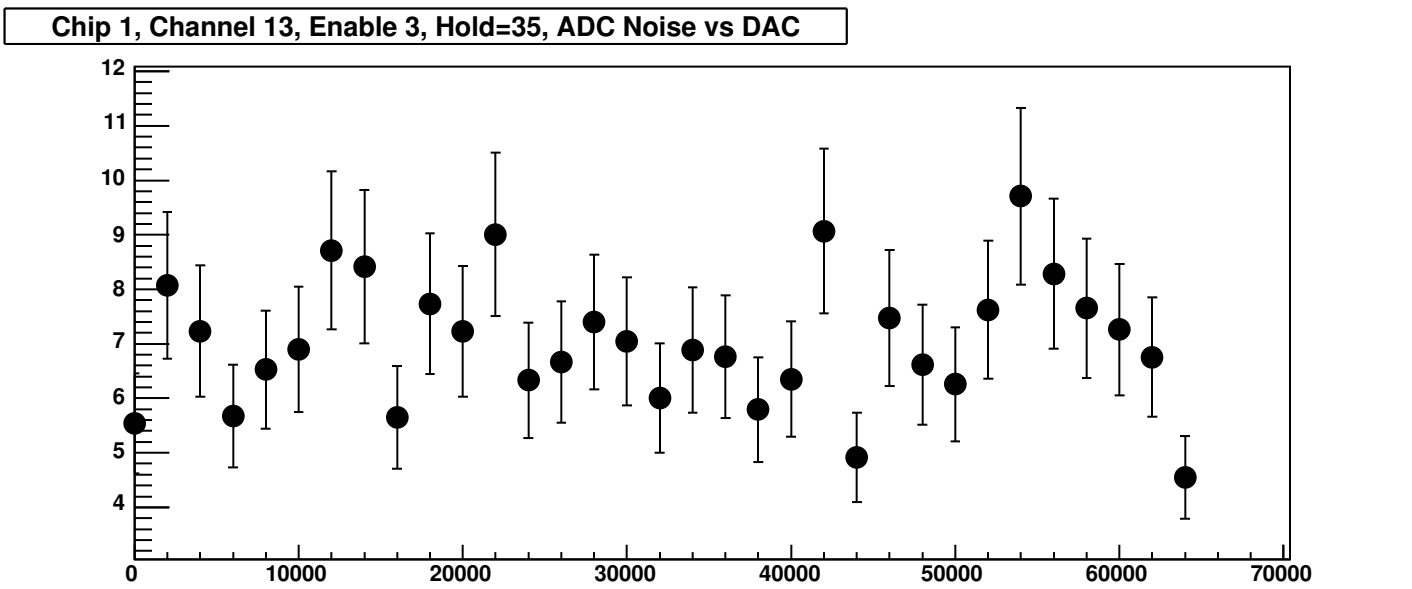

Chip 1, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
10000
20000
30000
40000
50000
60000
70000

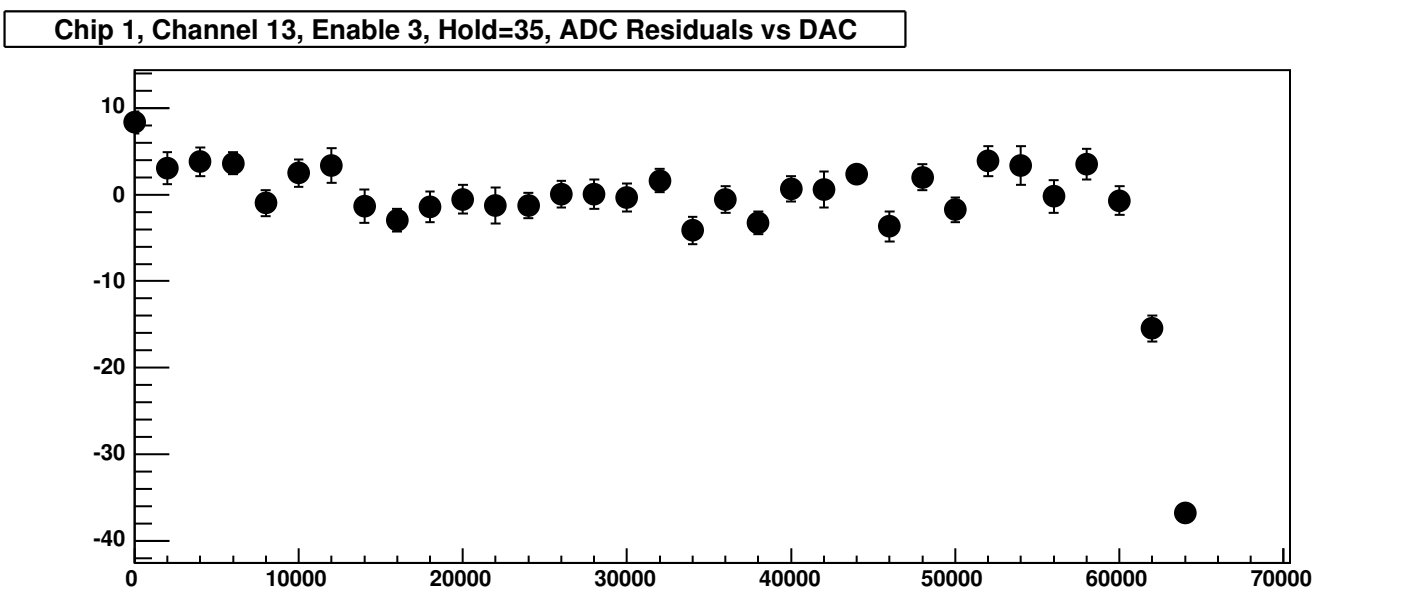

Chip 1, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 242.3 / 23
p0 70.32 ± 0.722
p1 -0.008554 ± 2.299e-05
100
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000
70000

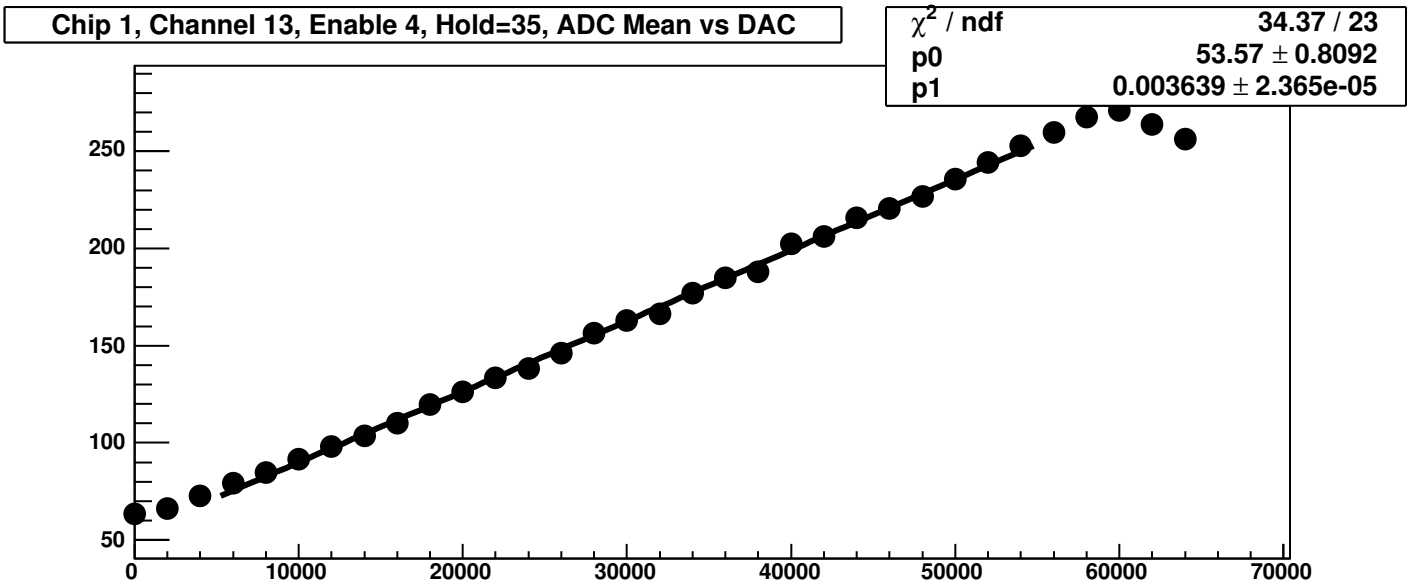

Chip 1, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

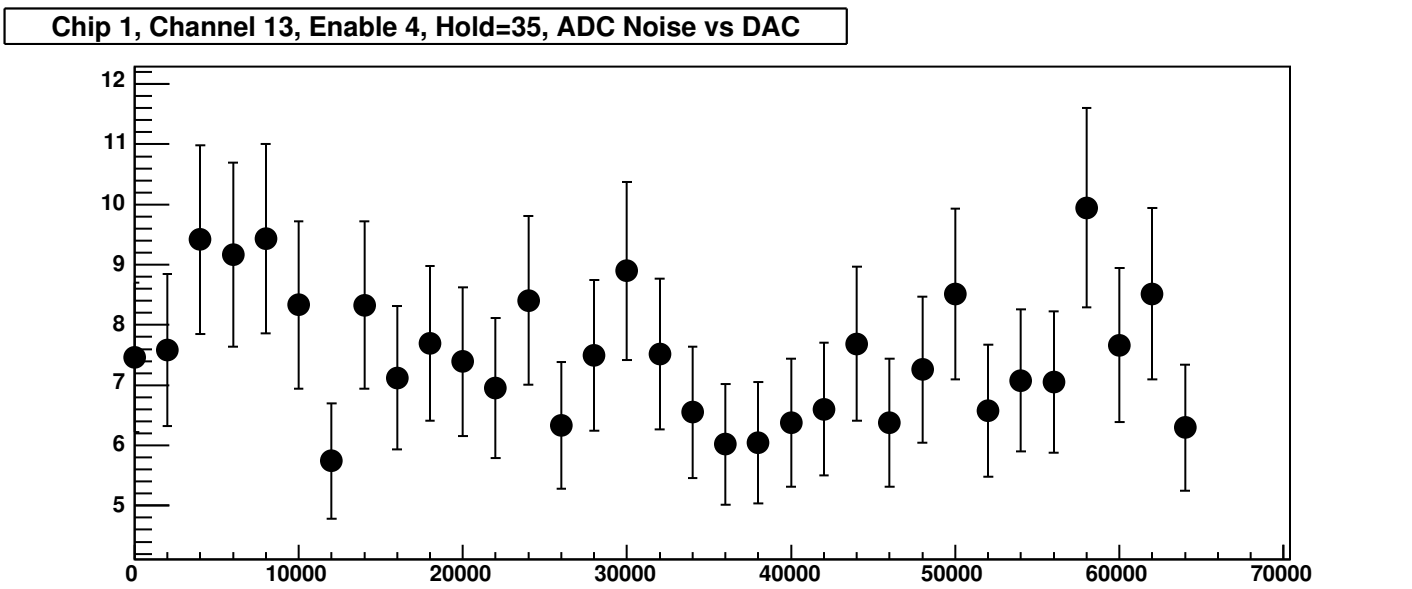

Chip 1, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
10000
20000
30000
40000
50000
60000
70000

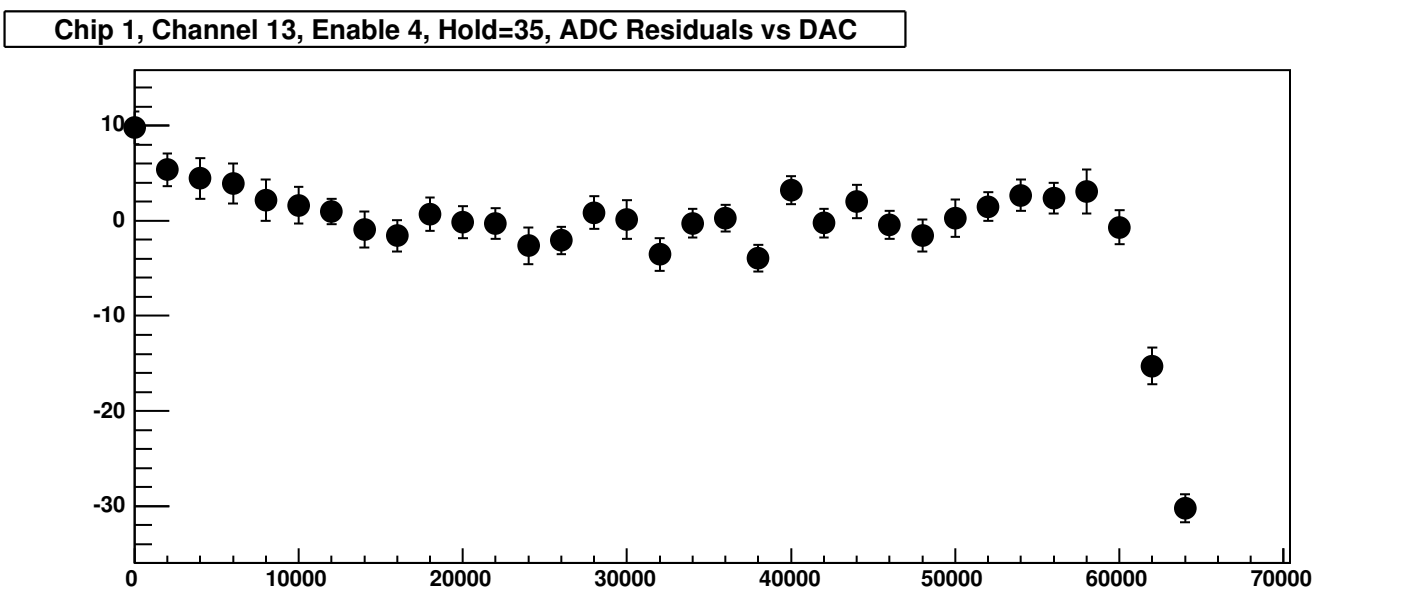

Chip 1, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 55.26 / 23
p0 52.27 ± 0.725
p1 0.003033 ± 2.185e-05
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

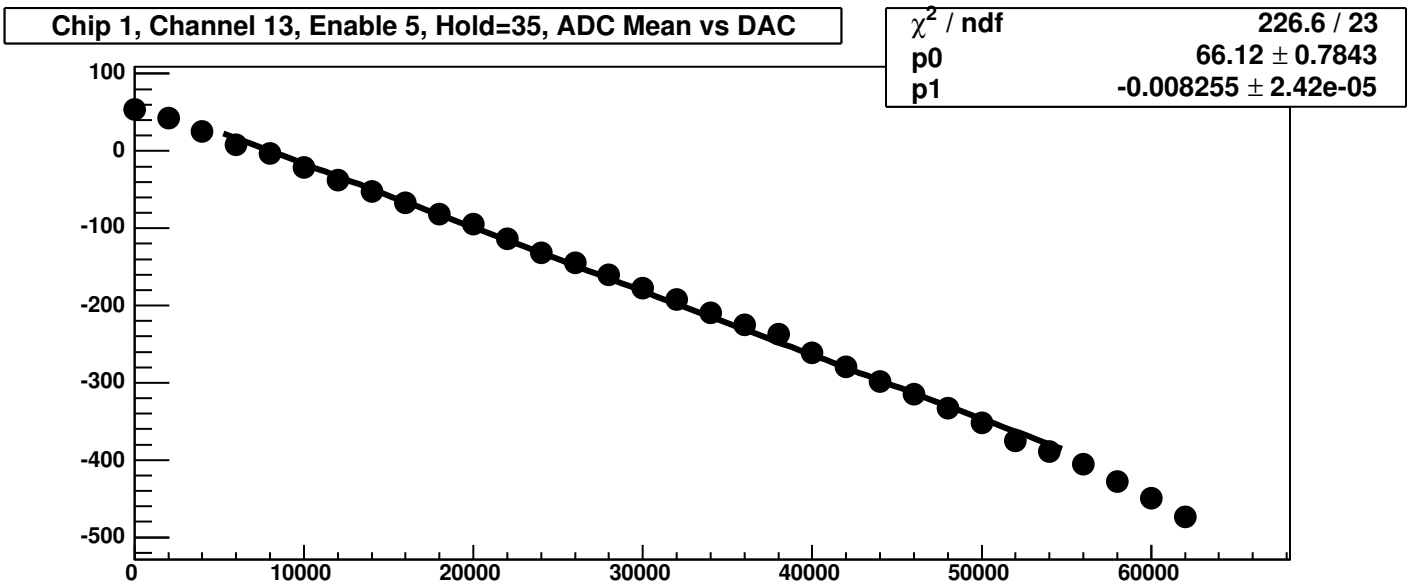

Chip 1, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

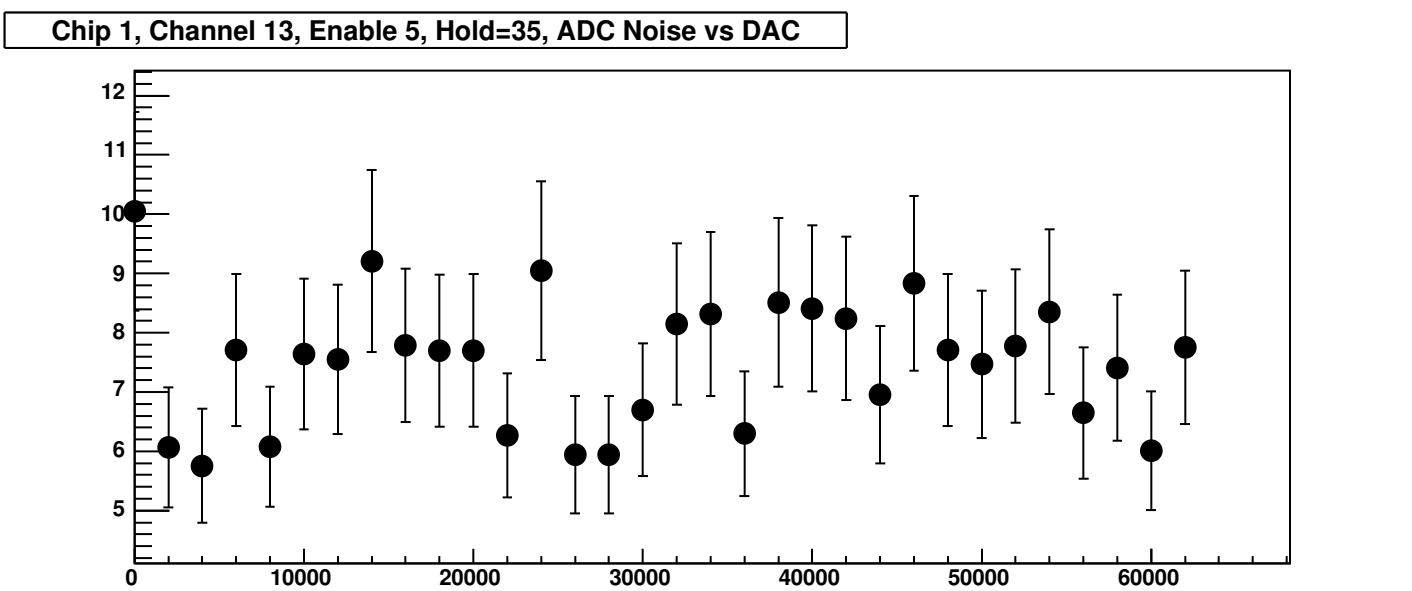

Chip 1, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
10000
20000
30000
40000
50000
60000
70000

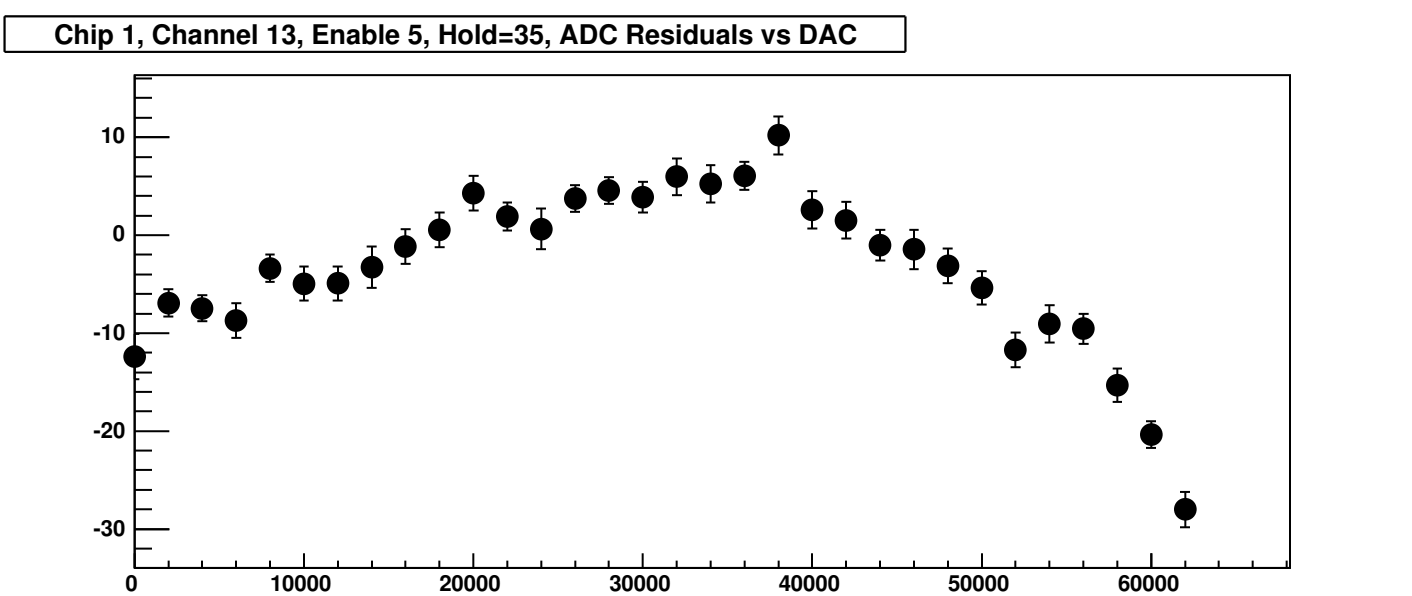

Chip 1, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 34.37 / 23
p0 53.57 ± 0.8092
p1 0.003639 ± 2.365e-05
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

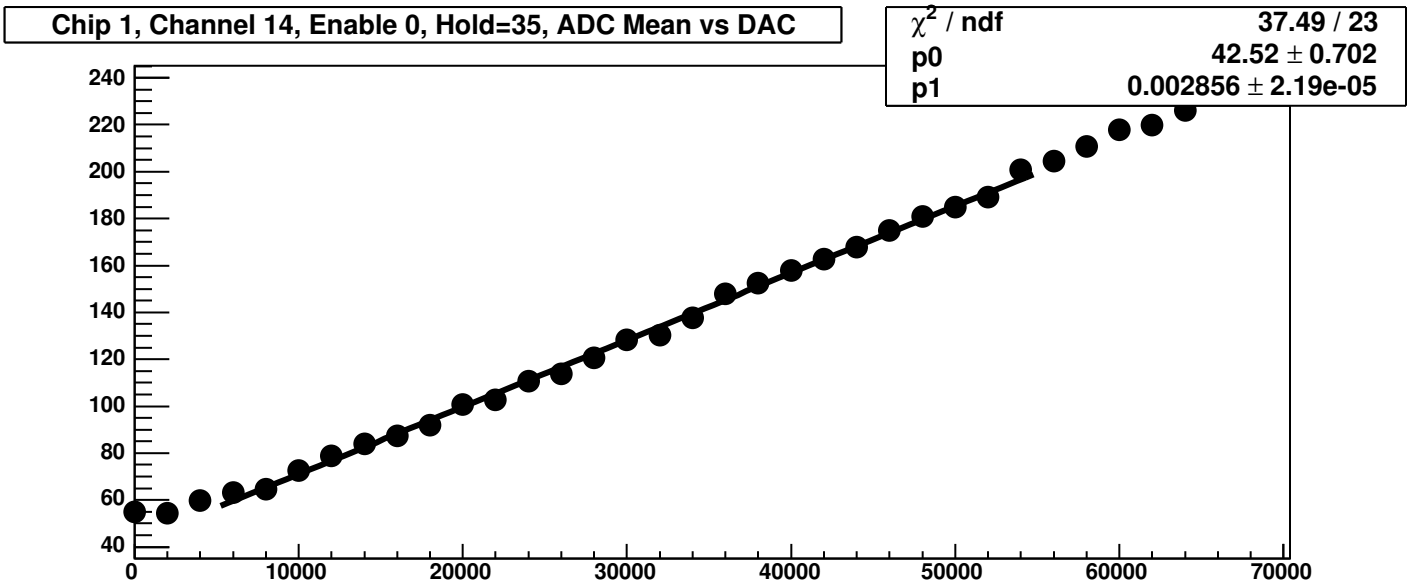

Chip 1, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000

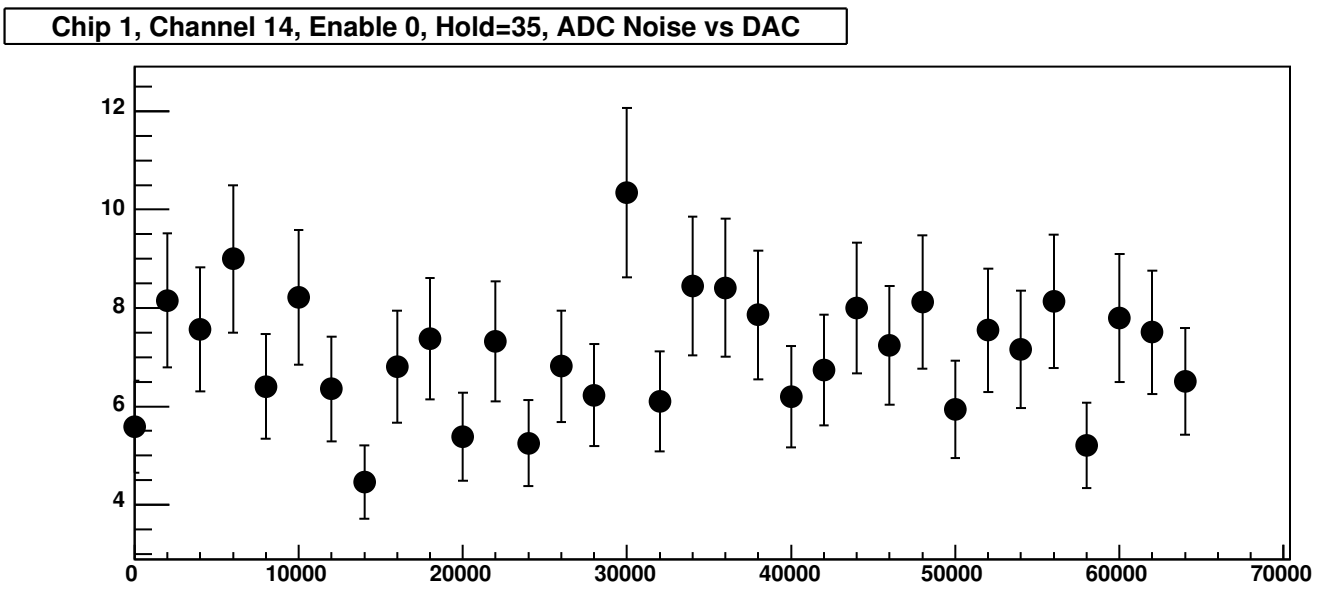

Chip 1, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
10000
20000
30000
40000
50000
60000
70000

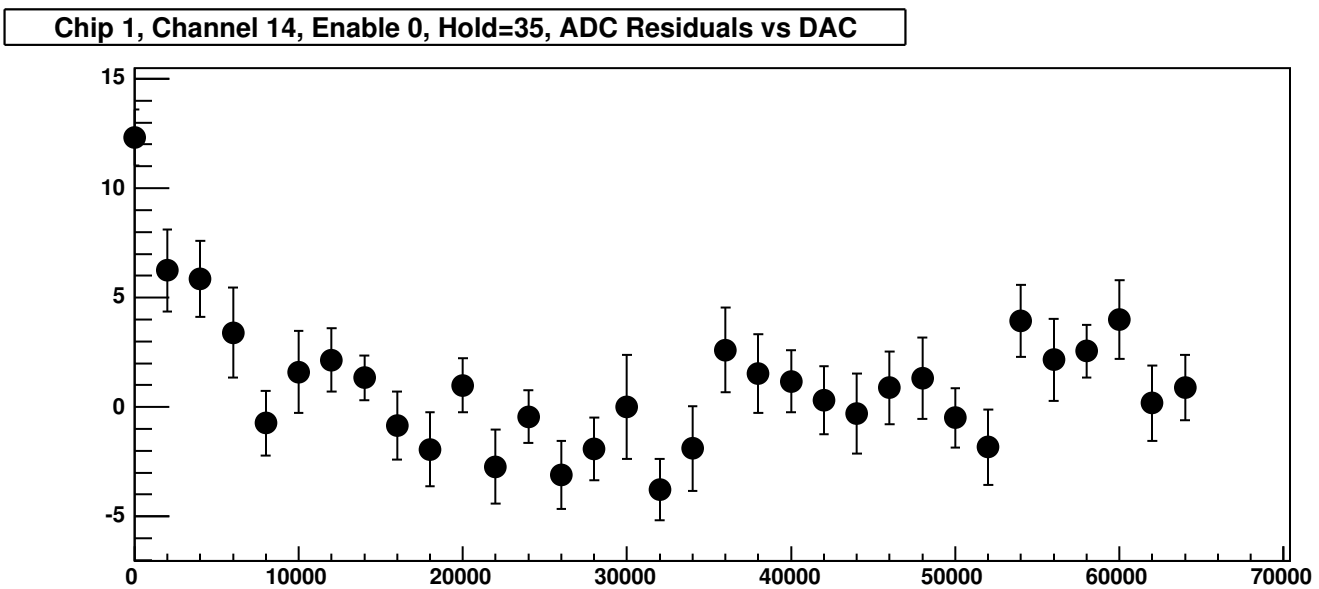

Chip 1, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 226.6 / 23
p0 66.12 ± 0.7843
p1 -0.008255 ± 2.42e-05
100
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000

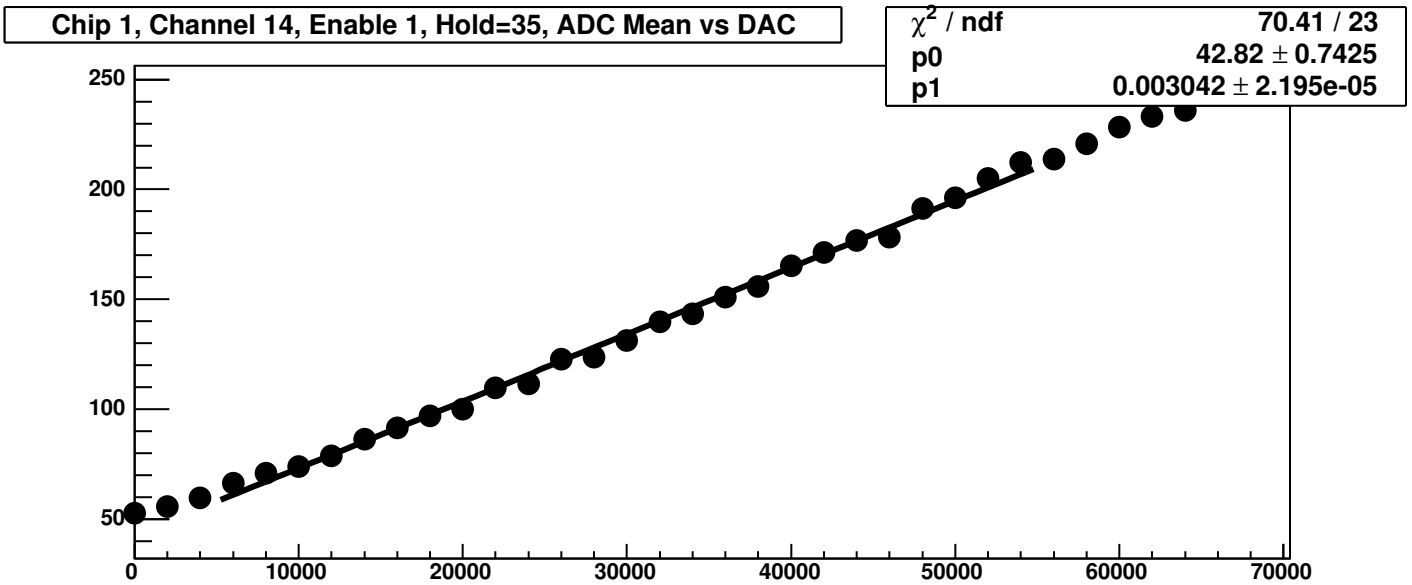

Chip 1, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000

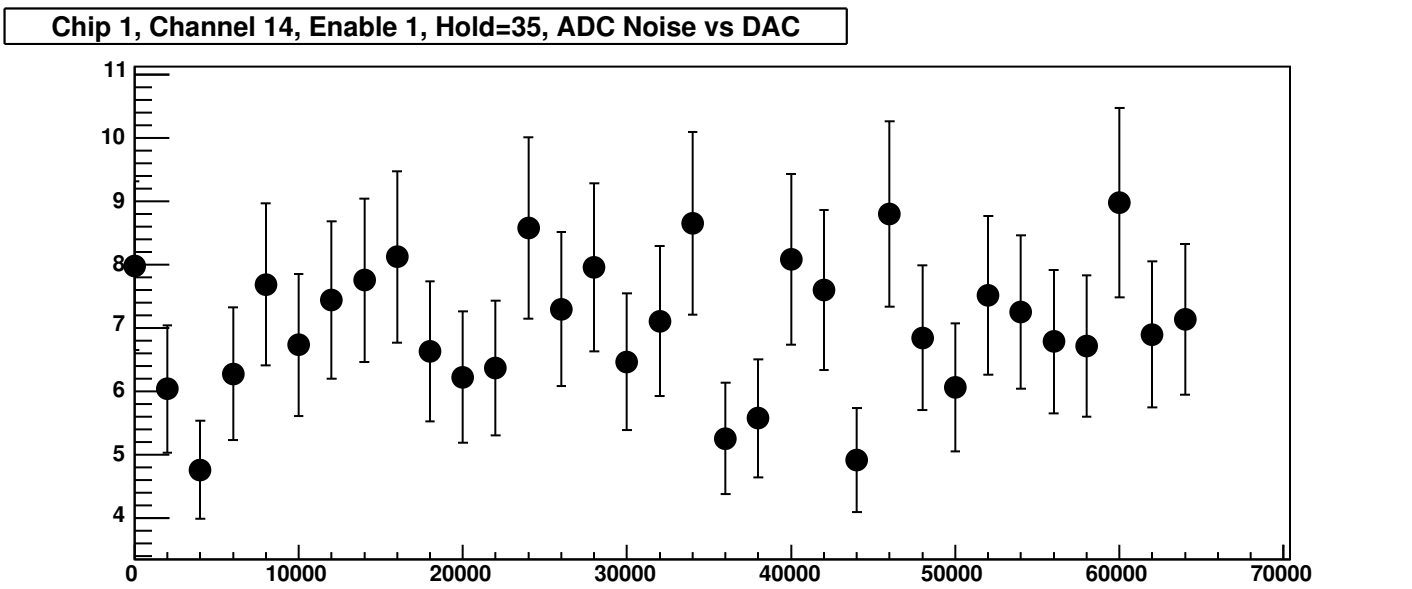

Chip 1, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
10000
20000
30000
40000
50000
60000

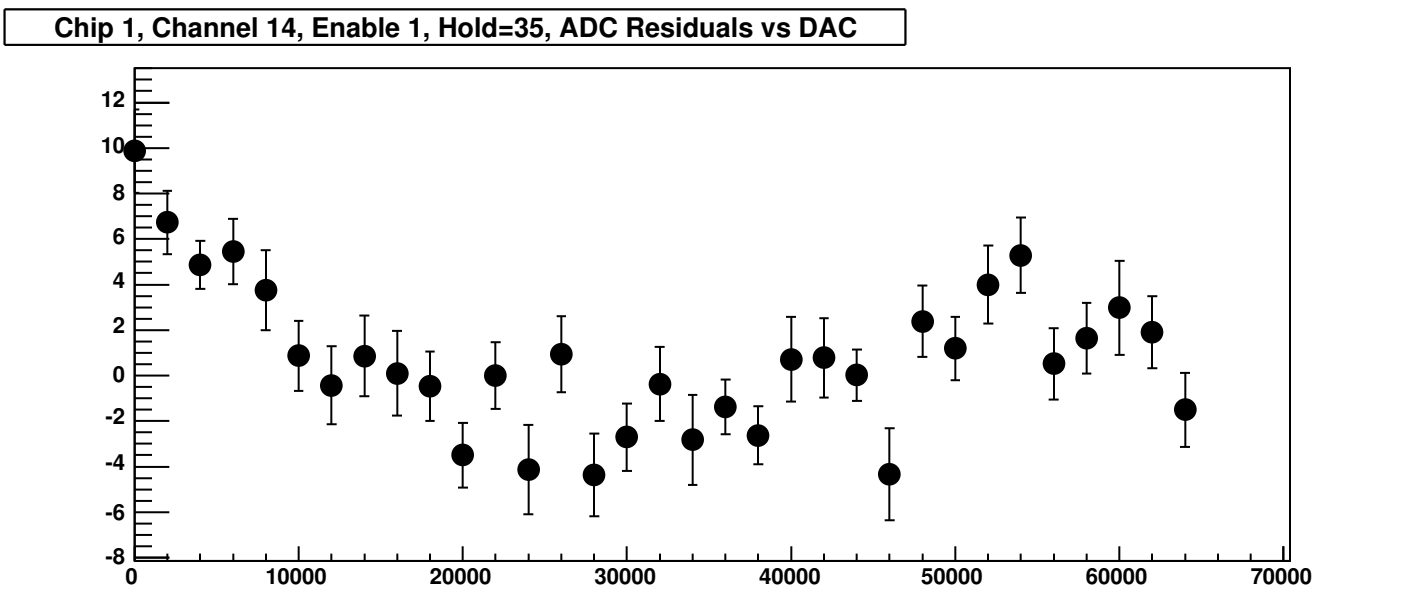

Chip 1, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 37.49 / 23
p0 42.52 ± 0.702
p1 0.002856 ± 2.19e-05
240
220
200
180
160
140
120
100
80
60
40
0
10000
20000
30000
40000
50000
60000
70000

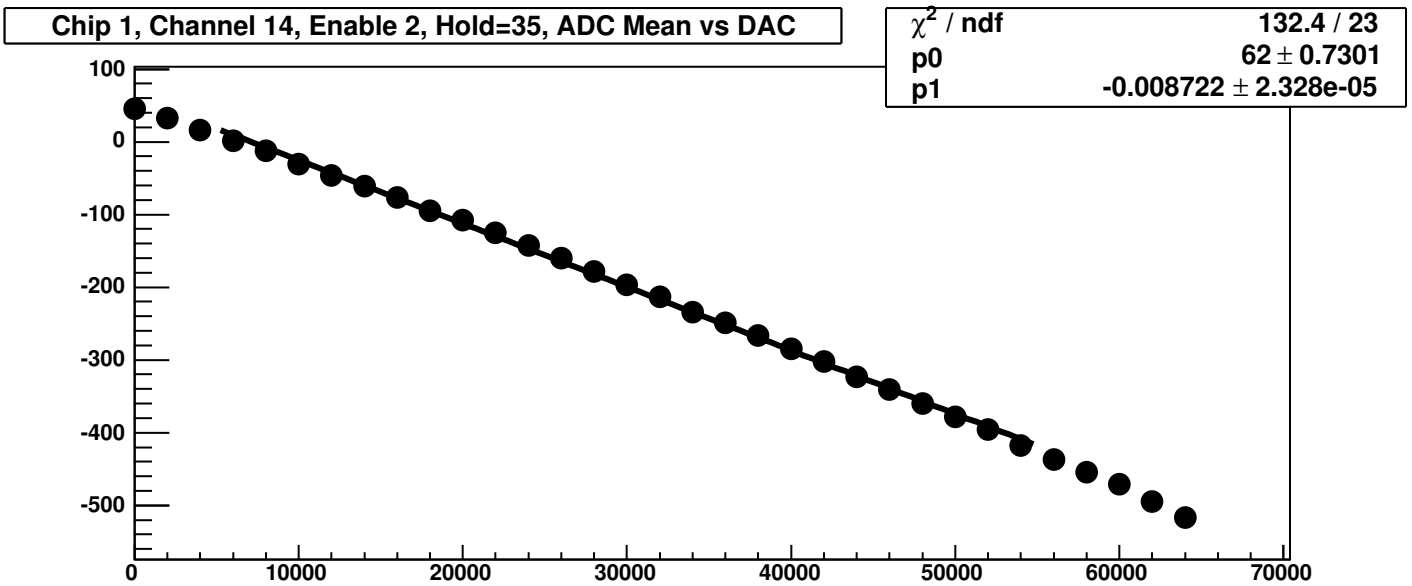

Chip 1, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

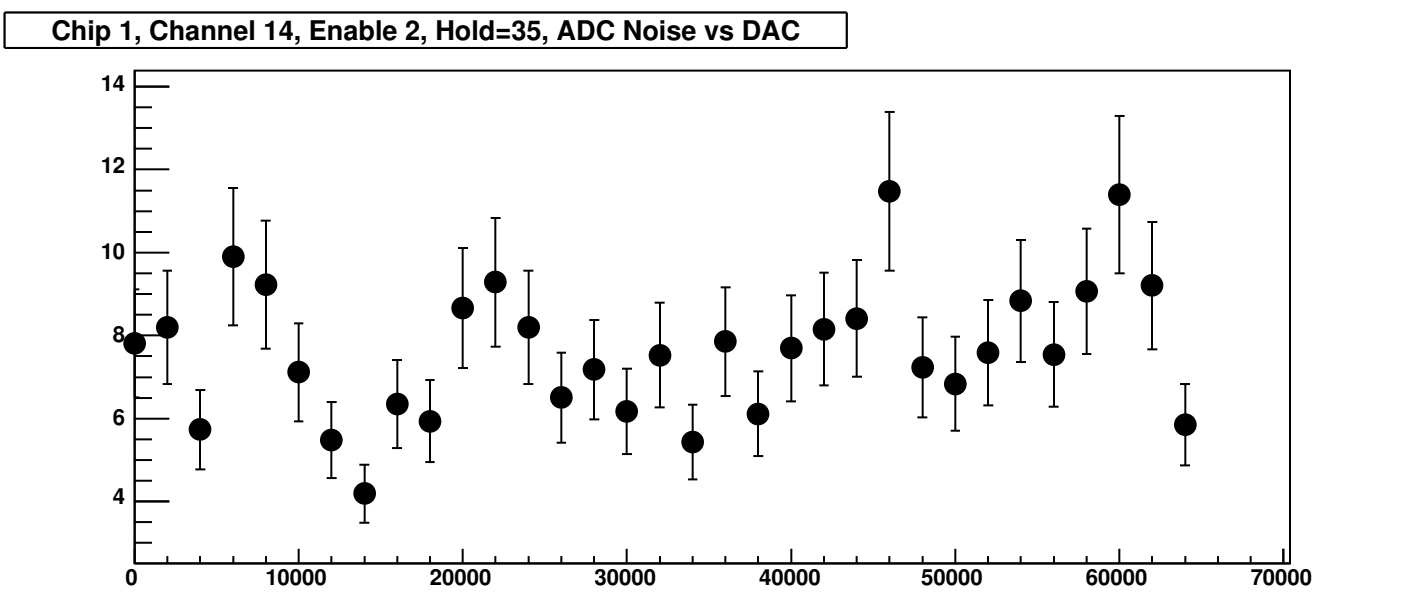

Chip 1, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

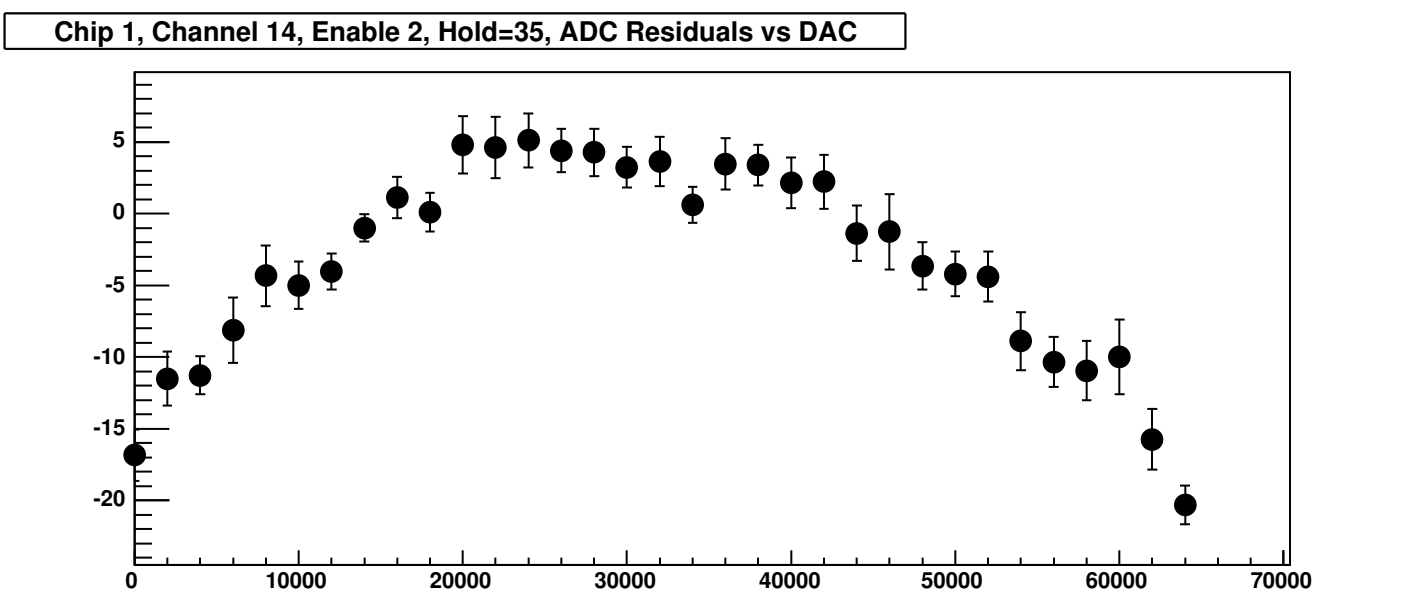

Chip 1, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 70.41 / 23
p0 42.82 ± 0.7425
p1 0.003042 ± 2.195e-05
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

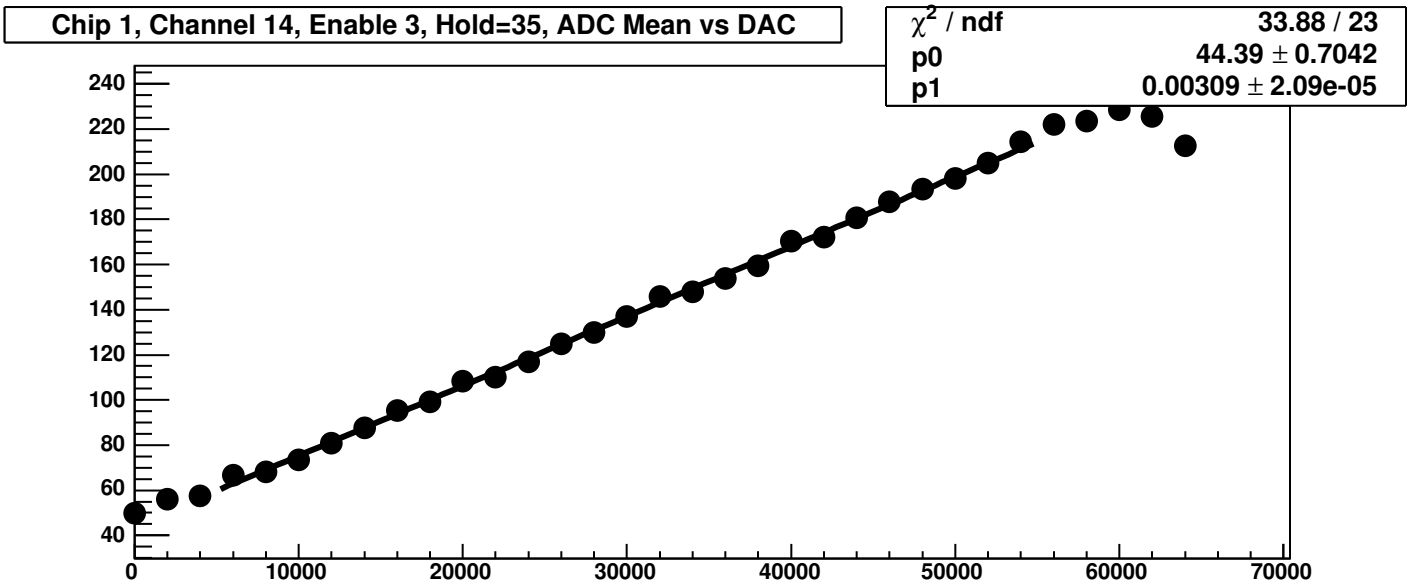

Chip 1, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

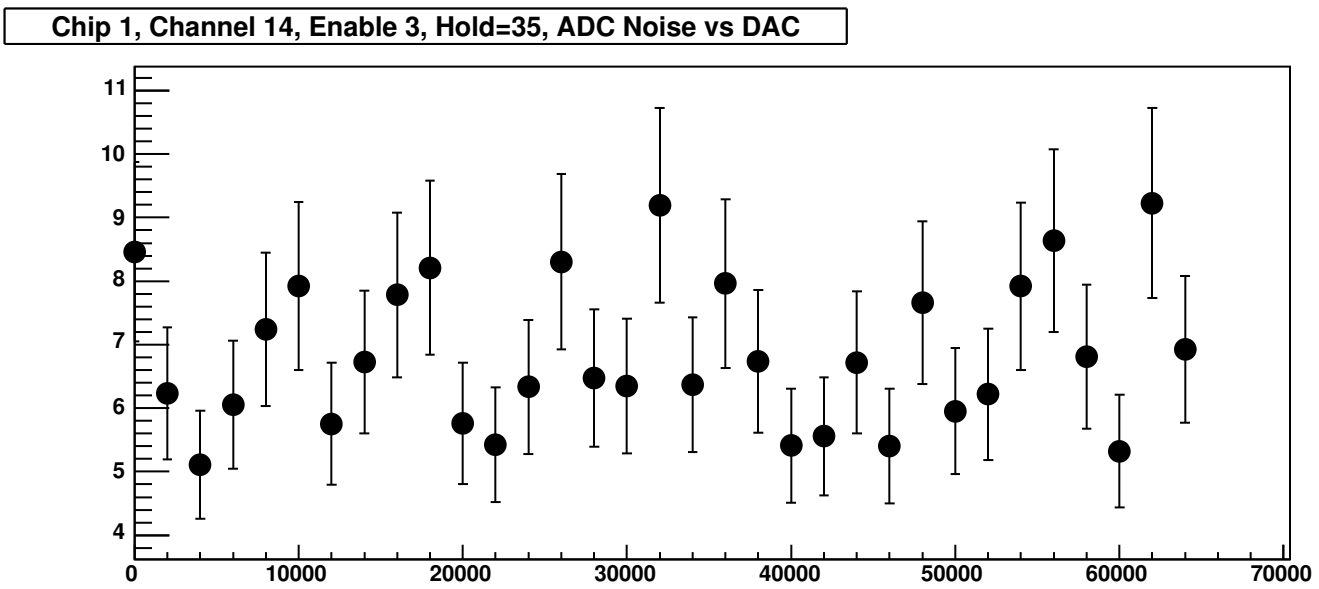

Chip 1, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

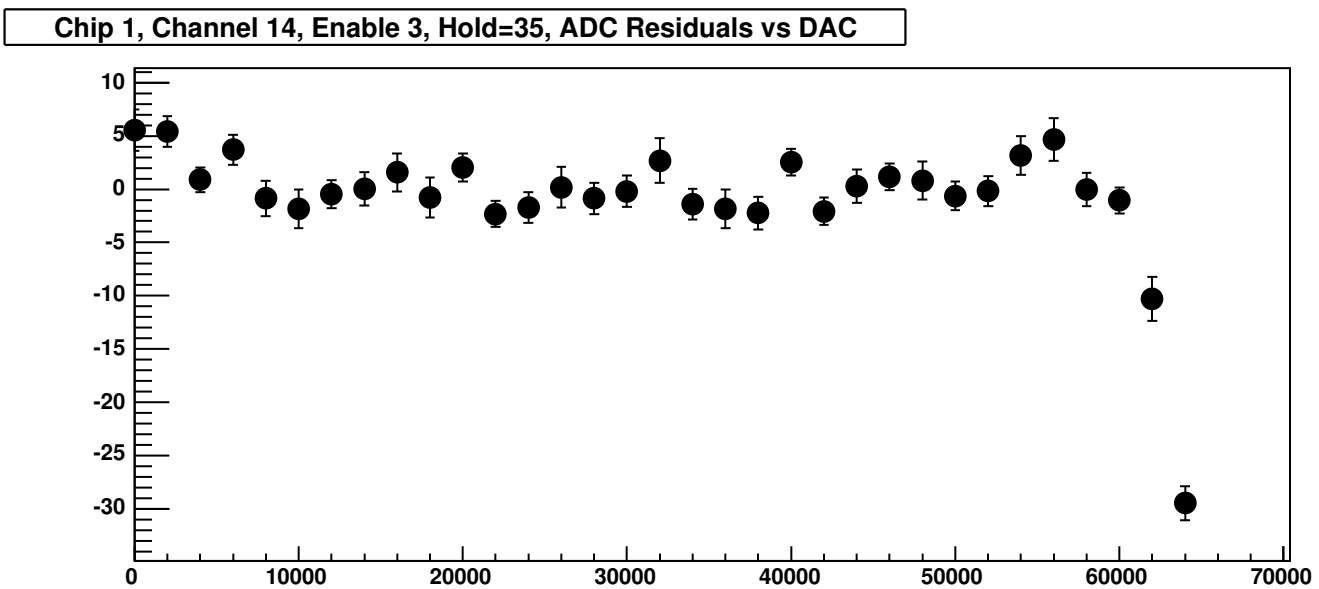

Chip 1, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 132.4 / 23
p0 62 ± 0.7301
p1 -0.008722 ± 2.328e-05
100
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000
70000

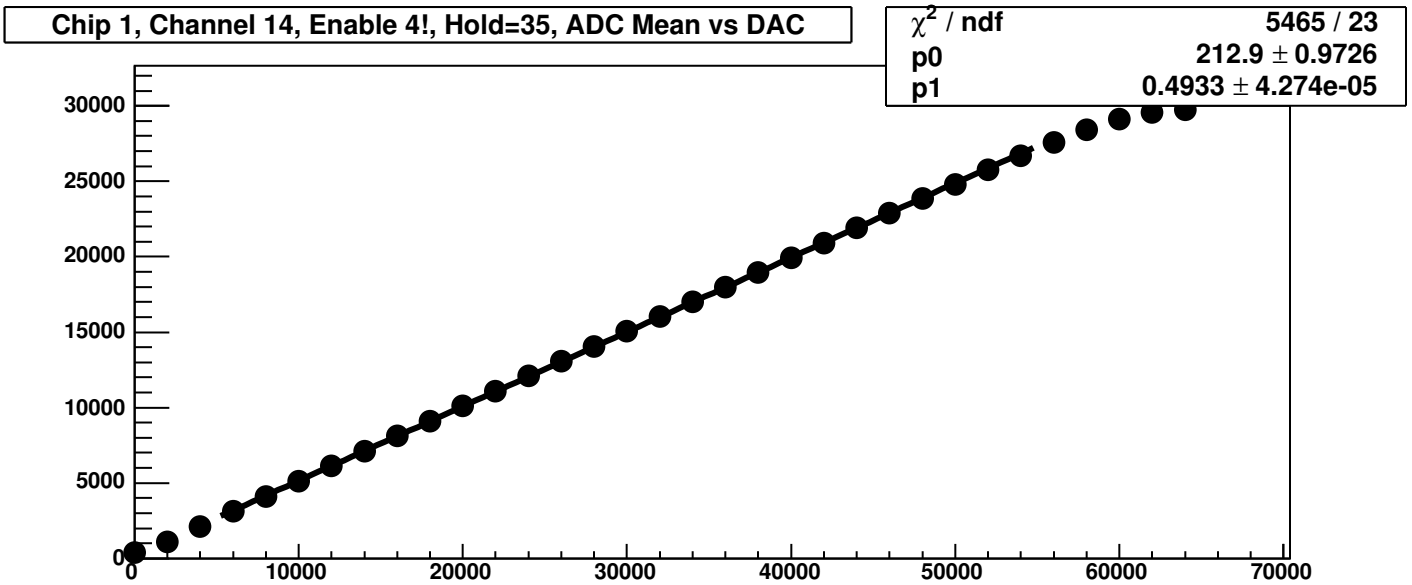

Chip 1, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

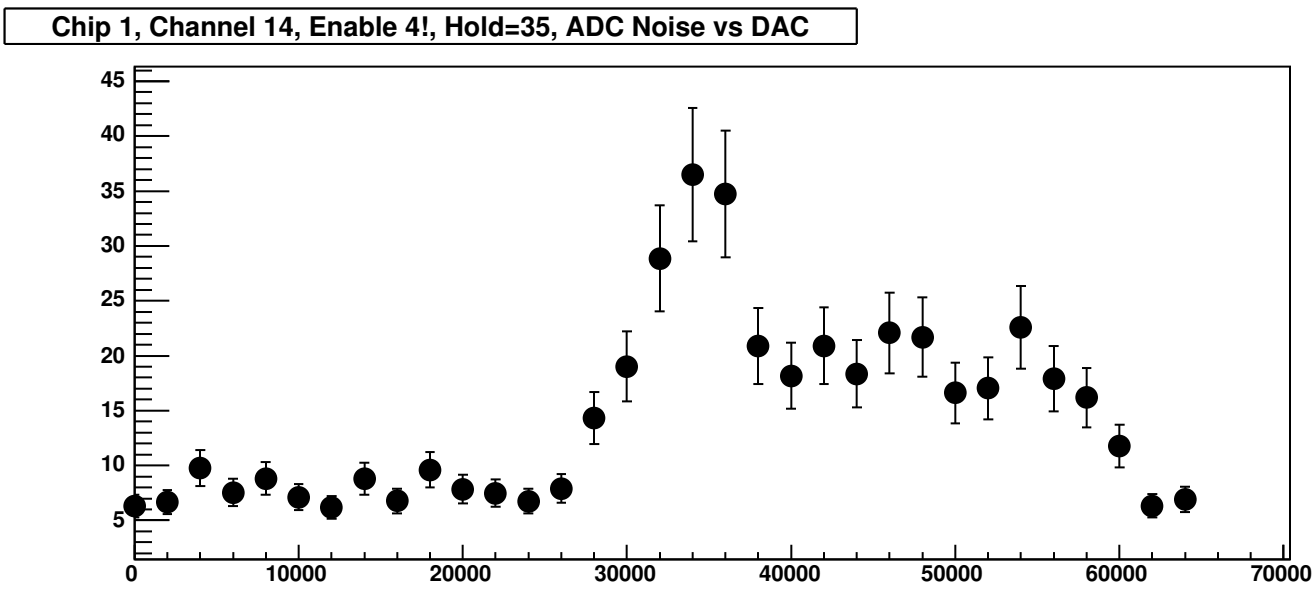

Chip 1, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
0
10000
20000
30000
40000
50000
60000
70000

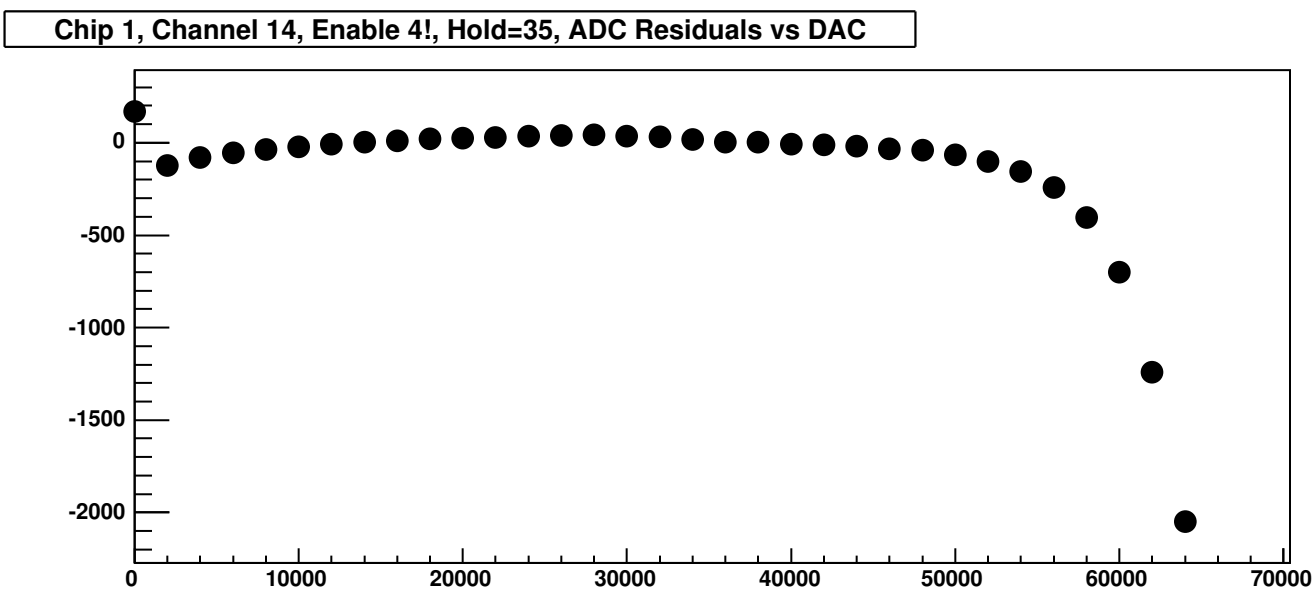

Chip 1, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 33.88 / 23
p0 44.39 ± 0.7042
p1 0.00309 ± 2.09e-05
240
220
200
180
160
140
120
100
80
60
40
0
10000
20000
30000
40000
50000
60000
70000

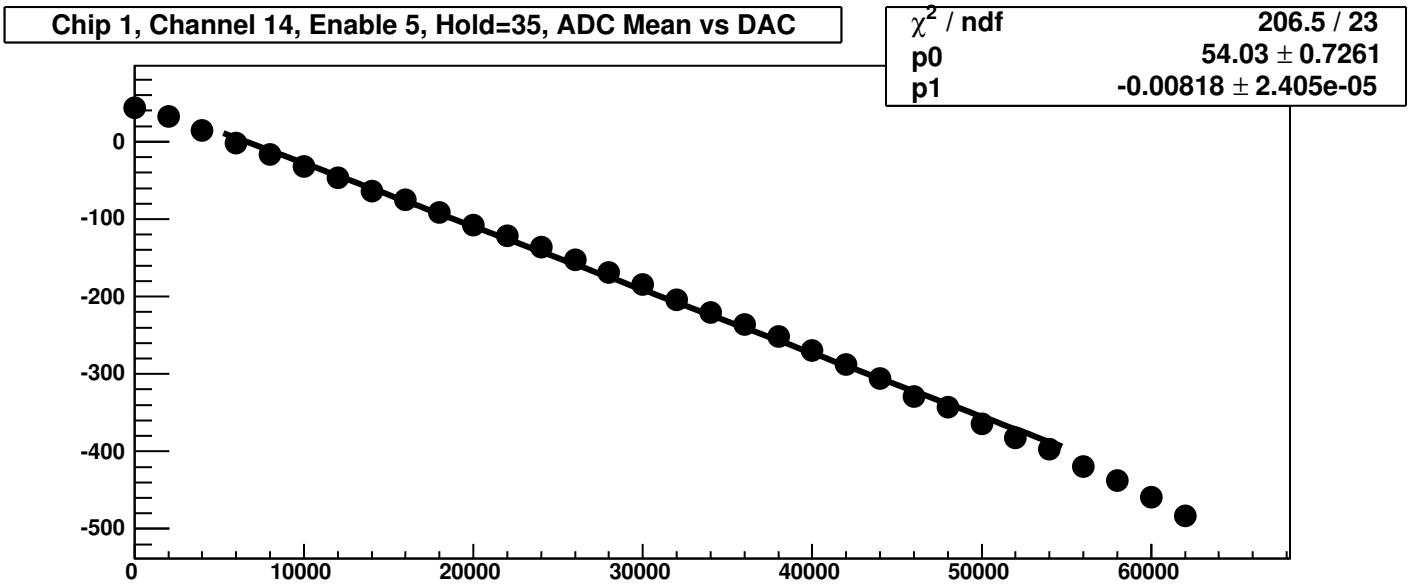

Chip 1, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

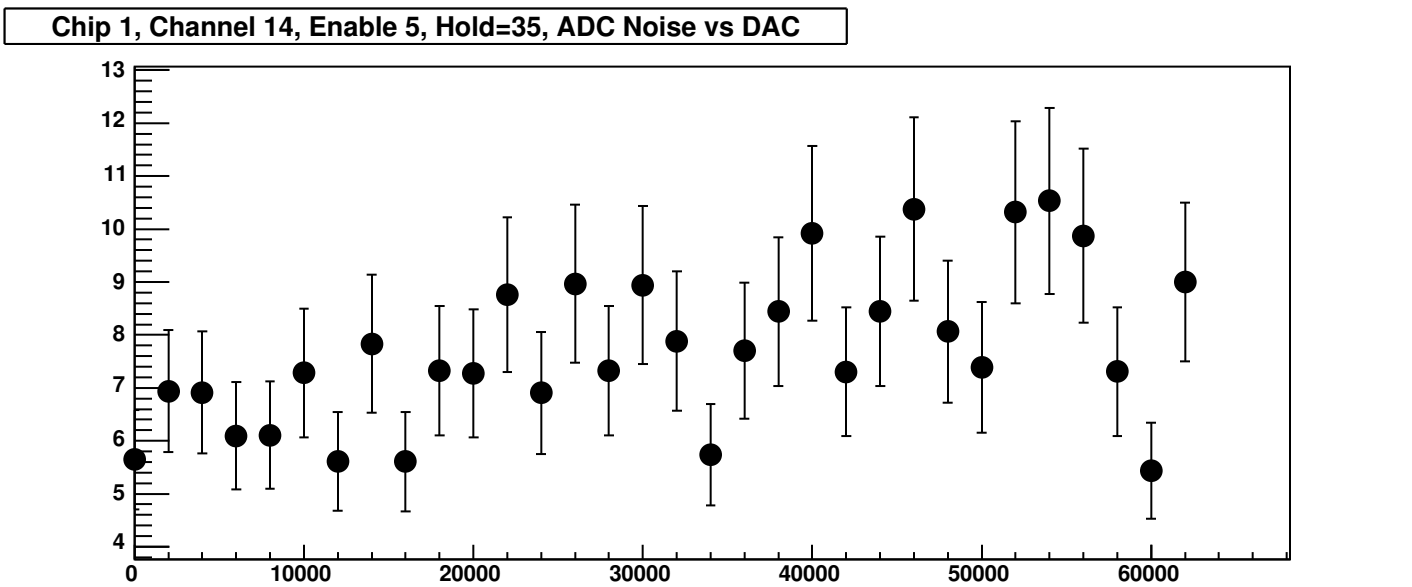

Chip 1, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
0
10000
20000
30000
40000
50000
60000
70000

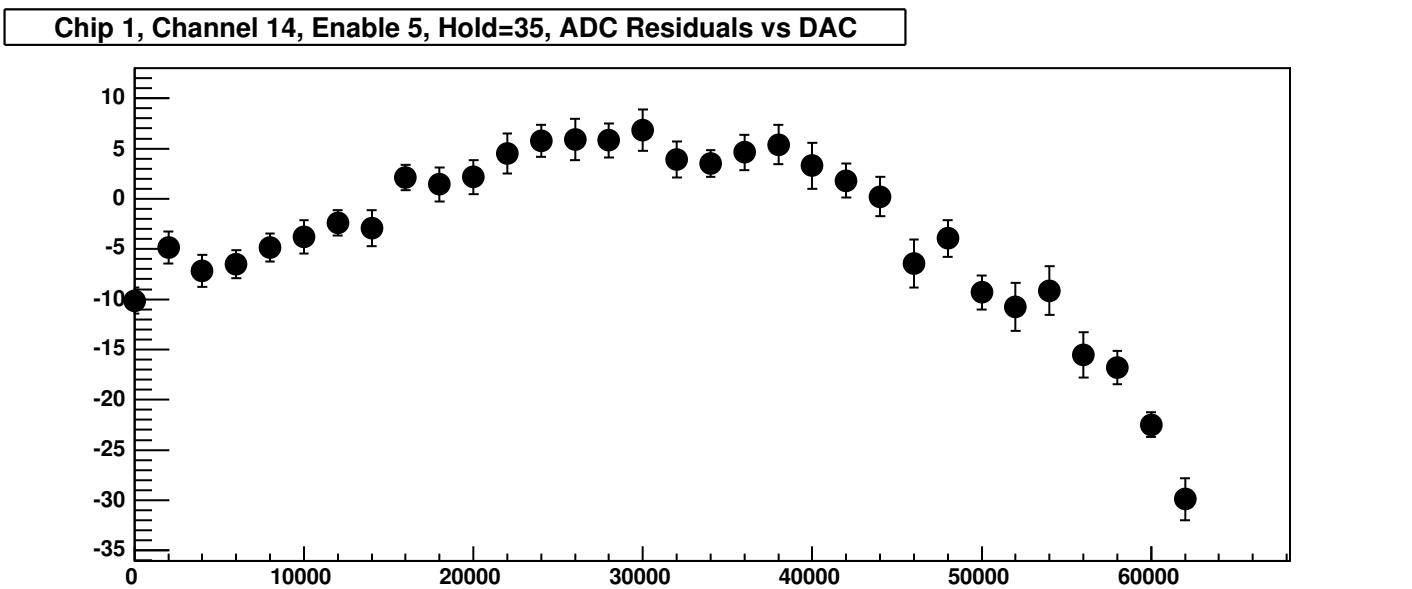

Chip 1, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 5465 / 23
p0 212.9 ± 0.9726
p1 0.4933 ± 4.274e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

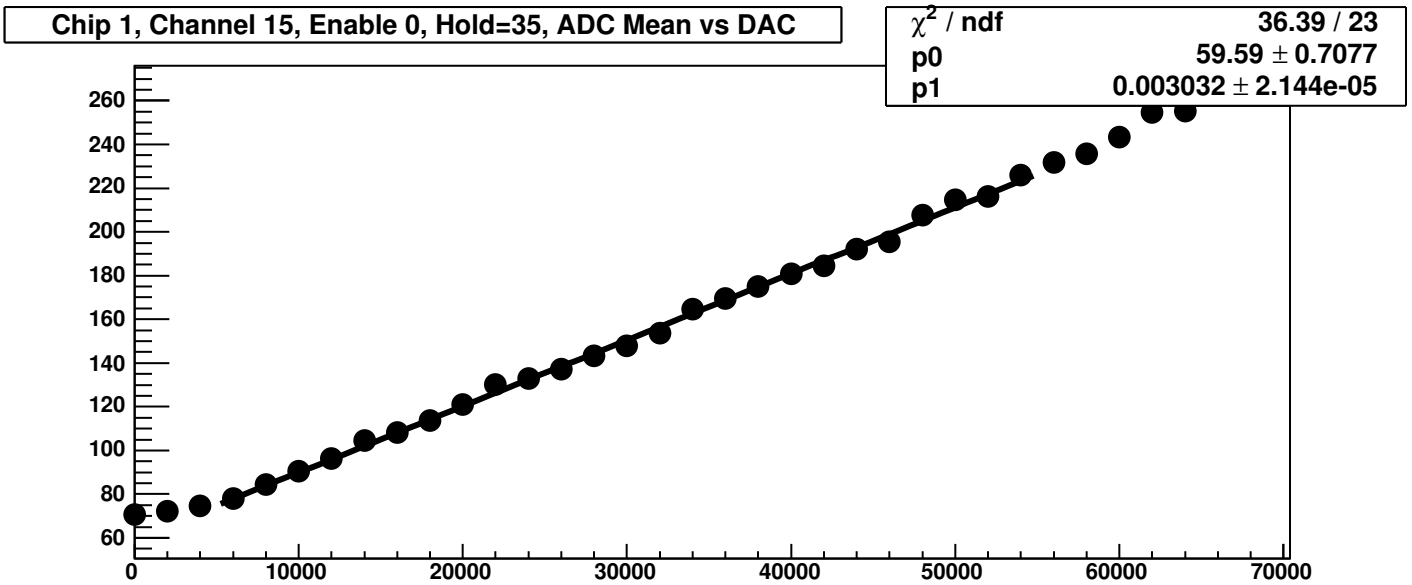

Chip 1, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000

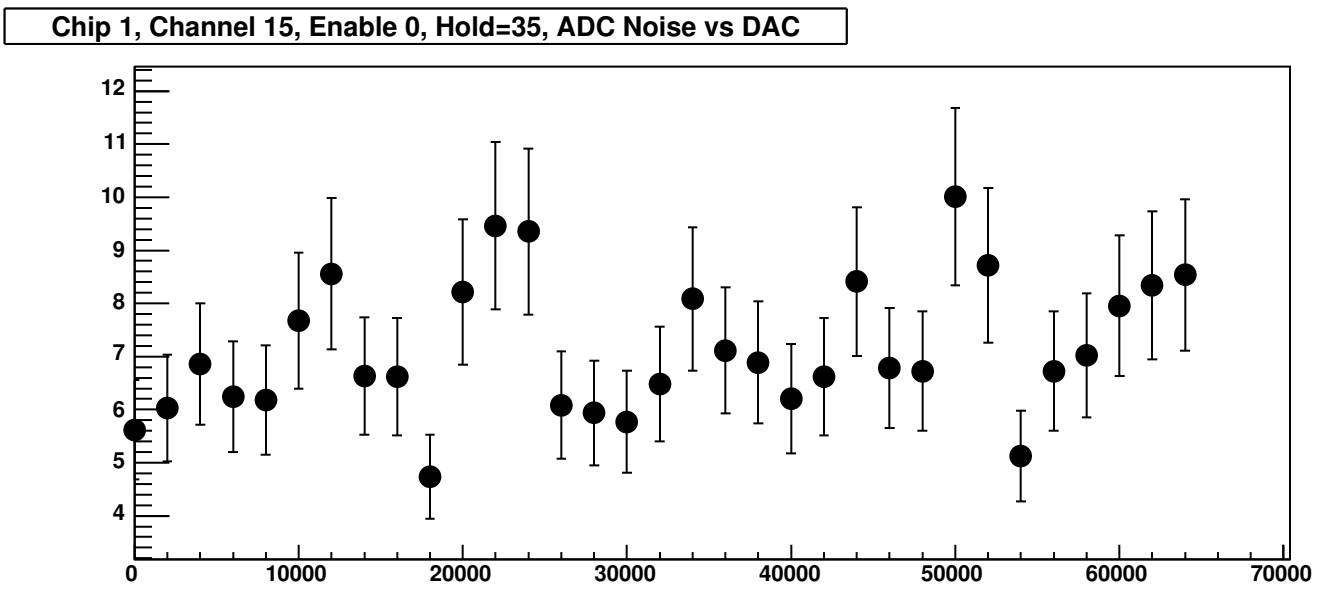

Chip 1, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000

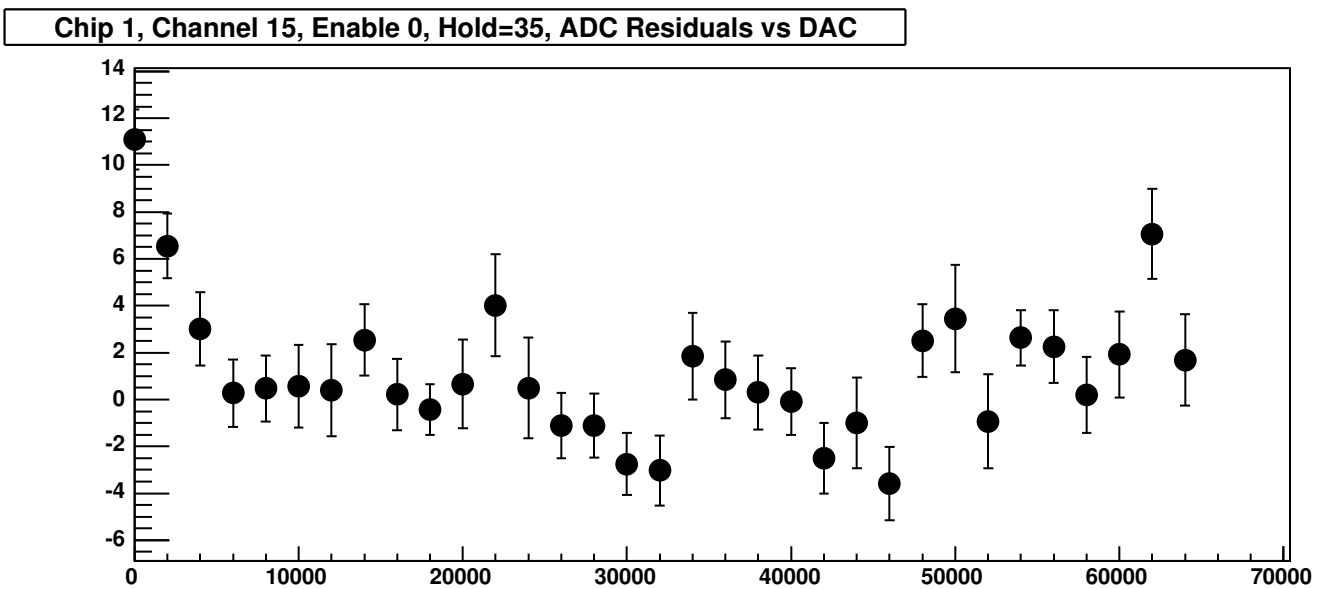

Chip 1, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 206.5 / 23
p0 54.03 ± 0.7261
p1 -0.00818 ± 2.405e-05
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000

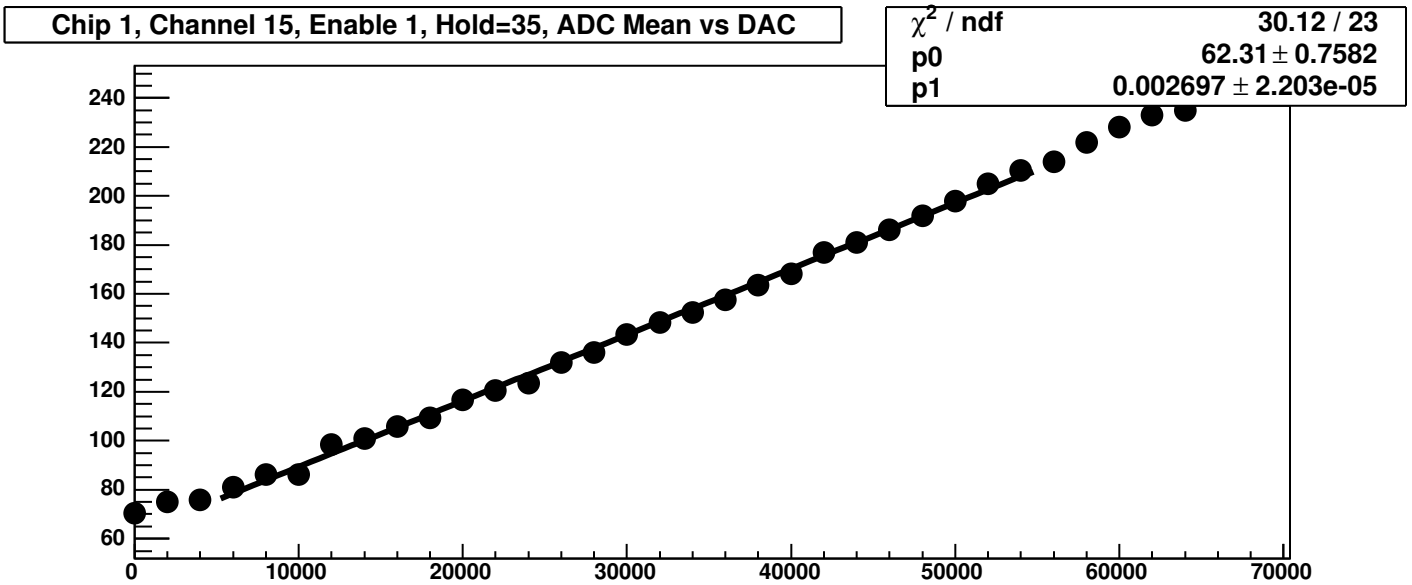

Chip 1, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

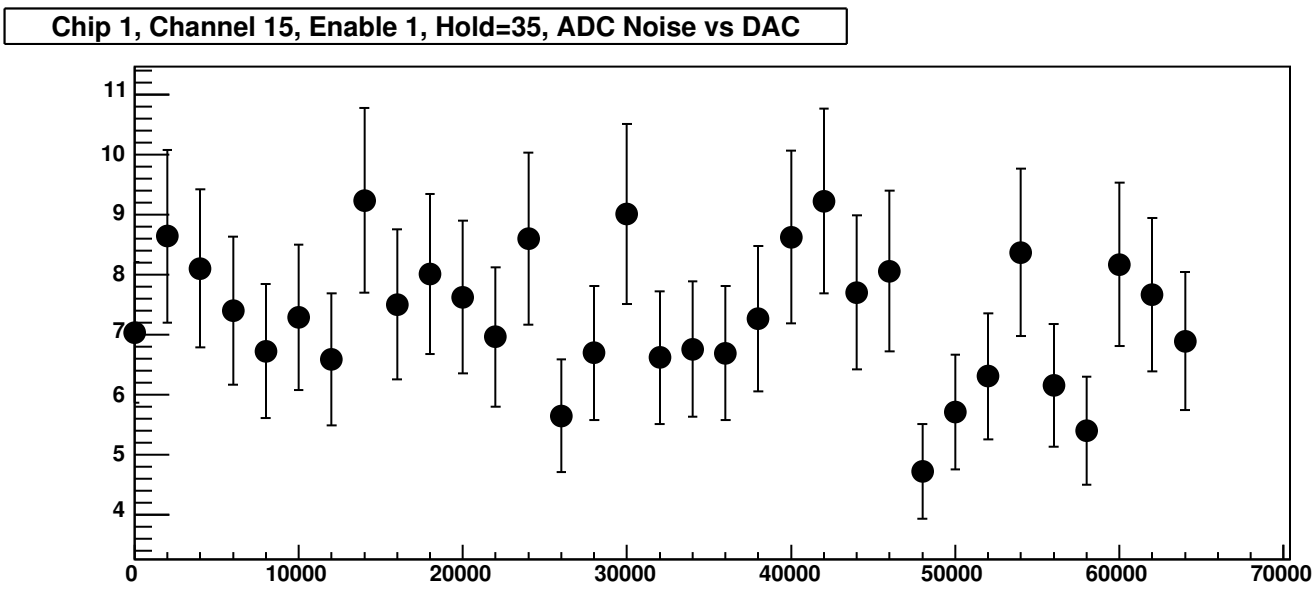

Chip 1, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
-35
0
10000
20000
30000
40000
50000
60000

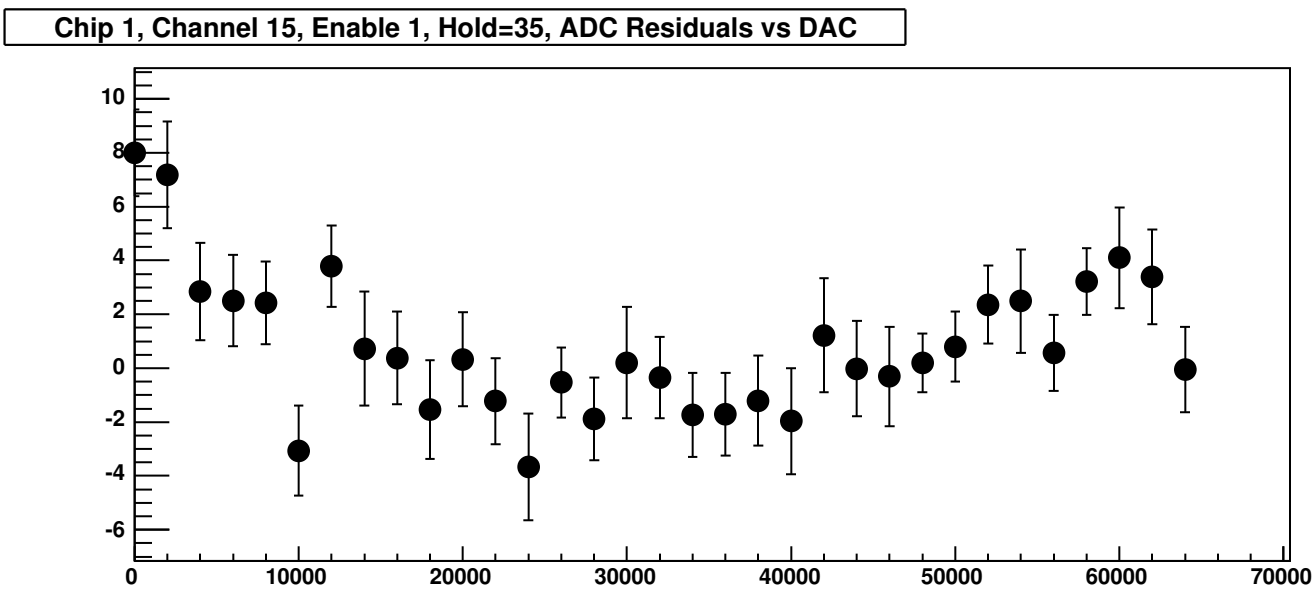

Chip 1, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 36.39 / 23
p0 59.59 ± 0.7077
p1 0.003032 ± 2.144e-05
260
240
220
200
180
160
140
120
100
80
60
0
10000
20000
30000
40000
50000
60000
70000

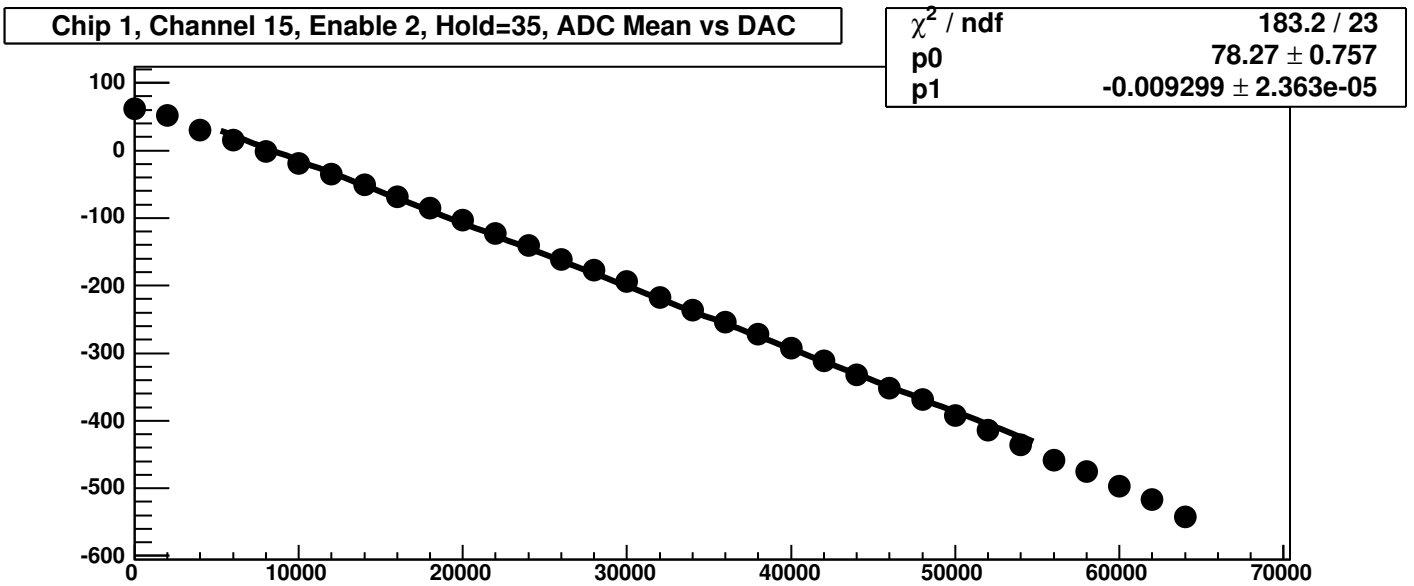

Chip 1, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

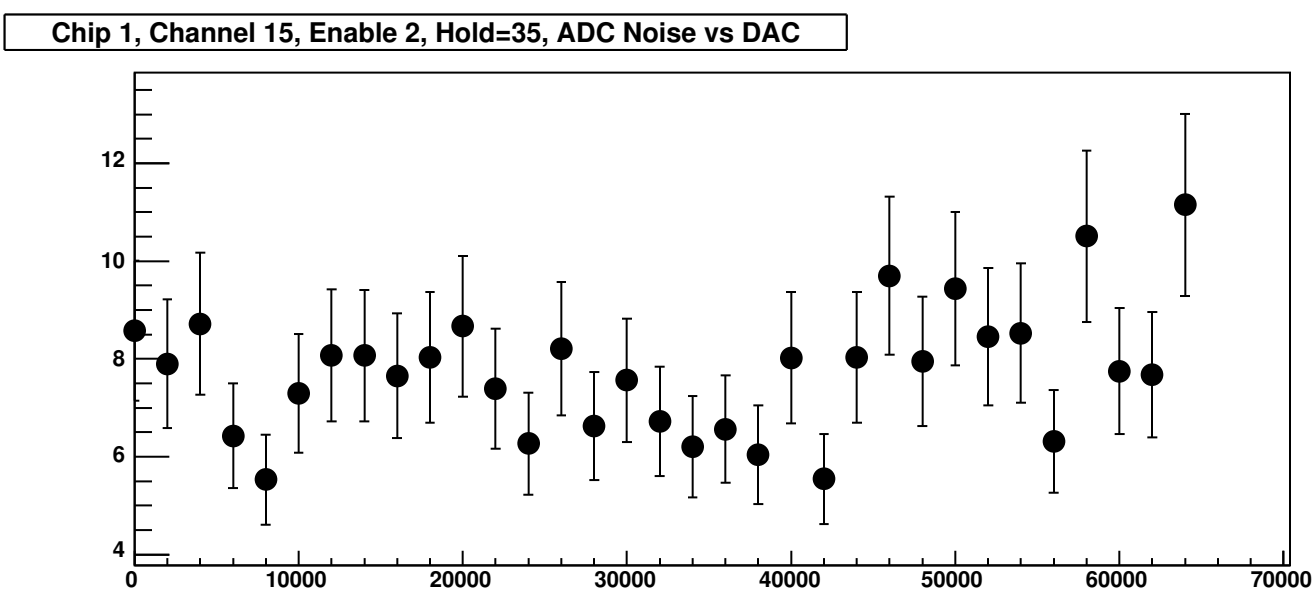

Chip 1, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

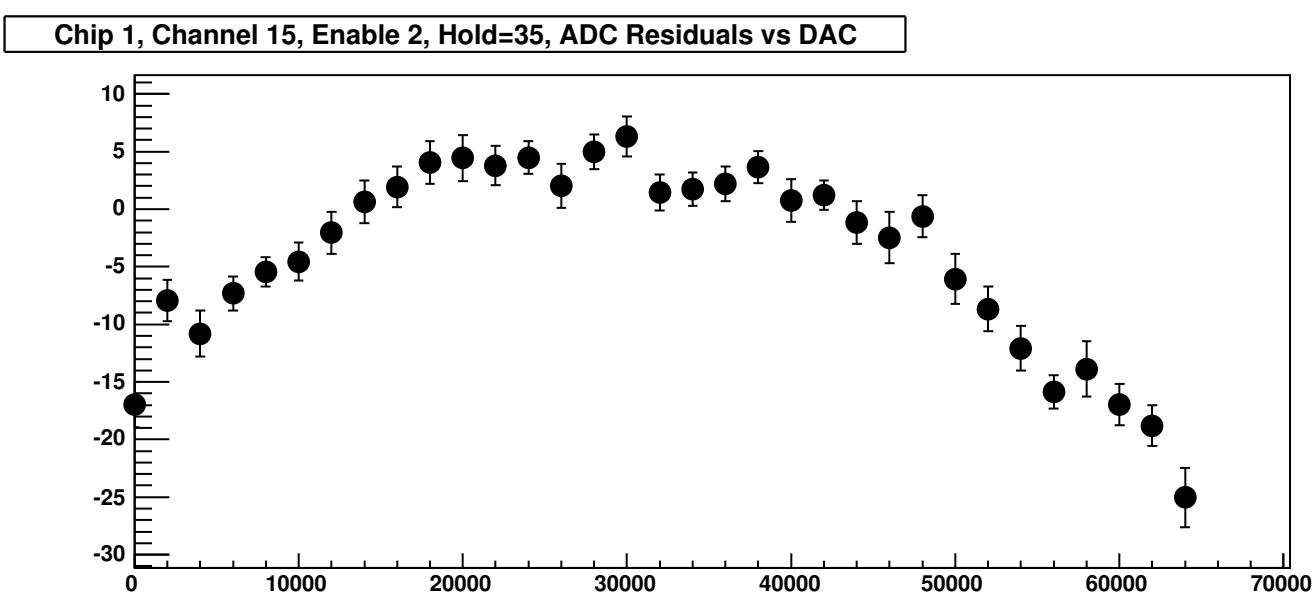

Chip 1, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 30.12 / 23
p0 62.31 ± 0.7582
p1 0.002697 ± 2.203e-05
240
220
200
180
160
140
120
100
80
60
0
10000
20000
30000
40000
50000
60000
70000

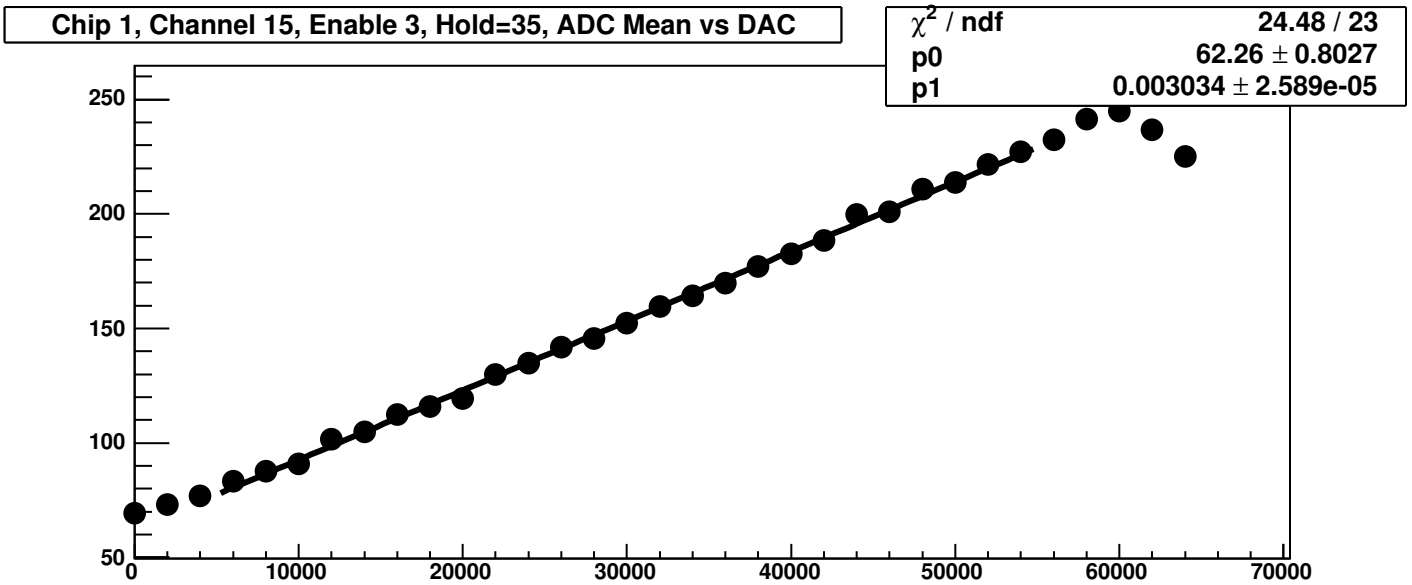

Chip 1, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

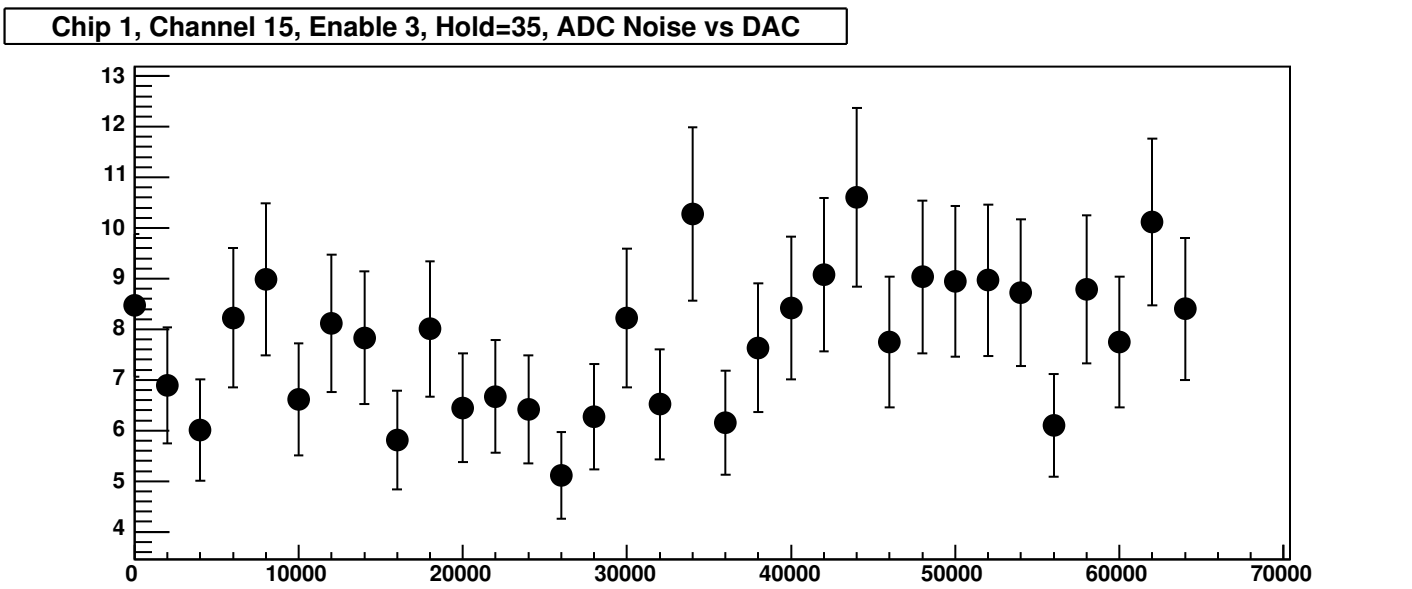

Chip 1, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

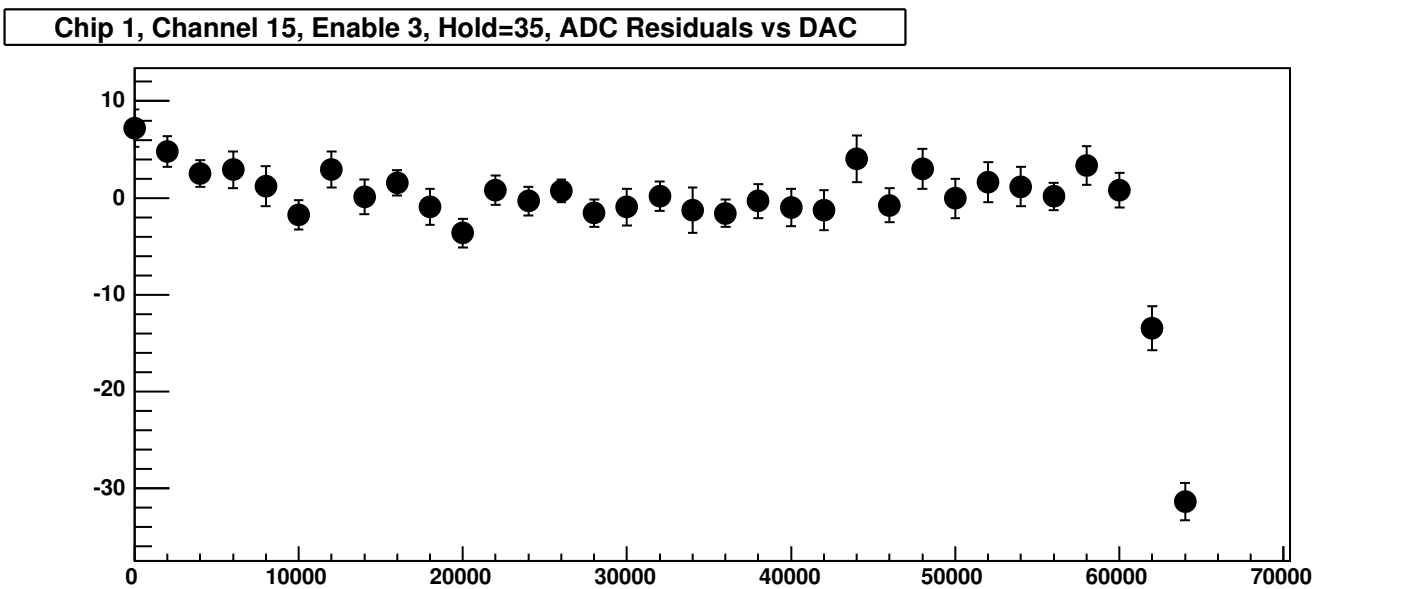

Chip 1, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 183.2 / 23
p0 78.27 ± 0.757
p1 -0.009299 ± 2.363e-05
100
0
-100
-200
-300
-400
-500
-600
0
10000
20000
30000
40000
50000
60000
70000

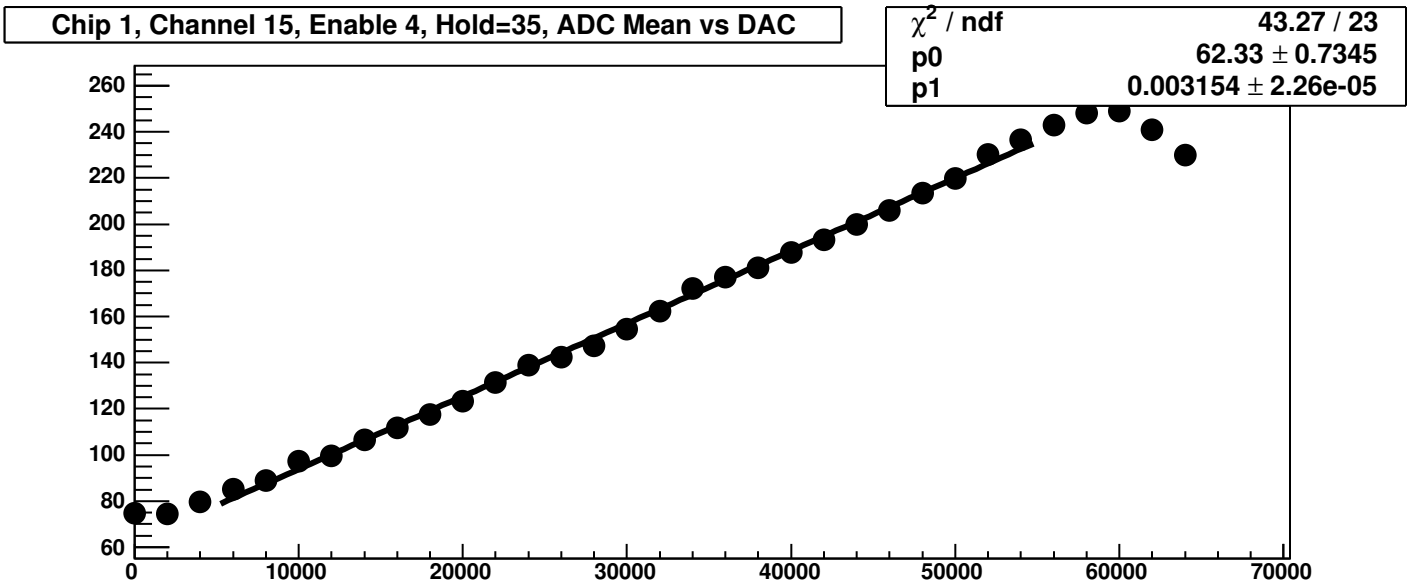

Chip 1, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

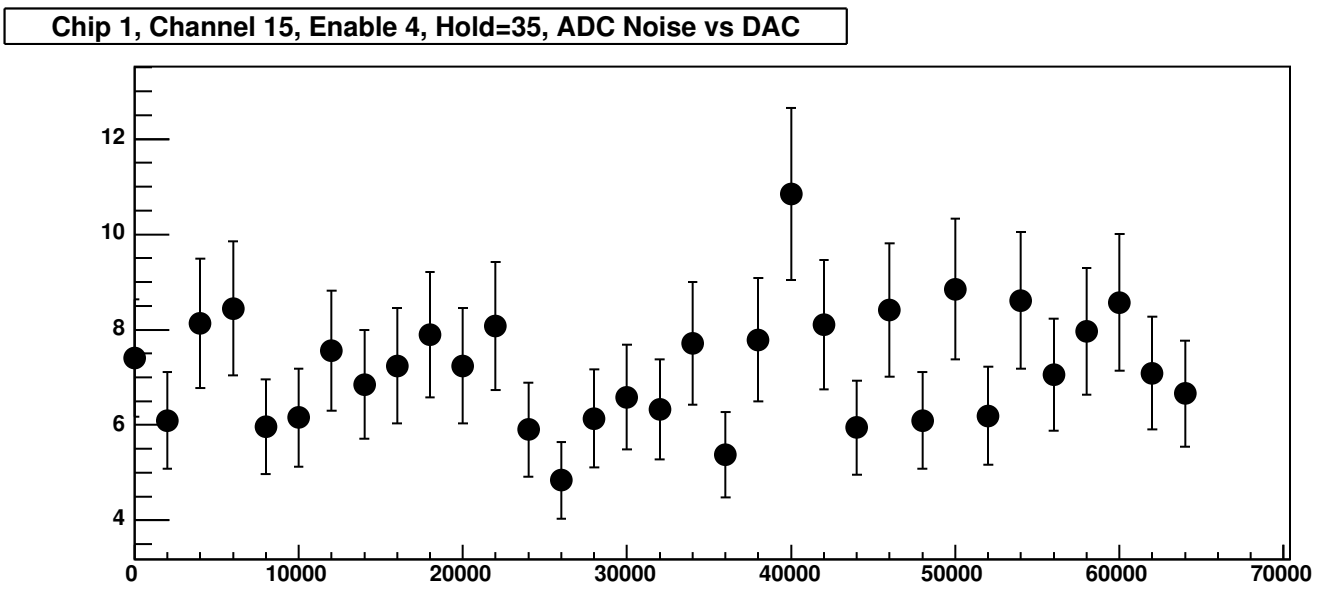

Chip 1, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
0
10000
20000
30000
40000
50000
60000
70000

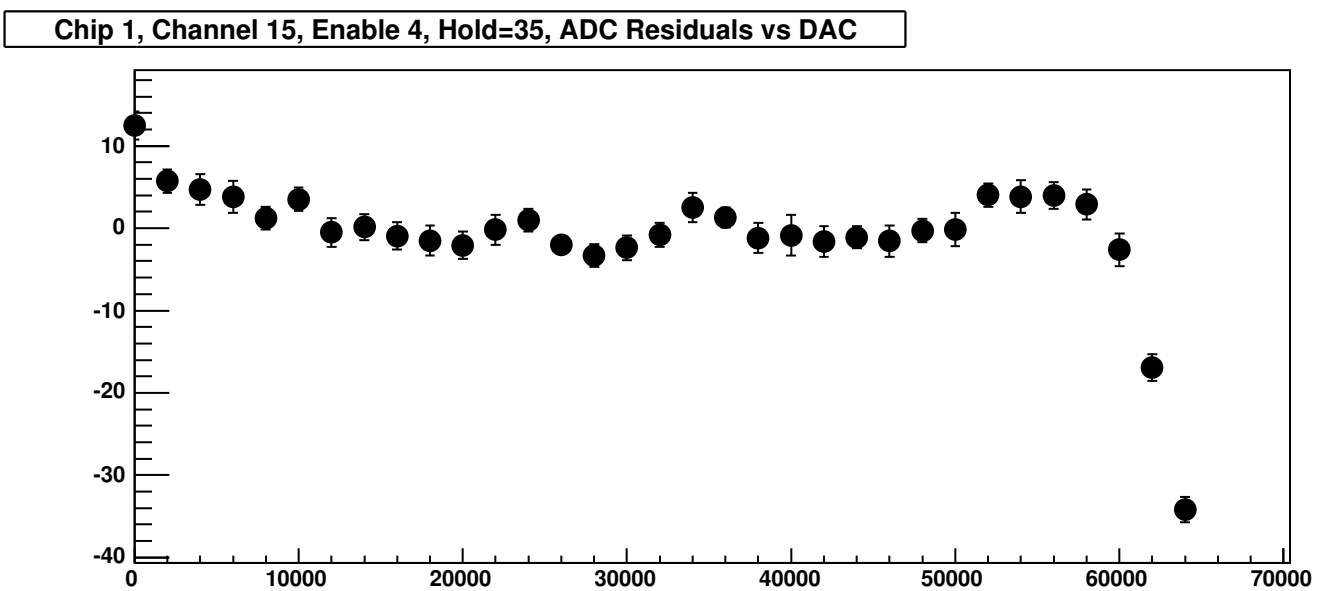

Chip 1, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 24.48 / 23
p0 62.26 ± 0.8027
p1 0.003034 ± 2.589e-05
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

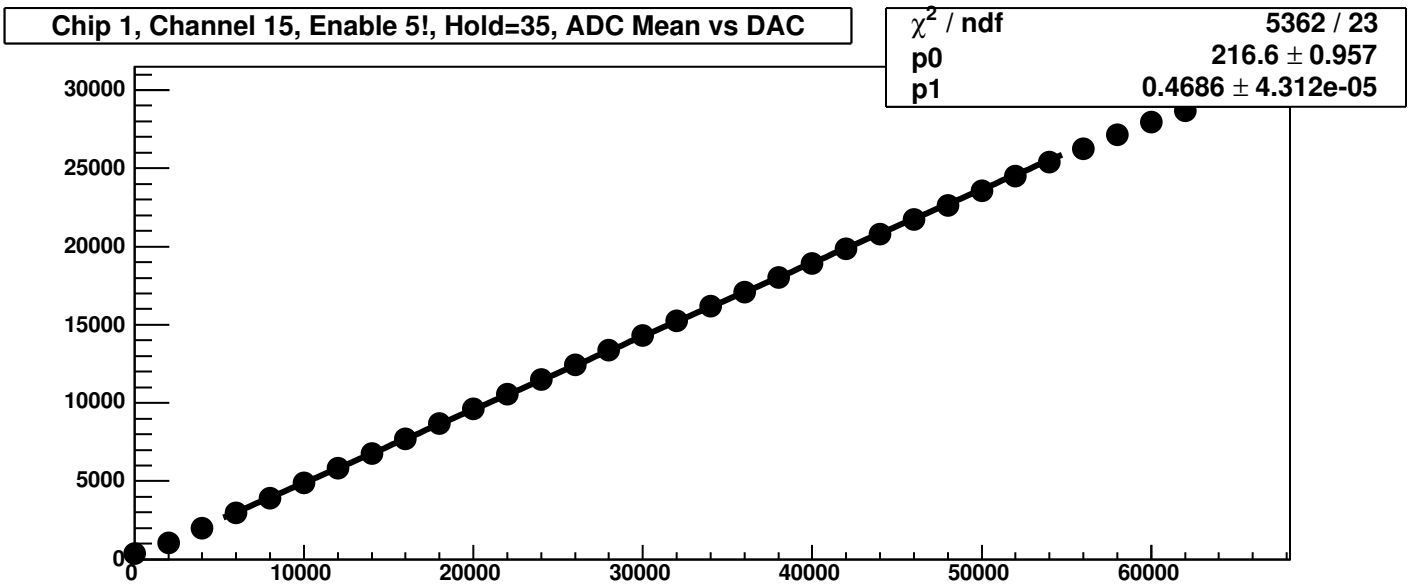

Chip 1, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

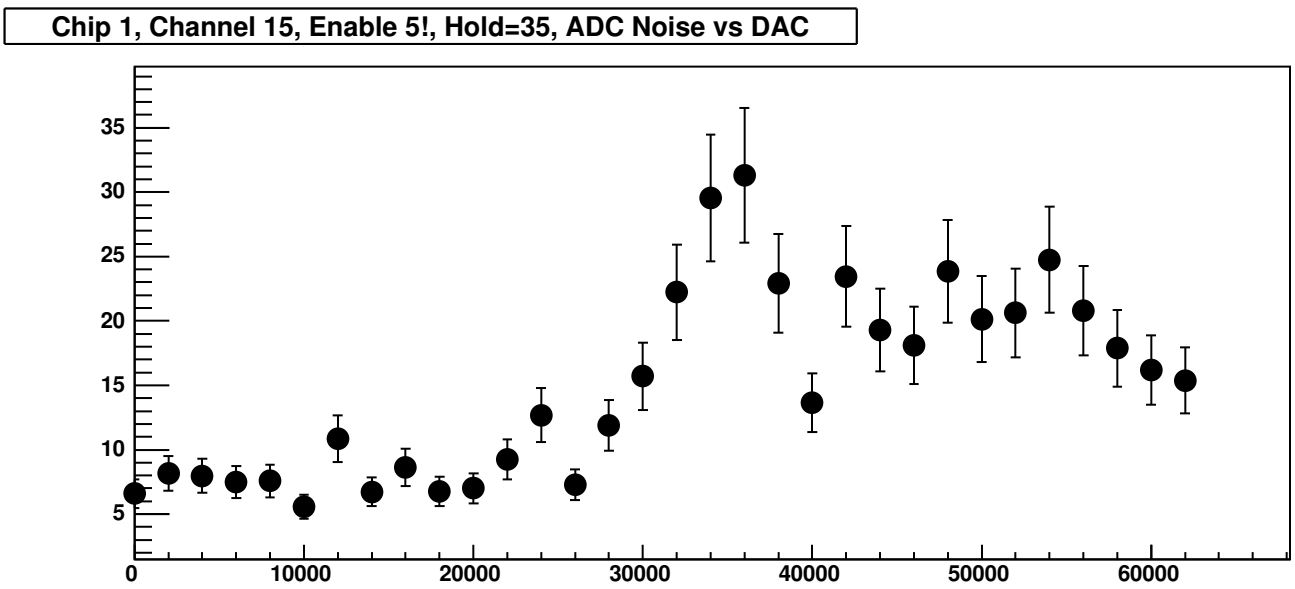

Chip 1, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
10000
20000
30000
40000
50000
60000
70000

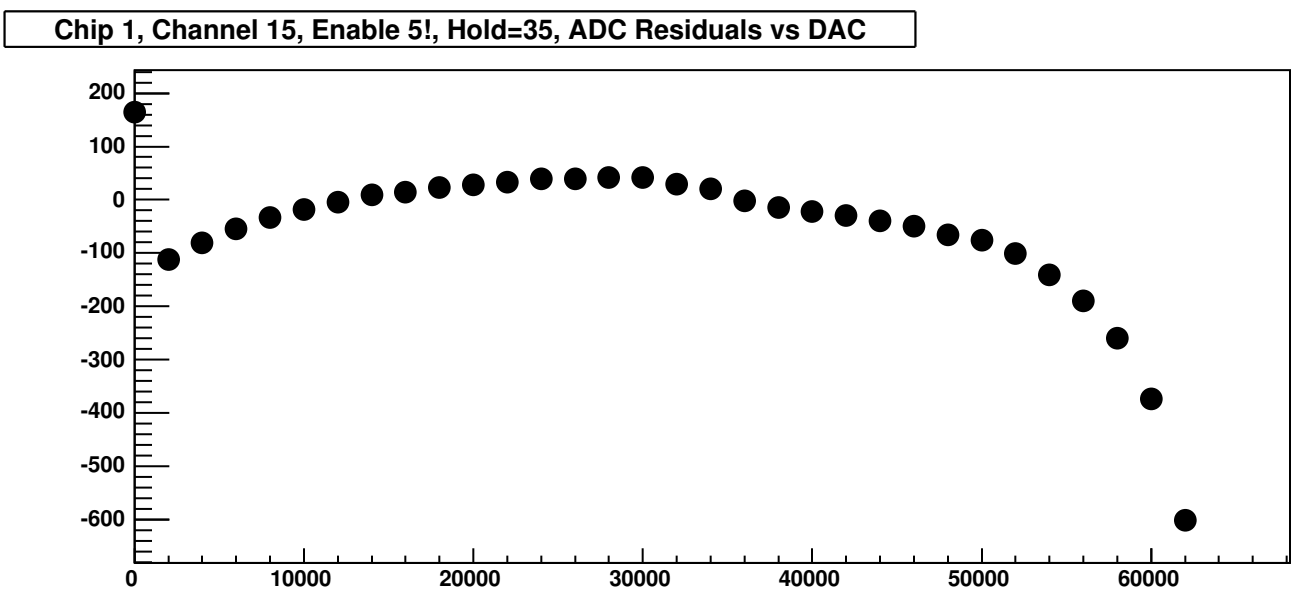

Chip 1, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 43.27 / 23
p0 62.33 ± 0.7345
p1 0.003154 ± 2.26e-05
260
240
220
200
180
160
140
120
100
80
60
0
10000
20000
30000
40000
50000
60000
70000

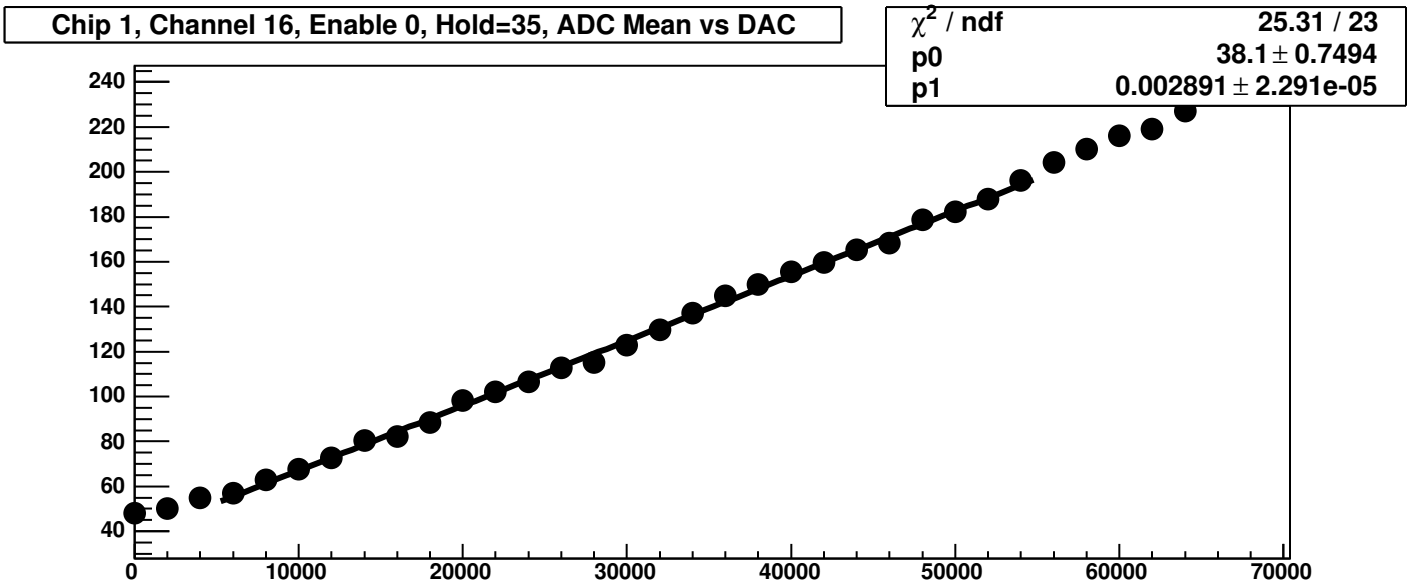

Chip 1, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

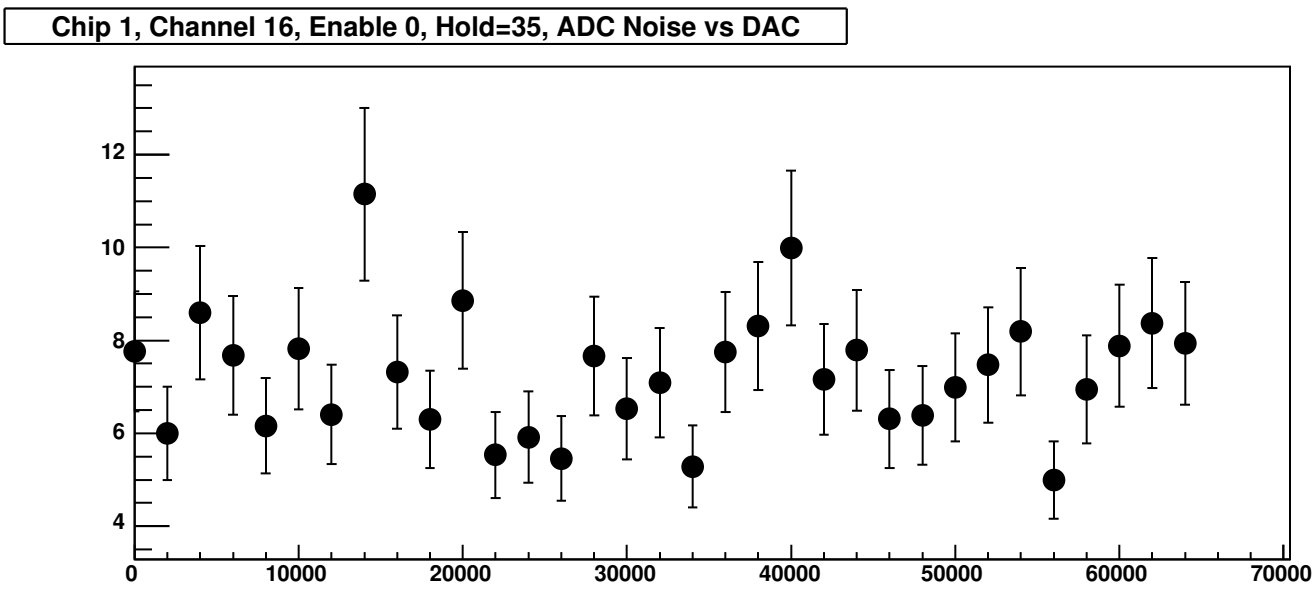

Chip 1, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
10000
20000
30000
40000
50000
60000
70000

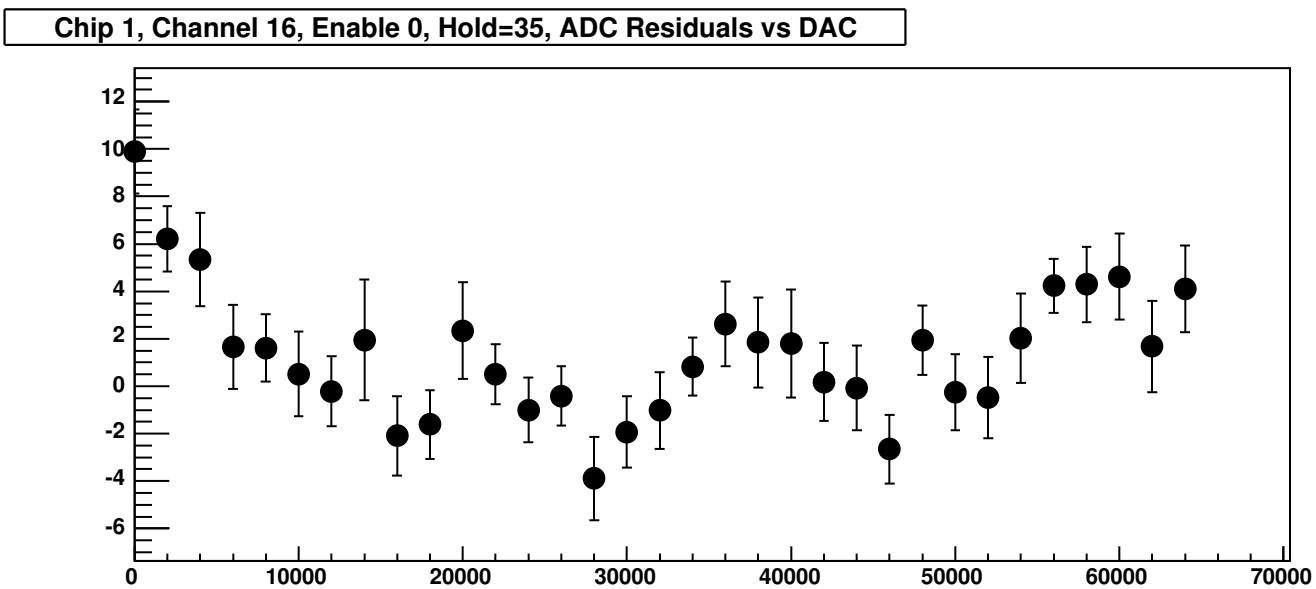

Chip 1, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 5362 / 23
p0 216.6 ± 0.957
p1 0.4686 ± 4.312e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

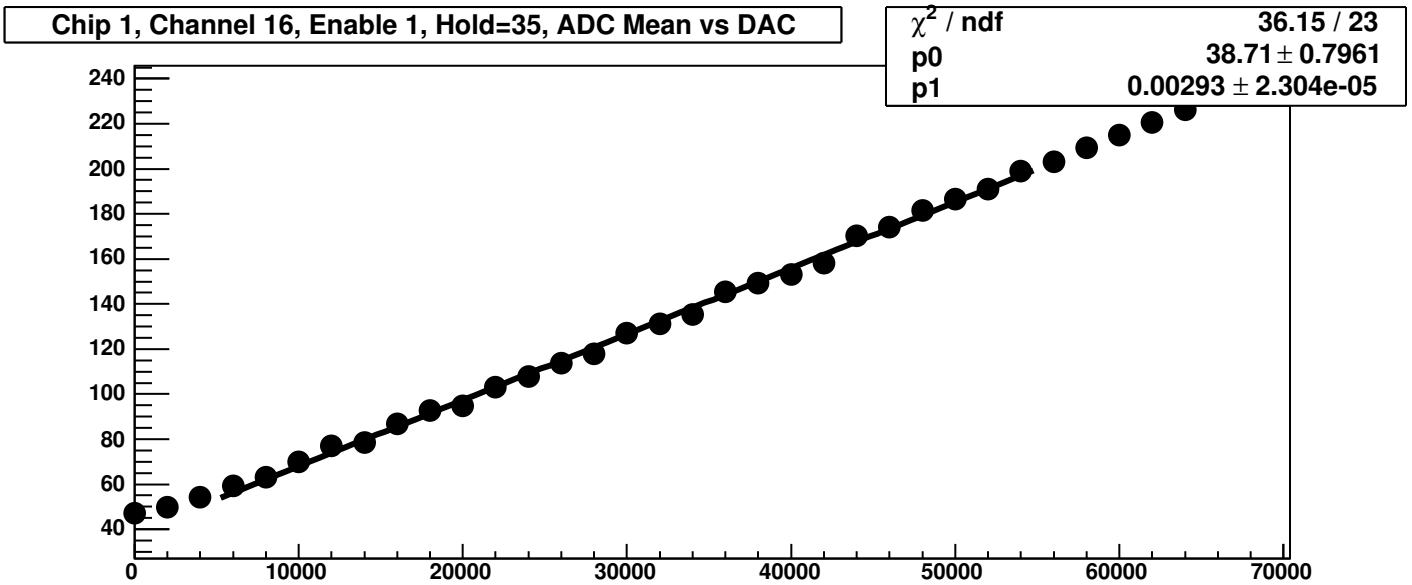

Chip 1, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000

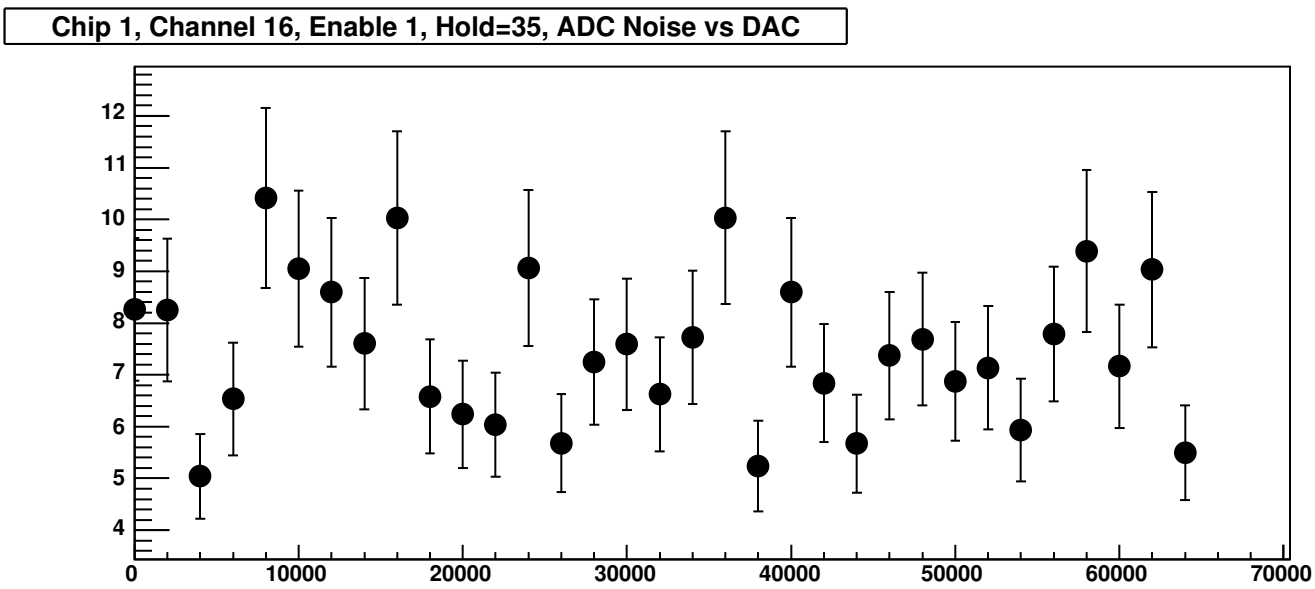

Chip 1, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
200
100
0
-100
-200
-300
-400
-500
-600
0
10000
20000
30000
40000
50000
60000

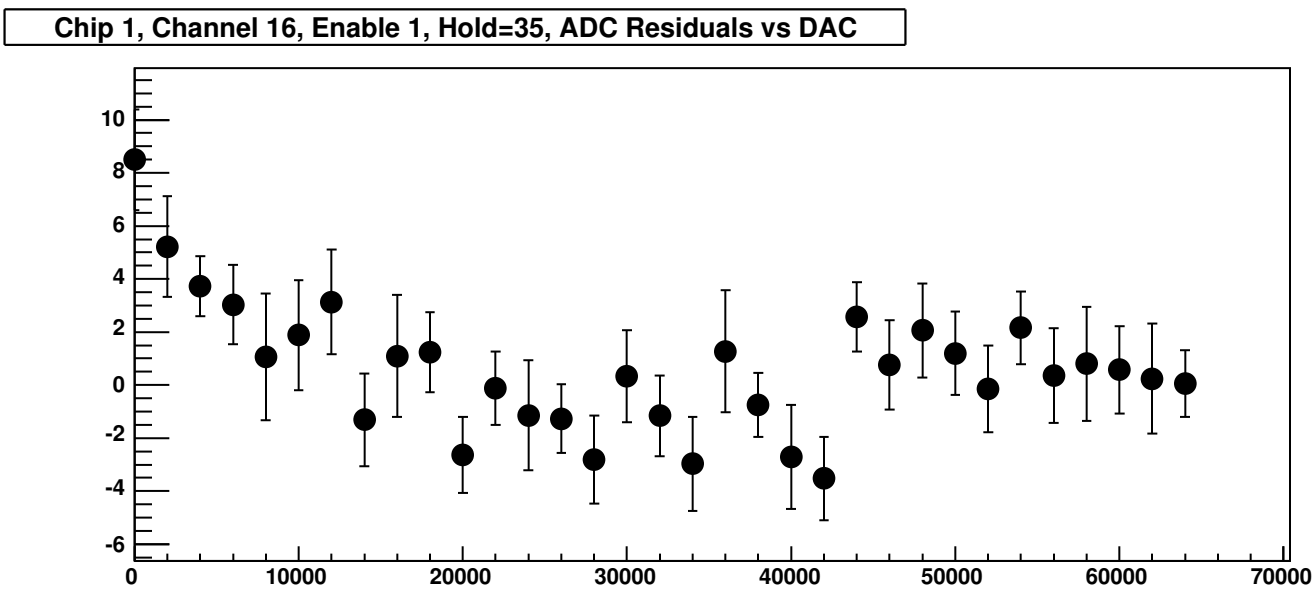

Chip 1, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 25.31 / 23
p0 38.1 ± 0.7494
p1 0.002891 ± 2.291e-05
240
220
200
180
160
140
120
100
80
60
40
0
10000
20000
30000
40000
50000
60000
70000

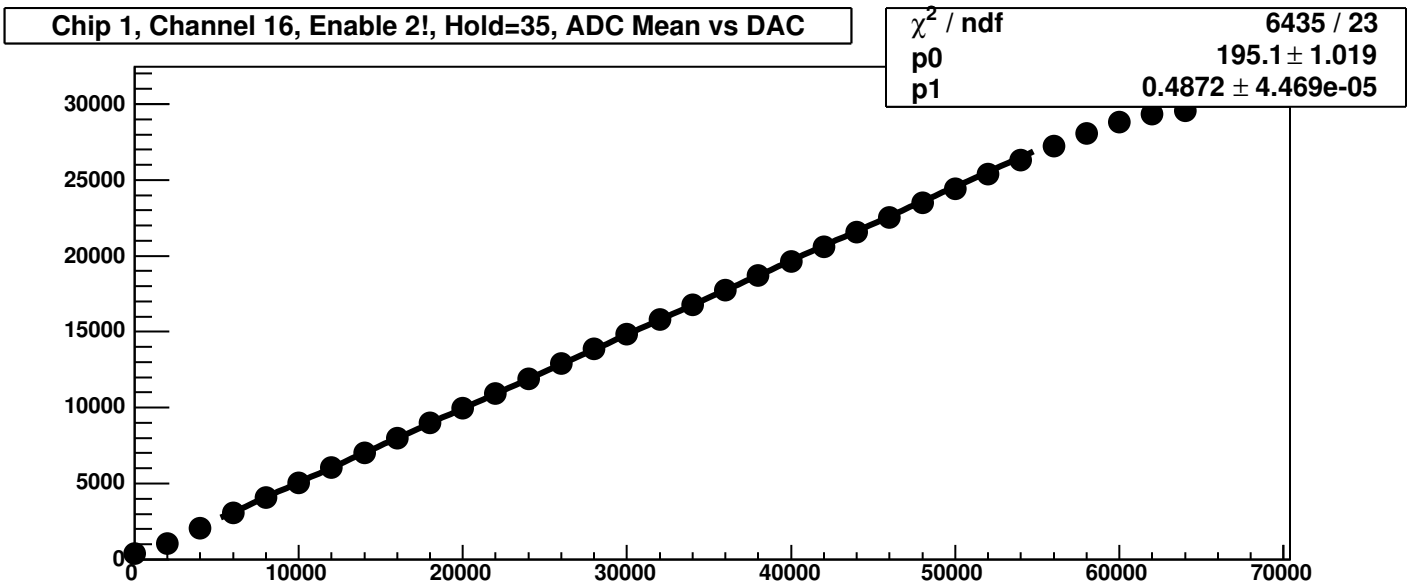

Chip 1, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

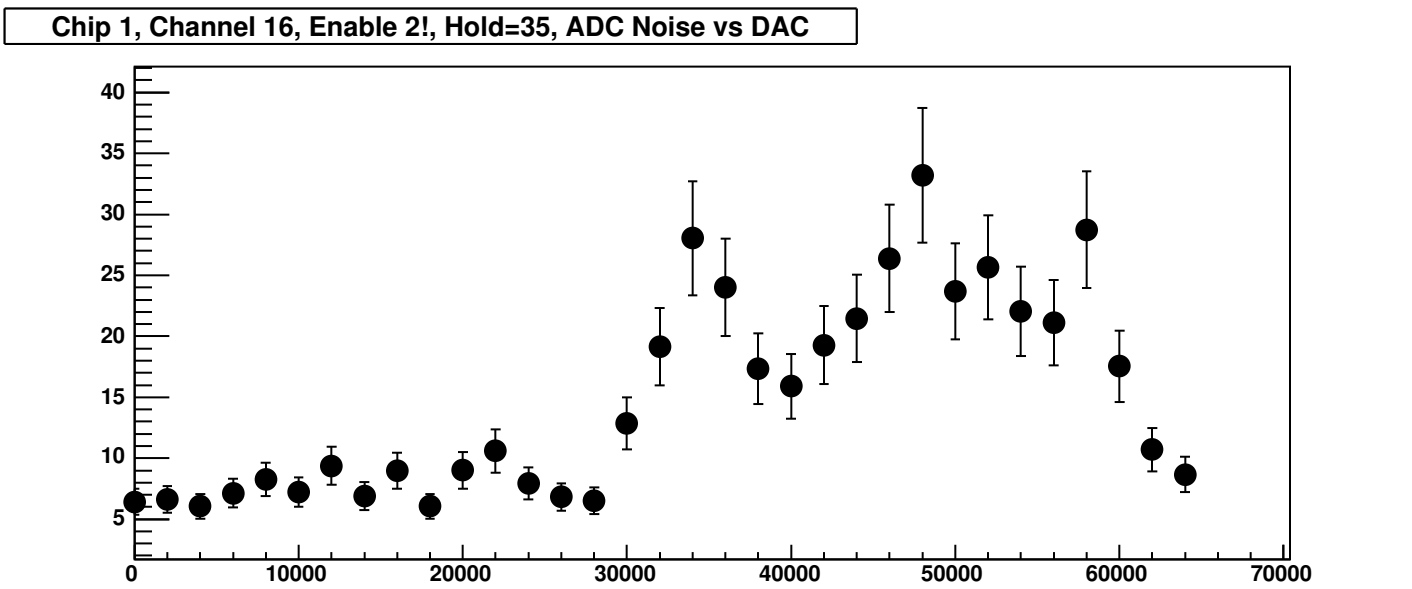

Chip 1, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

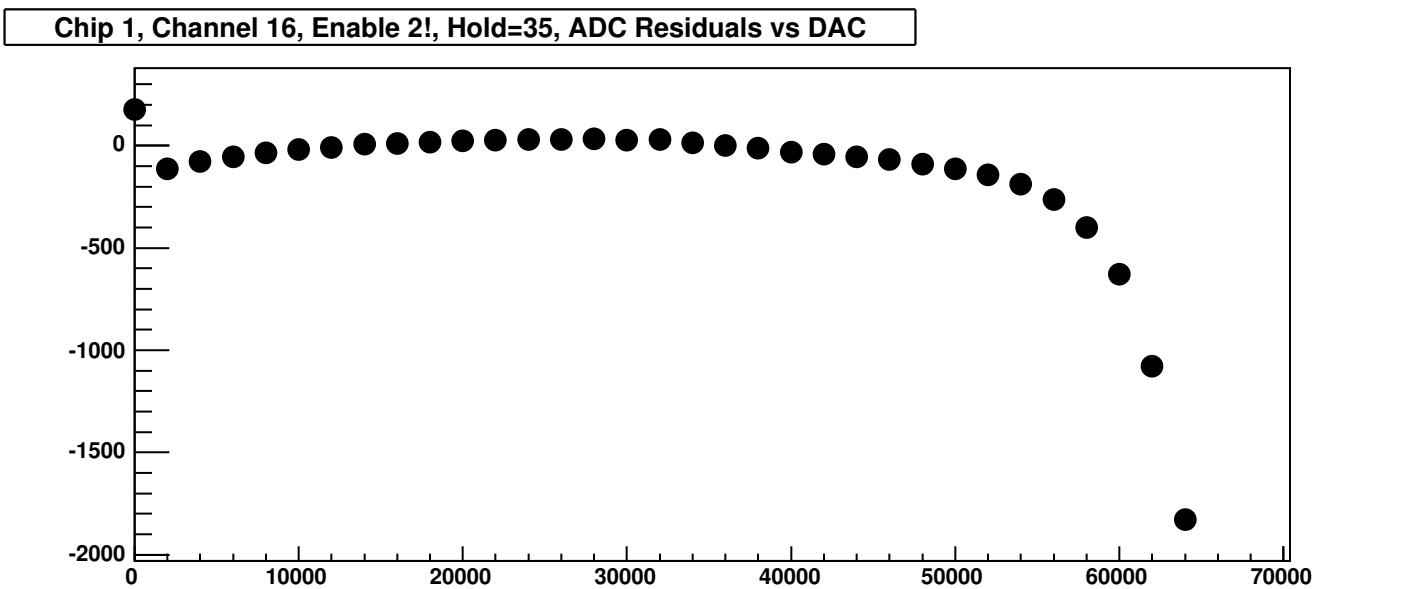

Chip 1, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 36.15 / 23
p0 38.71 ± 0.7961
p1 0.00293 ± 2.304e-05
240
220
200
180
160
140
120
100
80
60
40
0
10000
20000
30000
40000
50000
60000
70000

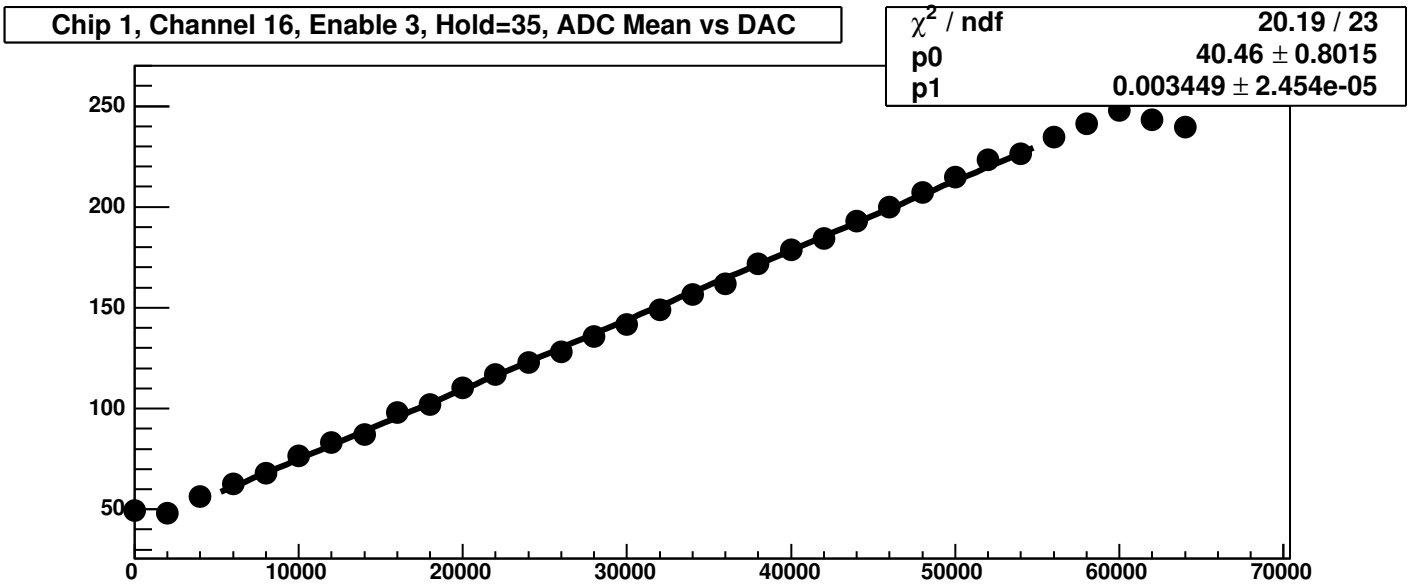

Chip 1, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

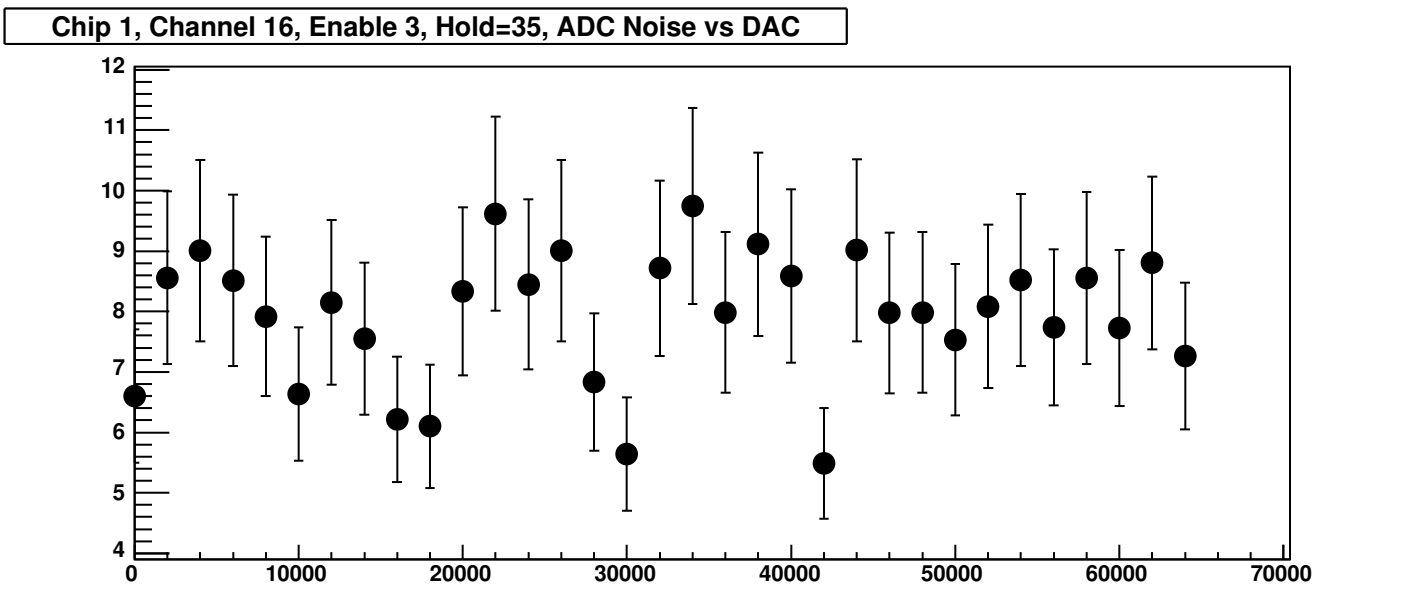

Chip 1, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

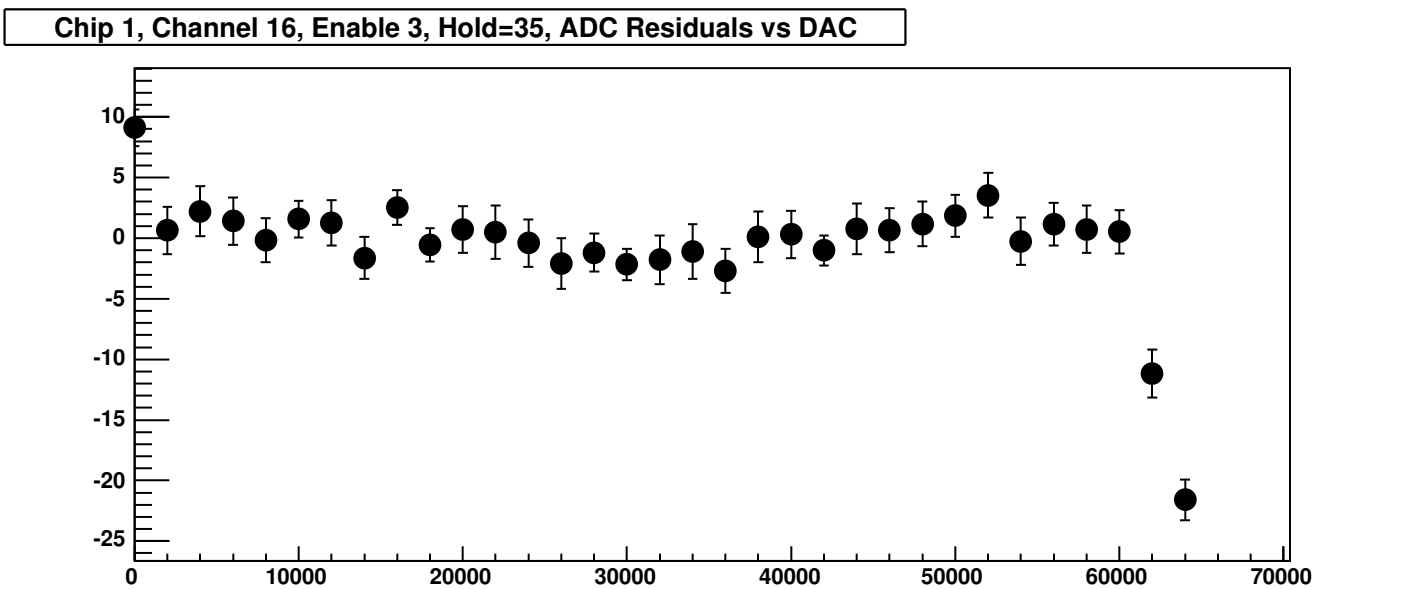

Chip 1, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 6435 / 23
p0 195.1 ± 1.019
p1 0.4872 ± 4.469e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

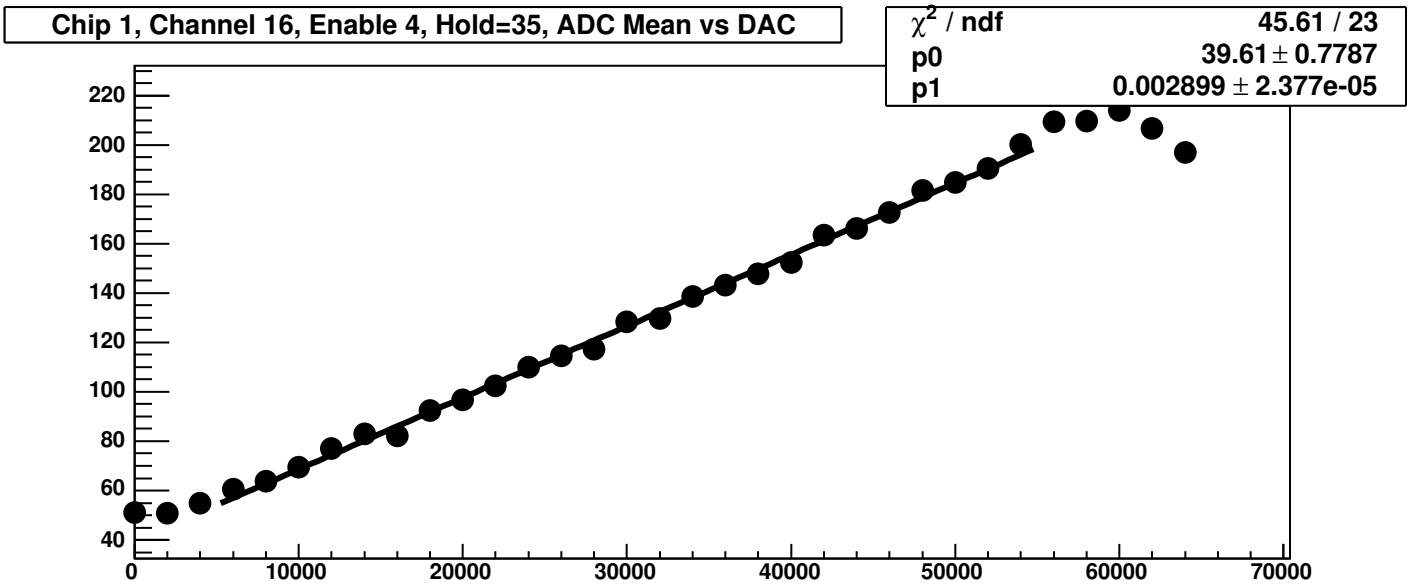

Chip 1, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000

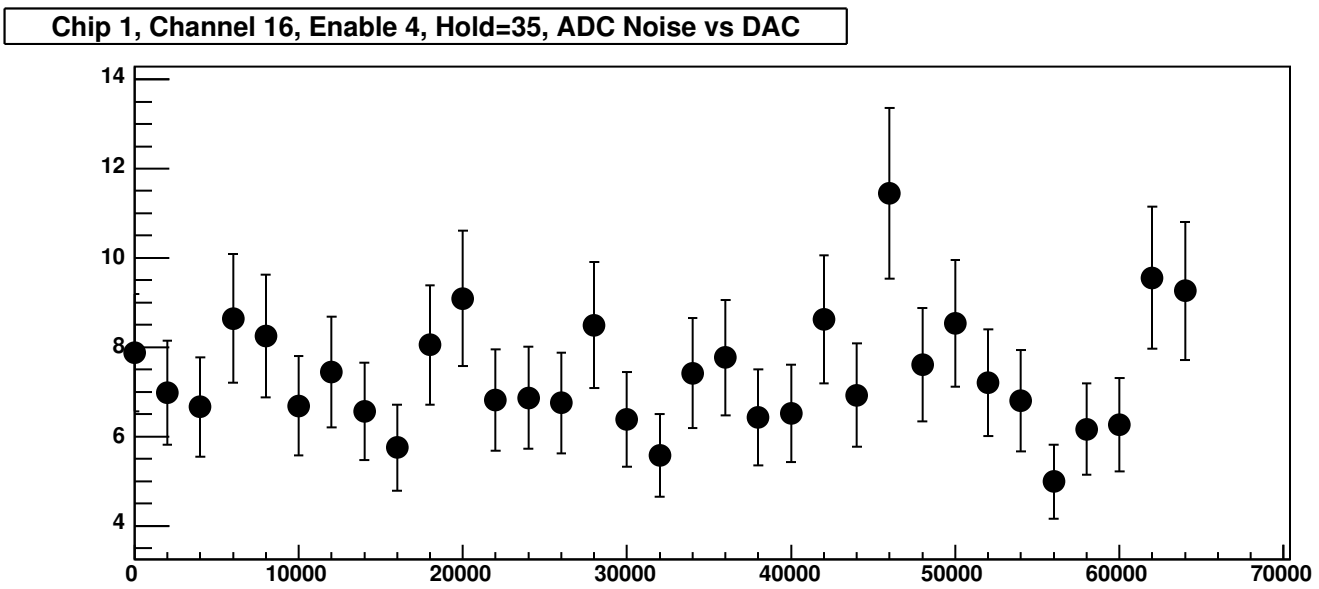

Chip 1, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000

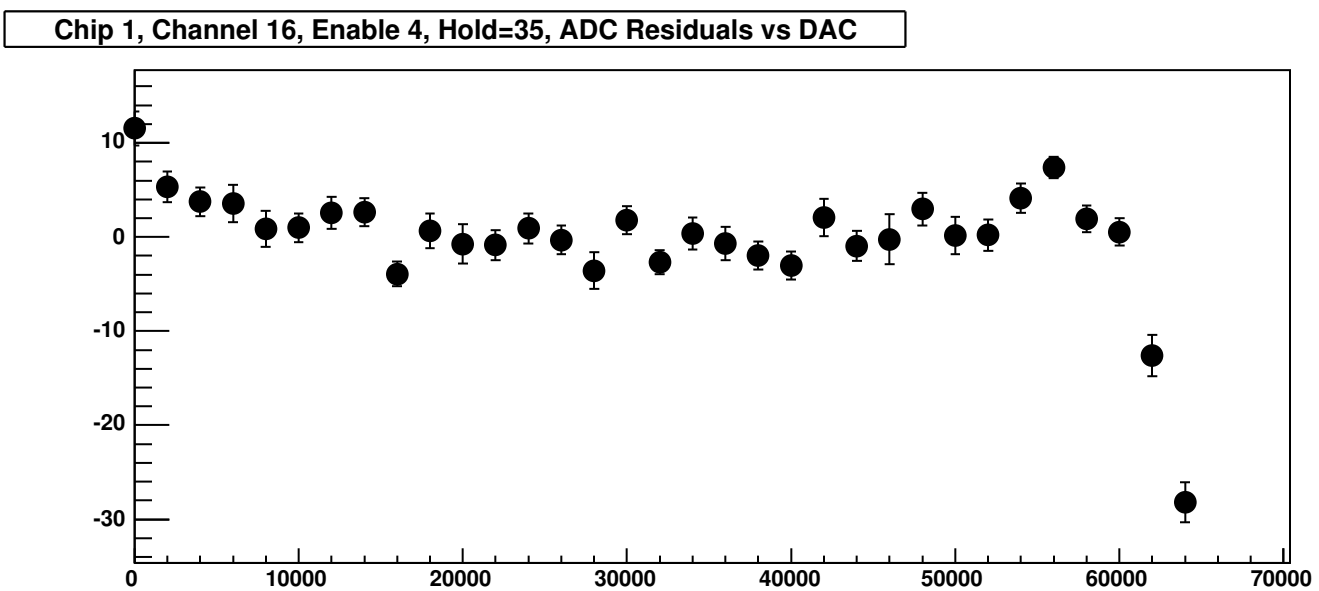

Chip 1, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 20.19 / 23
p0 40.46 ± 0.8015
p1 0.003449 ± 2.454e-05
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

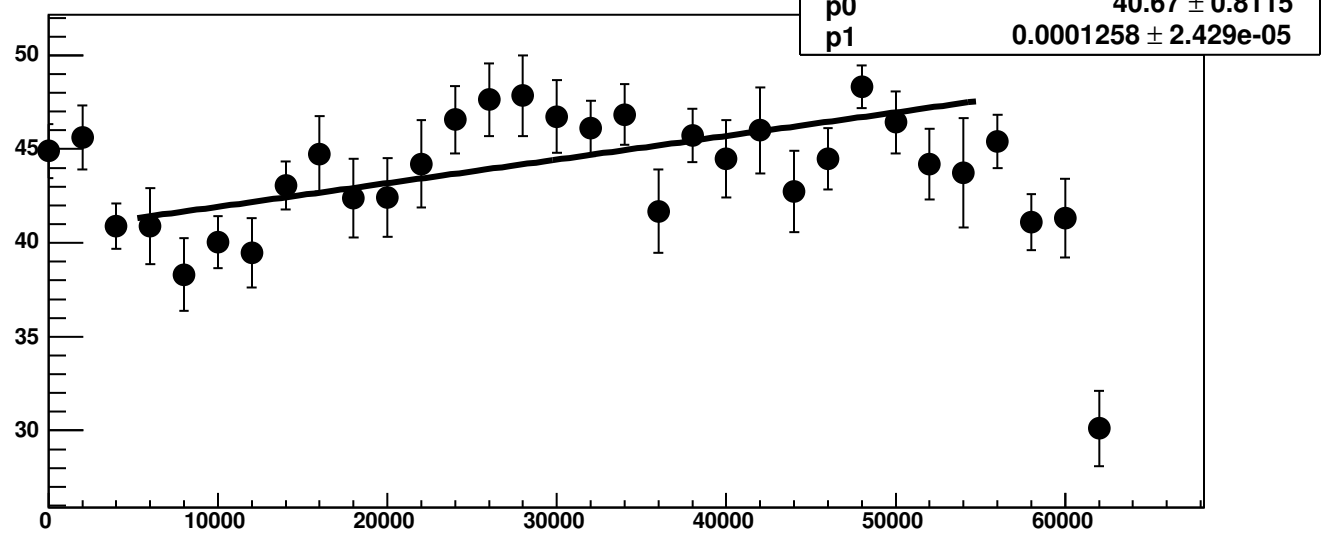

Chip 1, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 45.61 / 23
p0 39.61 ± 0.7787
p1 0.002899 ± 2.377e-05
220
200
180
160
140
120
100
80
60
40
0
10000
20000
30000
40000
50000
60000
70000

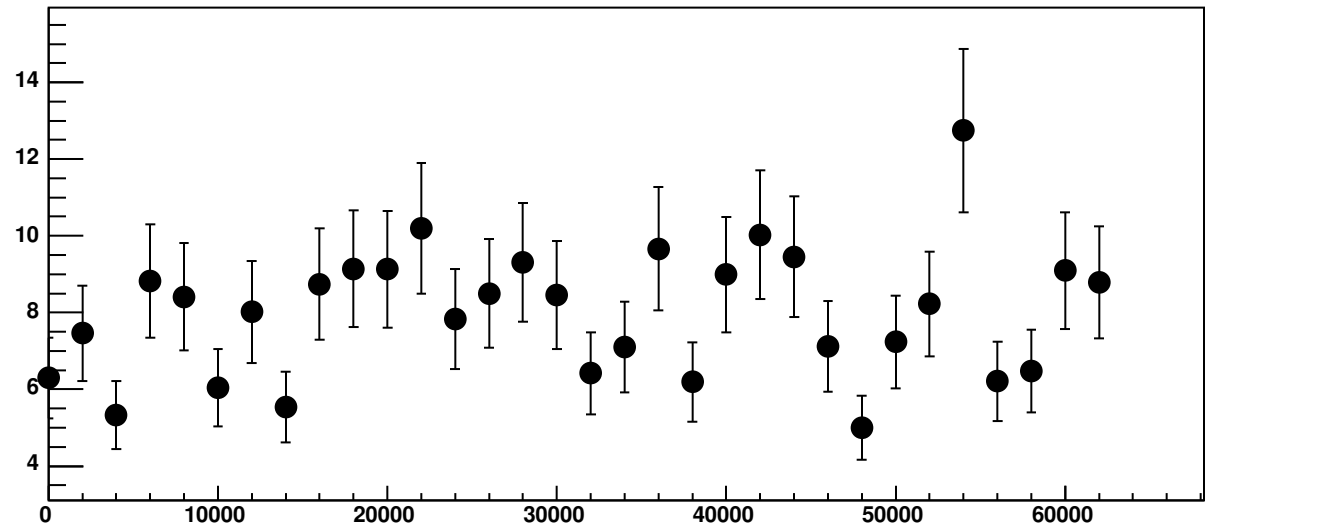

Chip 1, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
10000
20000
30000
40000
50000
60000
70000

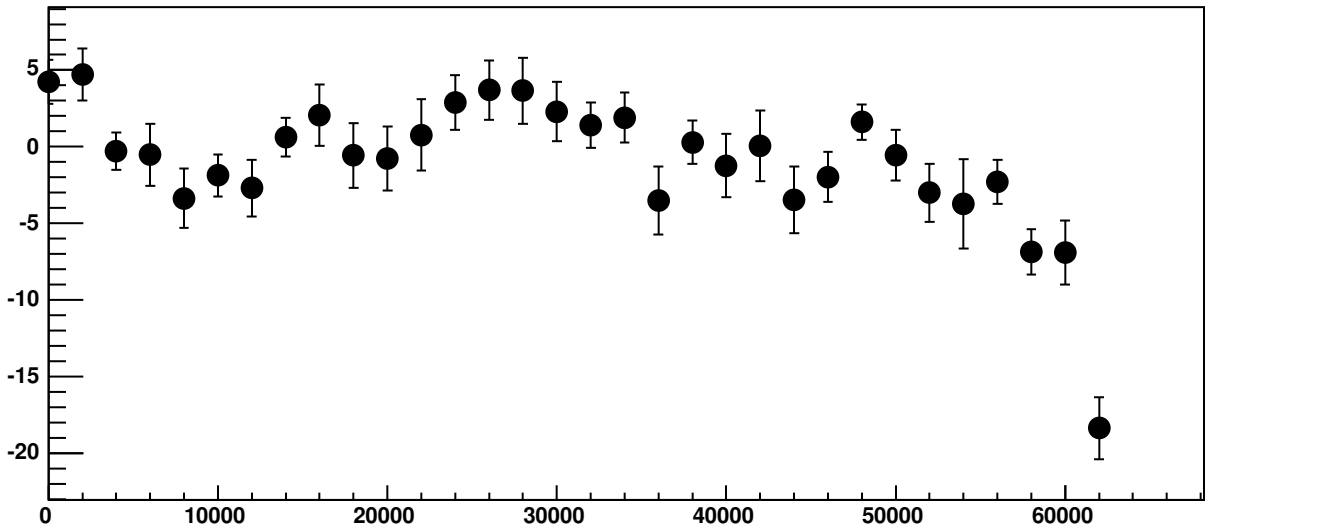
Chip 1, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC

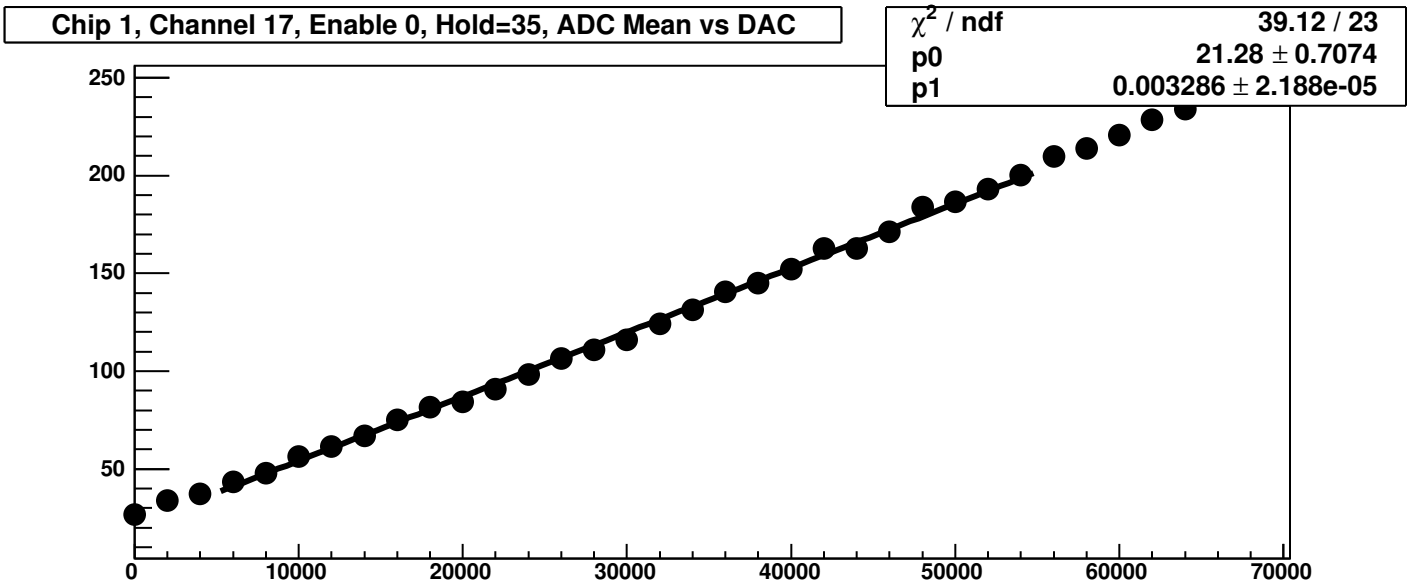


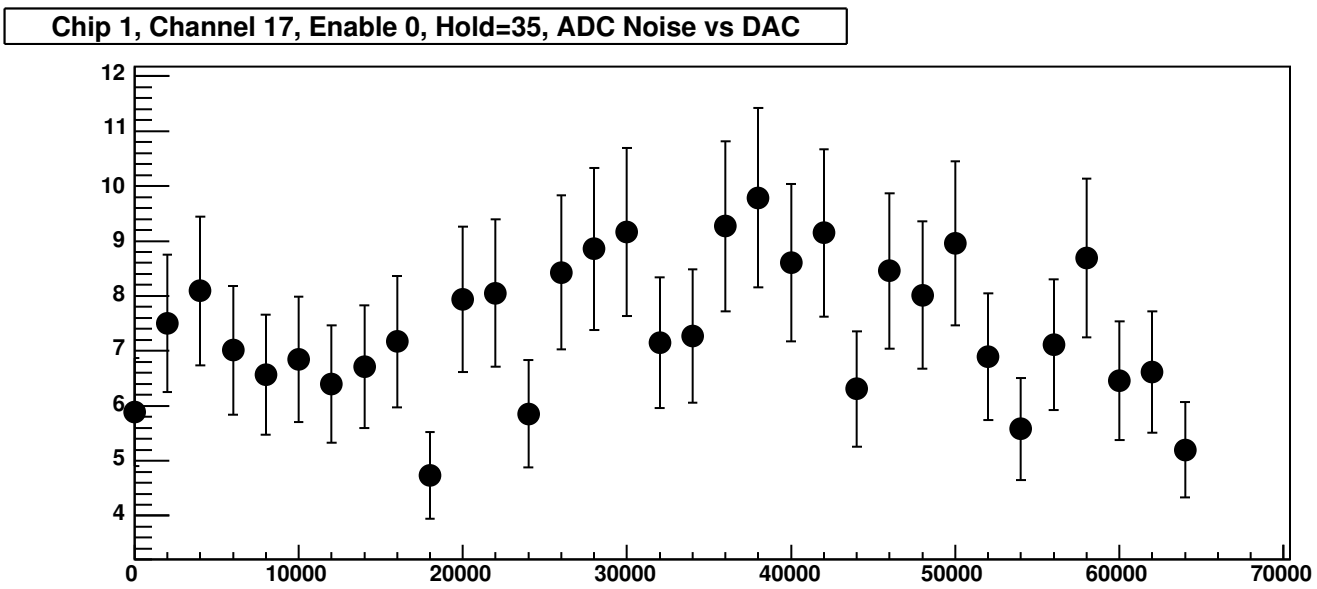
Chip 1, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC

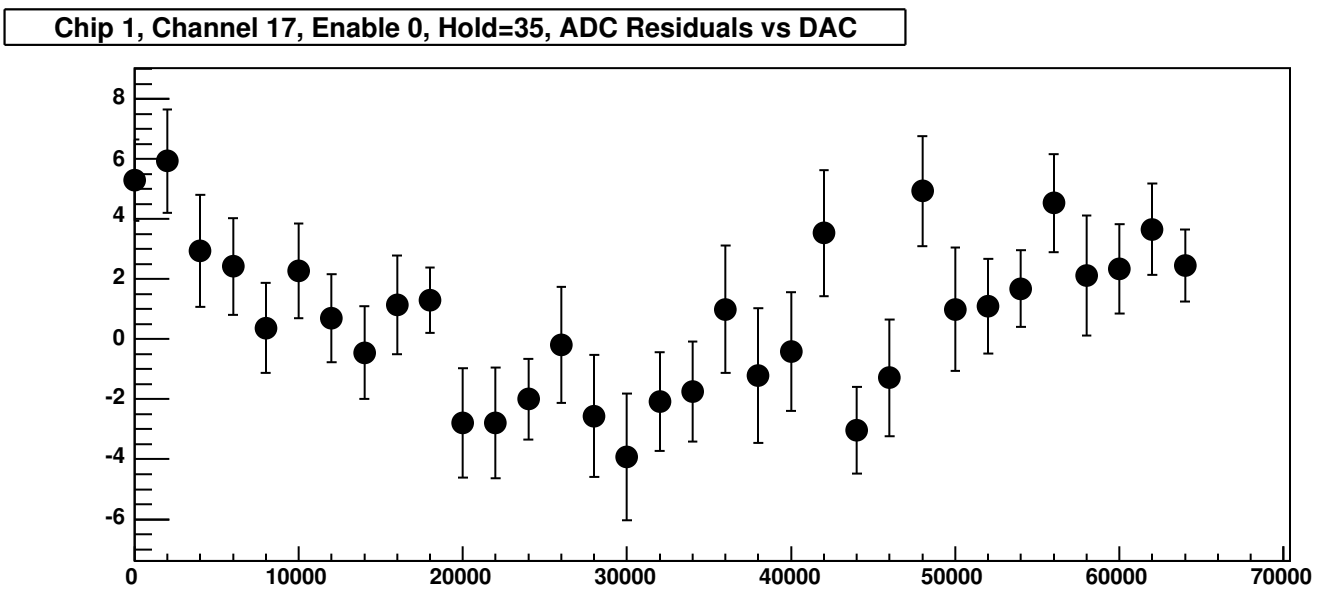


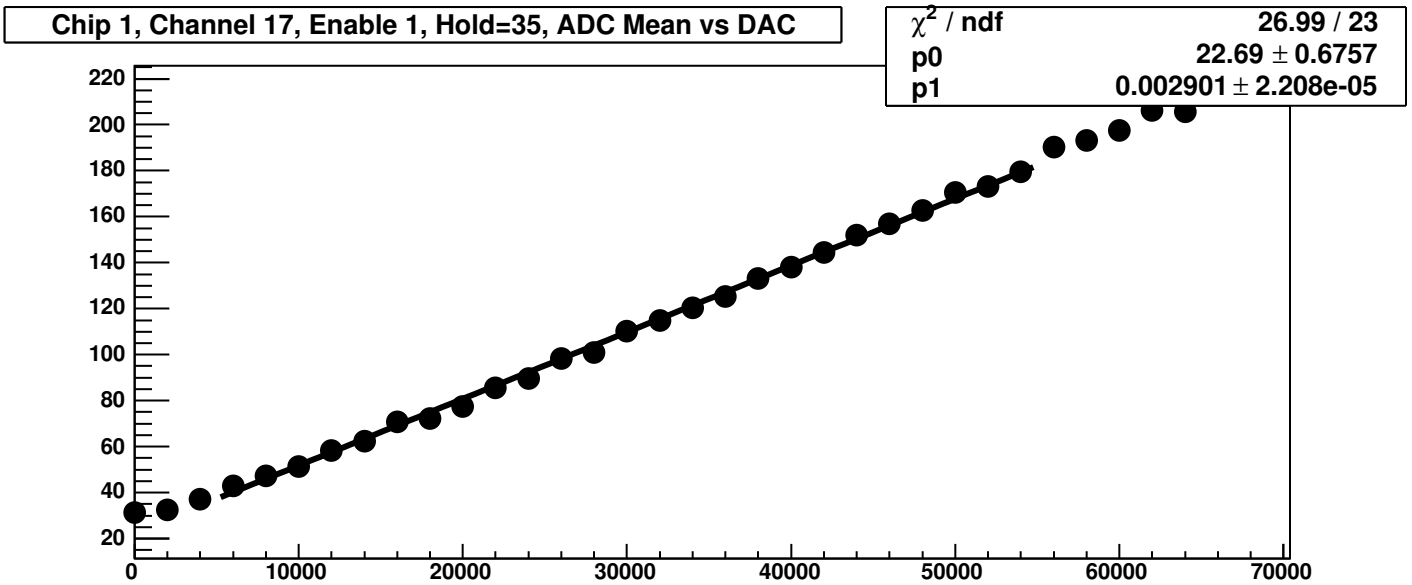
Chip 1, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC

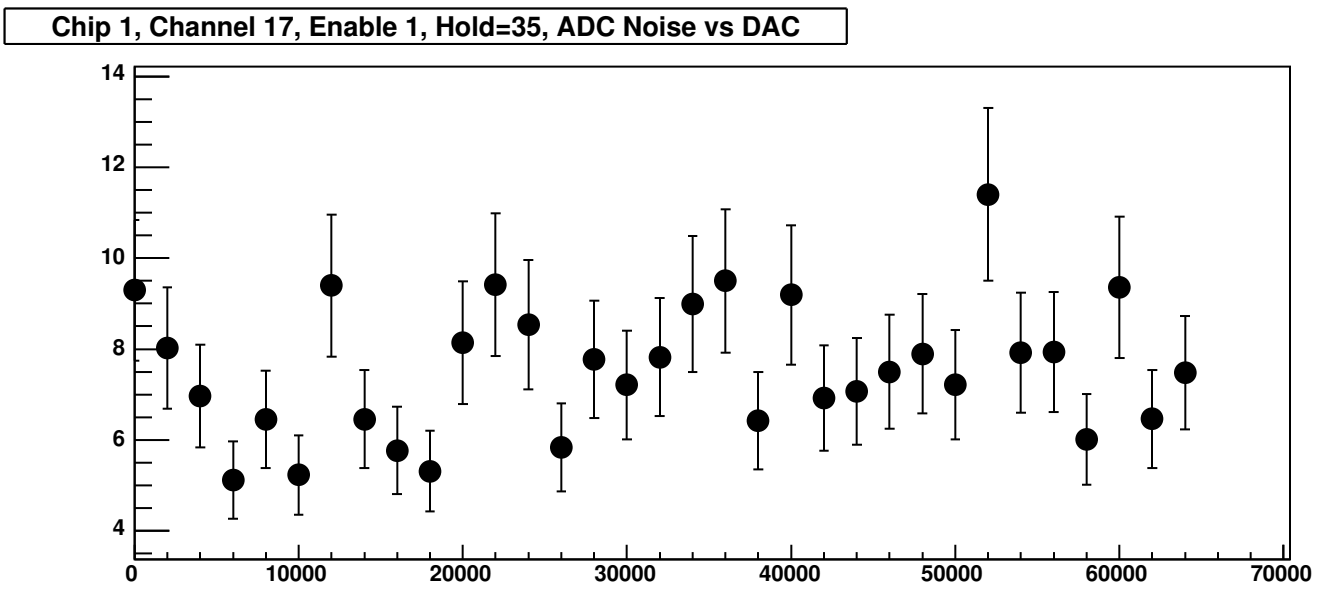


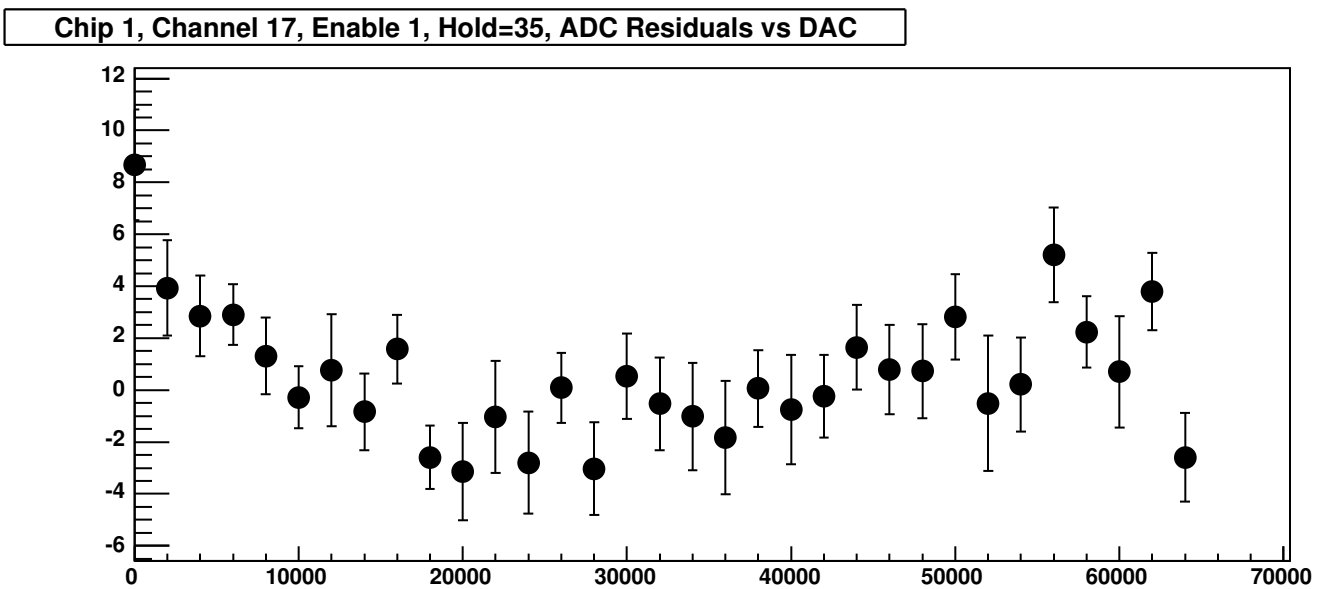

Chip 1, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 39.12 / 23
p0 21.28 ± 0.7074
p1 0.003286 ± 2.188e-05
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

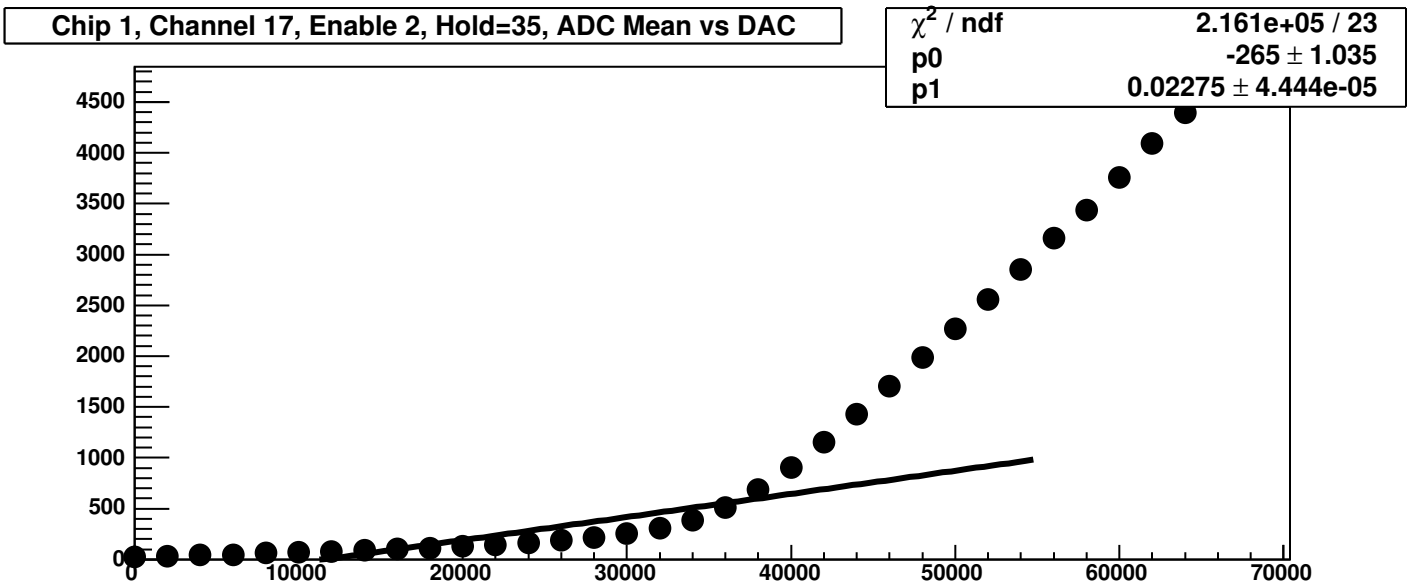

Chip 1, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

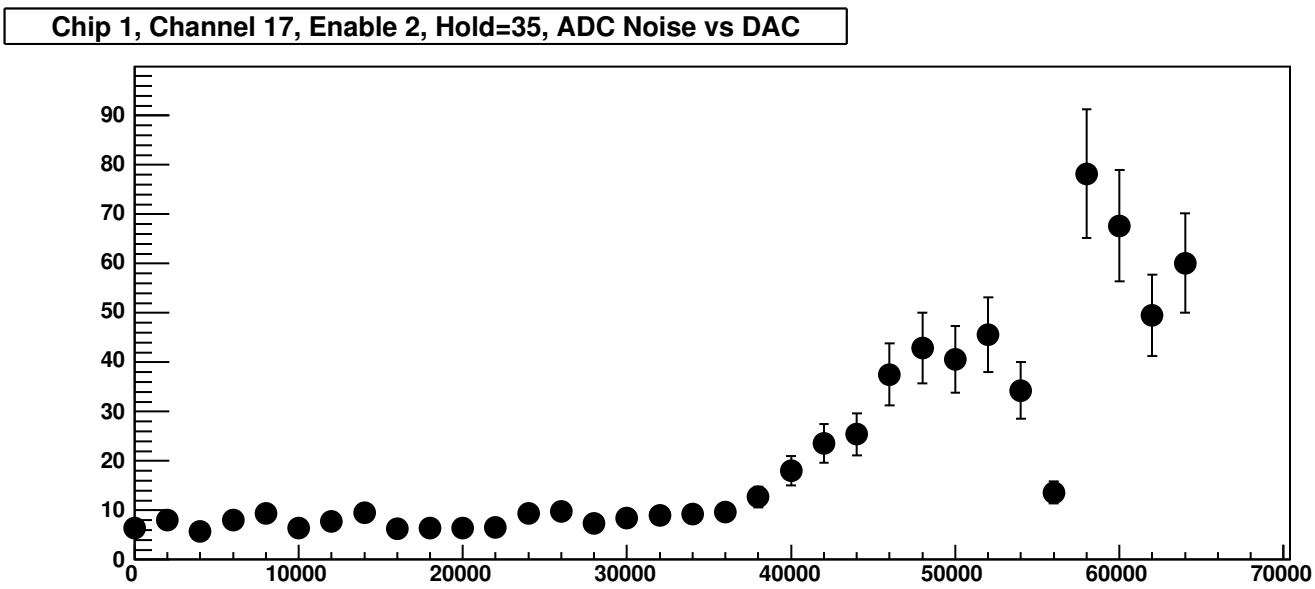

Chip 1, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

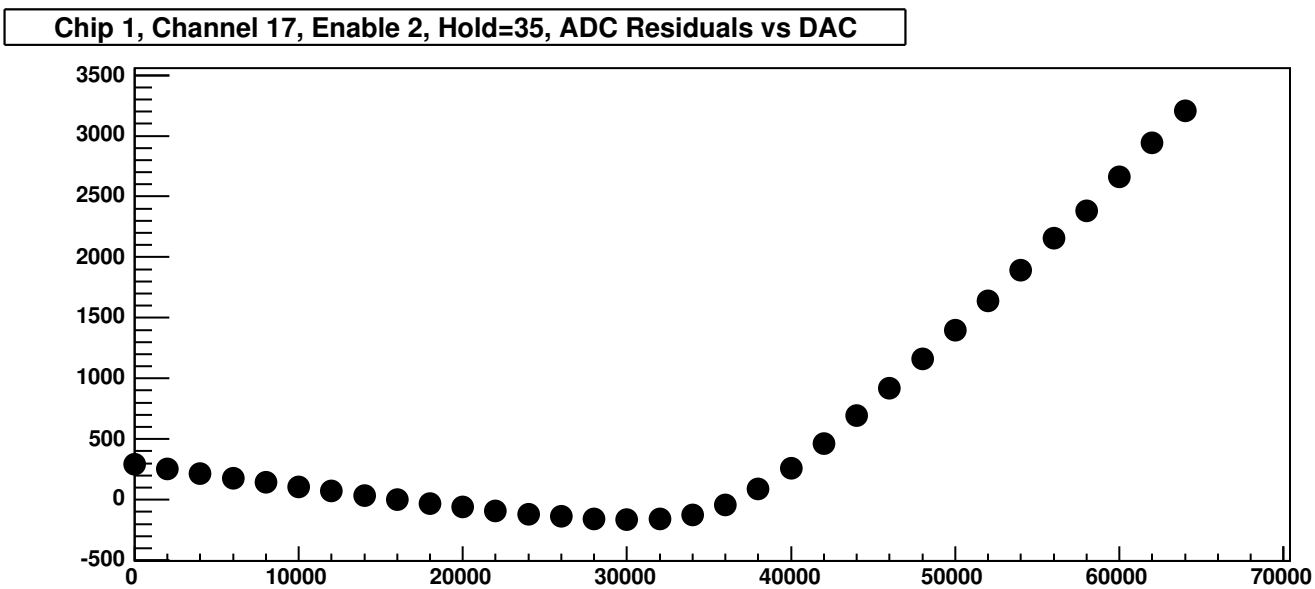

Chip 1, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 26.99 / 23
p0 22.69 ± 0.6757
p1 0.002901 ± 2.208e-05
220
200
180
160
140
120
100
80
60
40
20
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

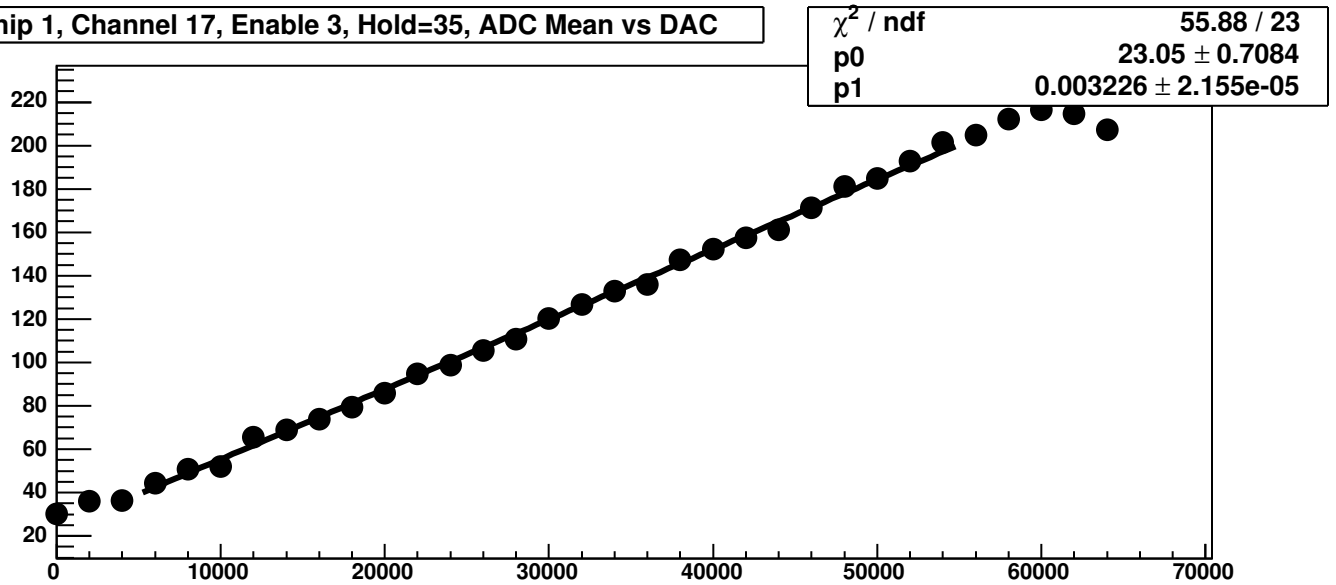

Chip 1, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 2.161e+05 / 23
p0 -265 ± 1.035
p1 0.02275 ± 4.444e-05
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
10000
20000
30000
40000
50000
60000
70000

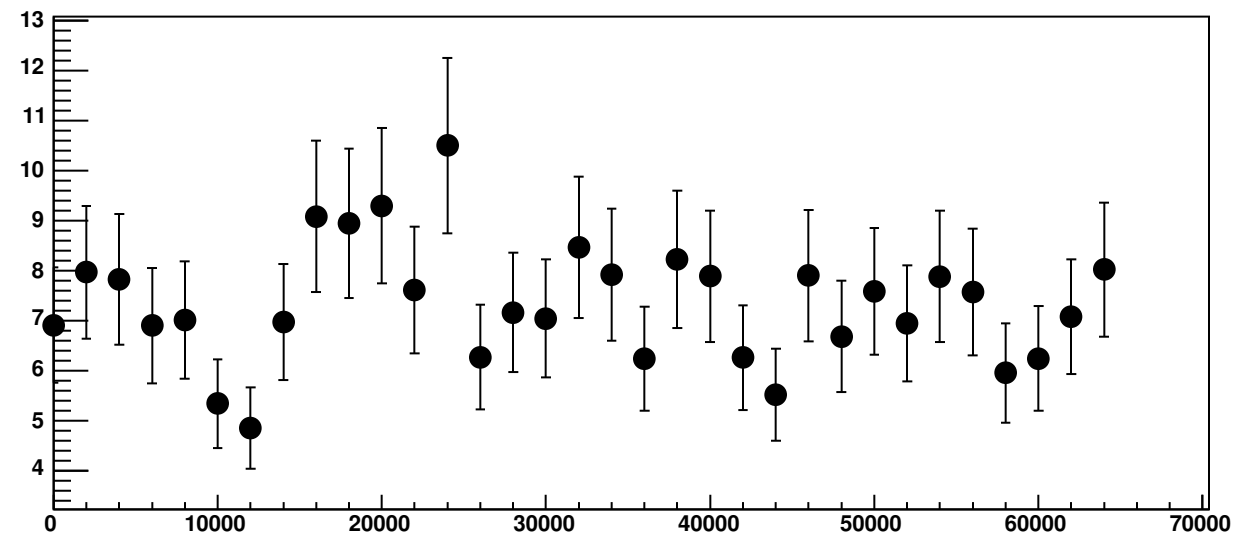

Chip 1, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 1, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
3500
3000
2500
2000
1500
1000
500
0
-500
0
10000
20000
30000
40000
50000
60000
70000

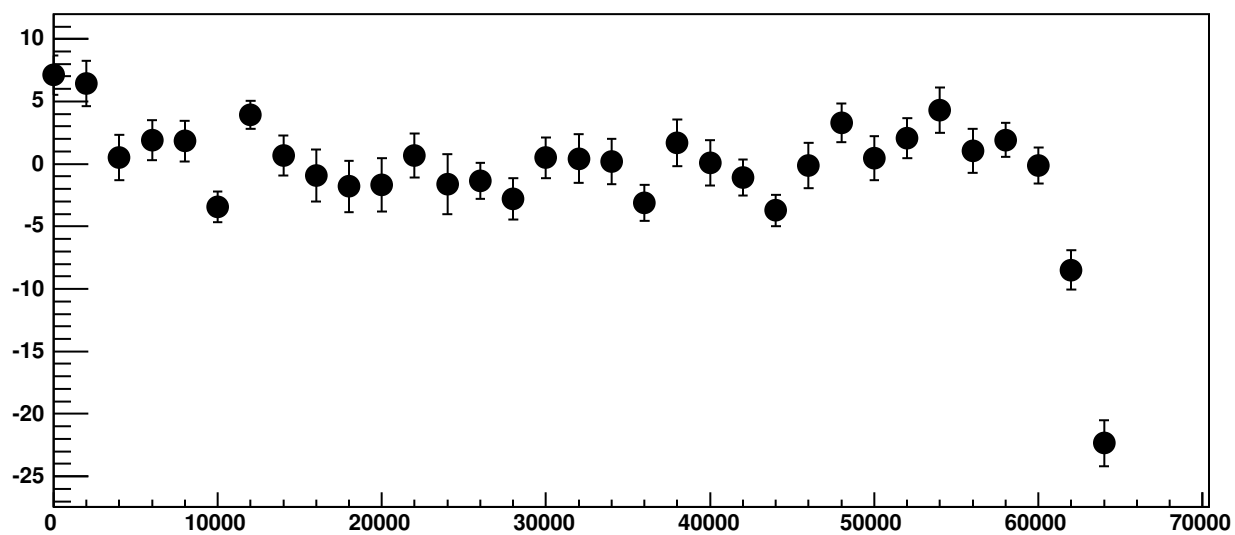
Chip 1, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC

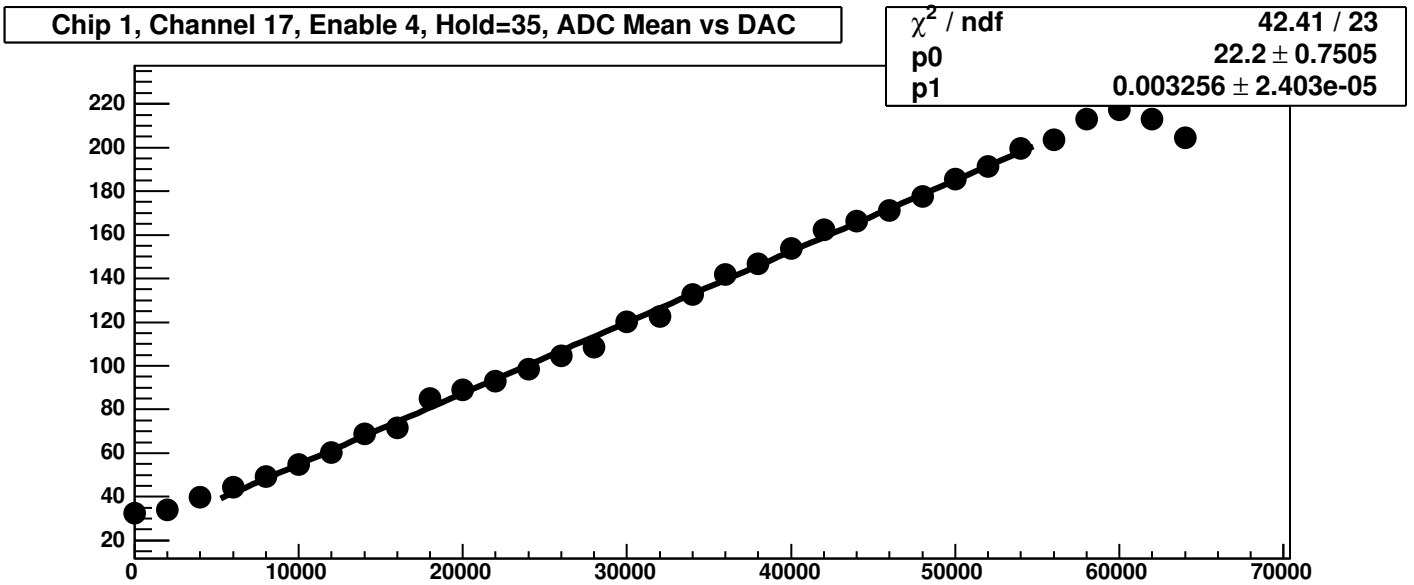


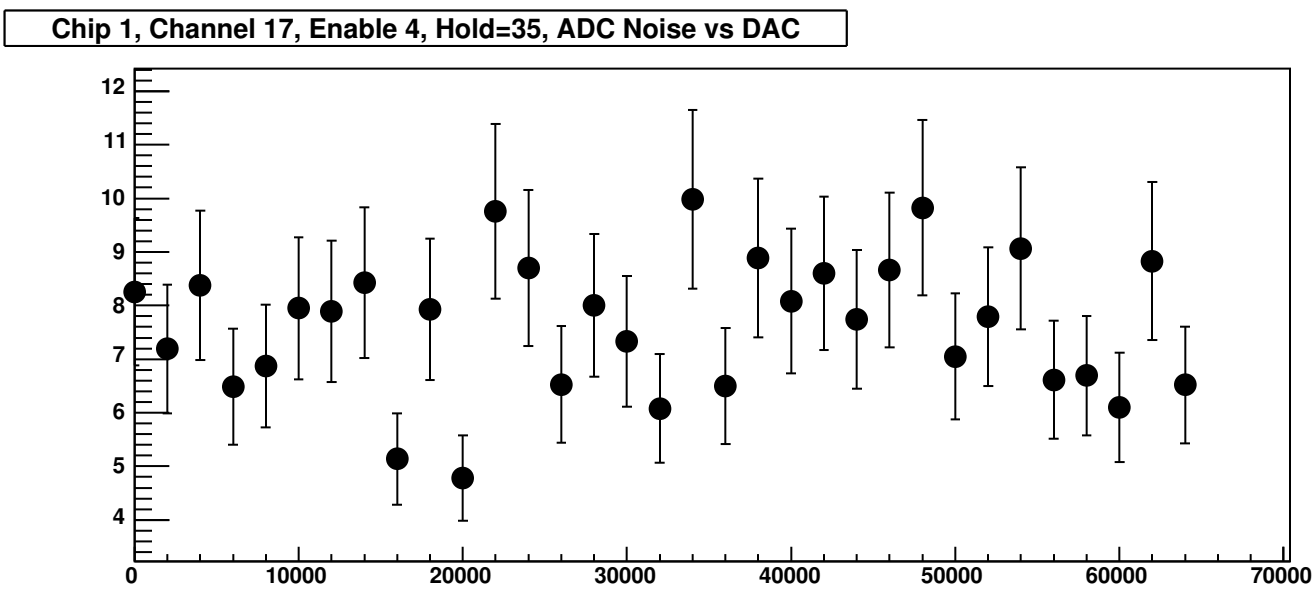
Chip 1, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC

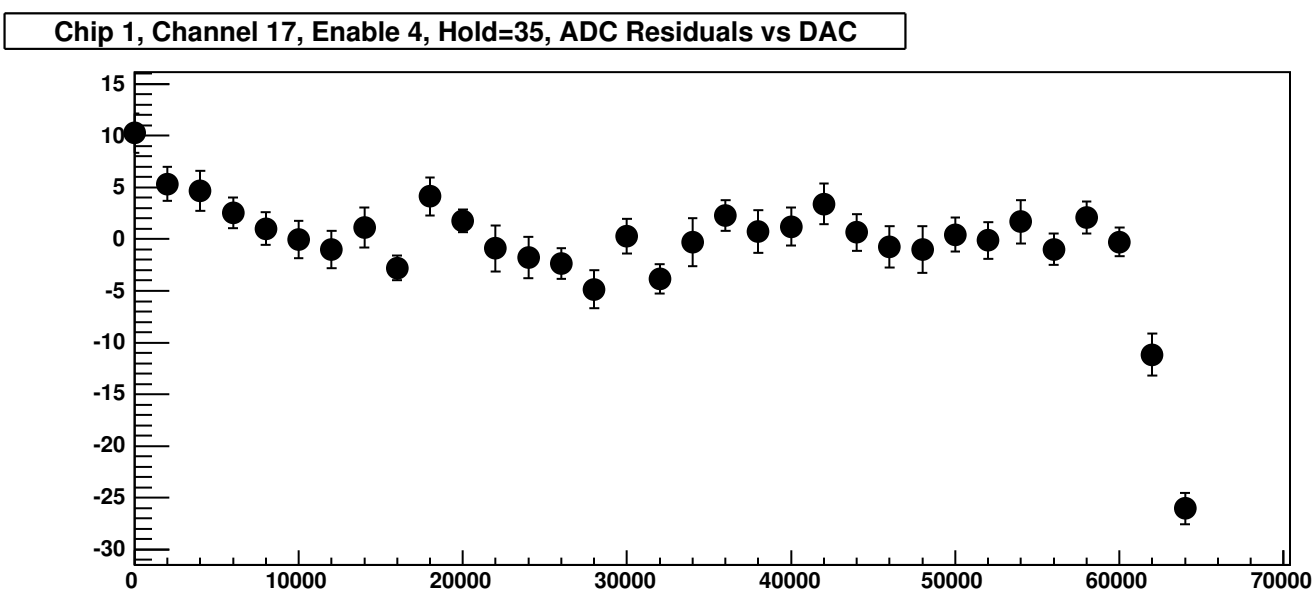


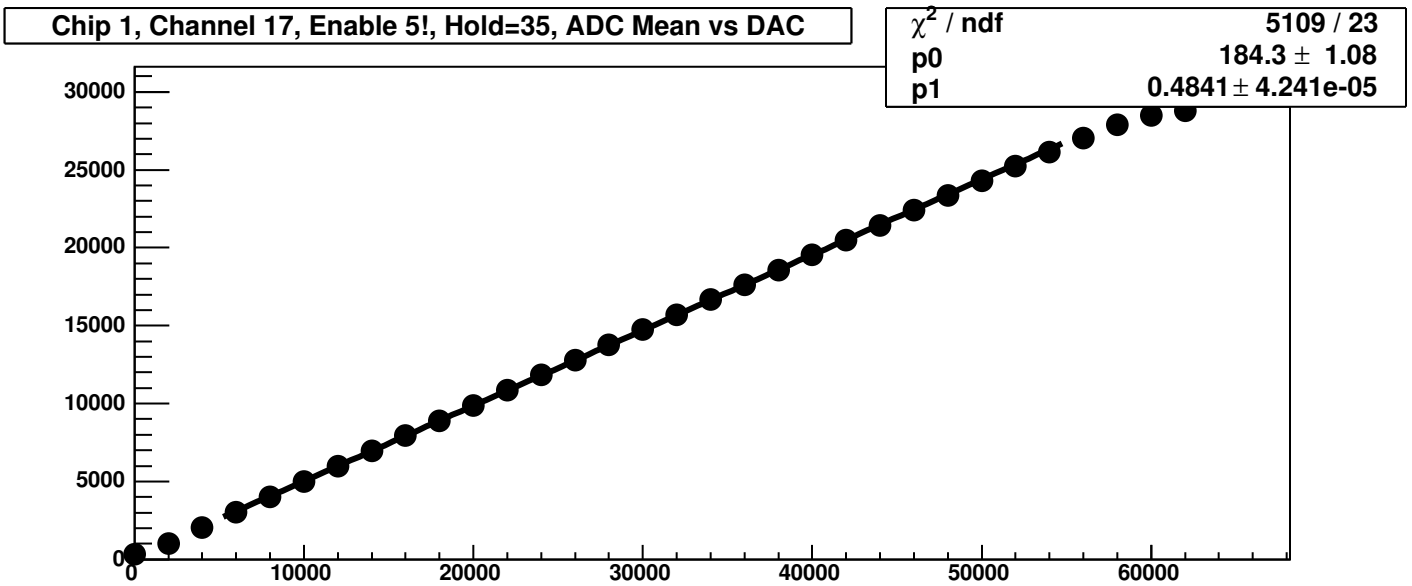
Chip 1, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC

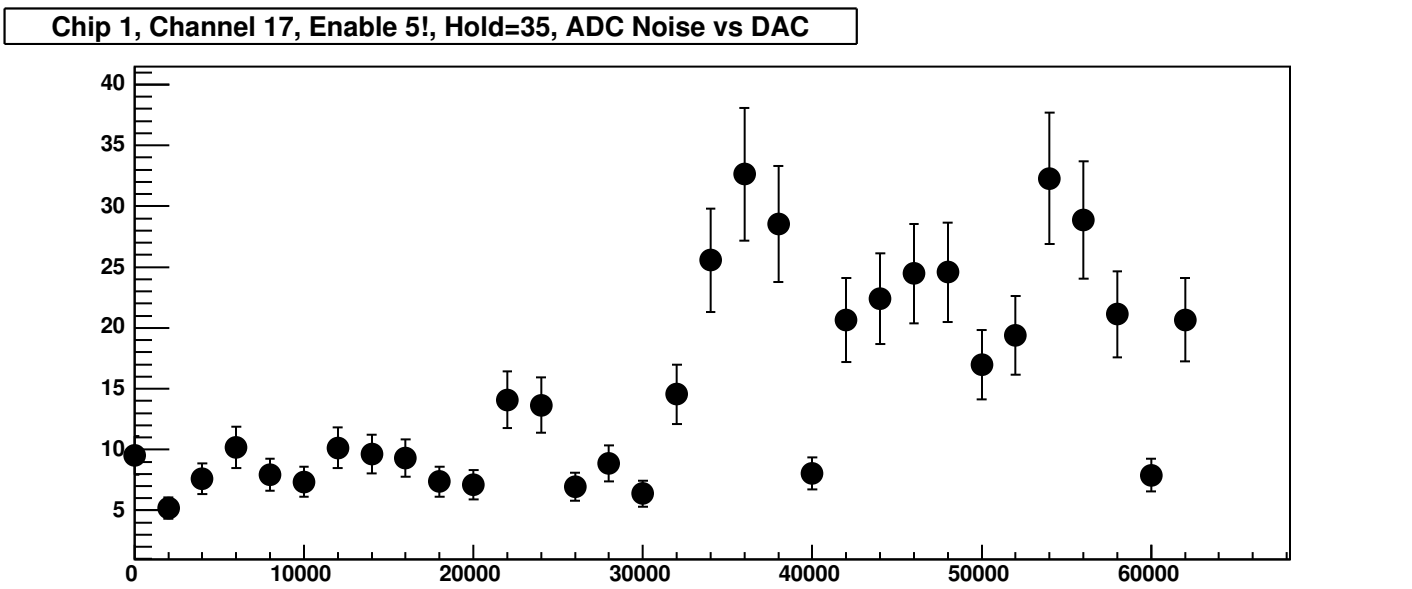


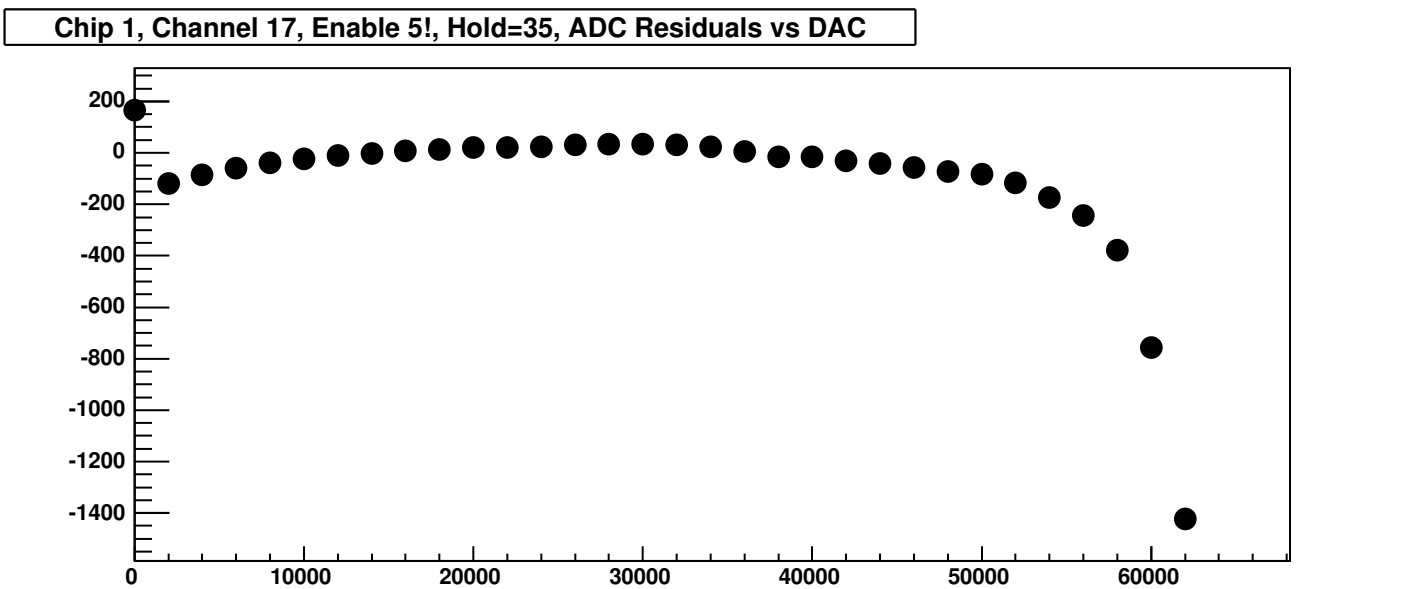

Chip 1, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 42.41 / 23
p0 22.2 ± 0.7505
p1 0.003256 ± 2.403e-05
220
200
180
160
140
120
100
80
60
40
20
0
10000
20000
30000
40000
50000
60000
70000

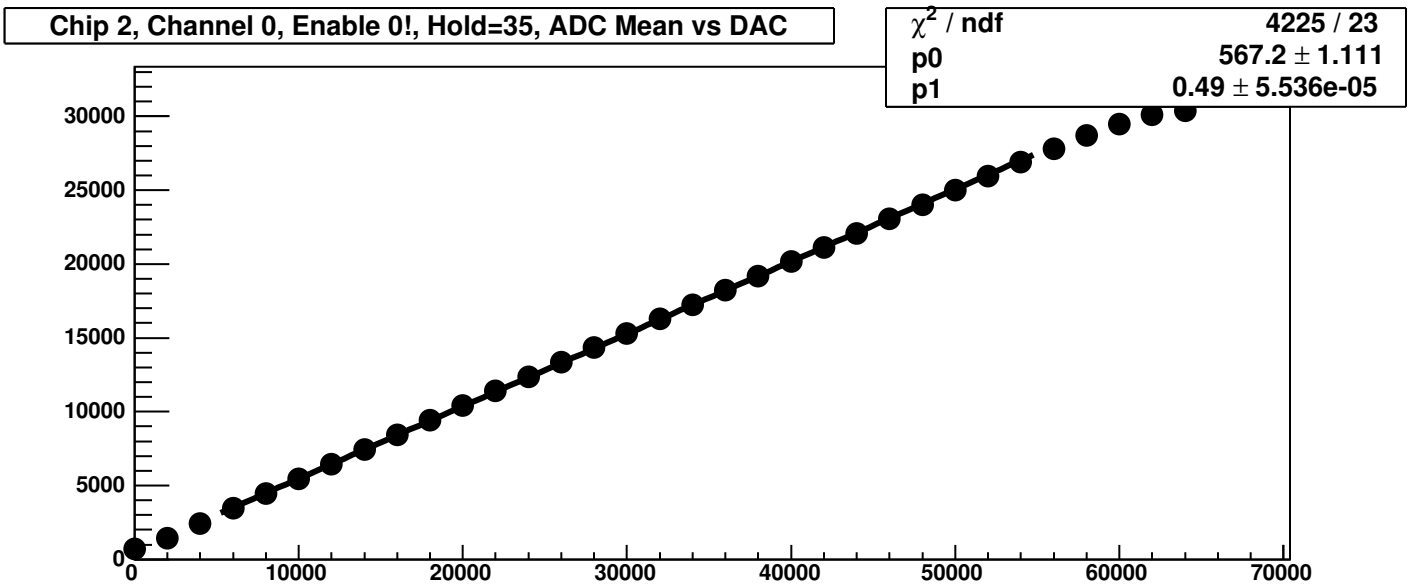

Chip 1, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

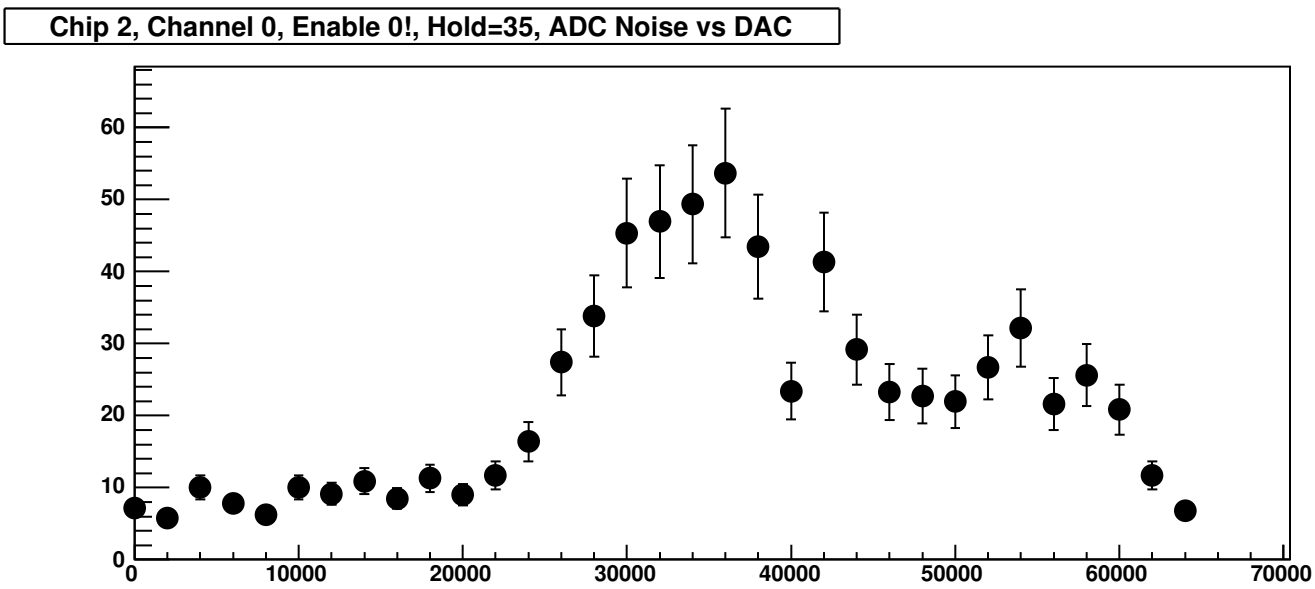

Chip 1, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
-25
-30
0
10000
20000
30000
40000
50000
60000
70000

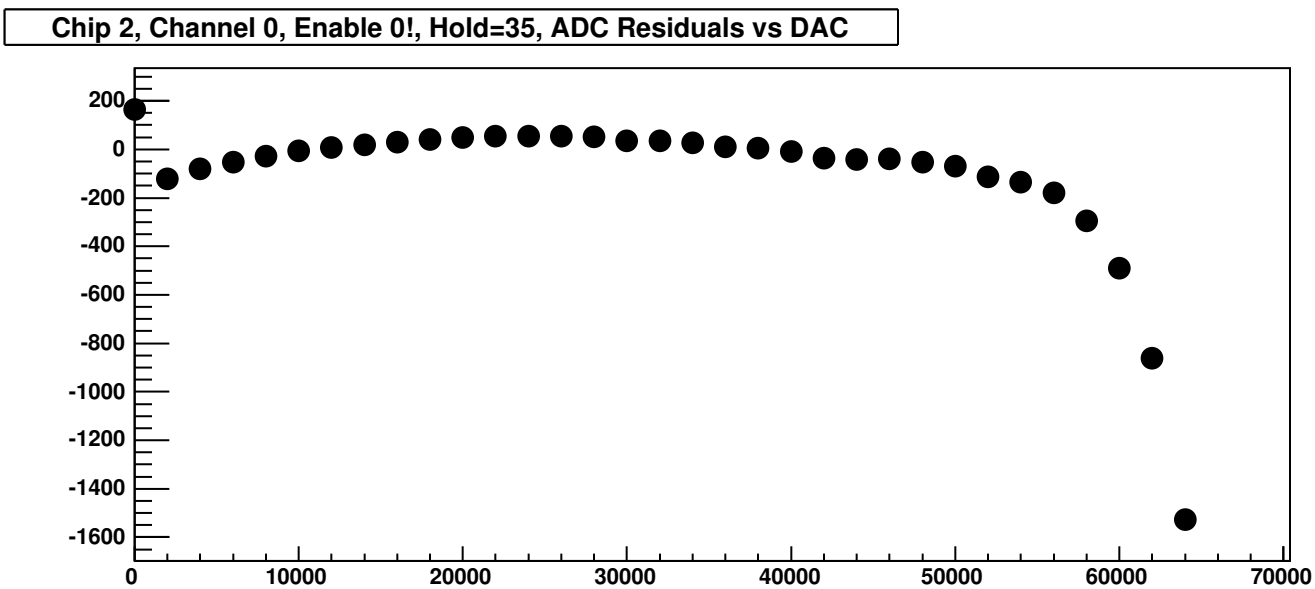

Chip 1, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 5109 / 23
p0 184.3 ± 1.08
p1 0.4841 ± 4.241e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

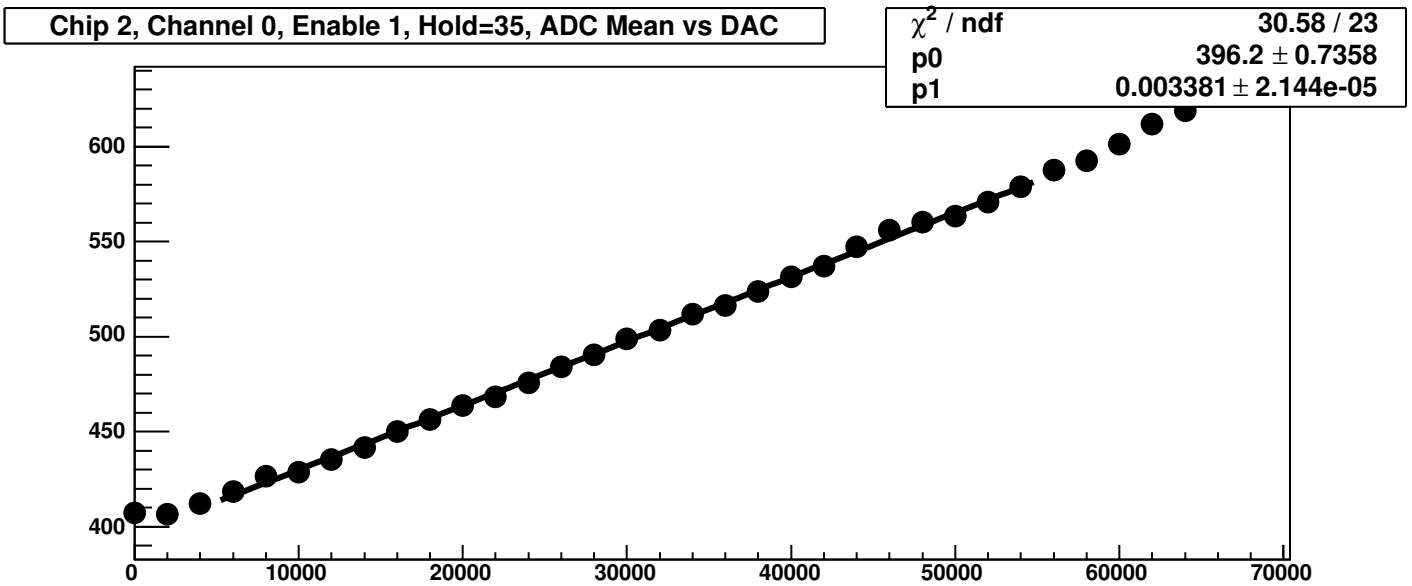

Chip 1, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000

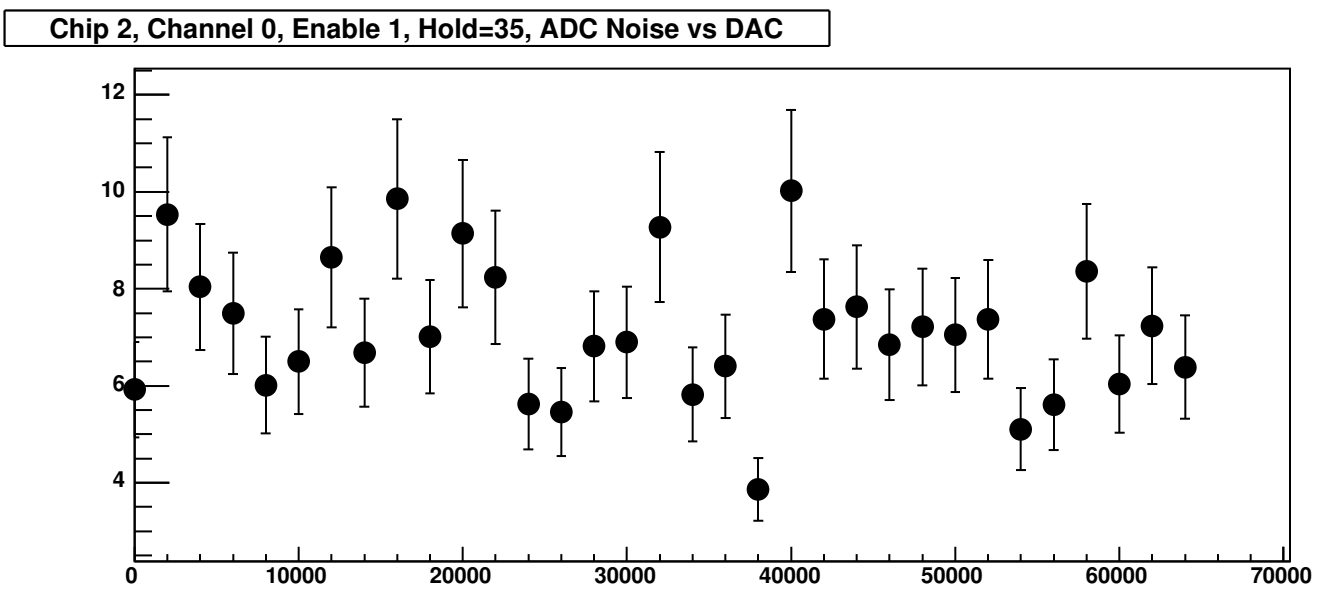

Chip 1, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
10000
20000
30000
40000
50000
60000

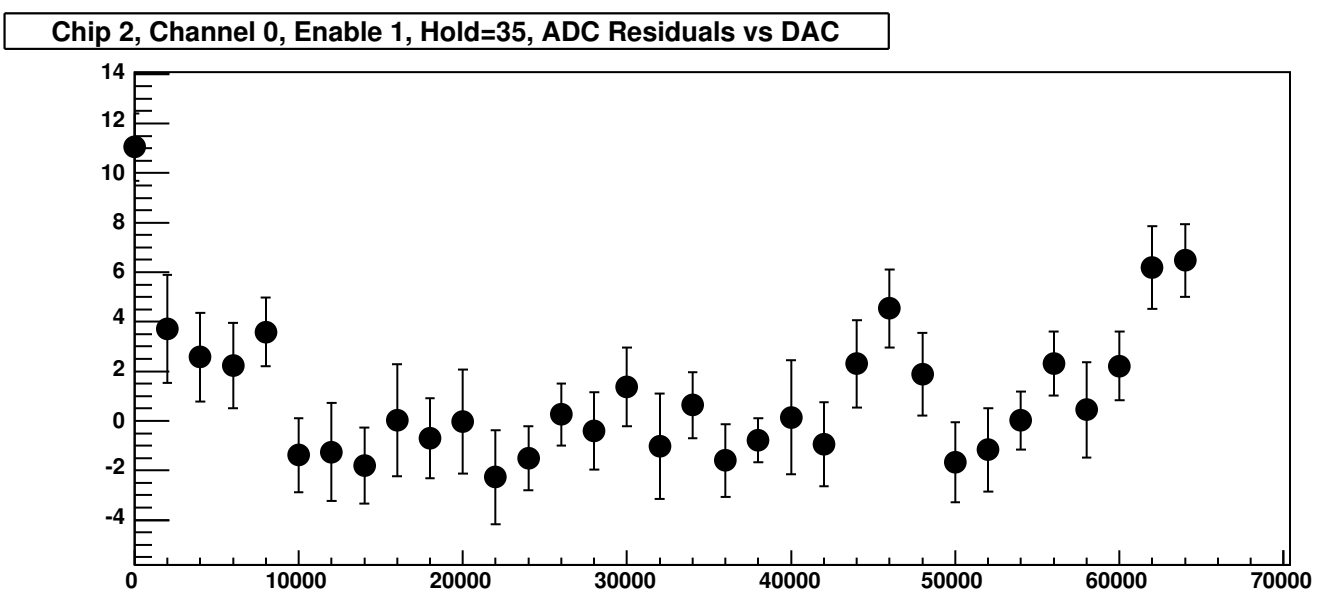

Chip 2, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 4225 / 23
p0 567.2 ± 1.111
p1 0.49 ± 5.536e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

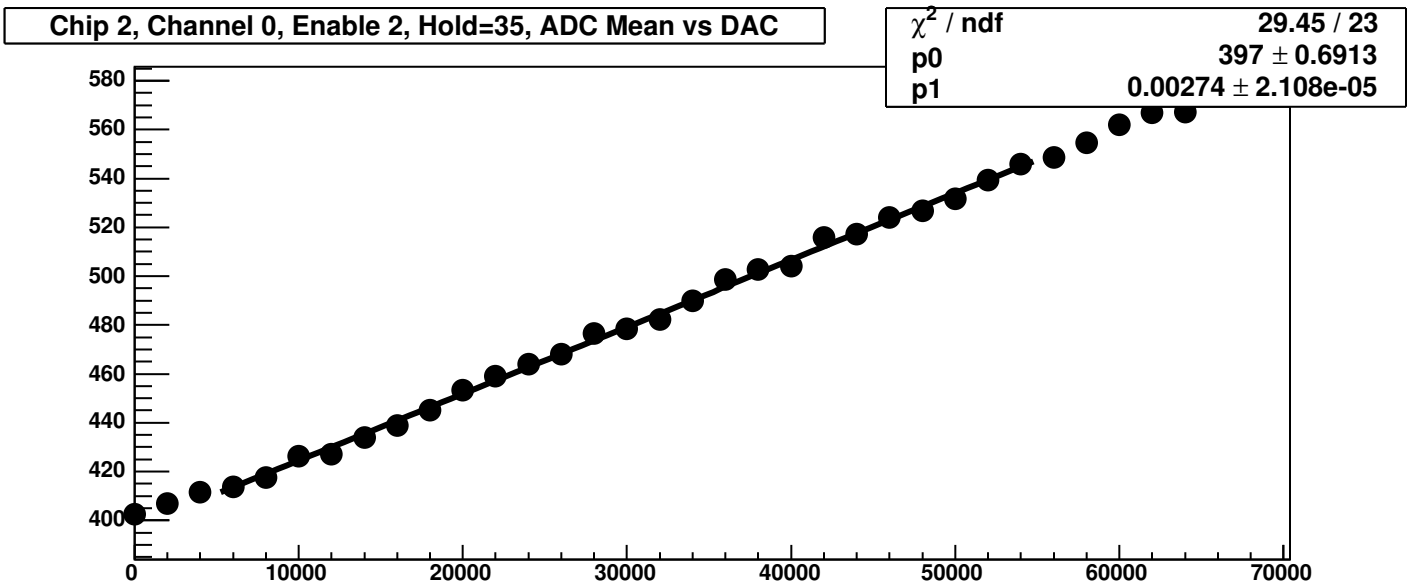

Chip 2, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

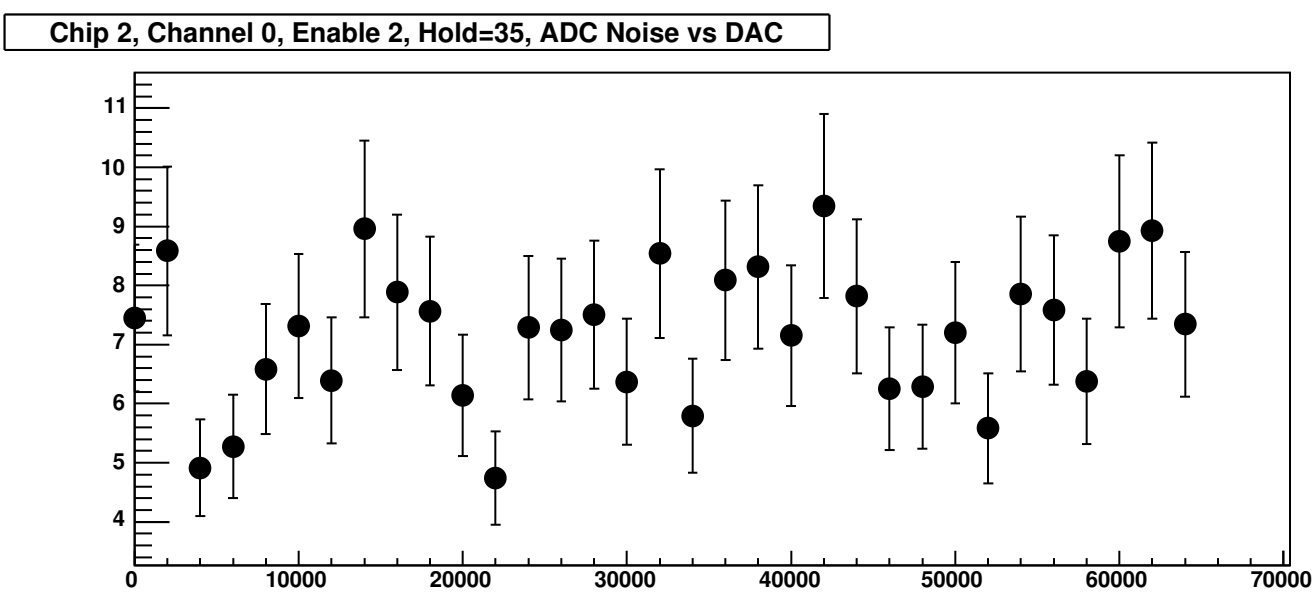

Chip 2, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
0
10000
20000
30000
40000
50000
60000
70000

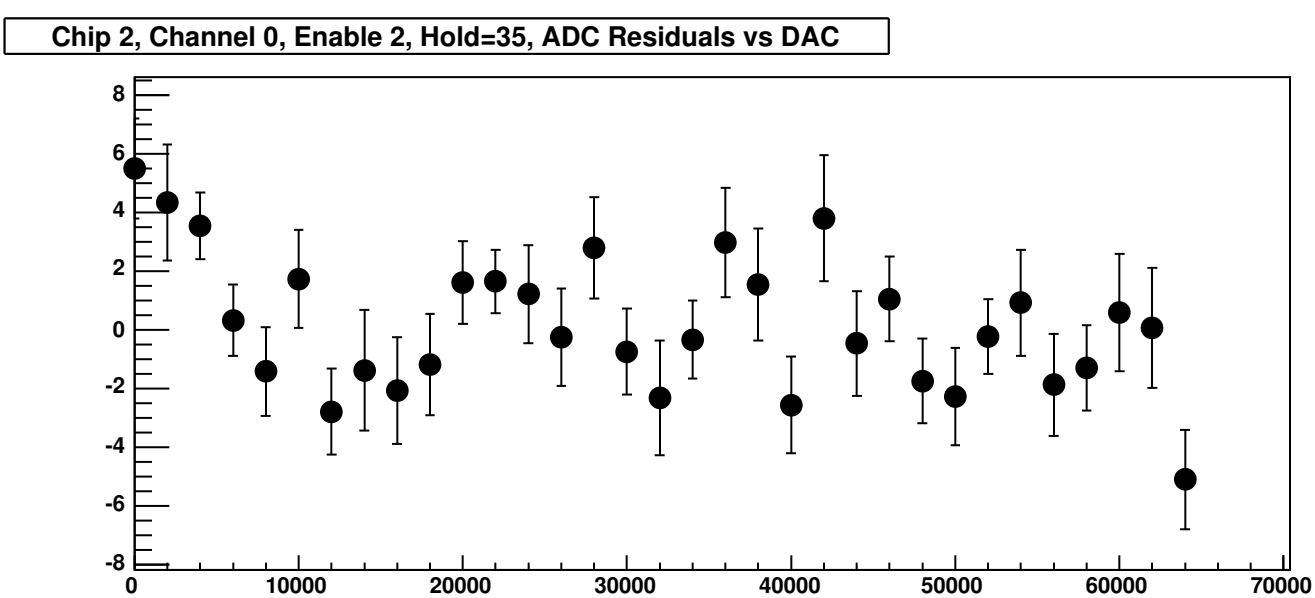

Chip 2, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 30.58 / 23
p0 396.2 ± 0.7358
p1 0.003381 ± 2.144e-05
600
550
500
450
400
0
10000
20000
30000
40000
50000
60000
70000

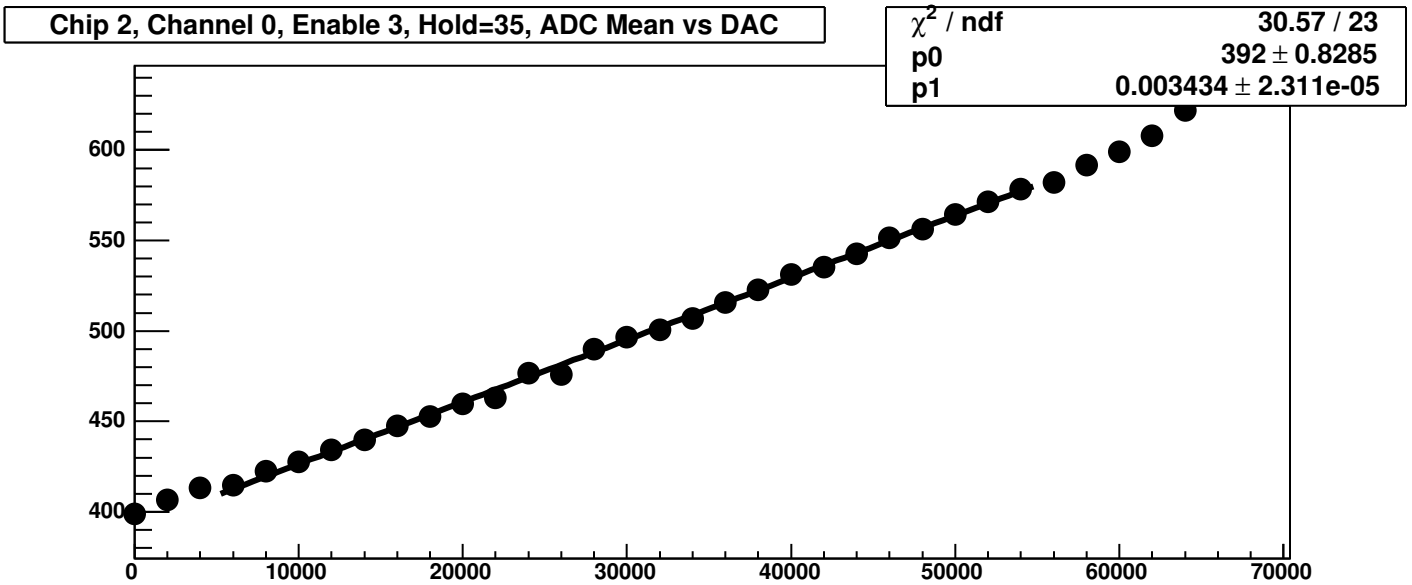

Chip 2, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

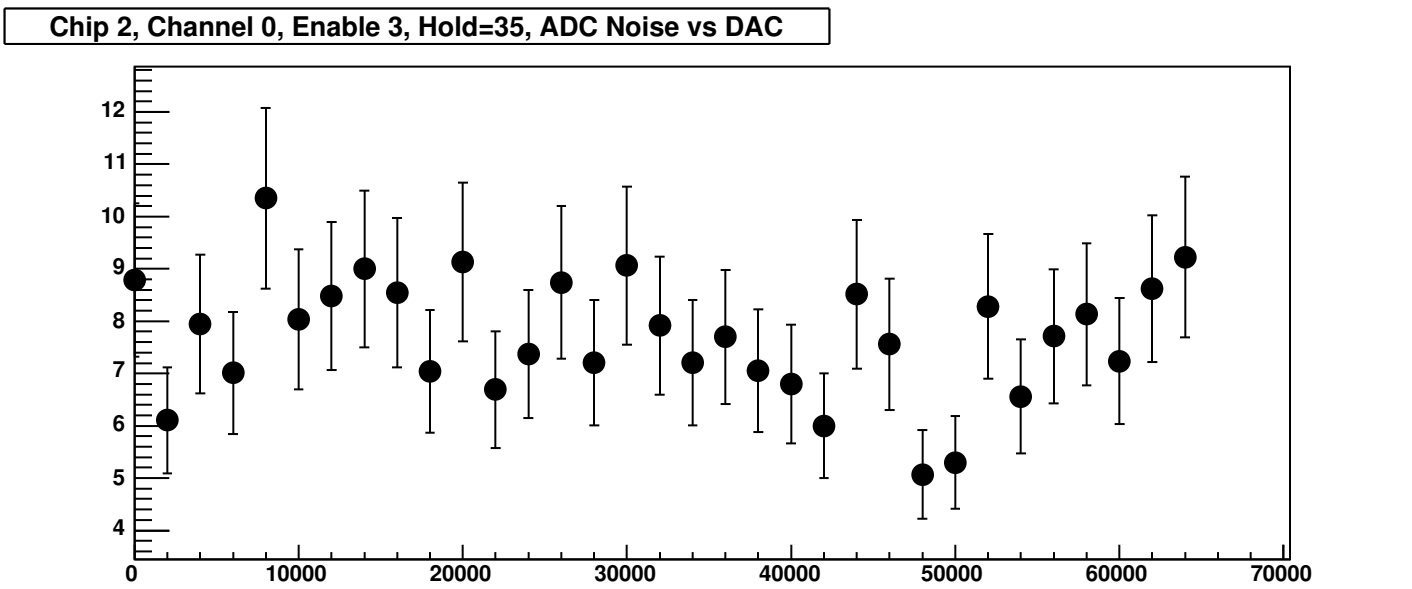

Chip 2, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
0
10000
20000
30000
40000
50000
60000
70000

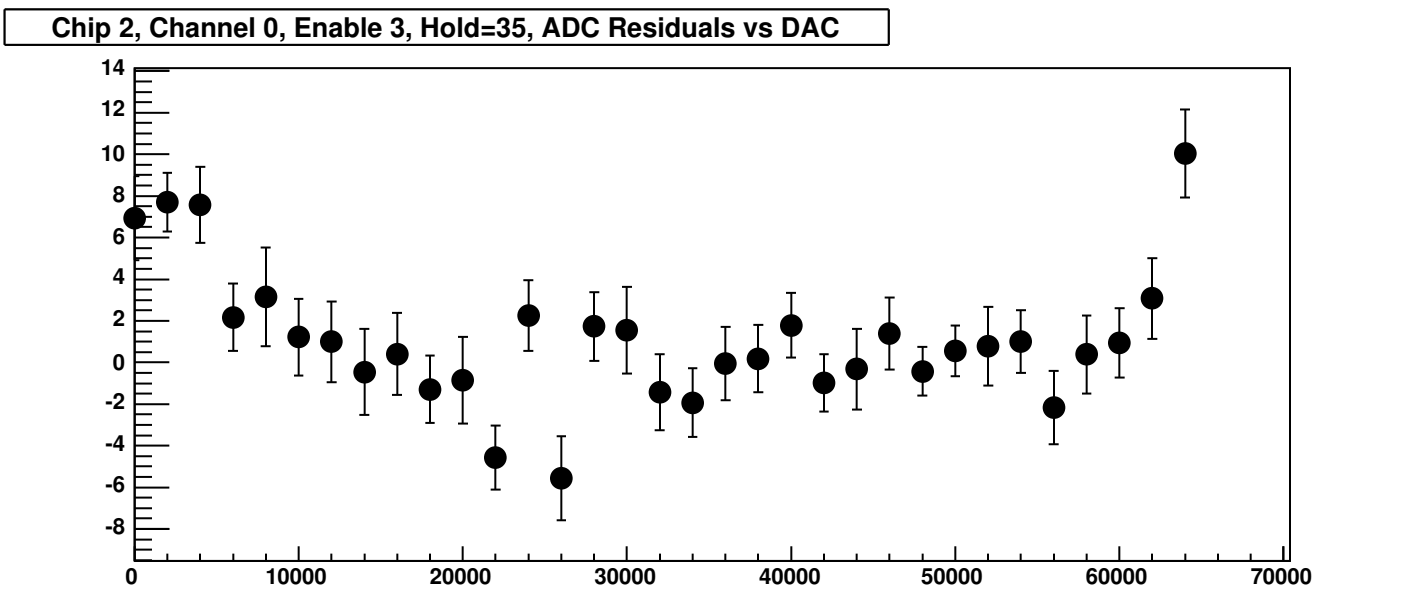

Chip 2, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 29.45 / 23
p0 397 ± 0.6913
p1 0.00274 ± 2.108e-05
580
560
540
520
500
480
460
440
420
400
0
10000
20000
30000
40000
50000
60000
70000

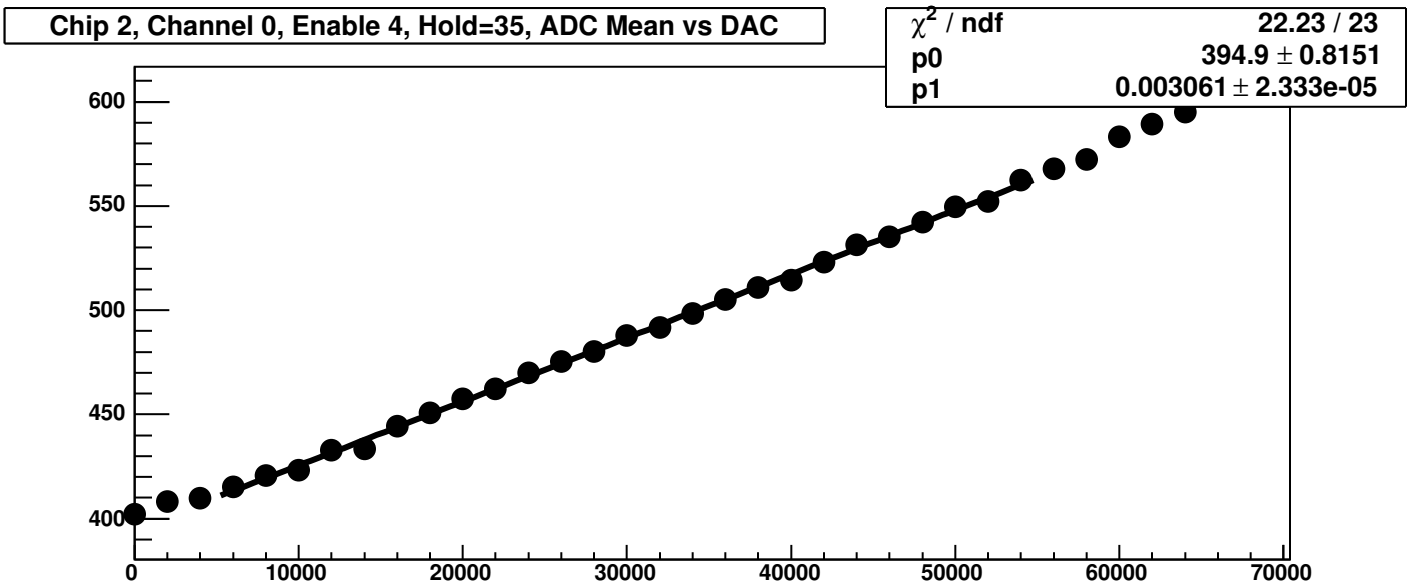

Chip 2, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

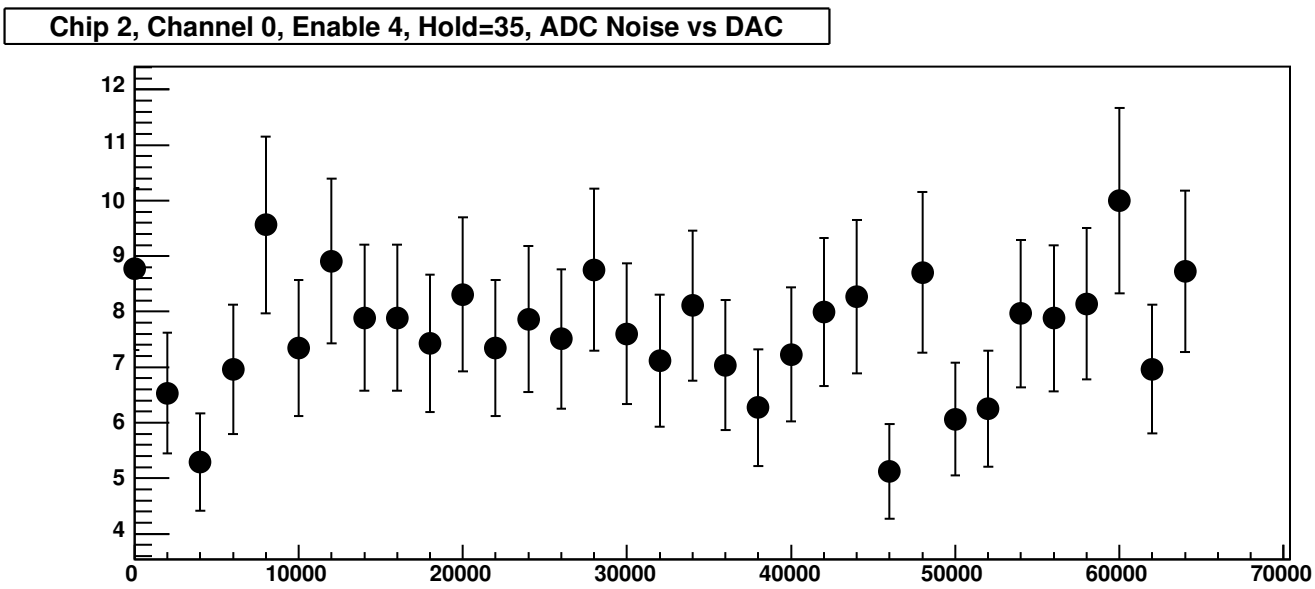

Chip 2, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

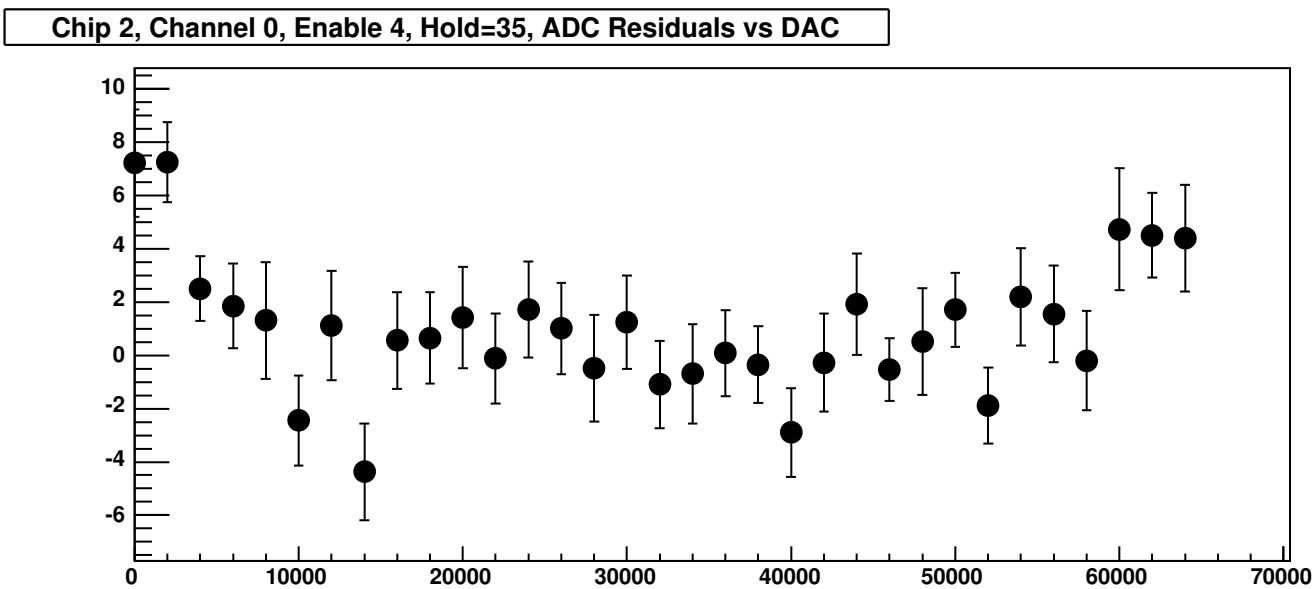

Chip 2, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 30.57 / 23
p0 392 ± 0.8285
p1 0.003434 ± 2.311e-05
600
550
500
450
400
0
10000
20000
30000
40000
50000
60000
70000

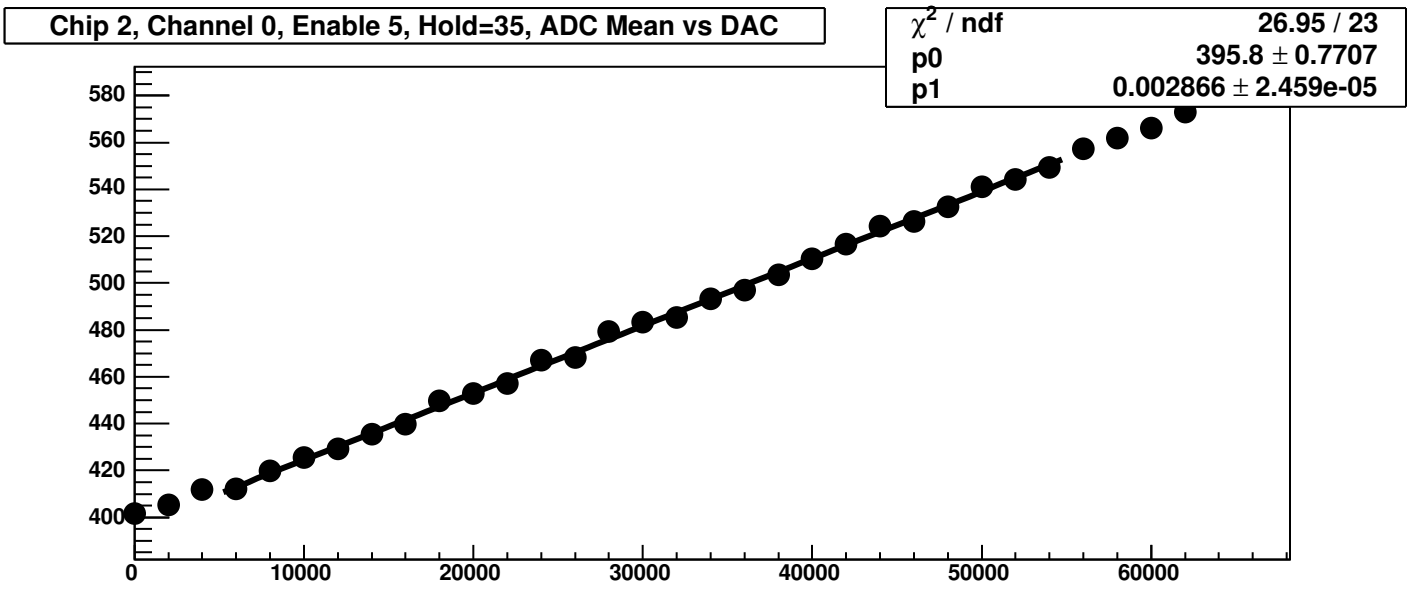

Chip 2, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

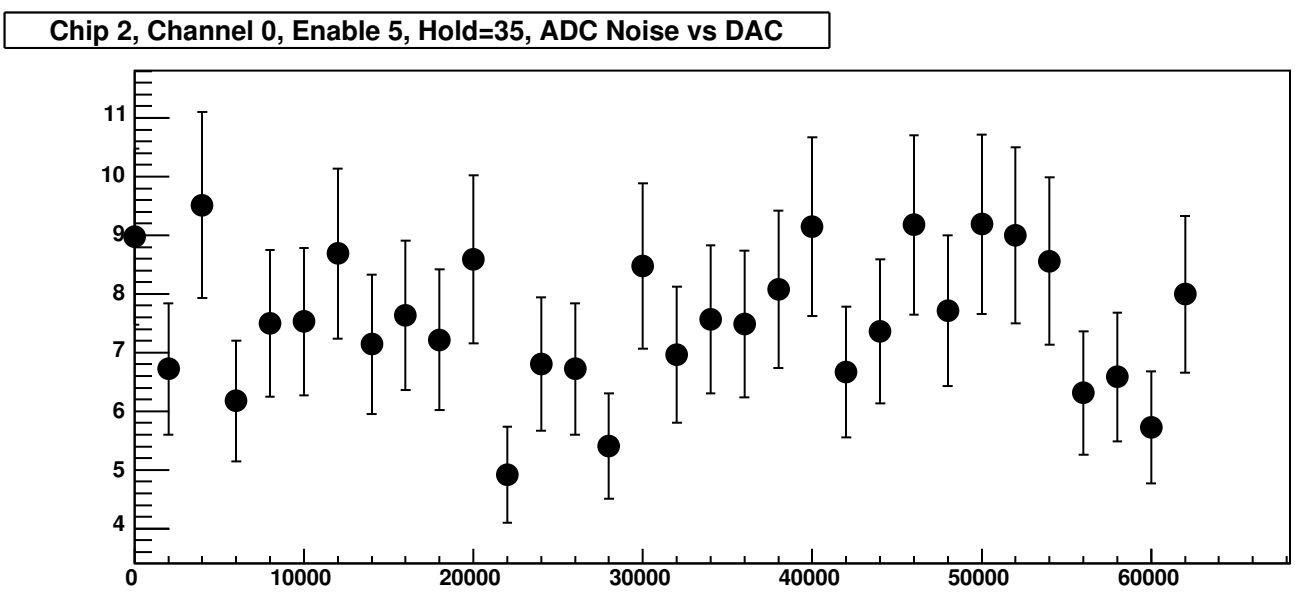

Chip 2, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

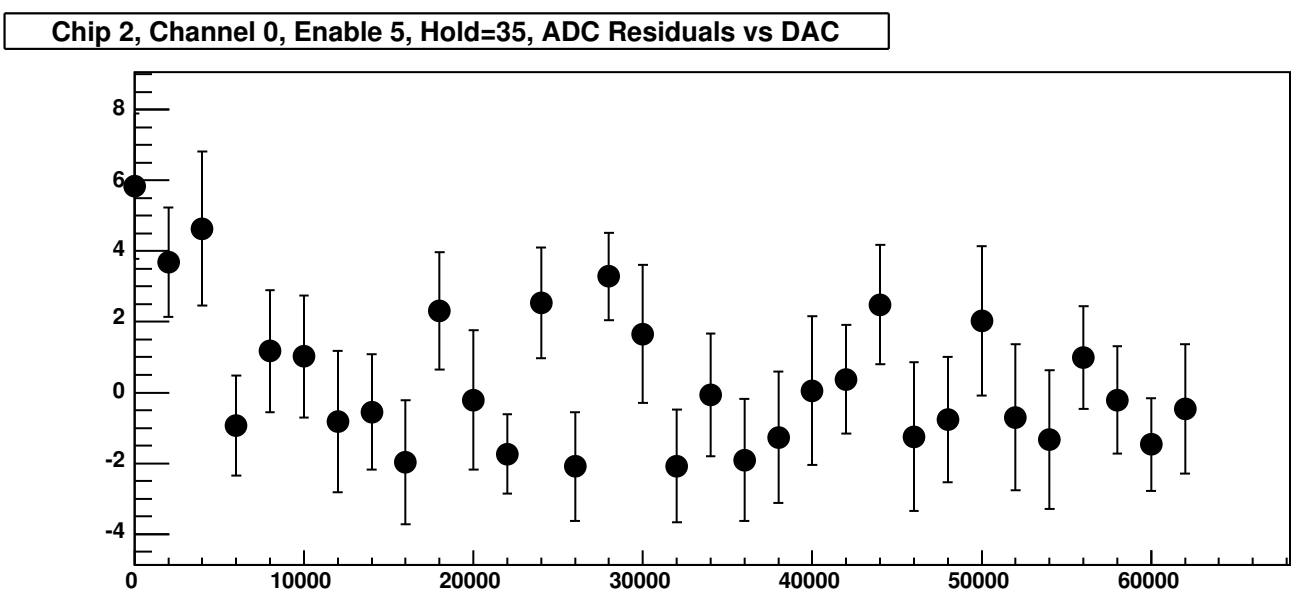

Chip 2, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 22.23 / 23
p0 394.9 ± 0.8151
p1 0.003061 ± 2.333e-05
600
550
500
450
400
0
10000
20000
30000
40000
50000
60000
70000

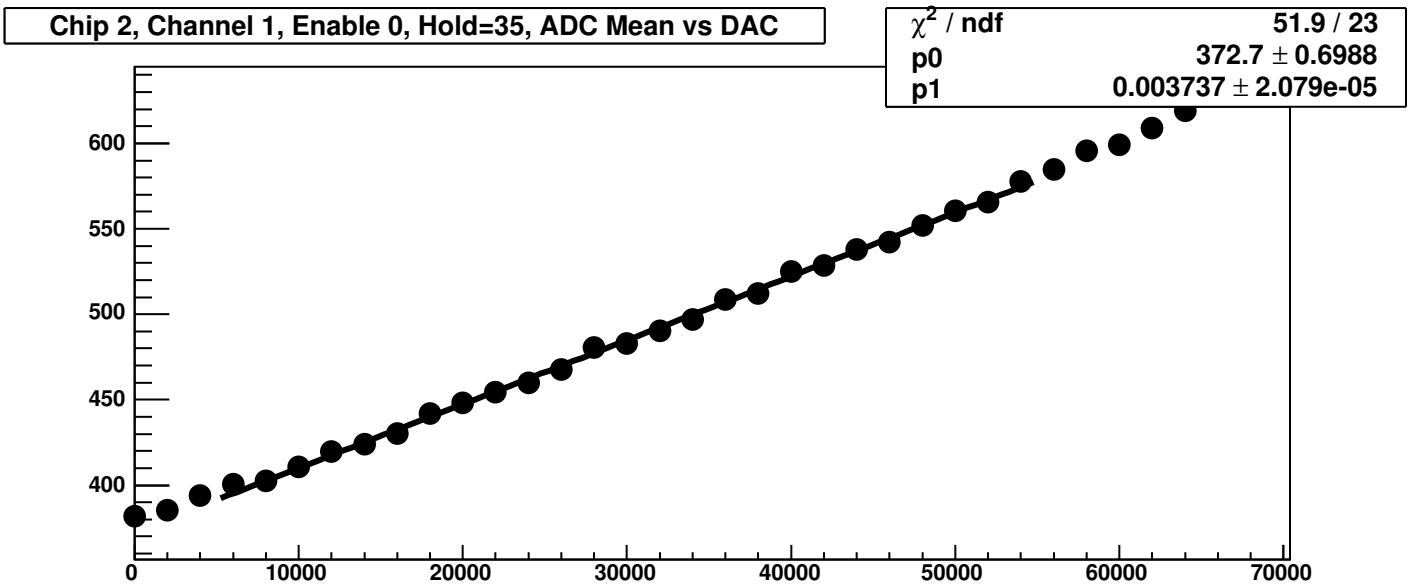

Chip 2, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

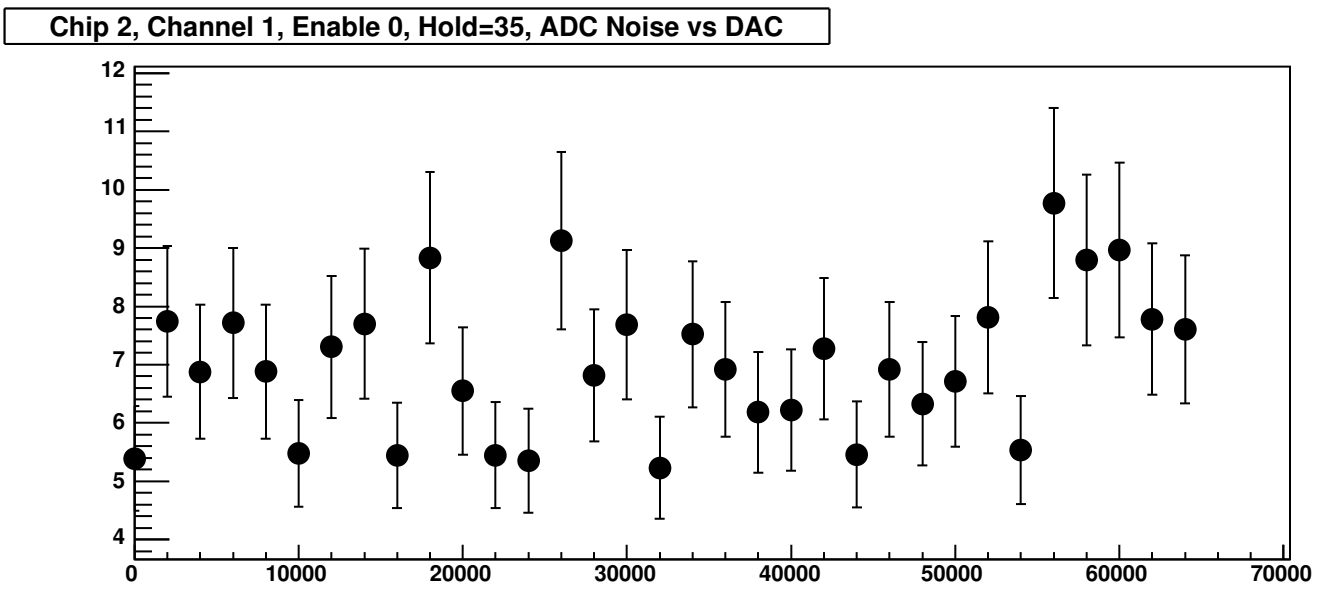

Chip 2, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

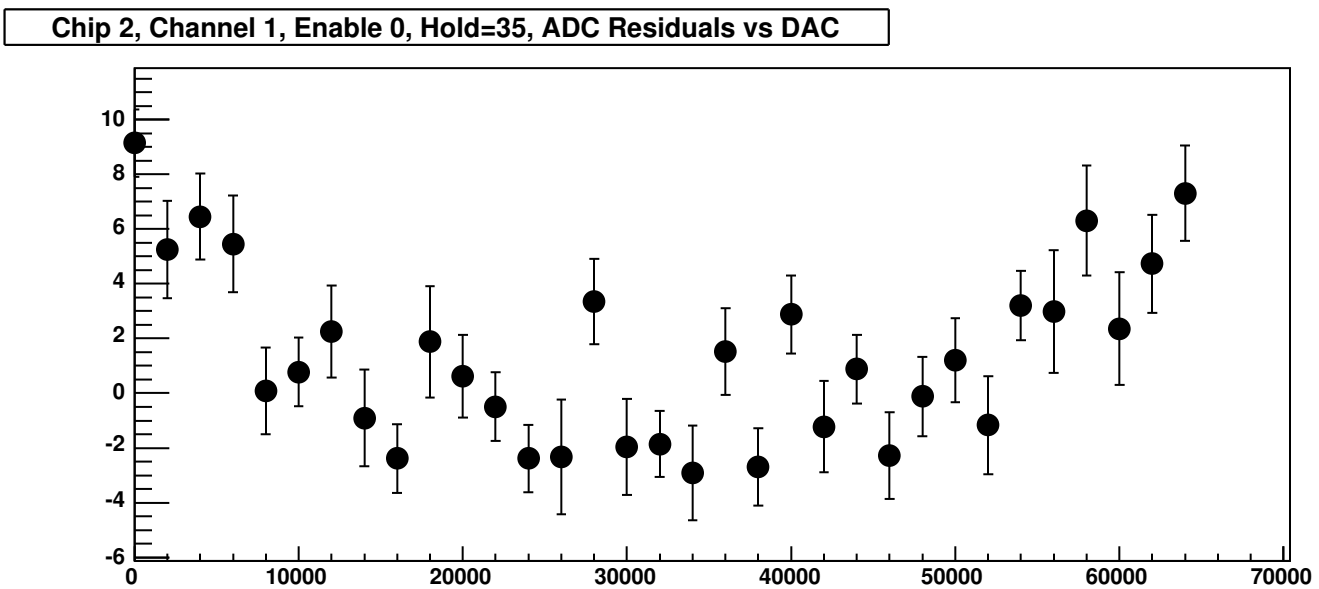

Chip 2, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 26.95 / 23
p0 395.8 ± 0.7707
p1 0.002866 ± 2.459e-05
580
560
540
520
500
480
460
440
420
400
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

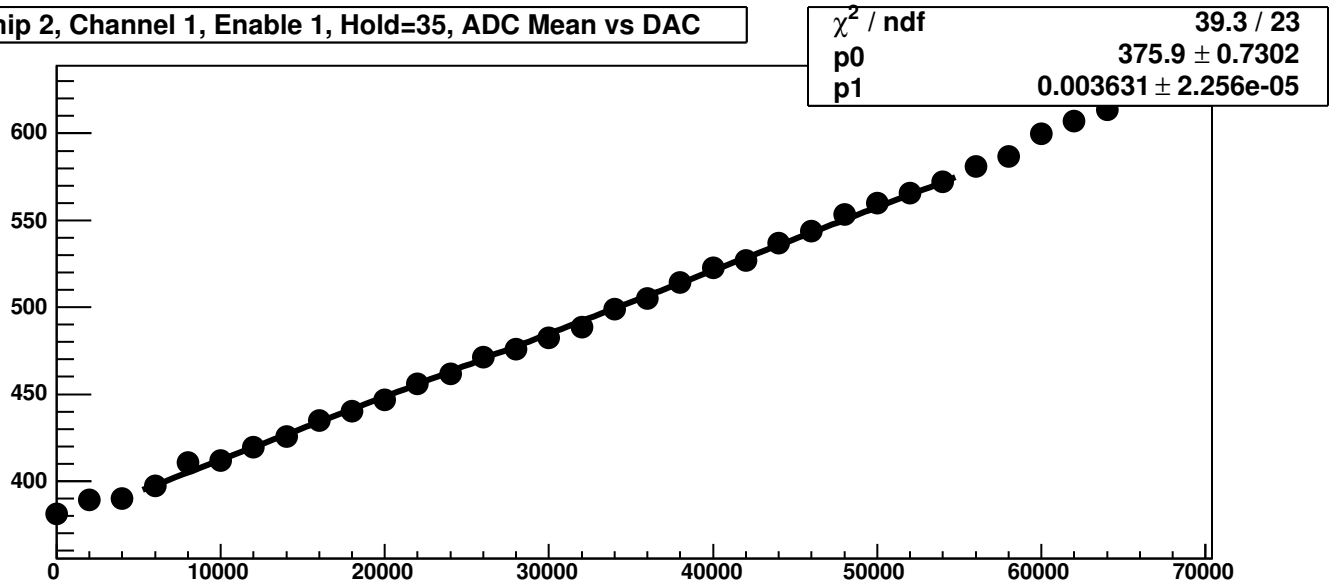

Chip 2, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 51.9 / 23
p0 372.7 ± 0.6988
p1 0.003737 ± 2.079e-05
600
550
500
450
400
0
10000
20000
30000
40000
50000
60000
70000

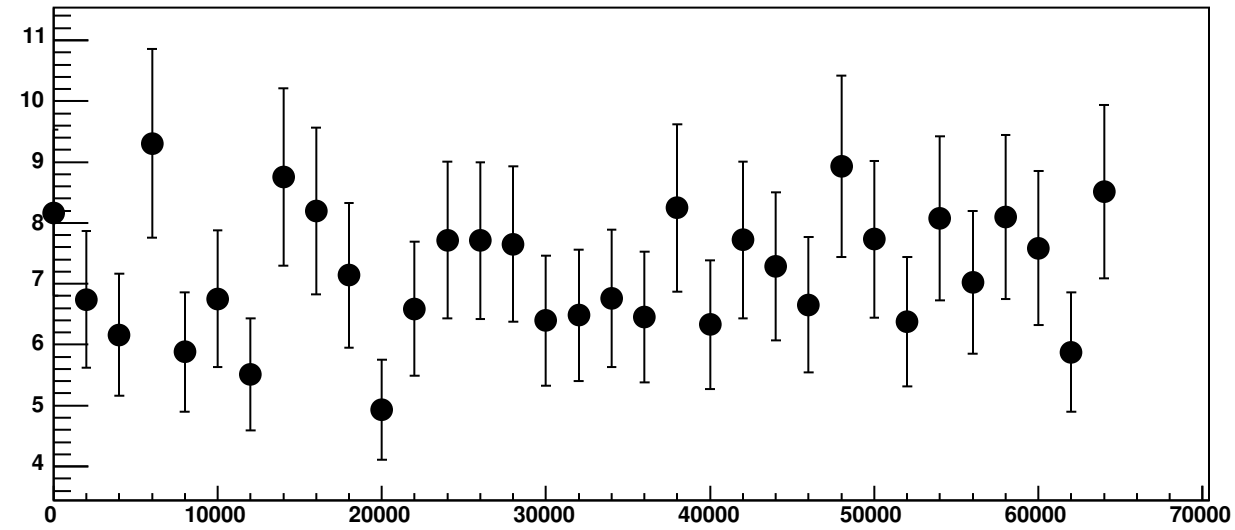

Chip 2, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

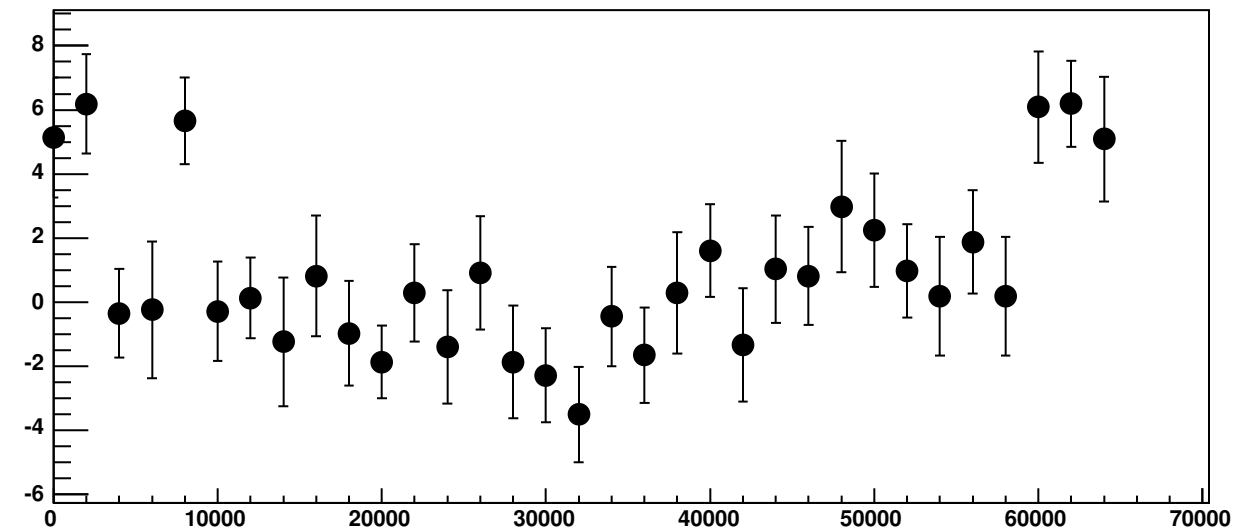
Chip 2, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC

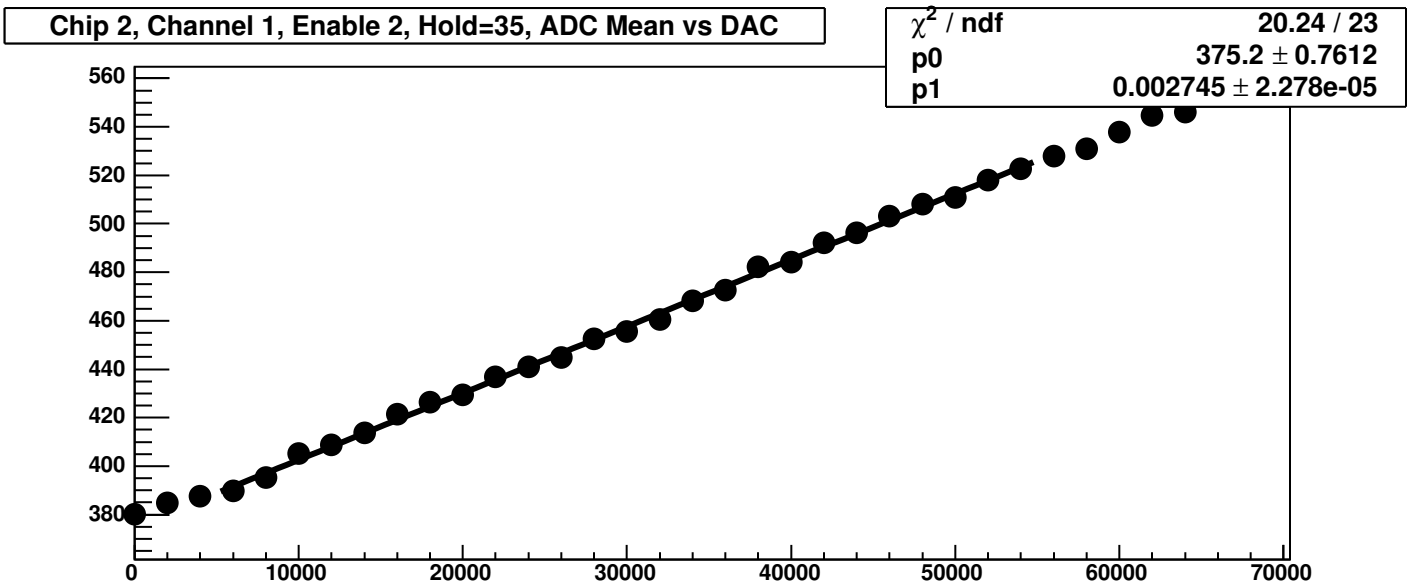


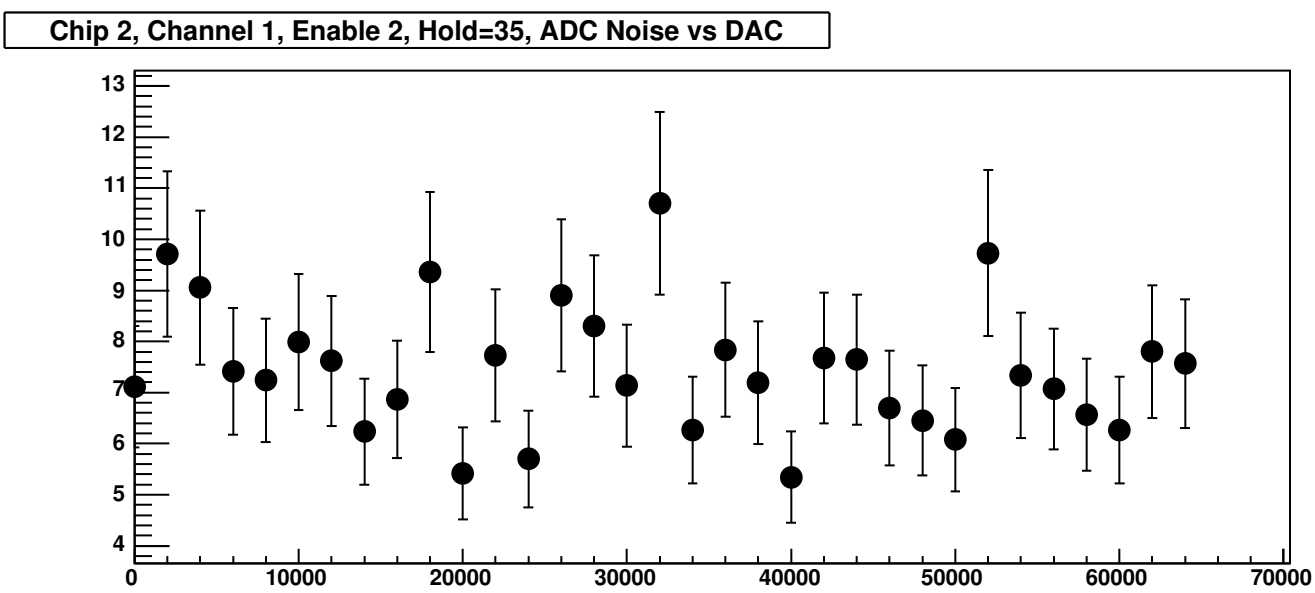
Chip 2, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC

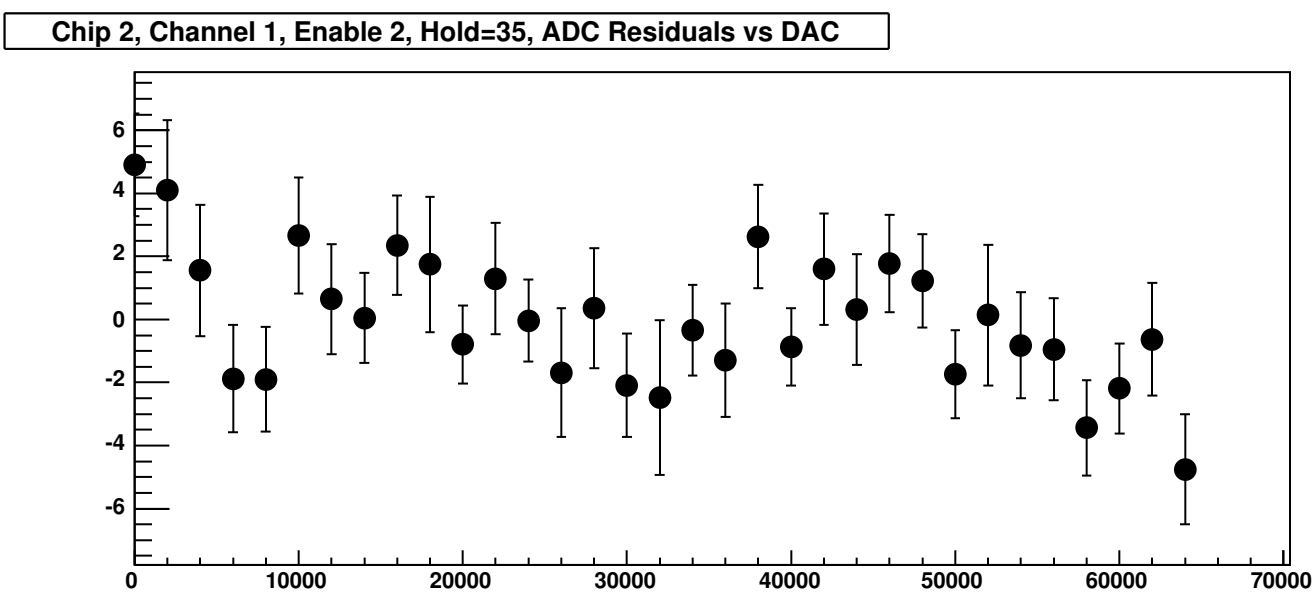


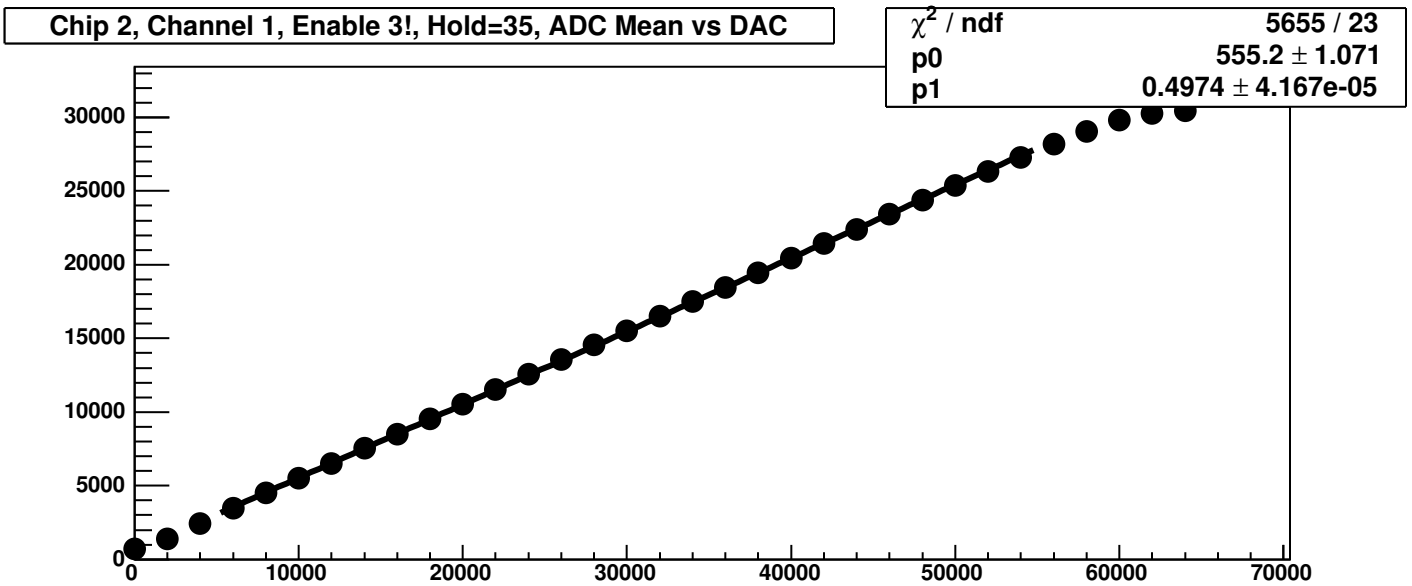
Chip 2, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC

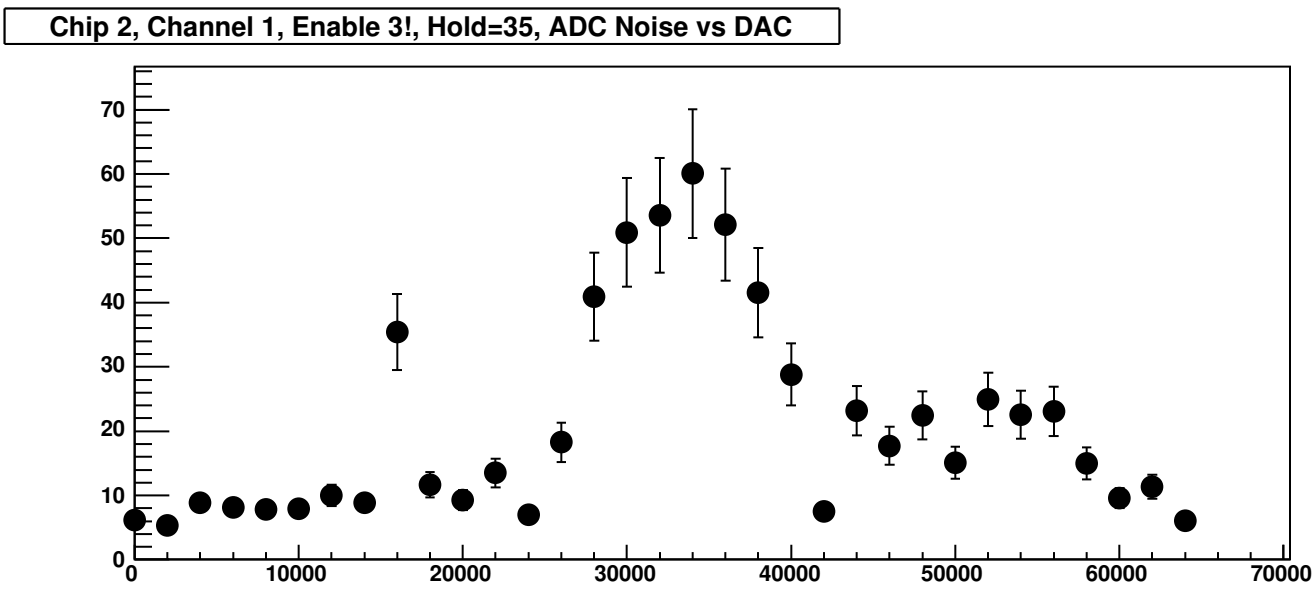


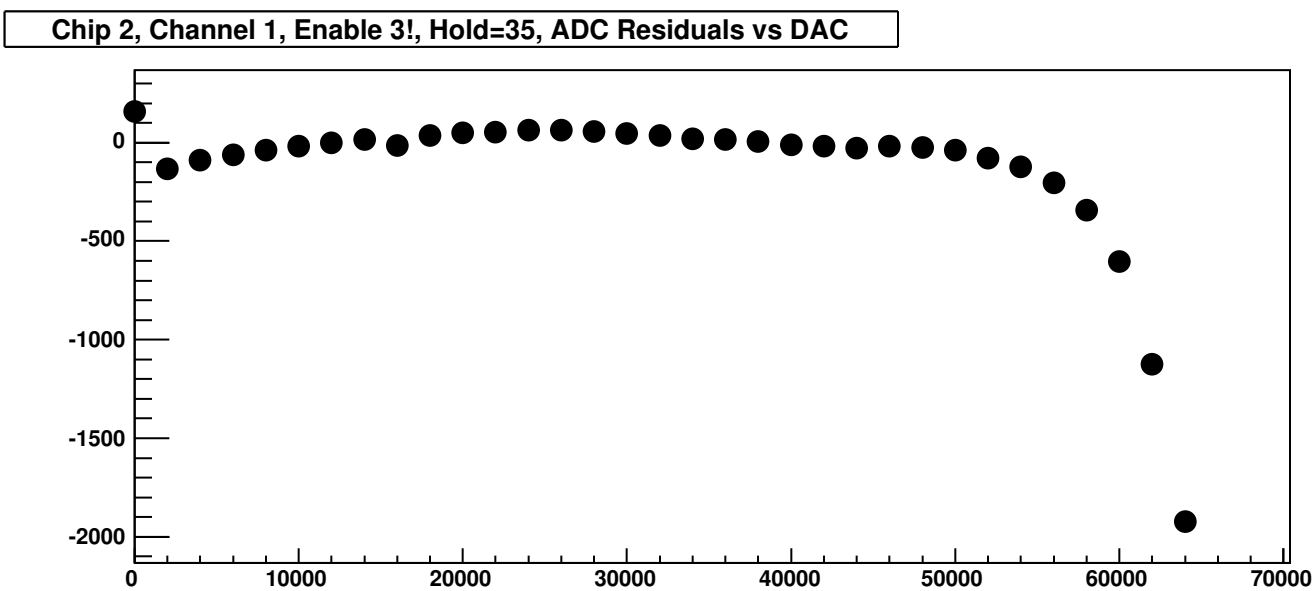

Chip 2, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 20.24 / 23
p0 375.2 ± 0.7612
p1 0.002745 ± 2.278e-05
560
540
520
500
480
460
440
420
400
380
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

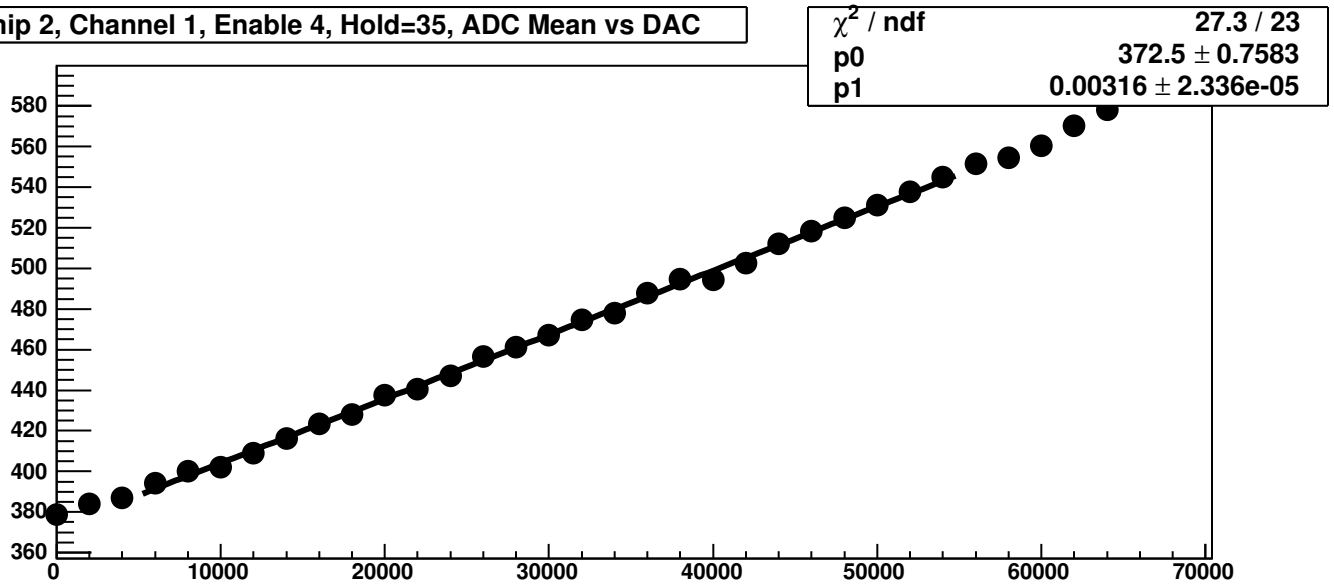

Chip 2, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 5655 / 23
p0 555.2 ± 1.071
p1 0.4974 ± 4.167e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

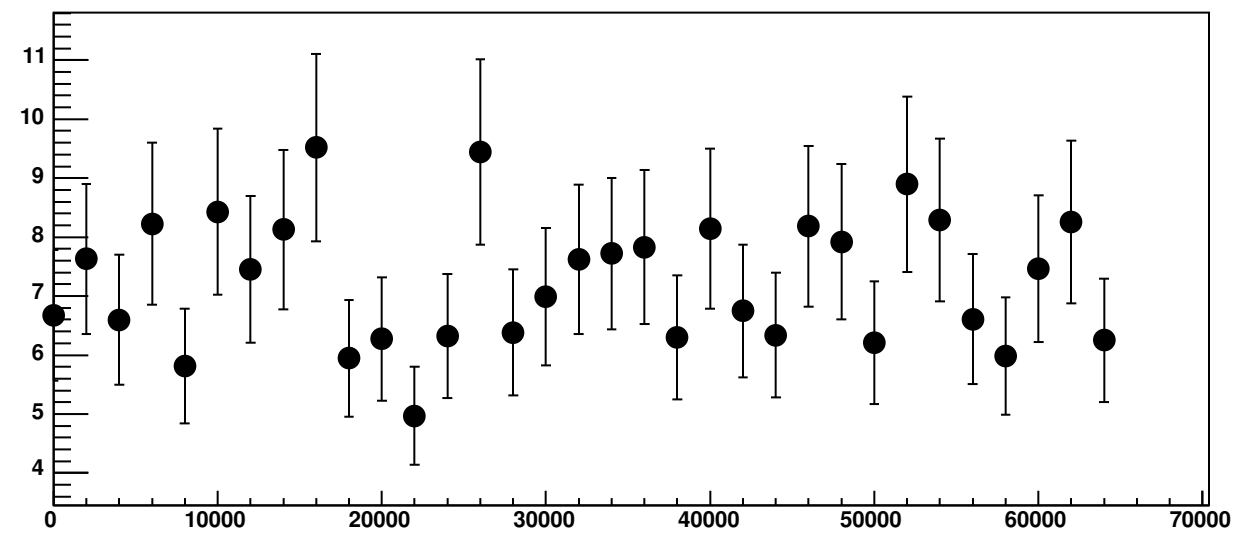

Chip 2, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000

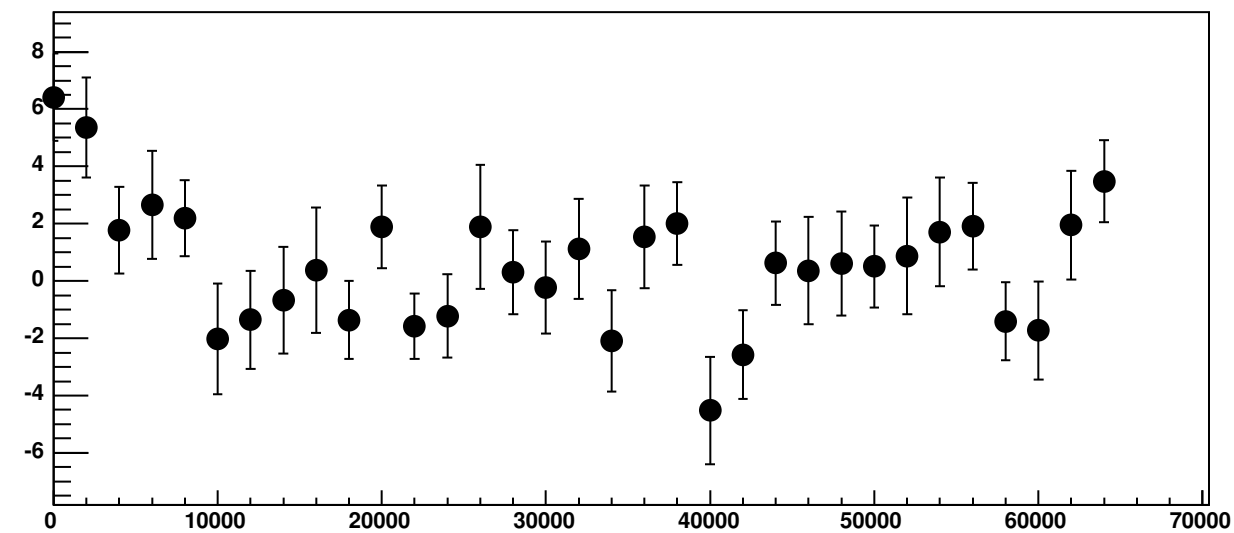
Chip 2, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

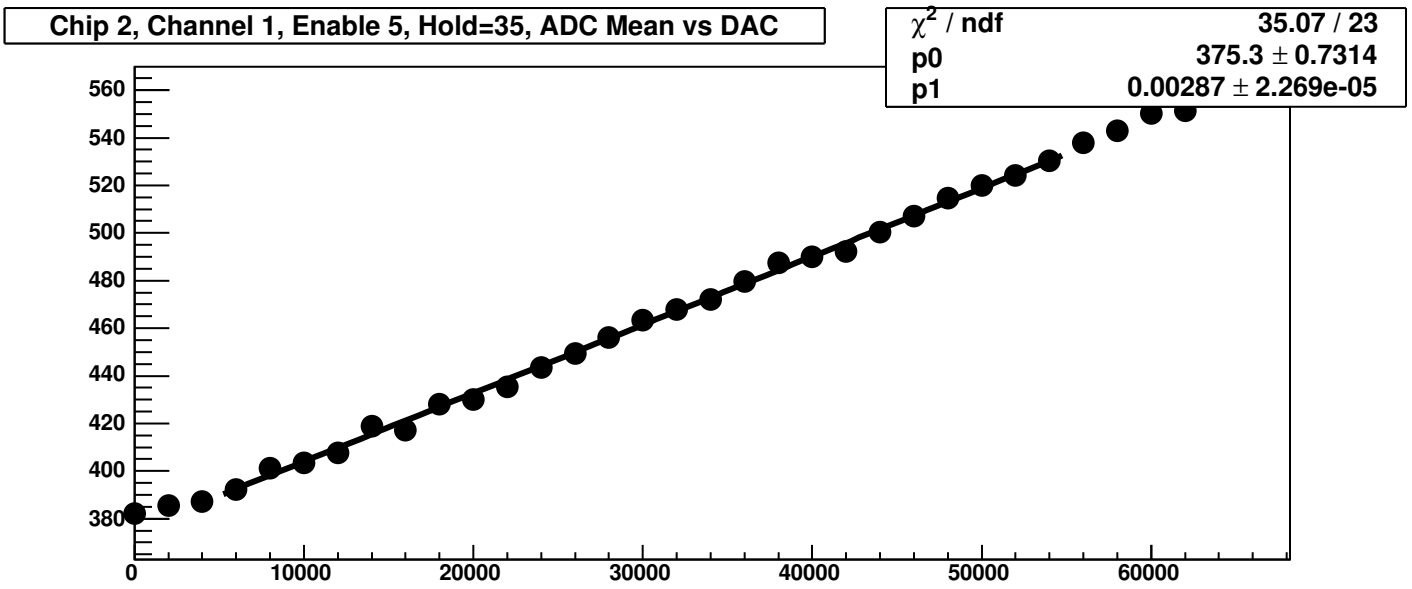


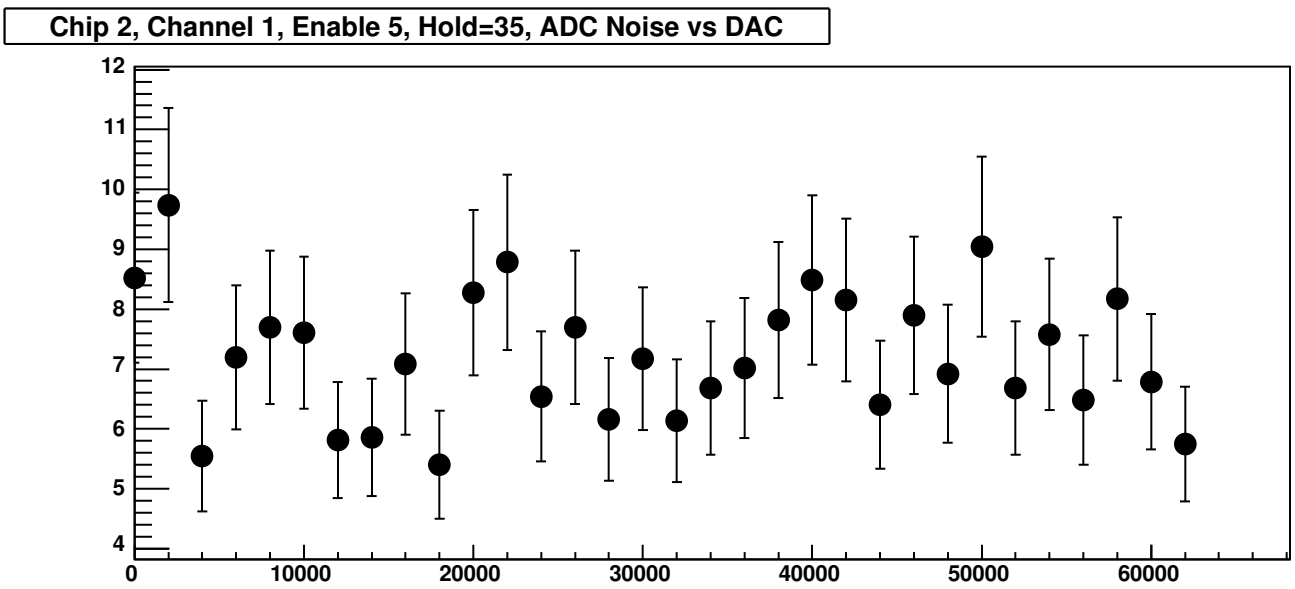
Chip 2, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

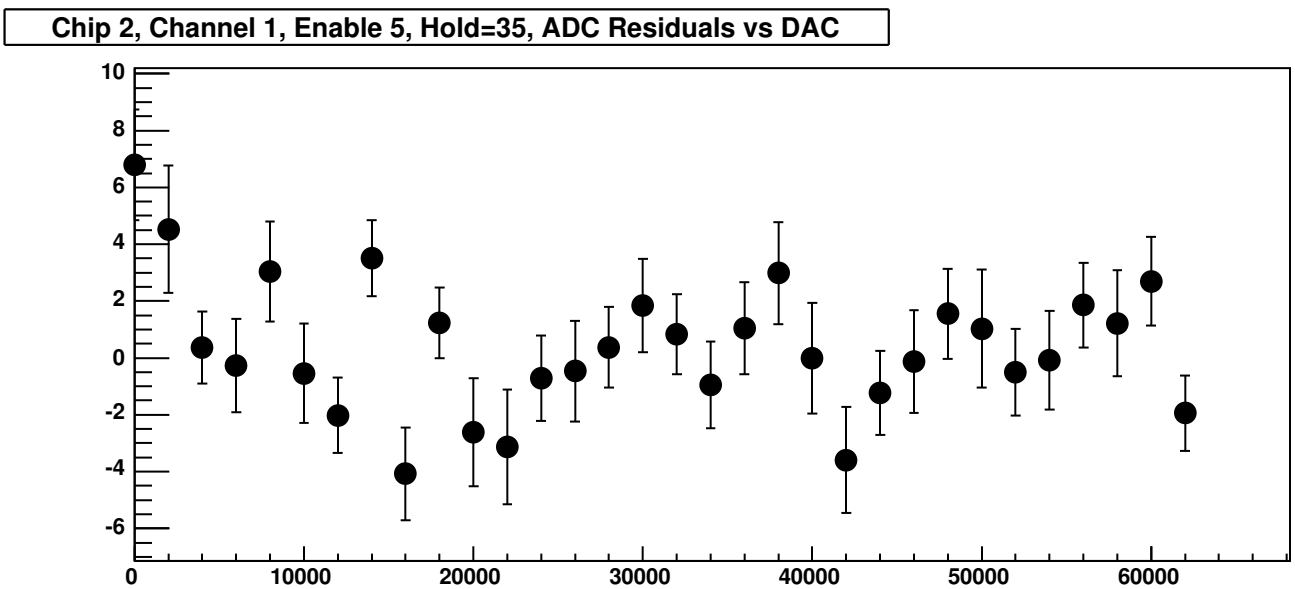


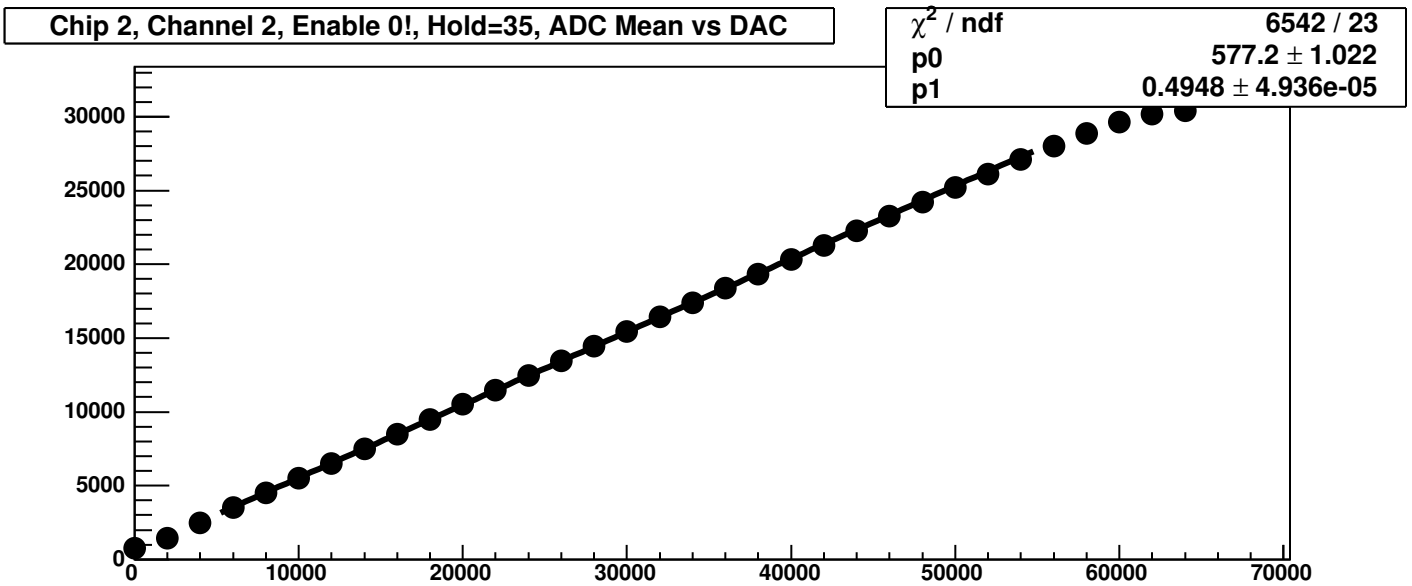
Chip 2, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

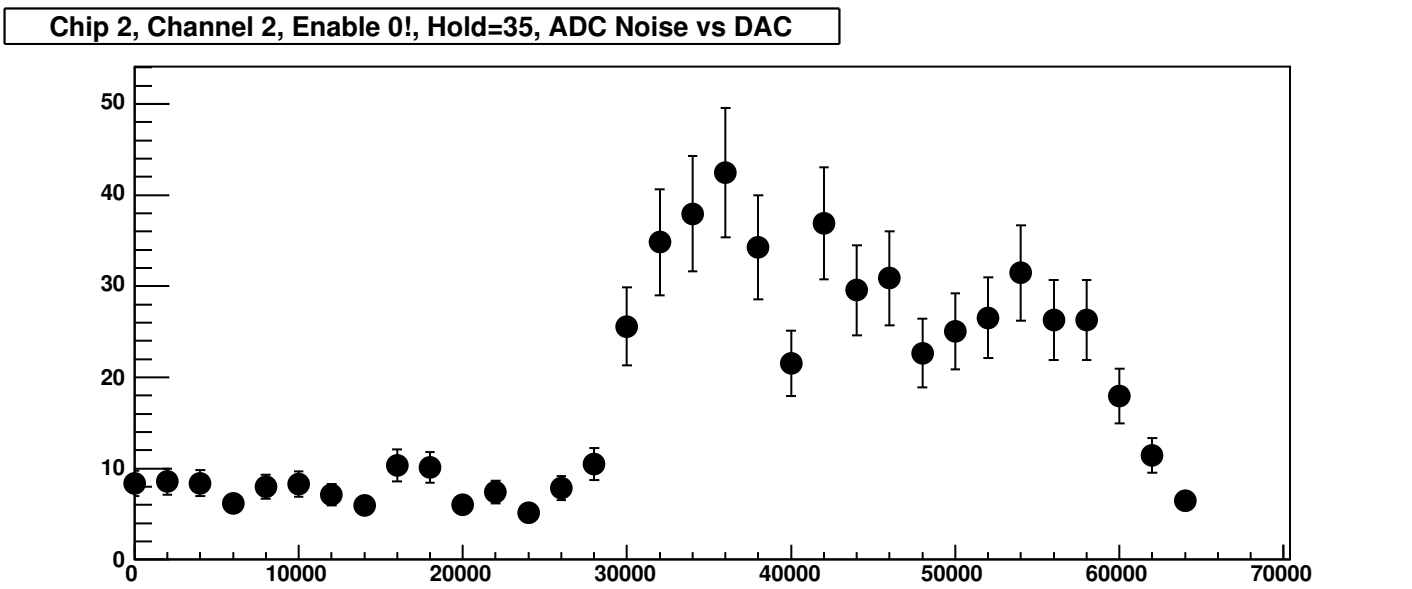


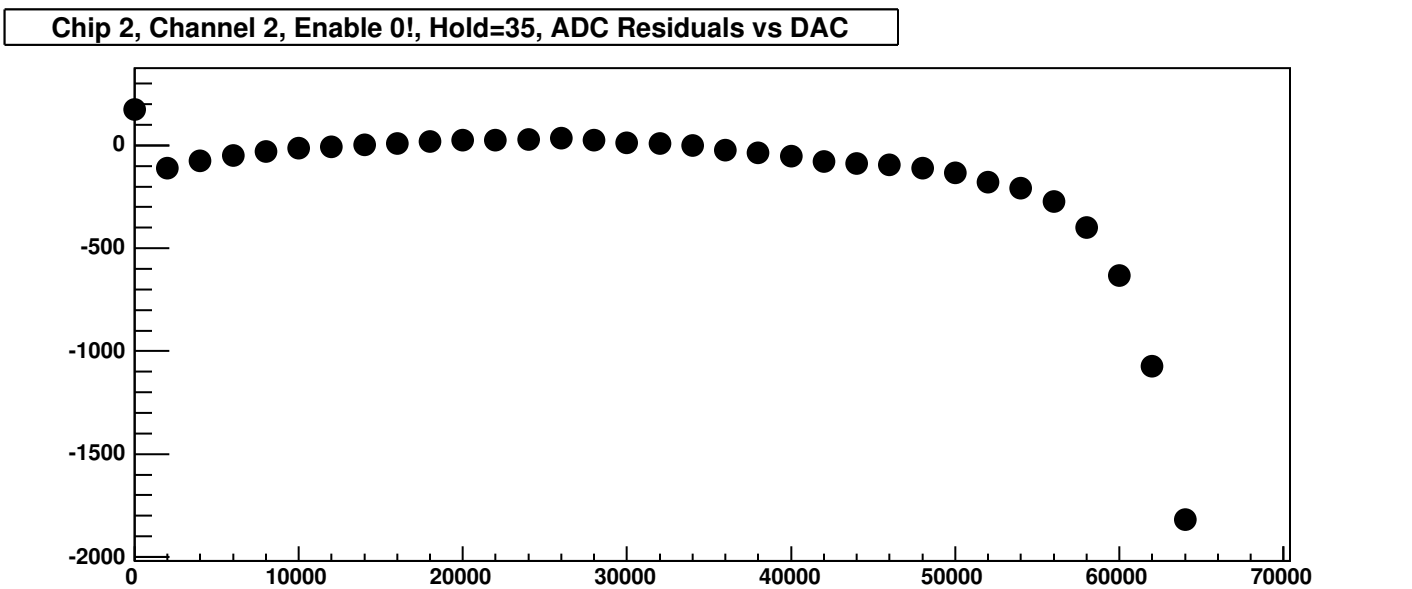

Chip 2, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 35.07 / 23
p0 375.3 ± 0.7314
p1 0.00287 ± 2.269e-05
560
540
520
500
480
460
440
420
400
380
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

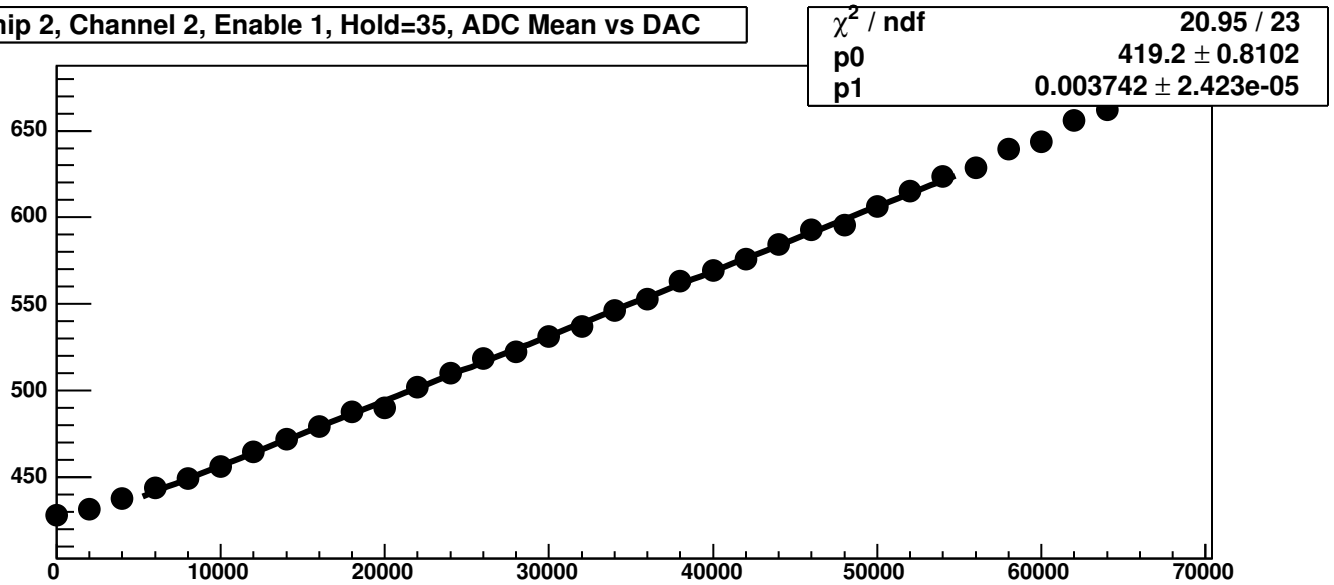

Chip 2, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 6542 / 23
p0 577.2 ± 1.022
p1 0.4948 ± 4.936e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

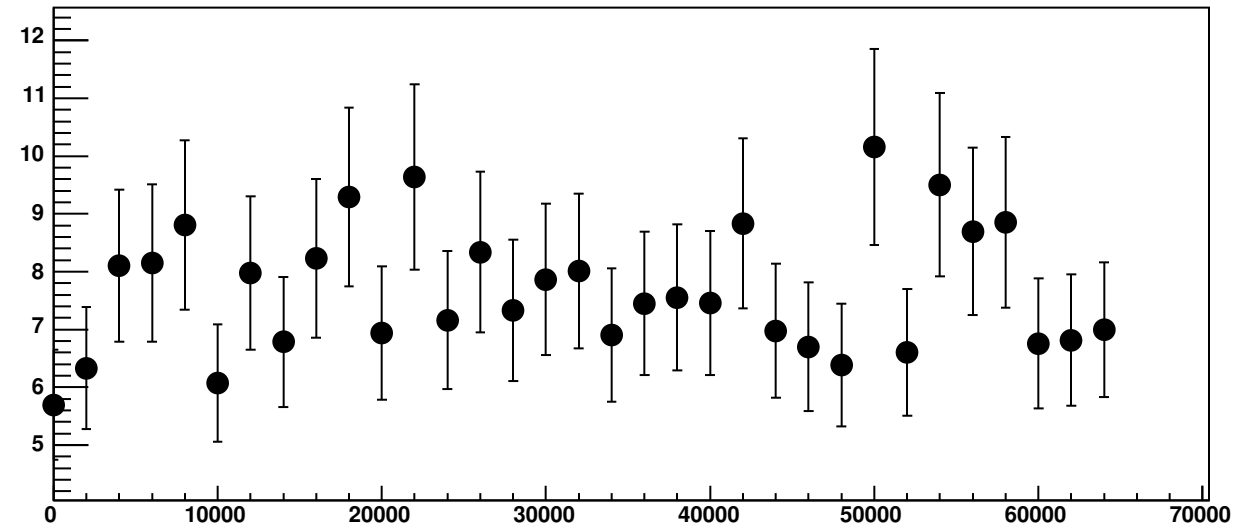

Chip 2, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000

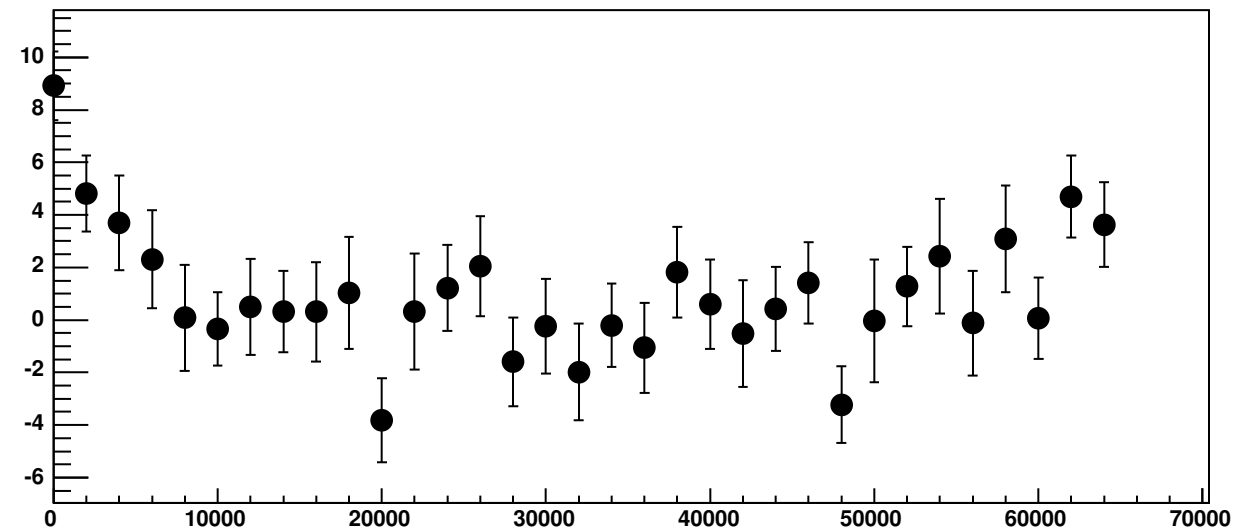
Chip 2, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

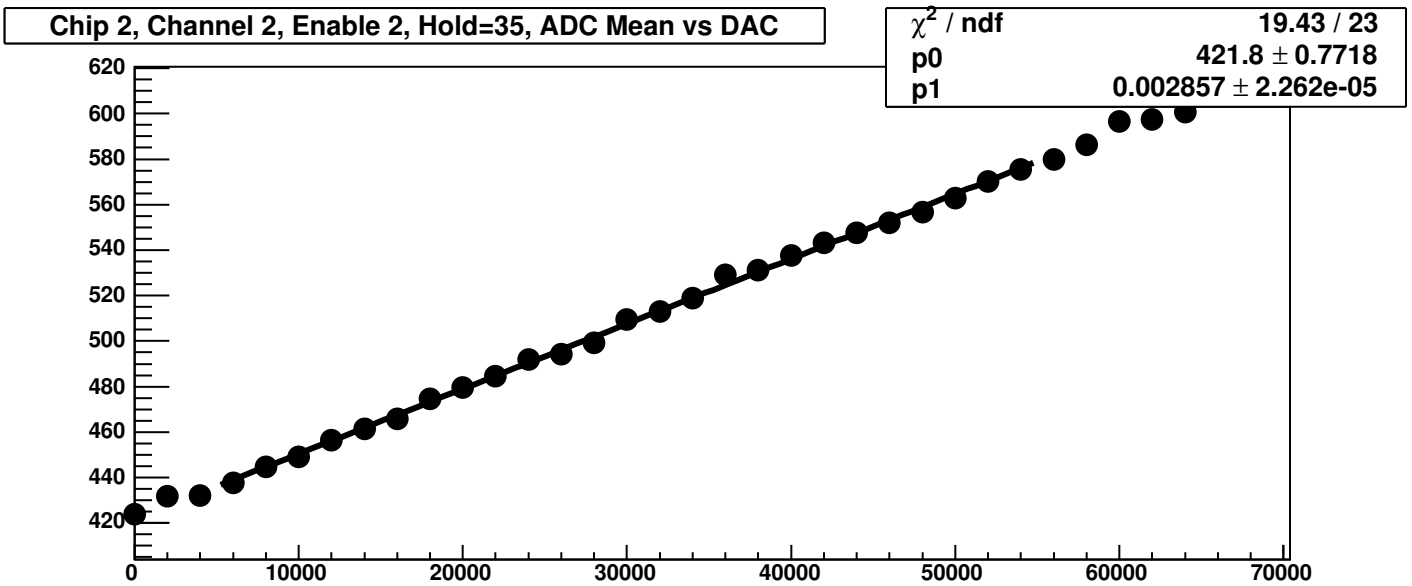


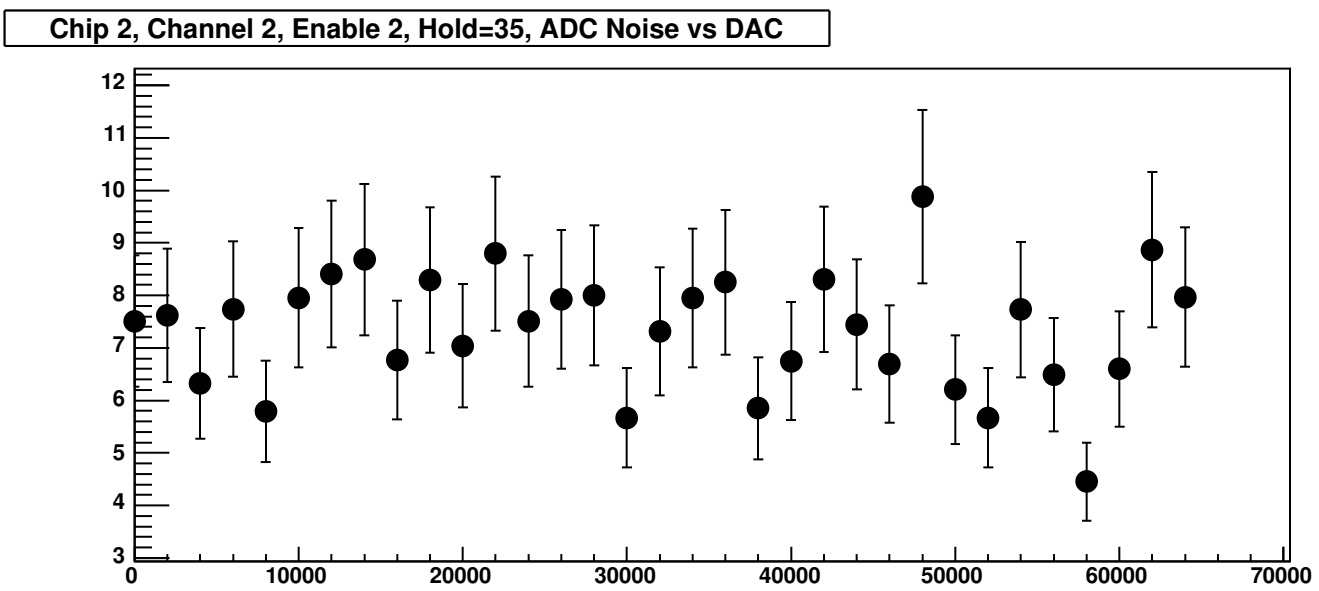
Chip 2, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC

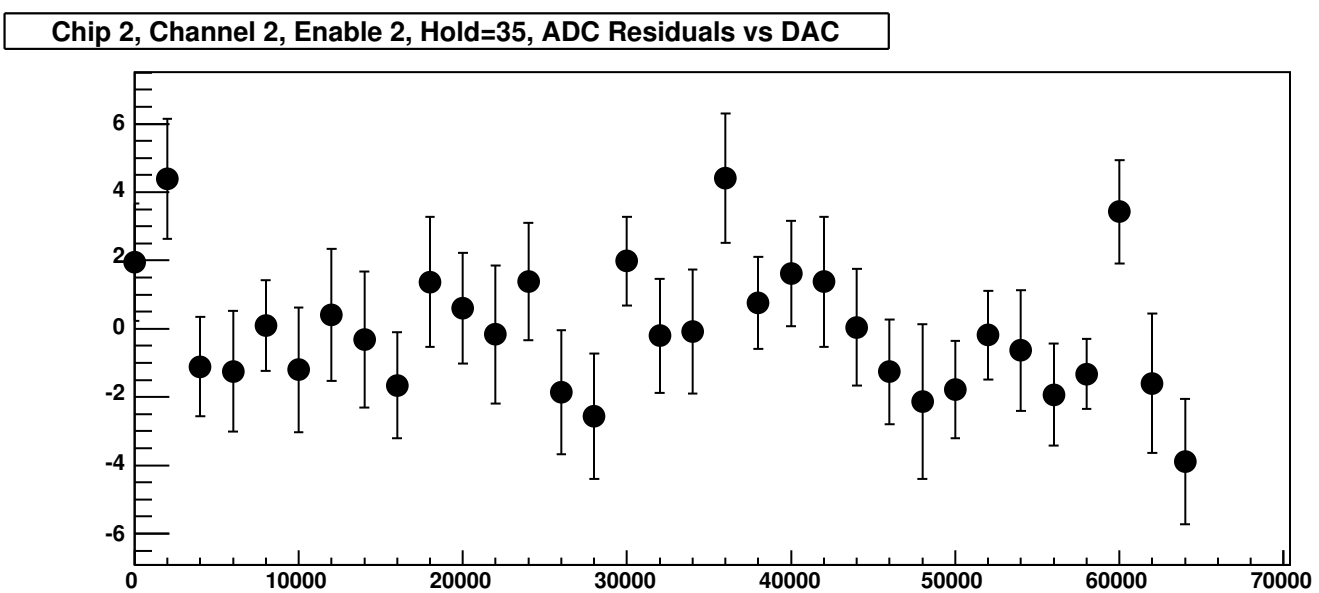


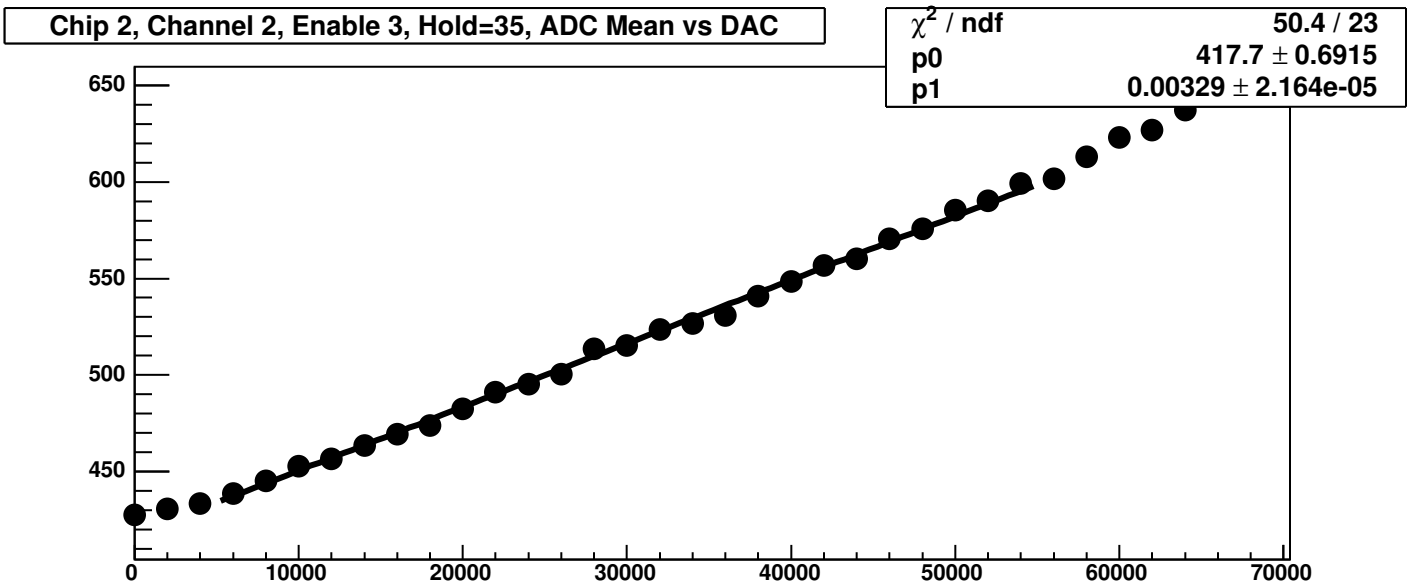
Chip 2, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

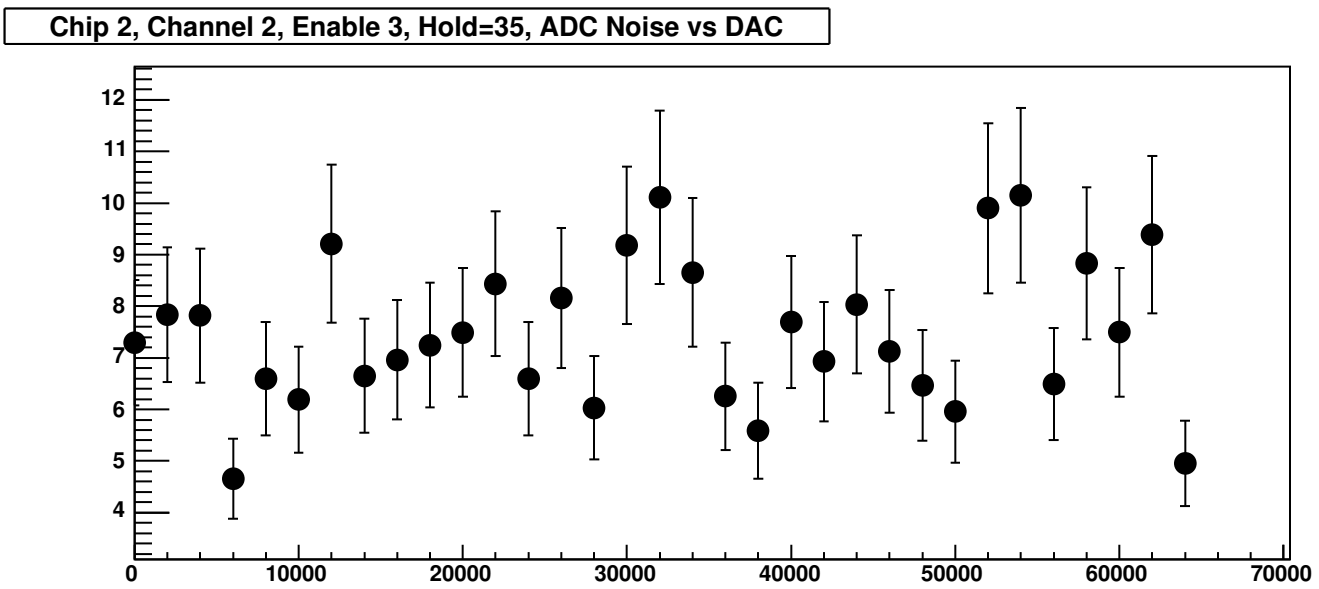


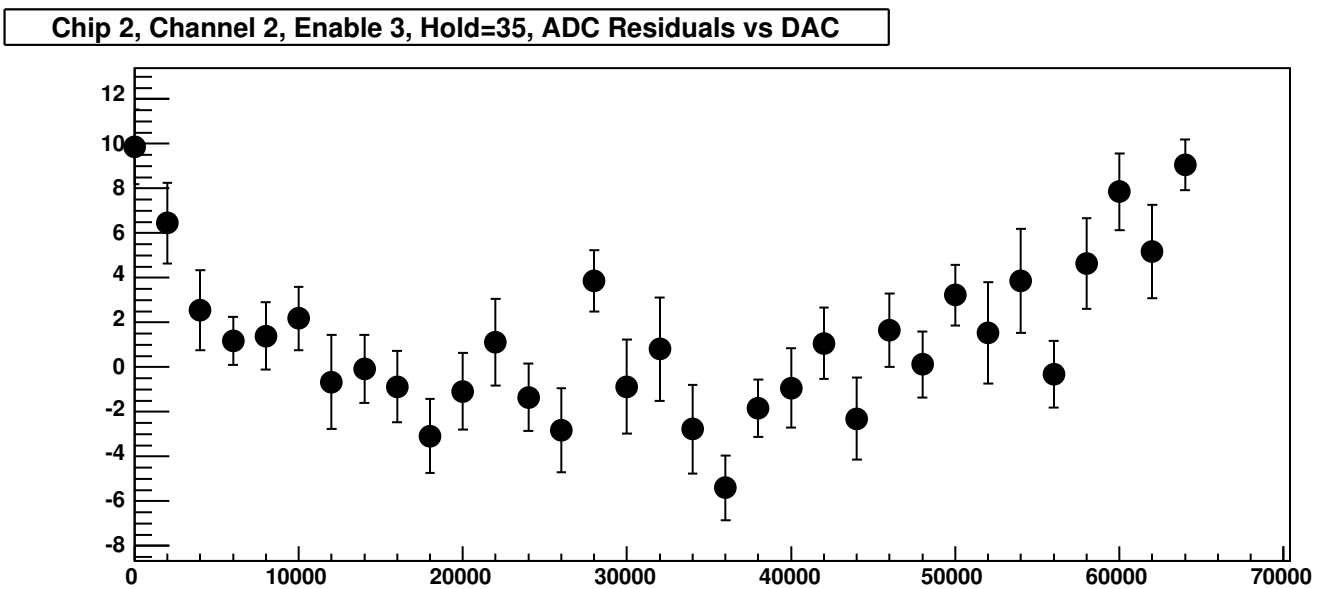

Chip 2, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 19.43 / 23
p0 421.8 ± 0.7718
p1 0.002857 ± 2.262e-05
620
600
580
560
540
520
500
480
460
440
420
0
10000
20000
30000
40000
50000
60000
70000

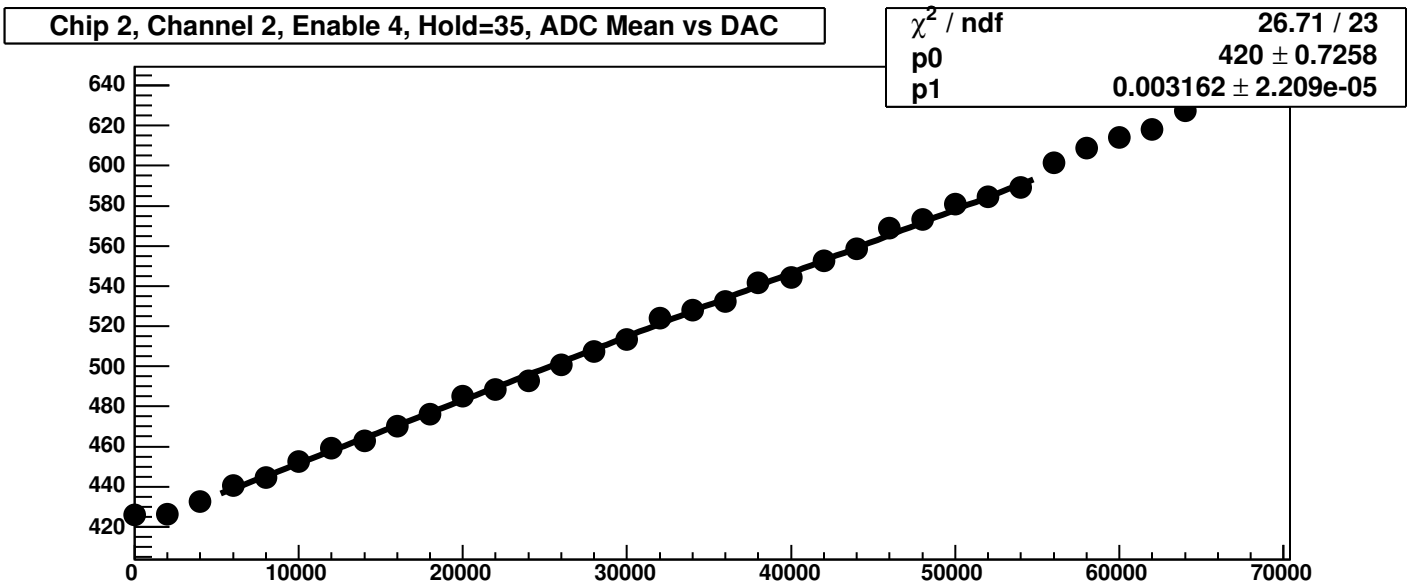

Chip 2, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

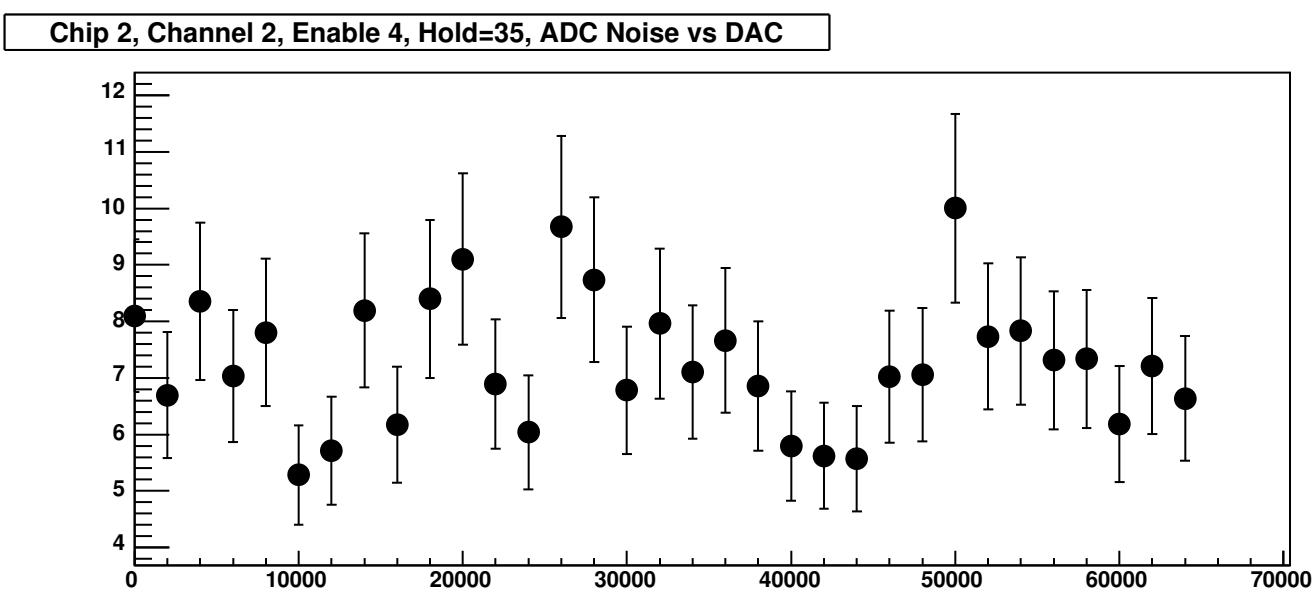

Chip 2, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

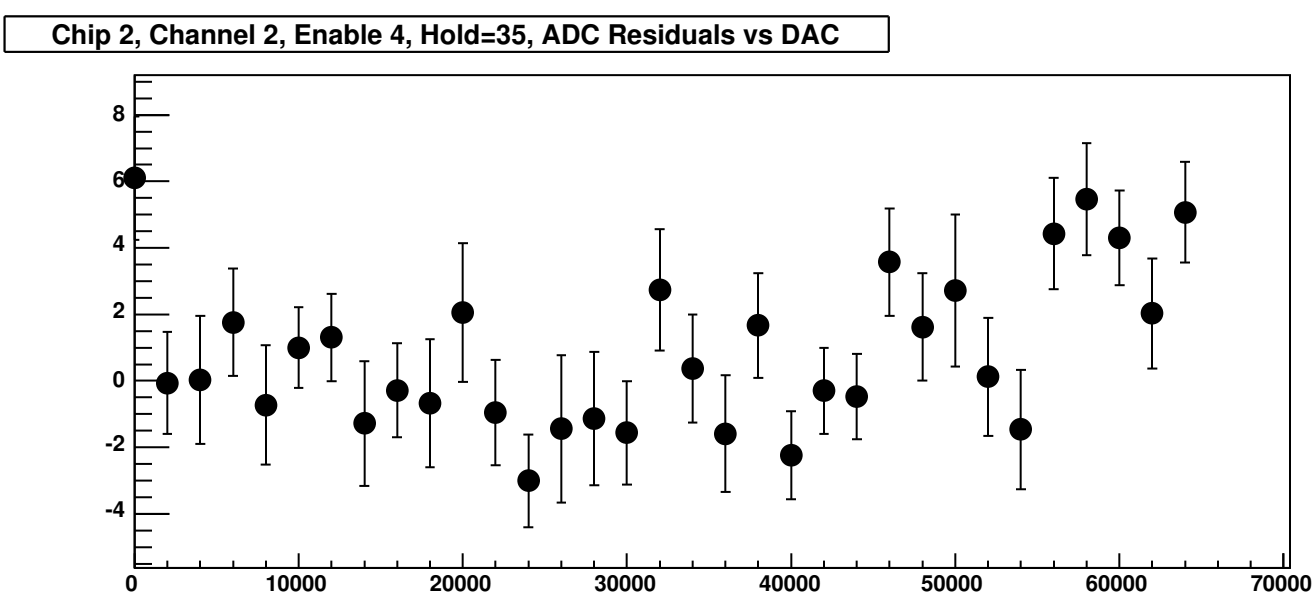

Chip 2, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 50.4 / 23
p0 417.7 ± 0.6915
p1 0.00329 ± 2.164e-05
650
600
550
500
450
0
10000
20000
30000
40000
50000
60000
70000

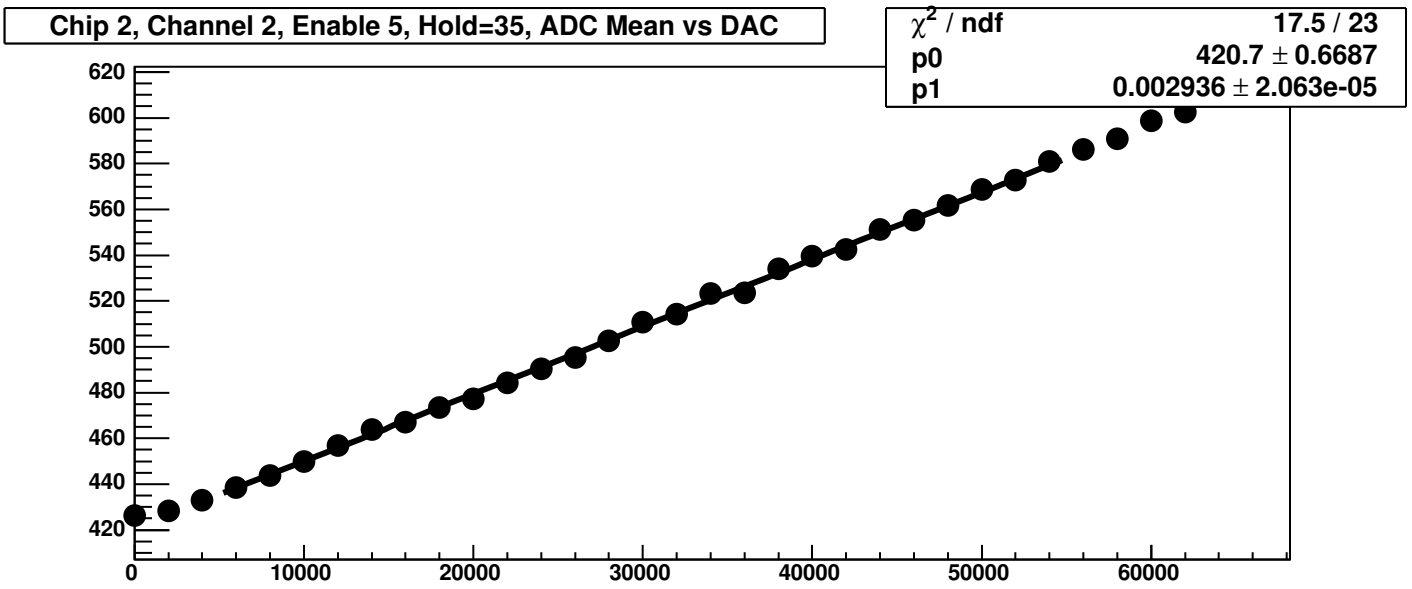

Chip 2, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

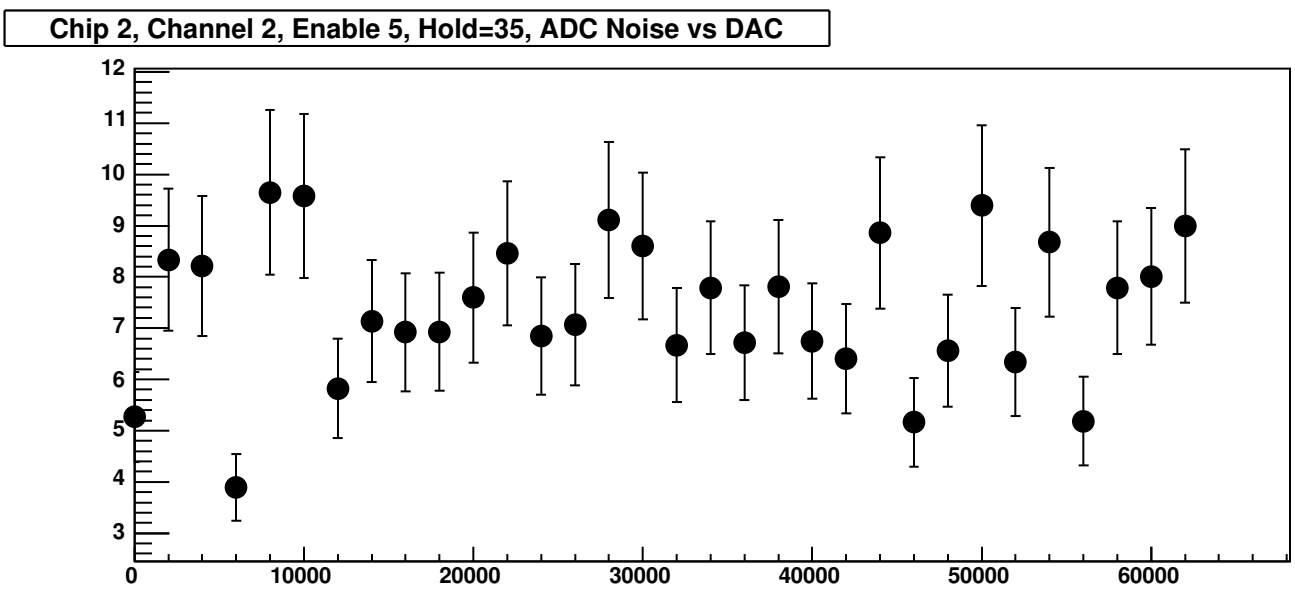

Chip 2, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

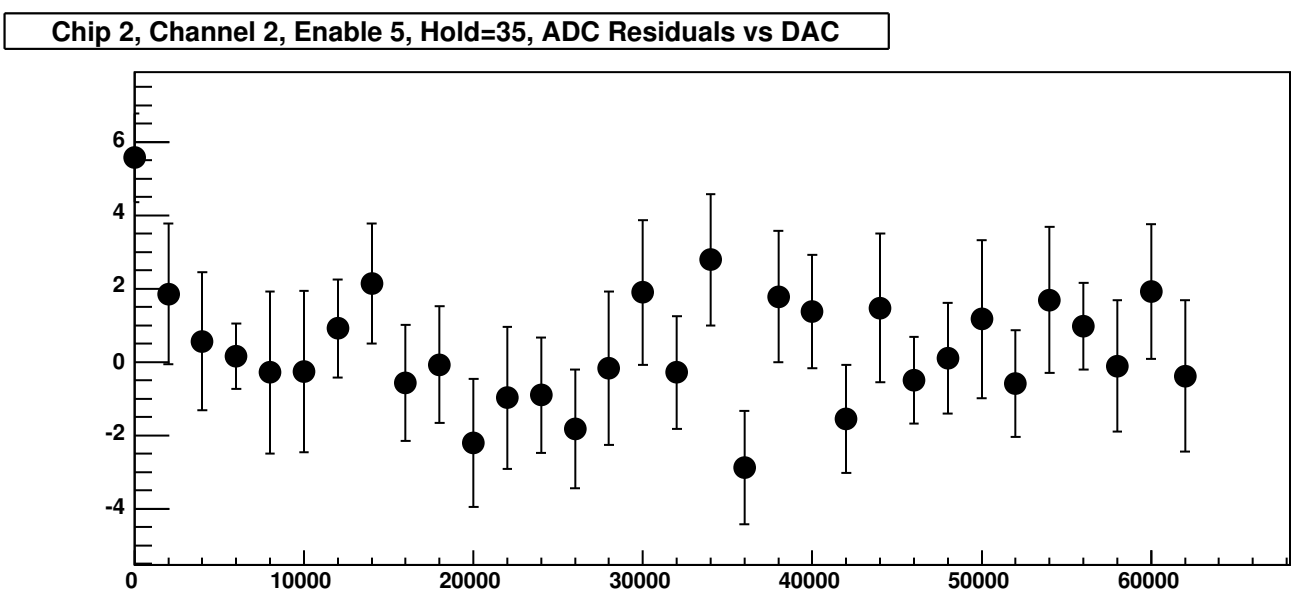

Chip 2, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.71 / 23
p0 420 ± 0.7258
p1 0.003162 ± 2.209e-05
640
620
600
580
560
540
520
500
480
460
440
420
0
10000
20000
30000
40000
50000
60000
70000

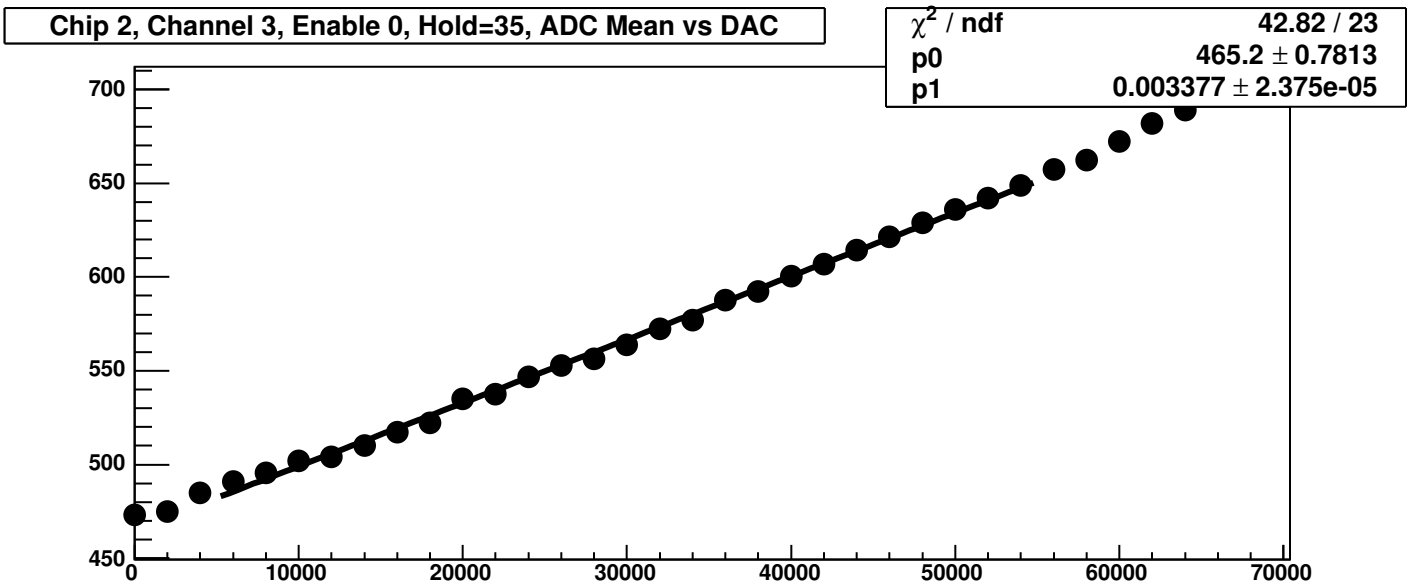

Chip 2, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

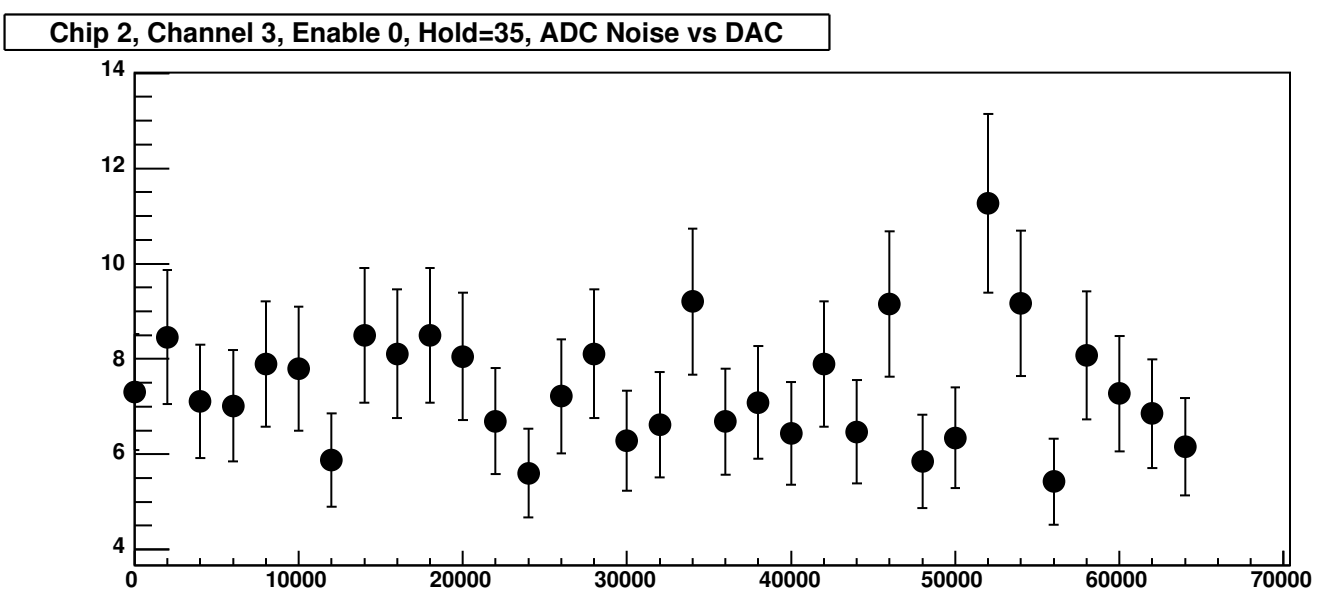

Chip 2, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
0
10000
20000
30000
40000
50000
60000
70000

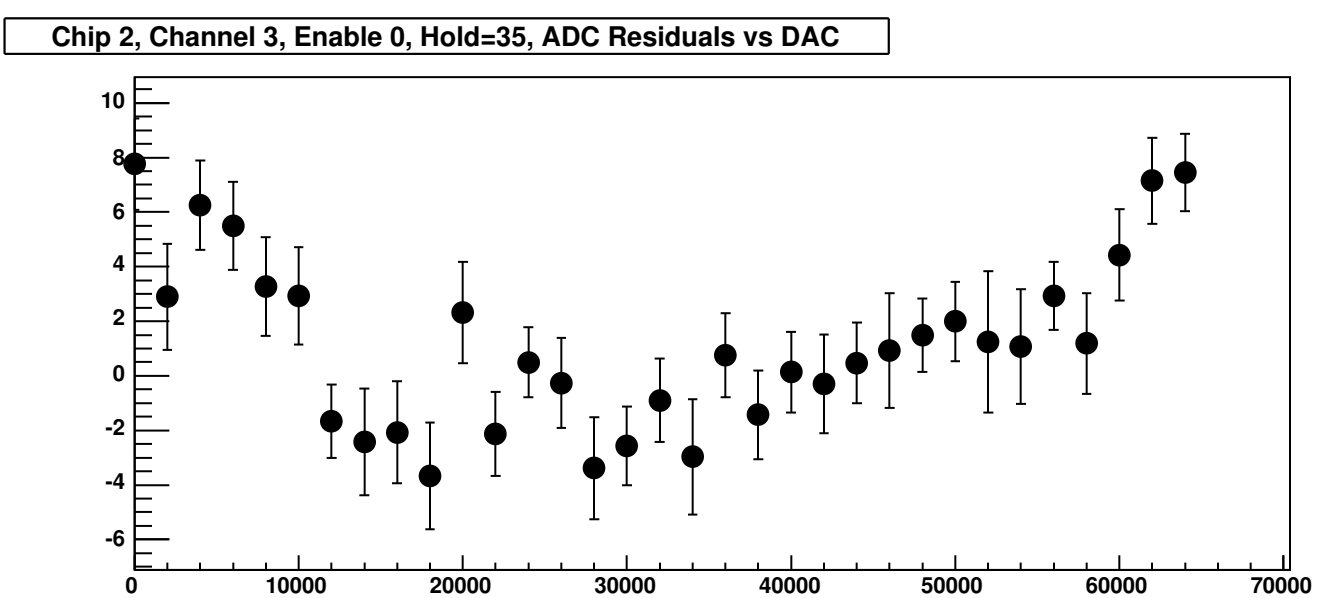

Chip 2, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 17.5 / 23
p0 420.7 ± 0.6687
p1 0.002936 ± 2.063e-05
620
600
580
560
540
520
500
480
460
440
420
0
10000
20000
30000
40000
50000
60000

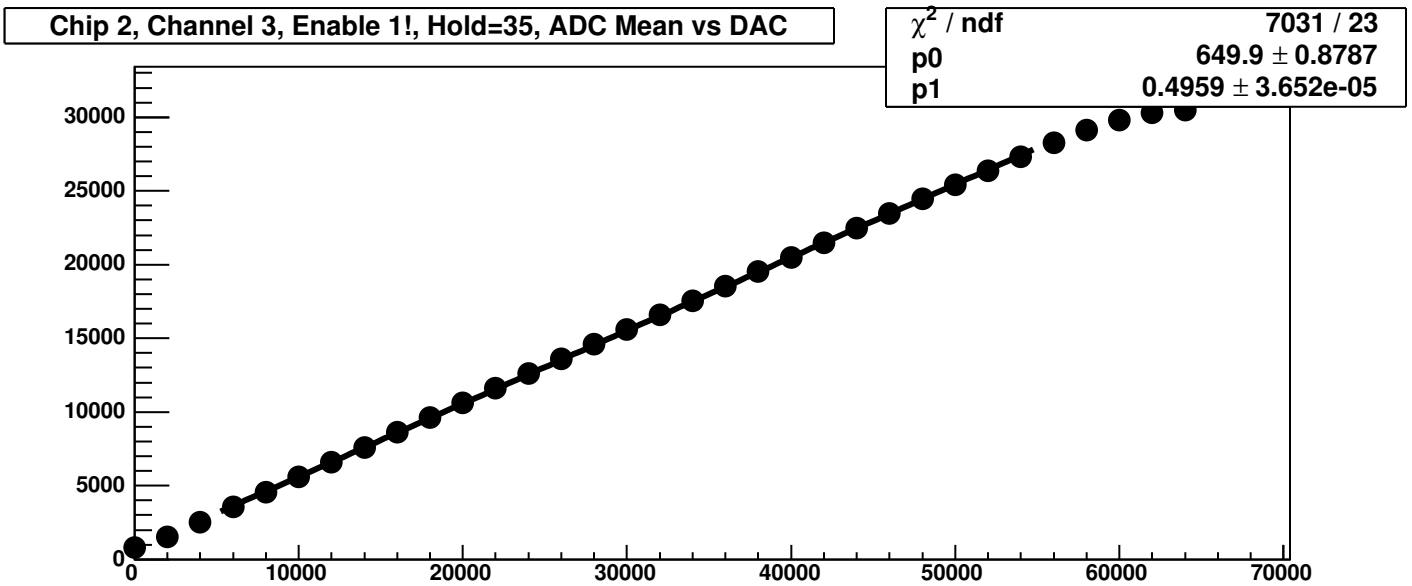

Chip 2, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000

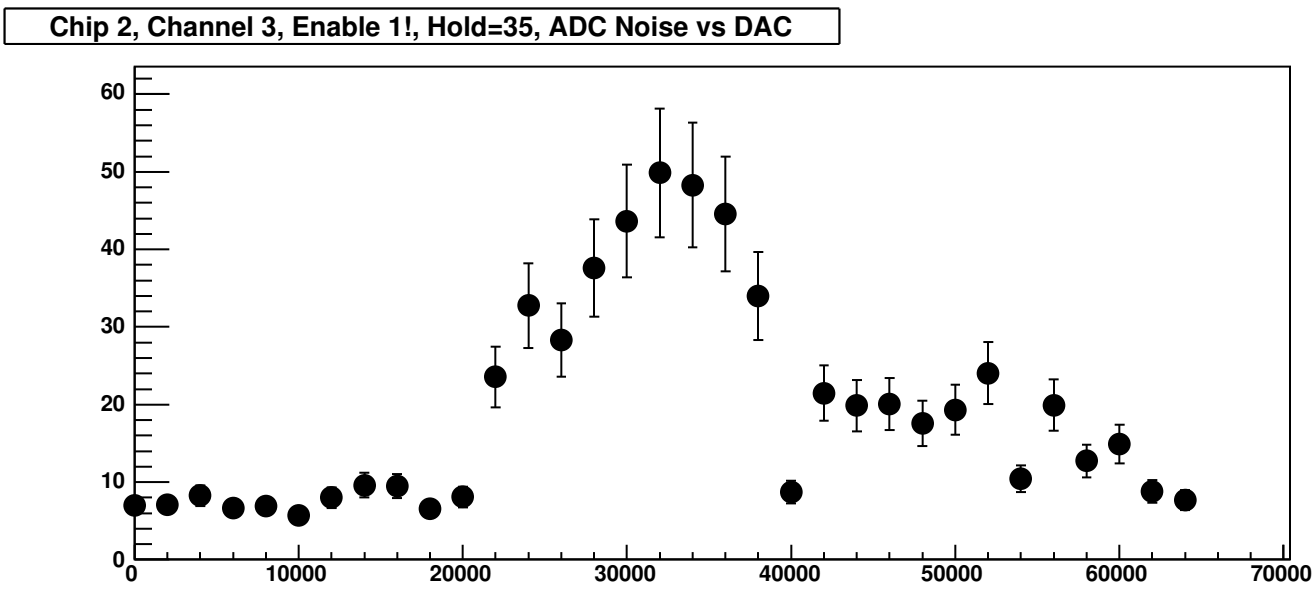

Chip 2, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
0
10000
20000
30000
40000
50000
60000

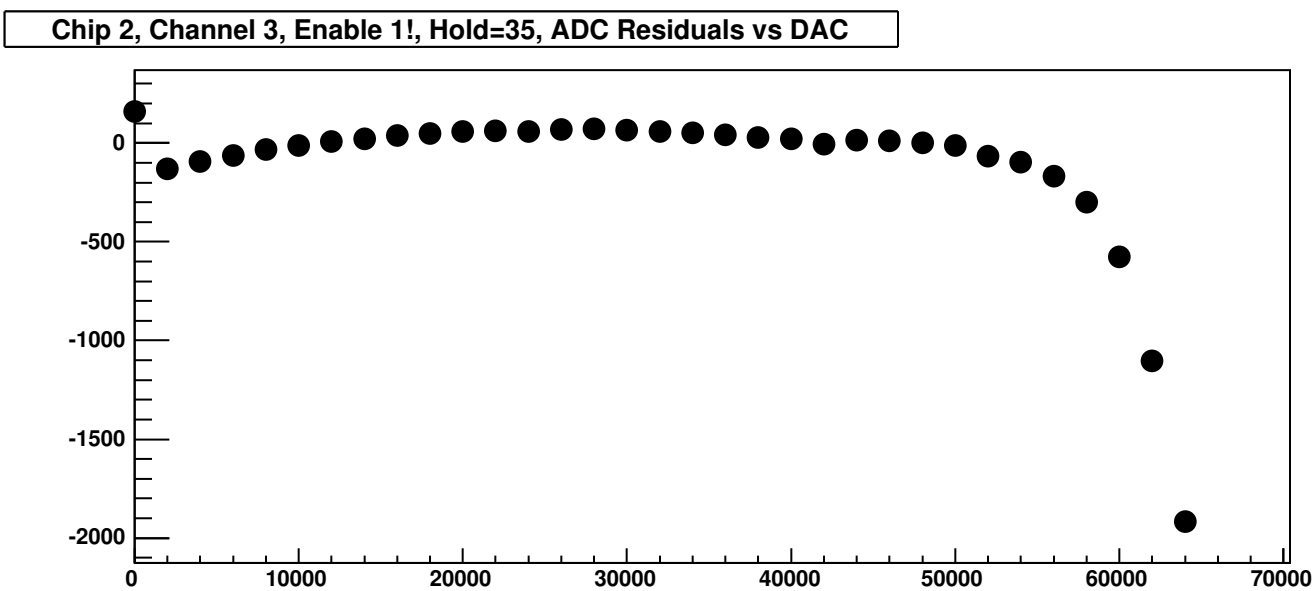

Chip 2, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 42.82 / 23
p0 465.2 ± 0.7813
p1 0.003377 ± 2.375e-05
700
650
600
550
500
450
0
10000
20000
30000
40000
50000
60000
70000

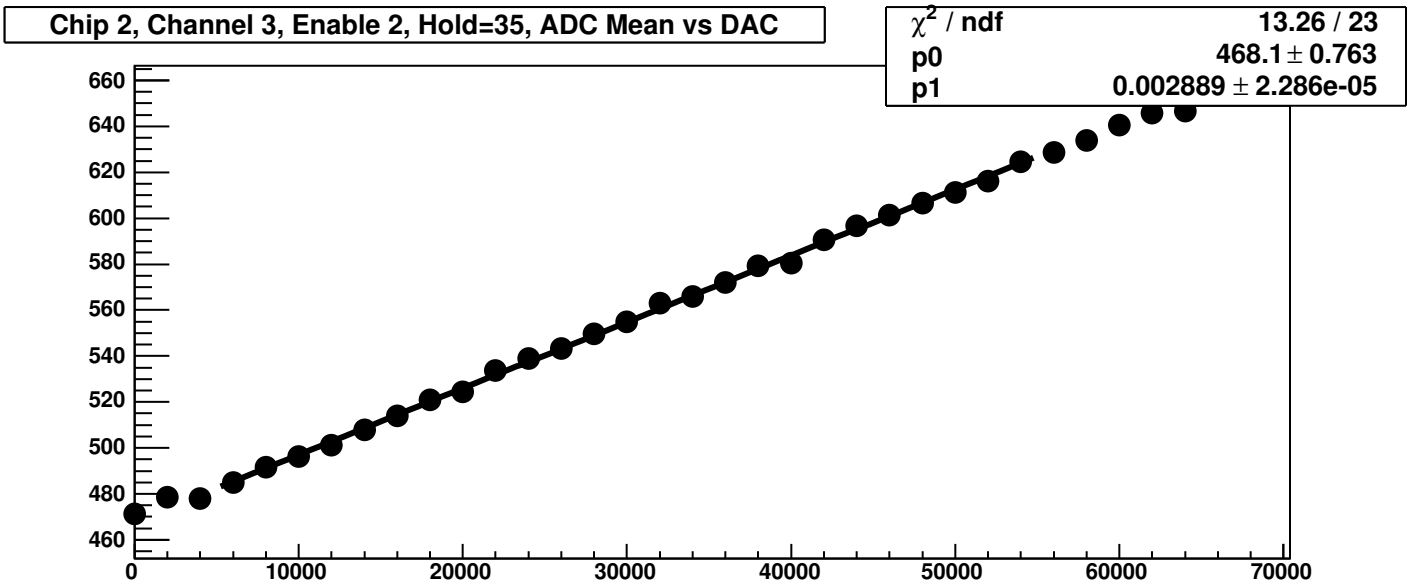

Chip 2, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

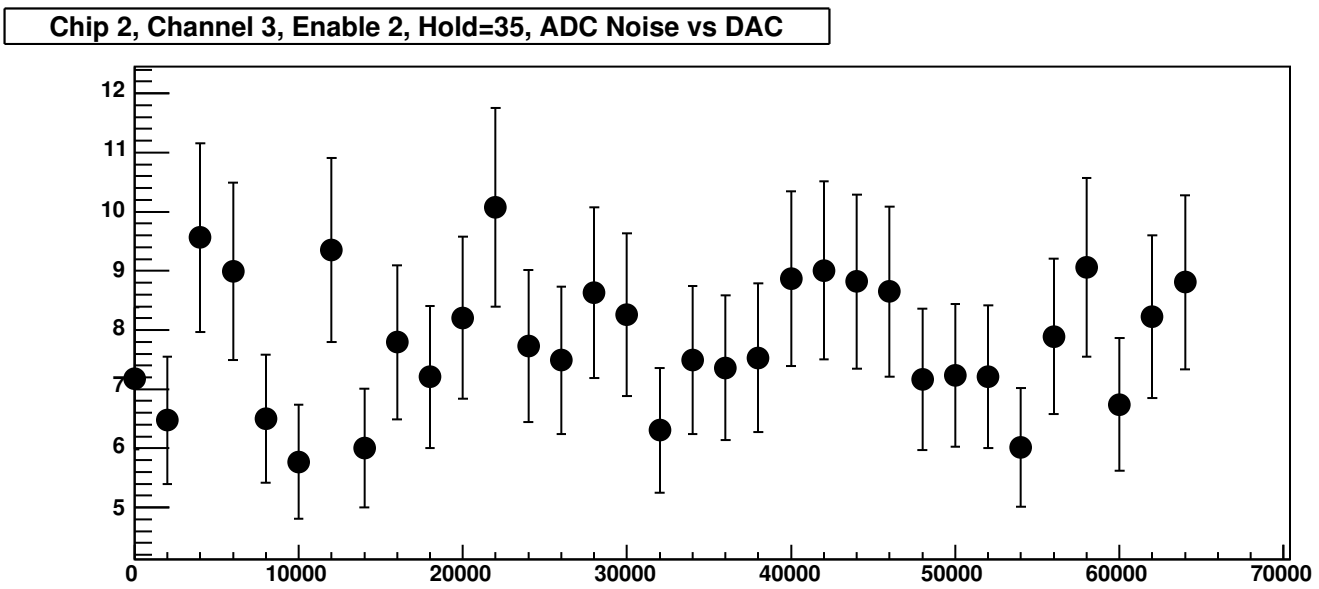

Chip 2, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

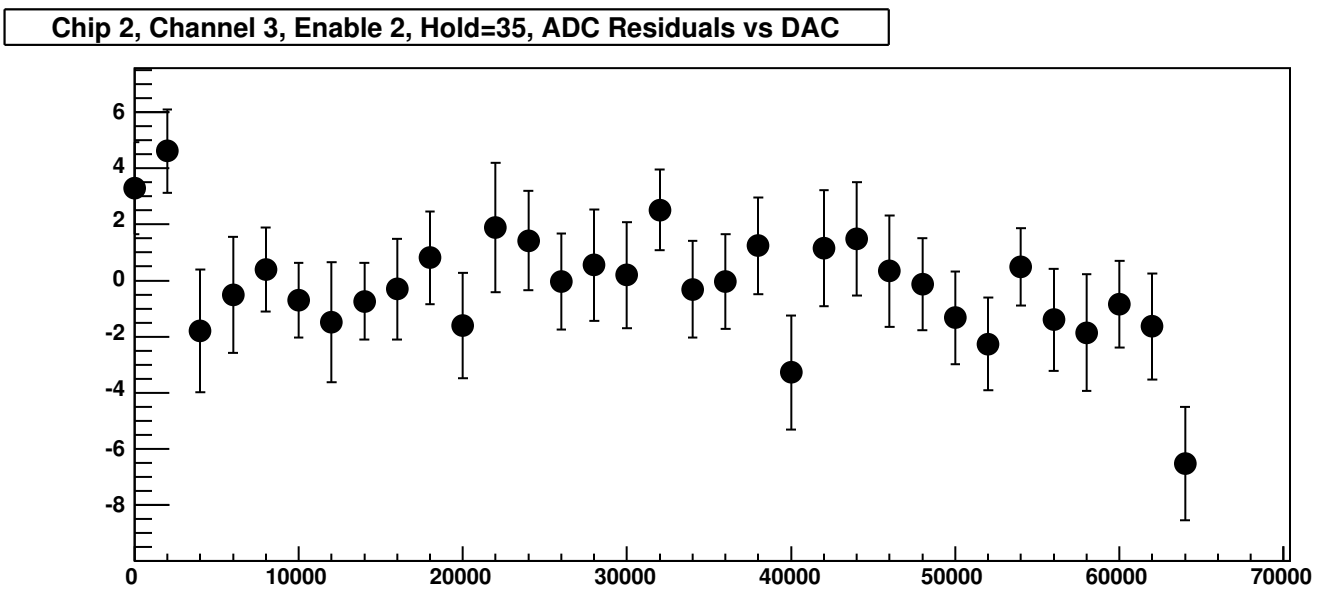

Chip 2, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 7031 / 23
p0 649.9 ± 0.8787
p1 0.4959 ± 3.652e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

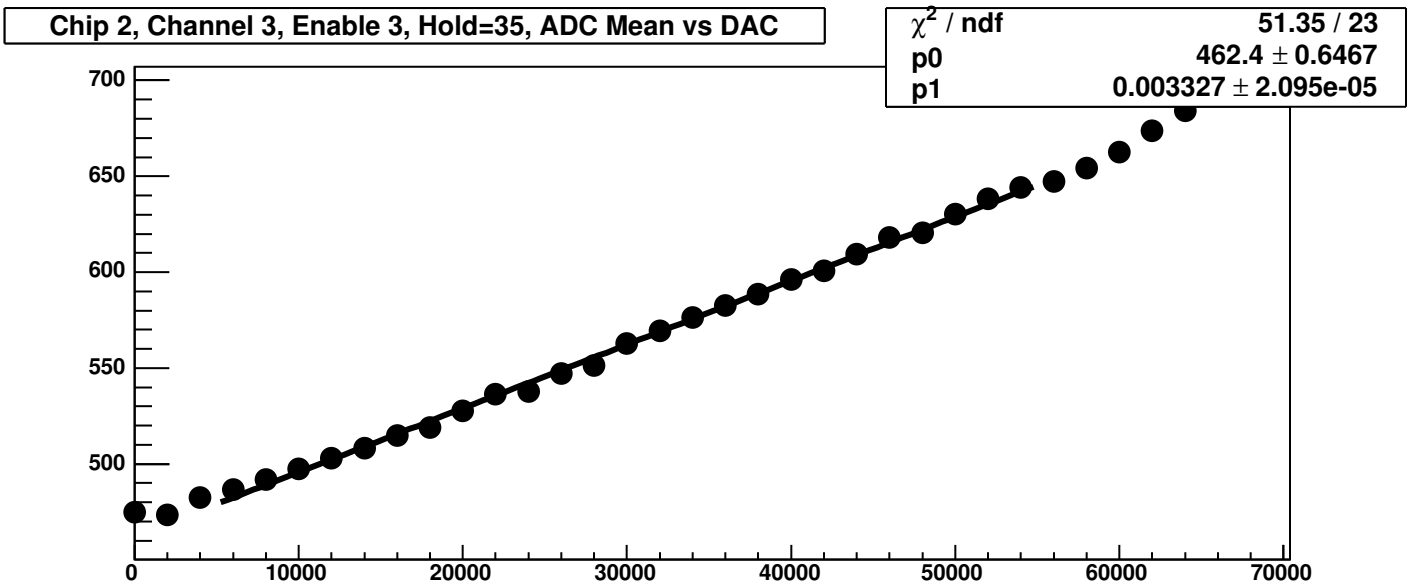

Chip 2, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

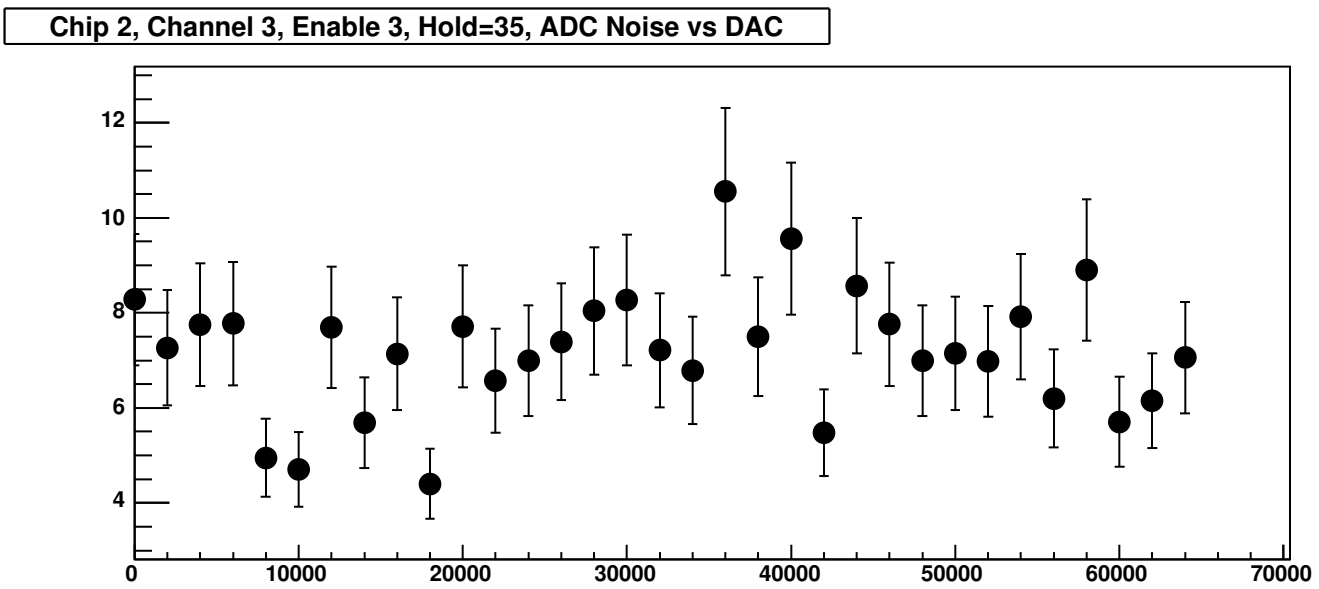

Chip 2, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000

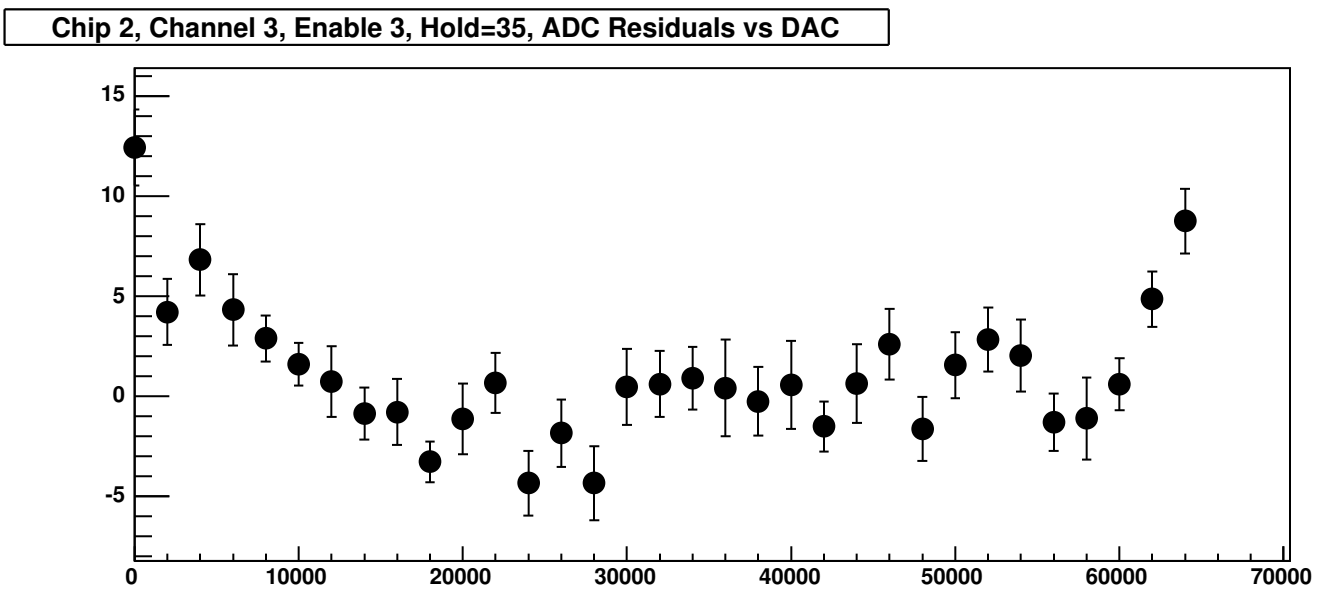

Chip 2, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 13.26 / 23
p0 468.1 ± 0.763
p1 0.002889 ± 2.286e-05
660
640
620
600
580
560
540
520
500
480
460
0
10000
20000
30000
40000
50000
60000
70000

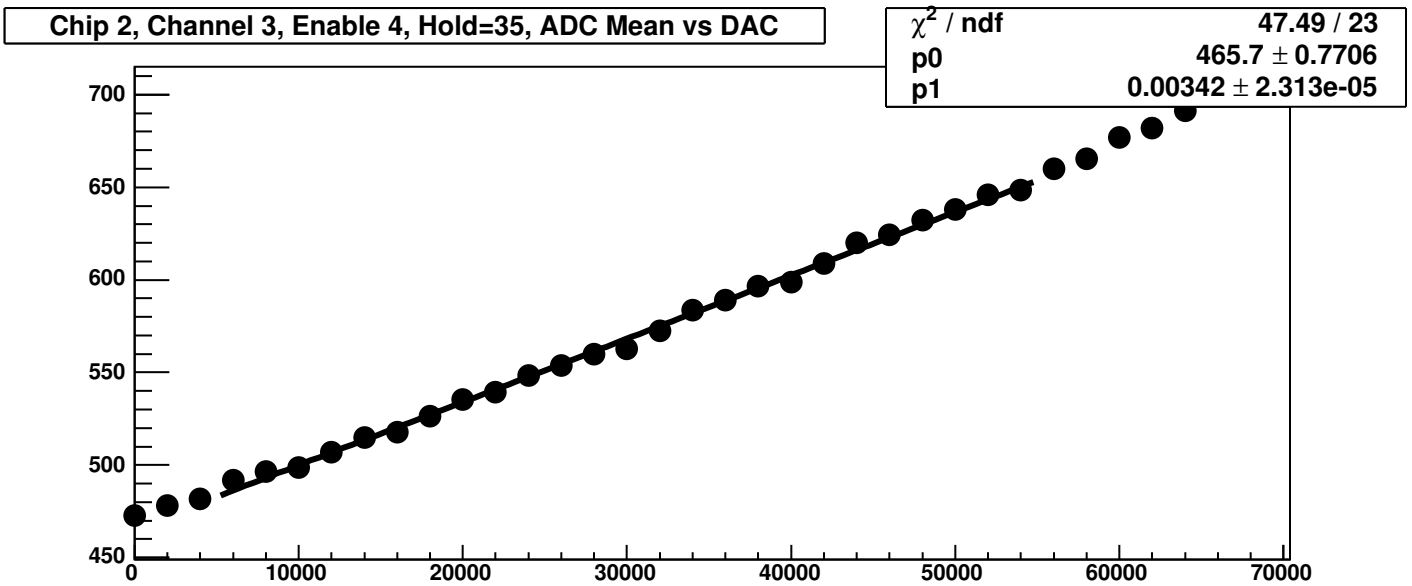

Chip 2, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000

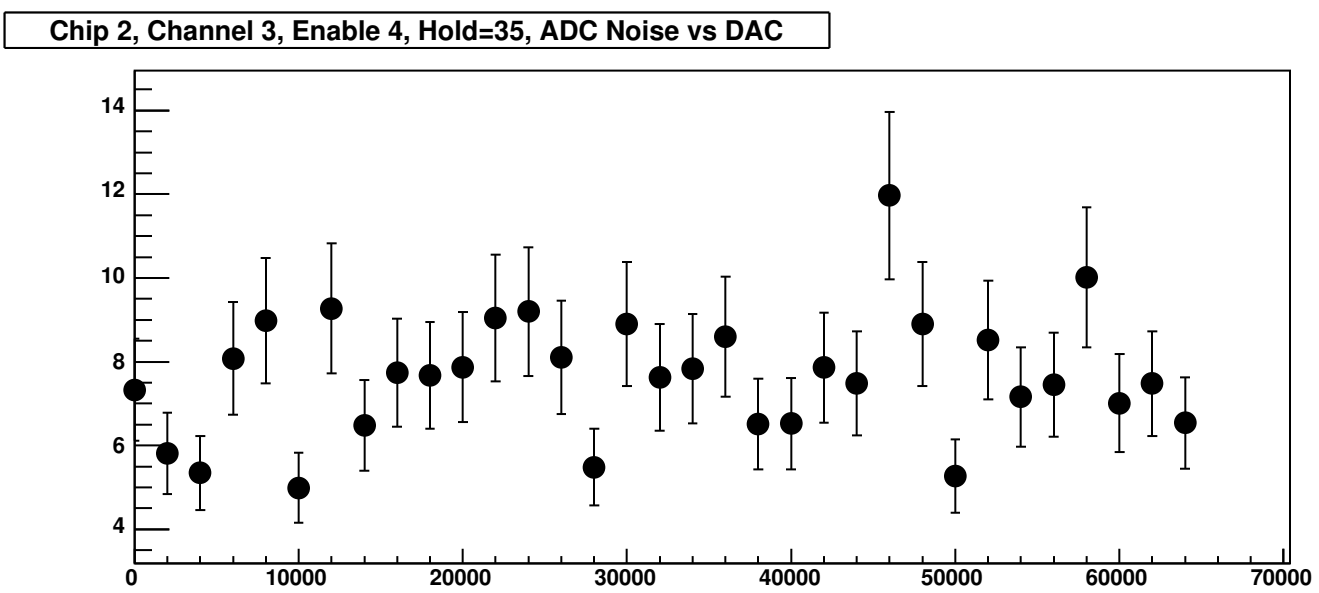

Chip 2, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

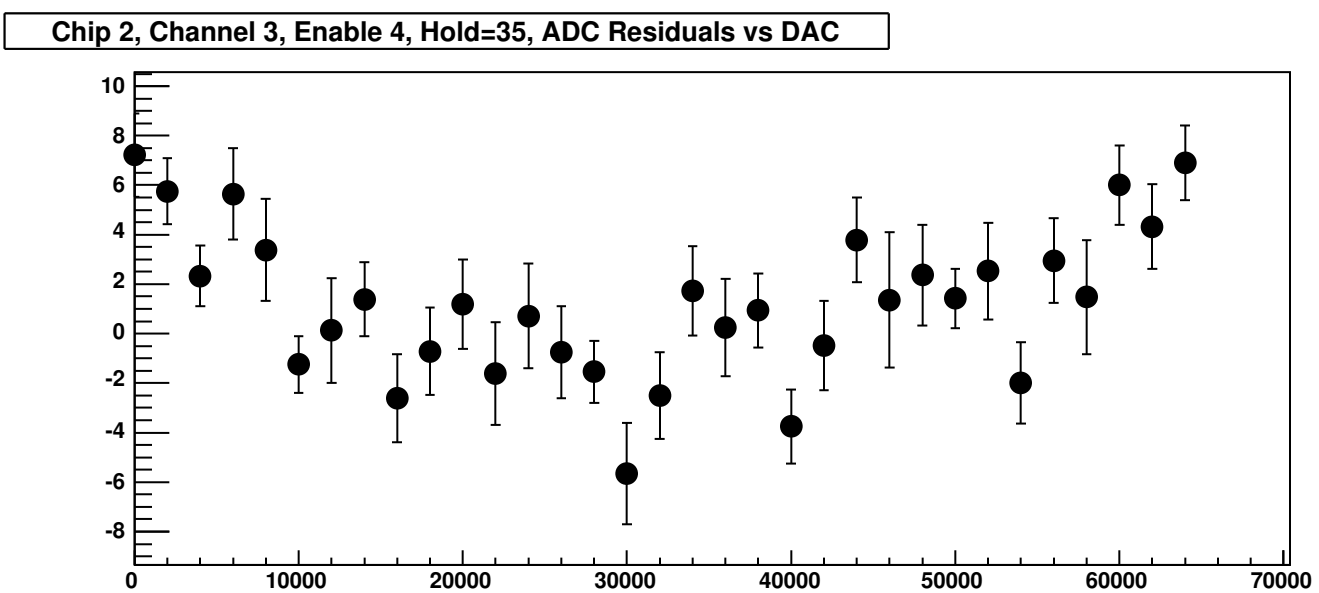

Chip 2, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 51.35 / 23
p0 462.4 ± 0.6467
p1 0.003327 ± 2.095e-05
700
650
600
550
500
0
10000
20000
30000
40000
50000
60000
70000

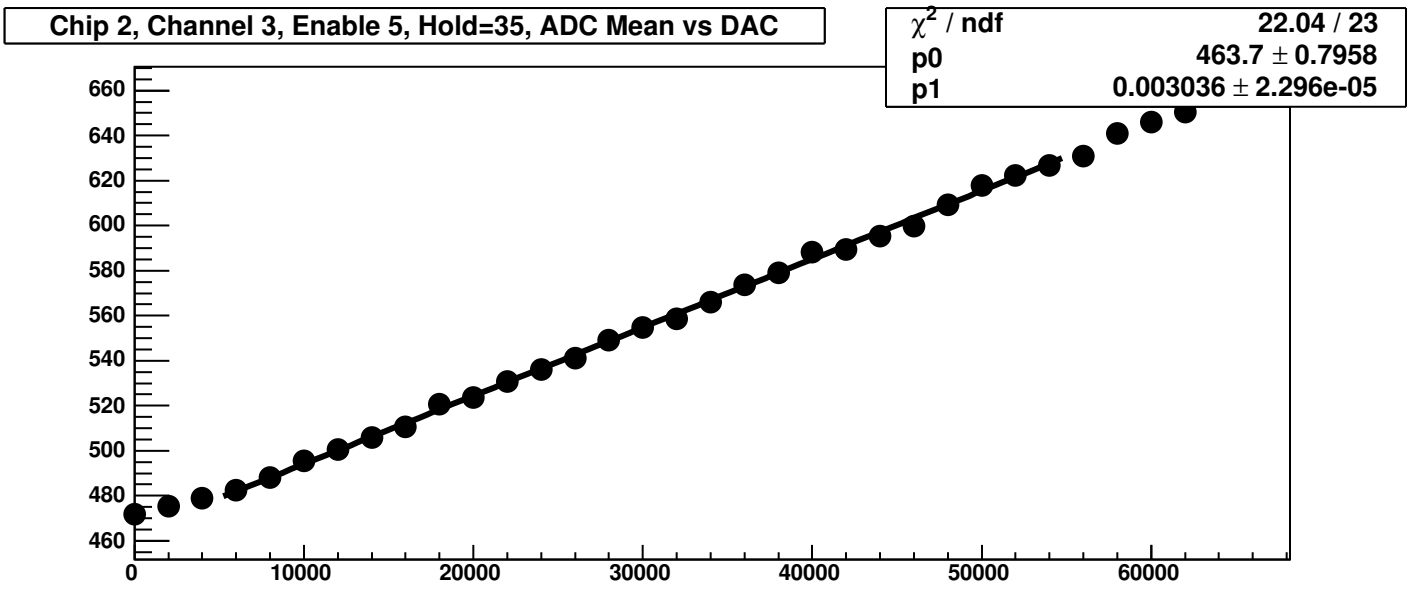

Chip 2, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

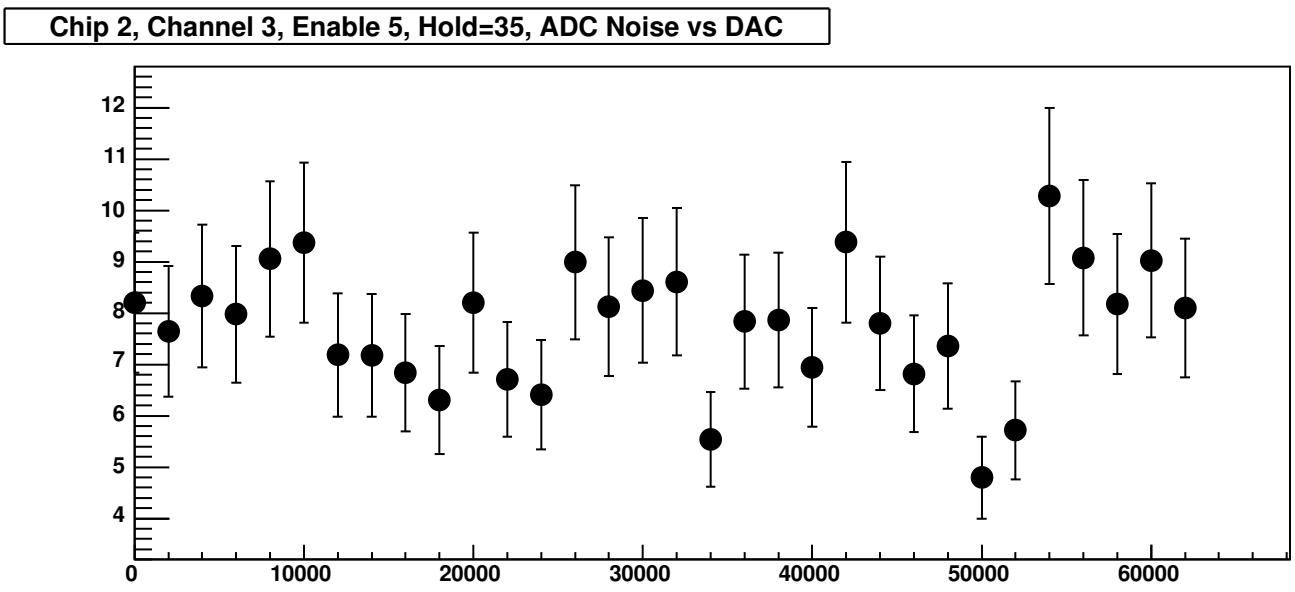

Chip 2, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

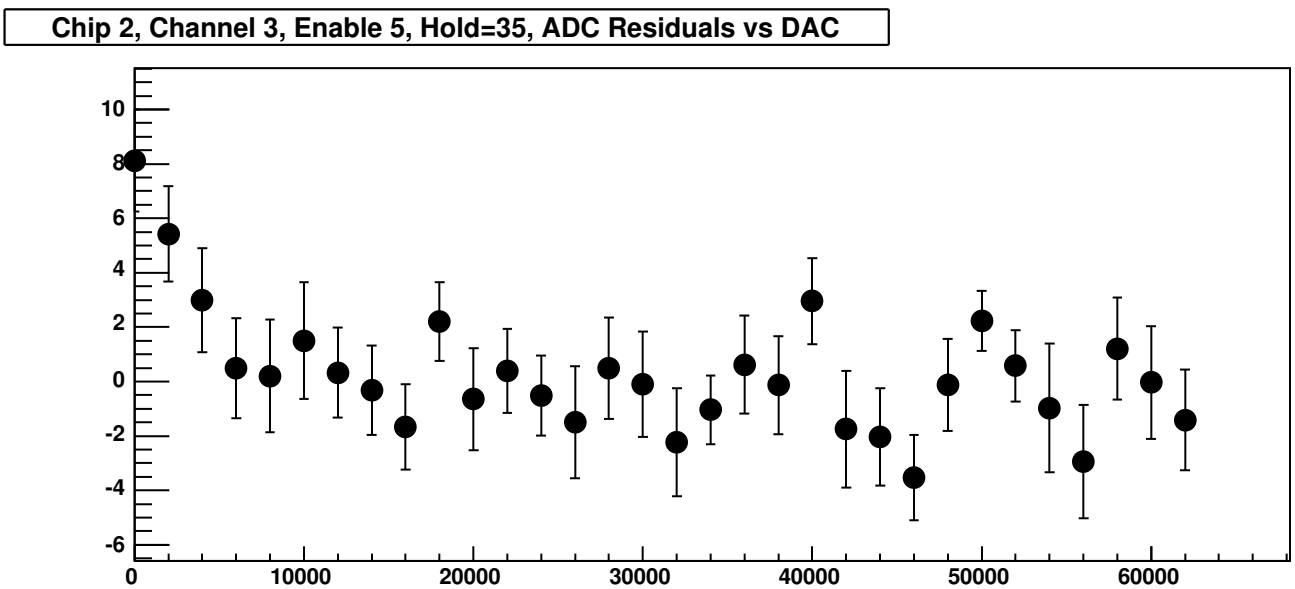

Chip 2, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 47.49 / 23
p0 465.7 ± 0.7706
p1 0.00342 ± 2.313e-05
700
650
600
550
500
450
0
10000
20000
30000
40000
50000
60000
70000

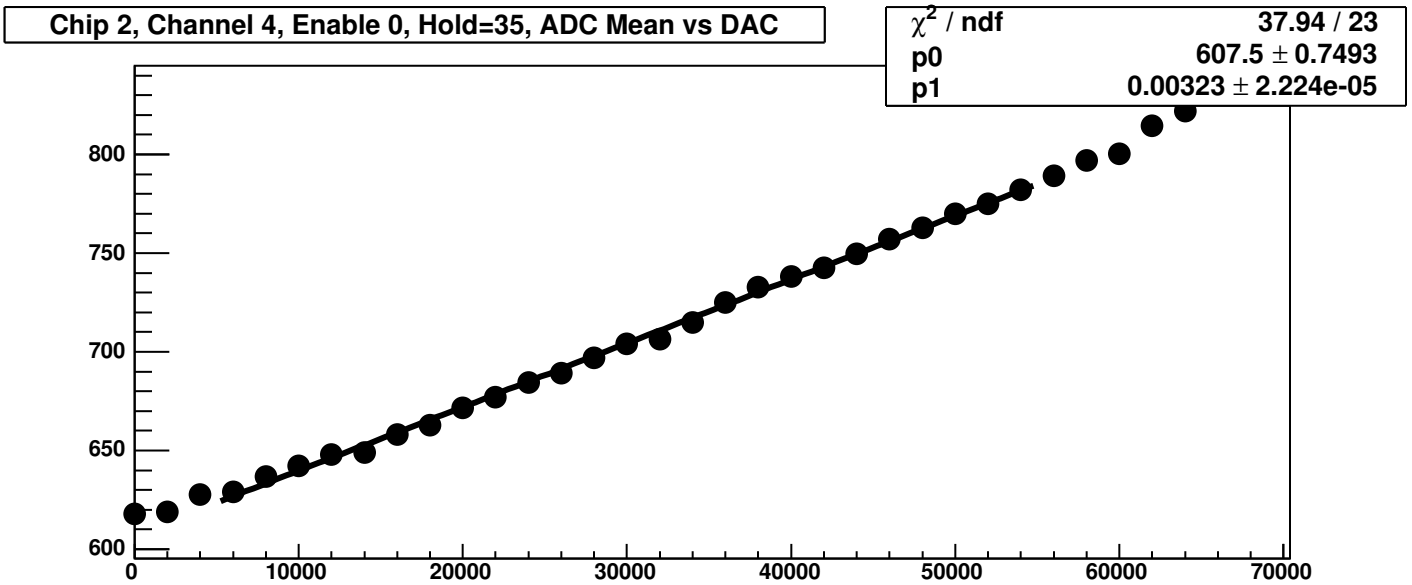

Chip 2, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

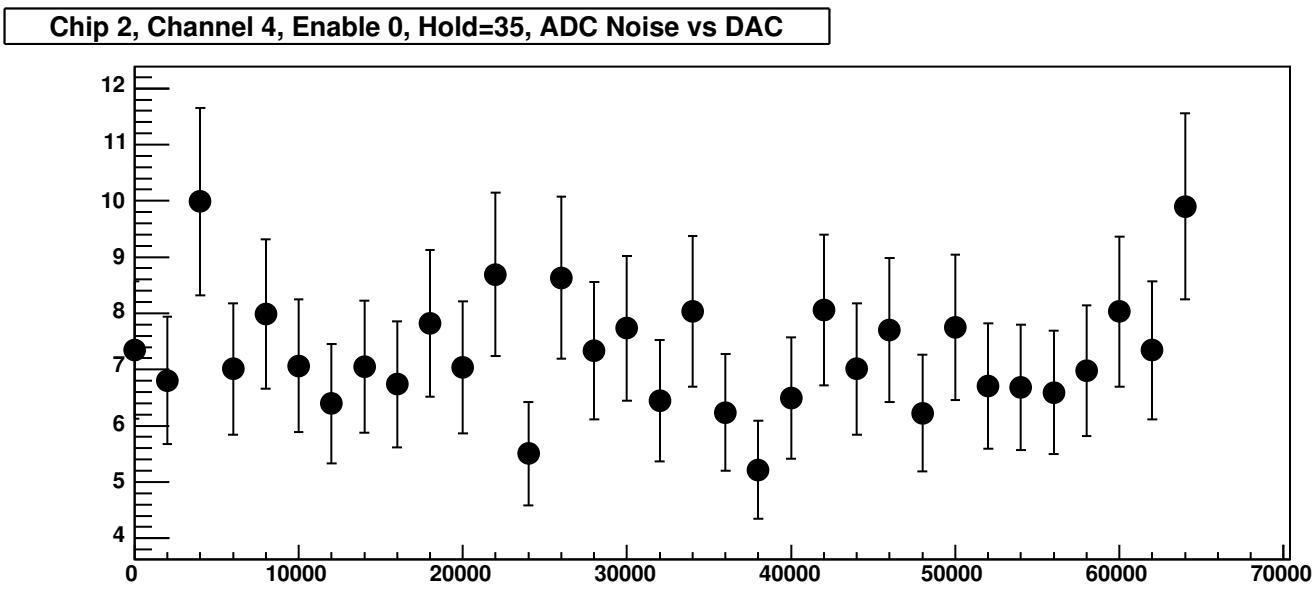

Chip 2, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

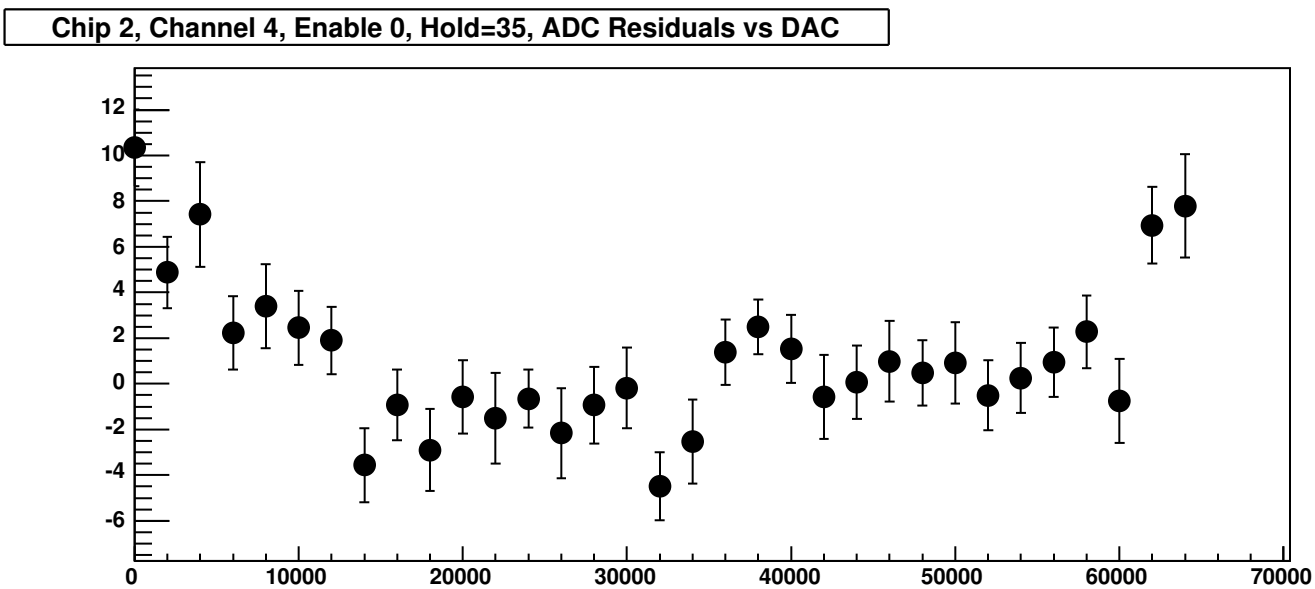

Chip 2, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 22.04 / 23
p0 463.7 ± 0.7958
p1 0.003036 ± 2.296e-05
660
640
620
600
580
560
540
520
500
480
460
0
10000
20000
30000
40000
50000
60000

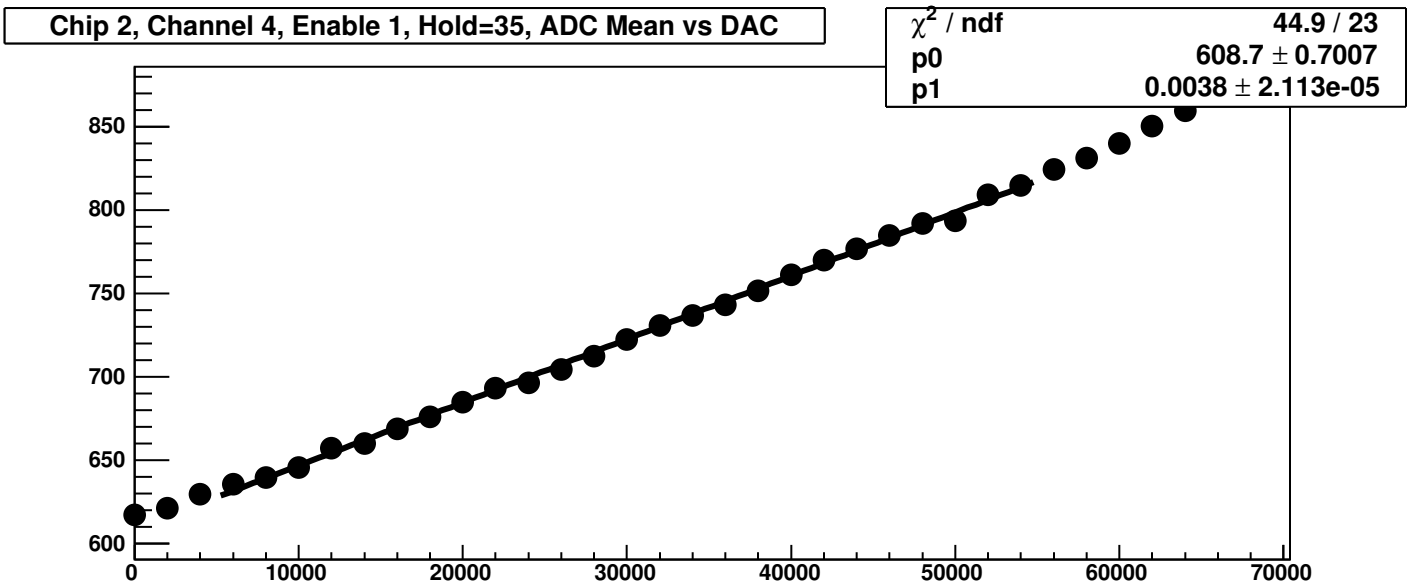

Chip 2, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

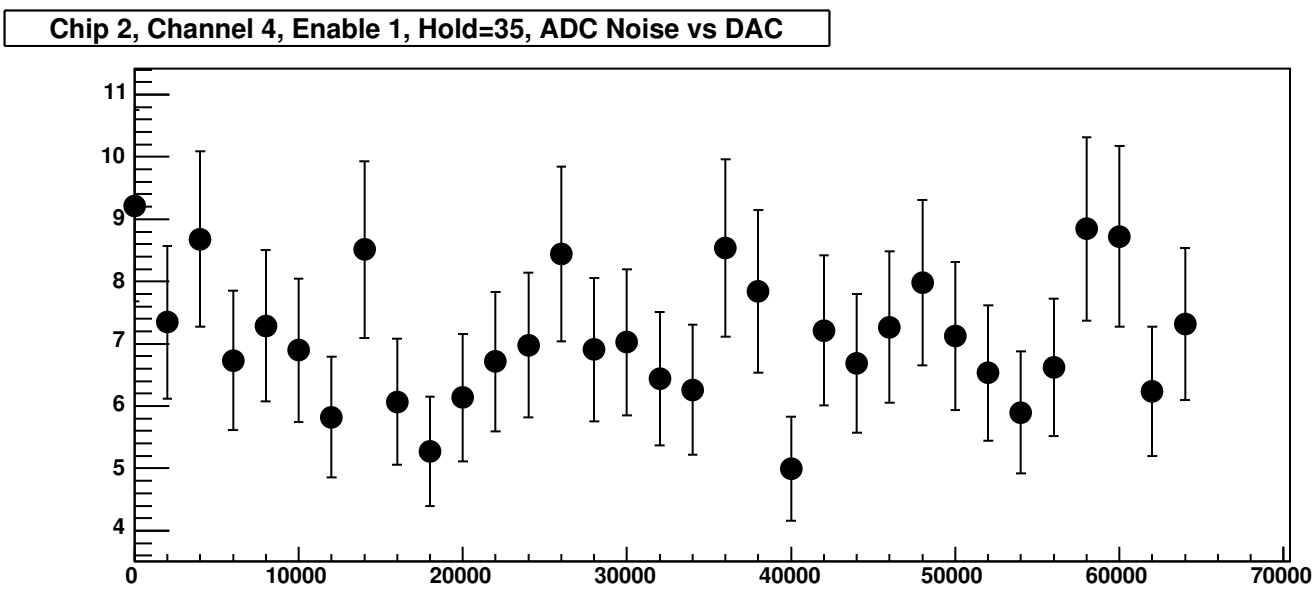

Chip 2, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000

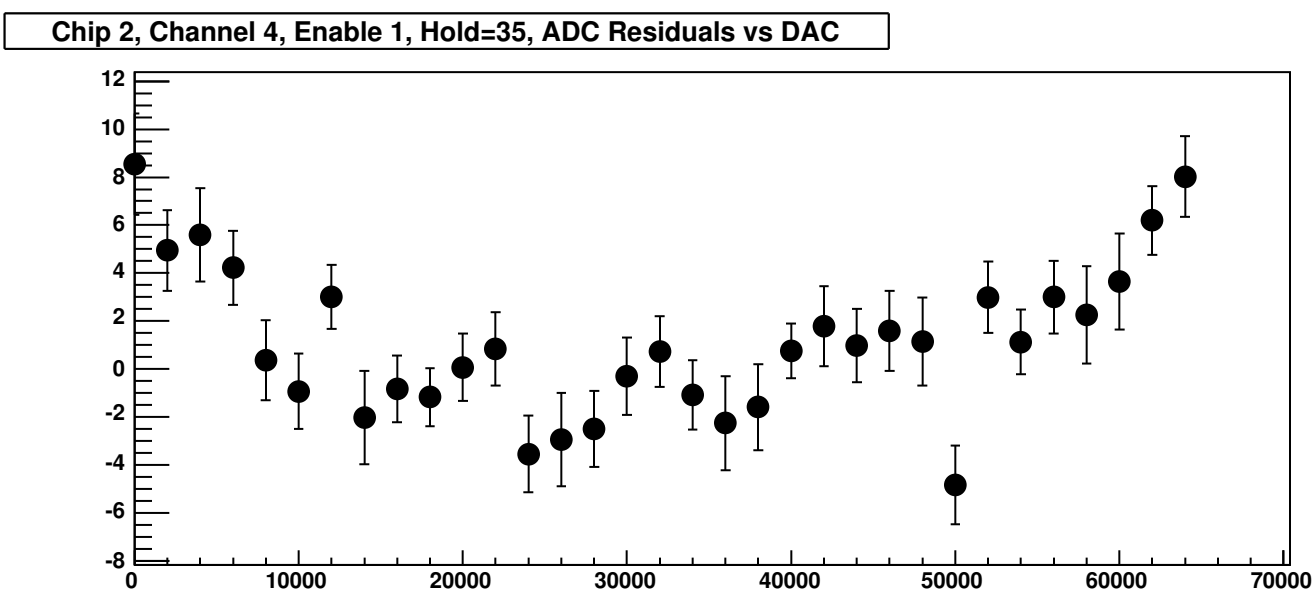

Chip 2, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 37.94 / 23
p0 607.5 ± 0.7493
p1 0.00323 ± 2.224e-05
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

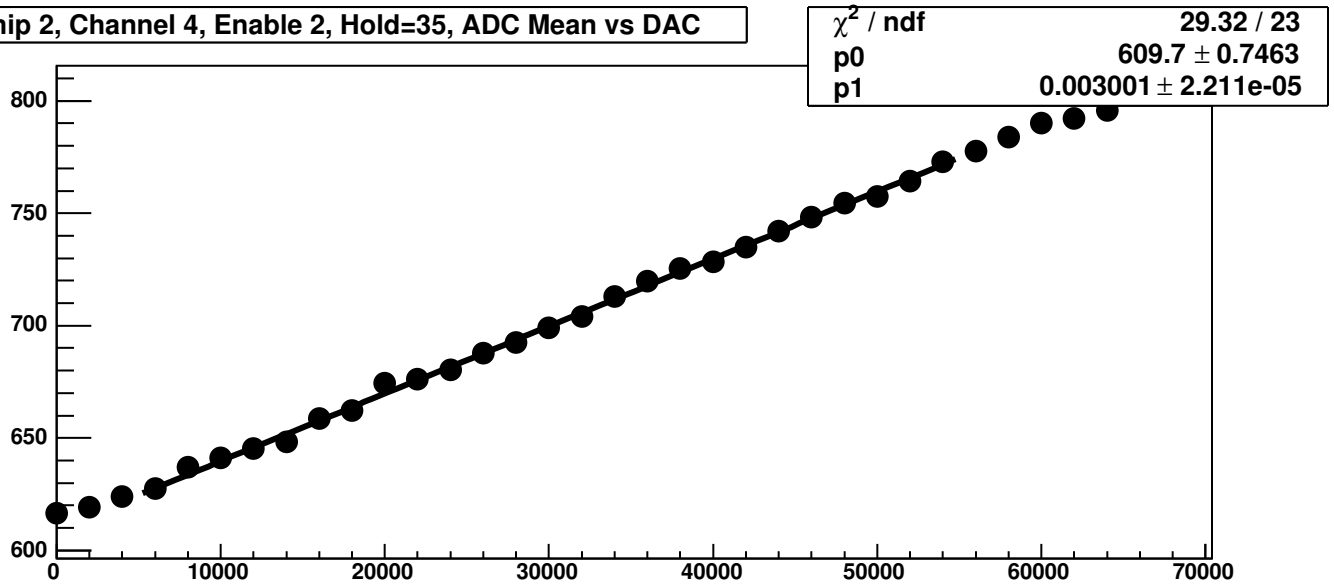

Chip 2, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 44.9 / 23
p0 608.7 ± 0.7007
p1 0.0038 ± 2.113e-05
850
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000
70000

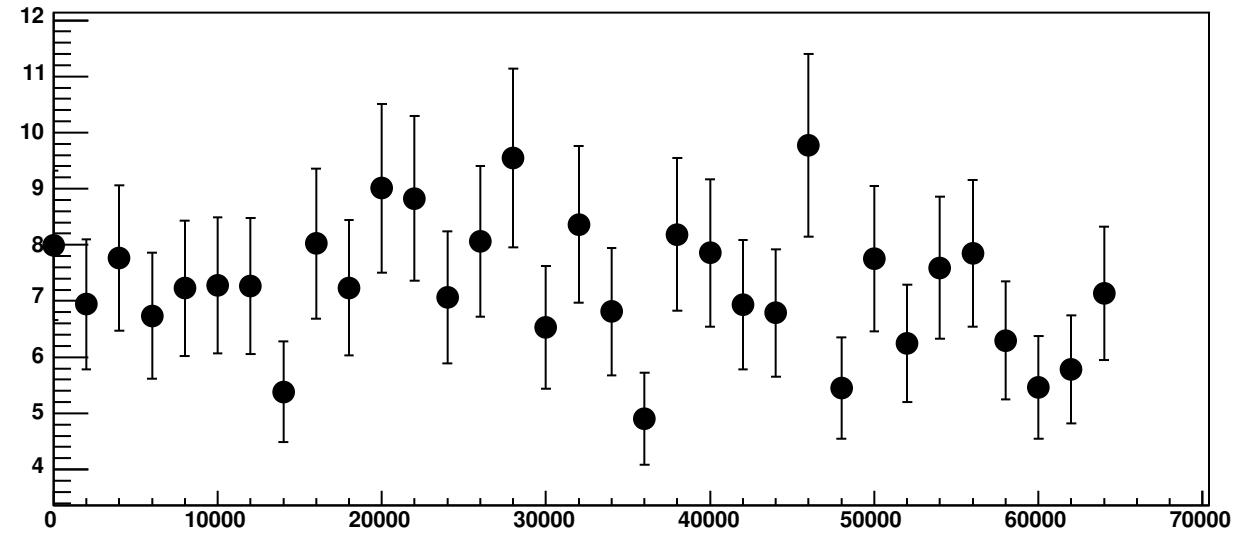

Chip 2, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

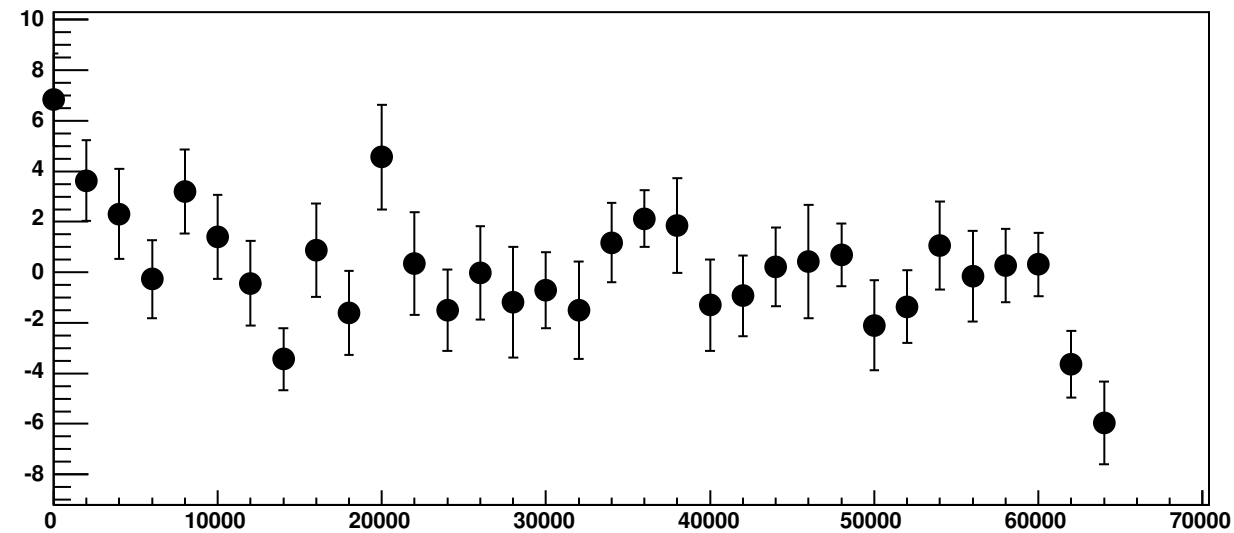
Chip 2, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC



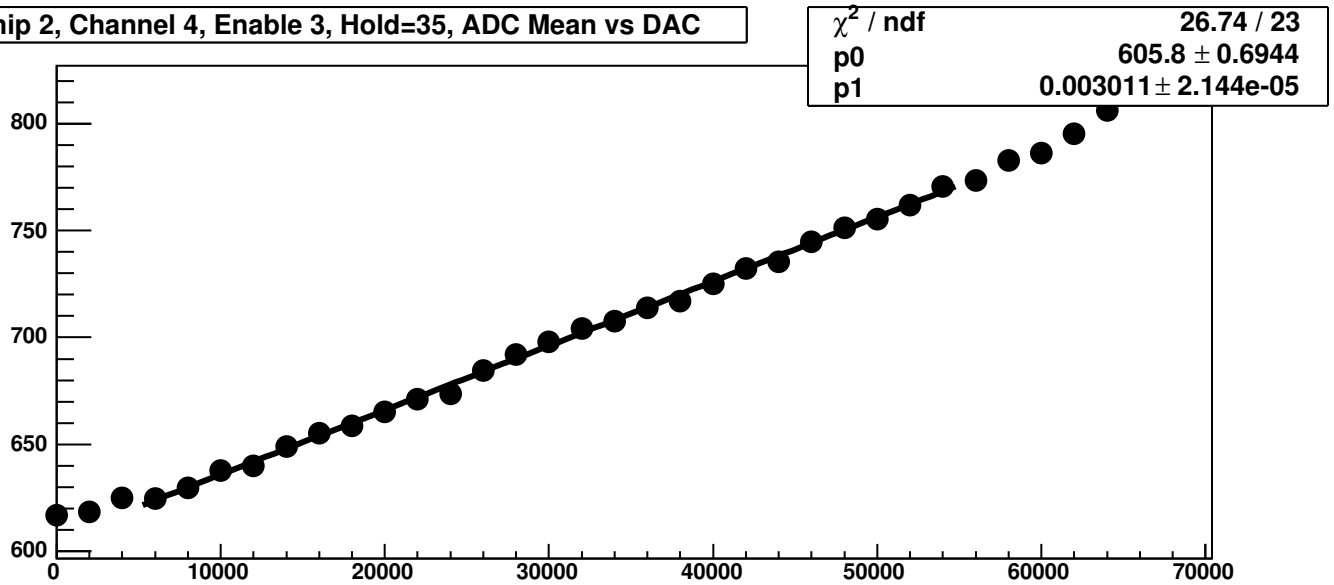
Chip 2, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC



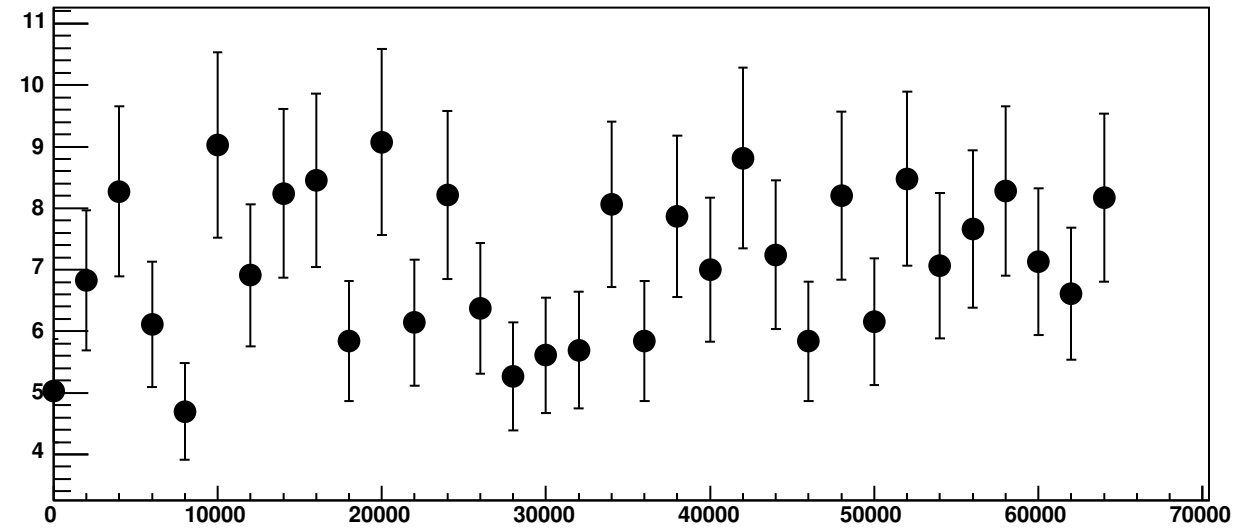
Chip 2, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC



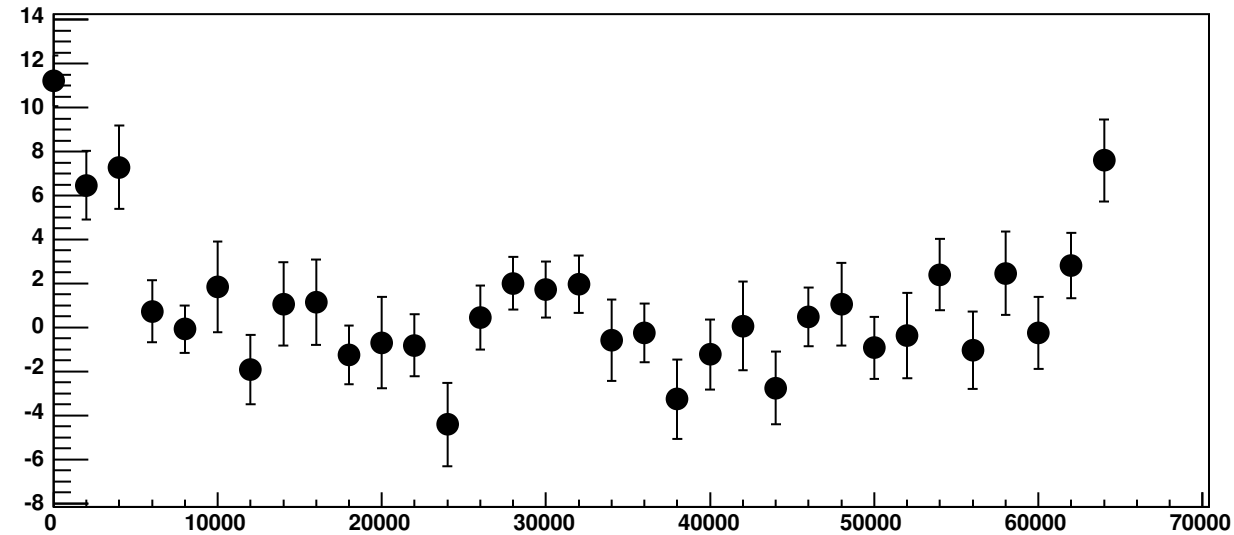
Chip 2, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC



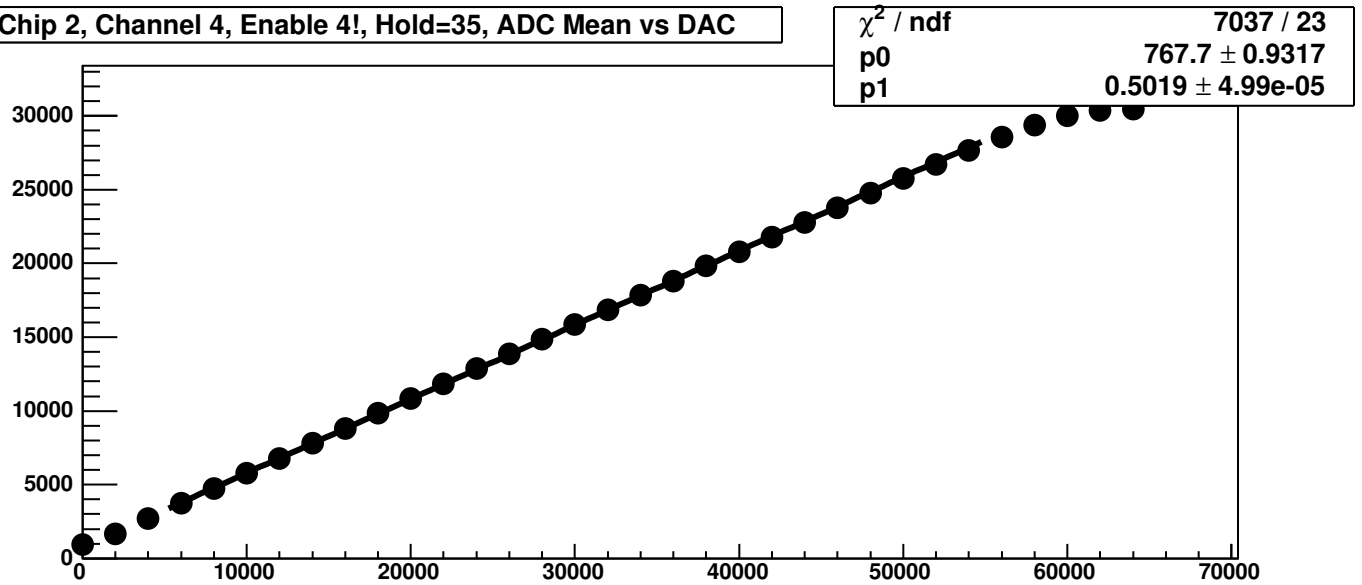
Chip 2, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC



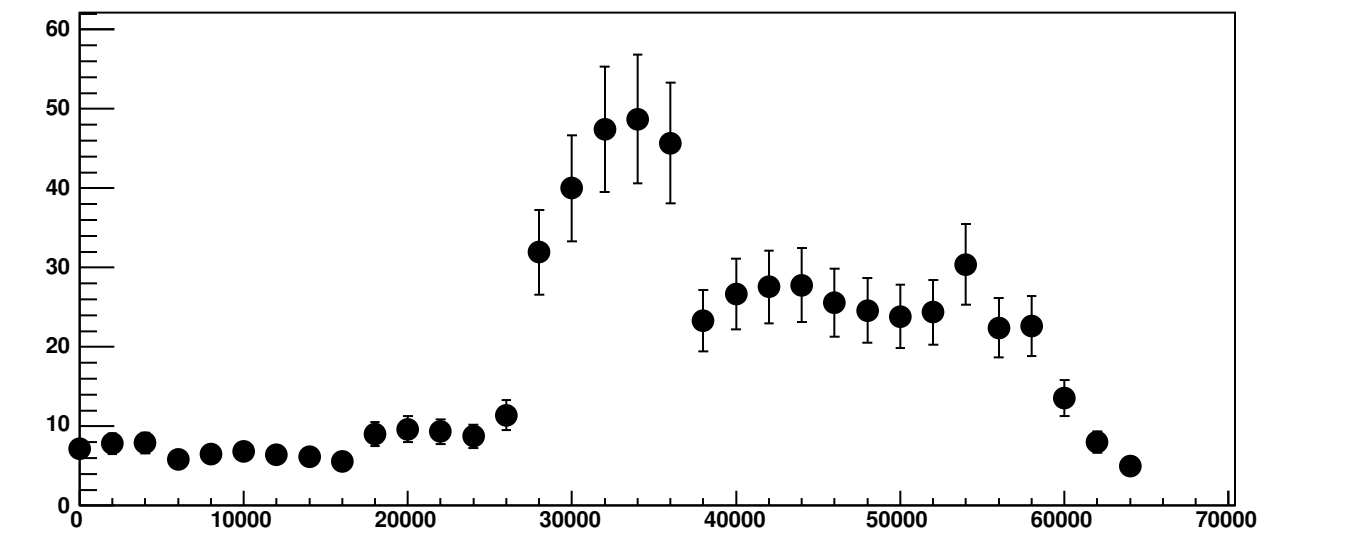
Chip 2, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC



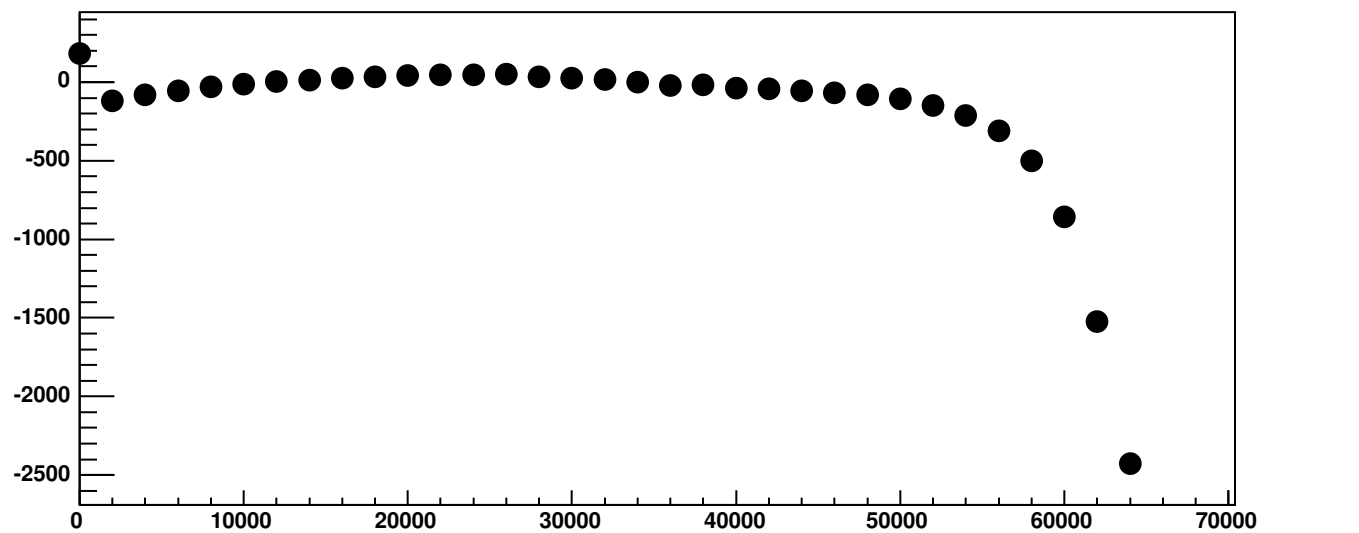
Chip 2, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC

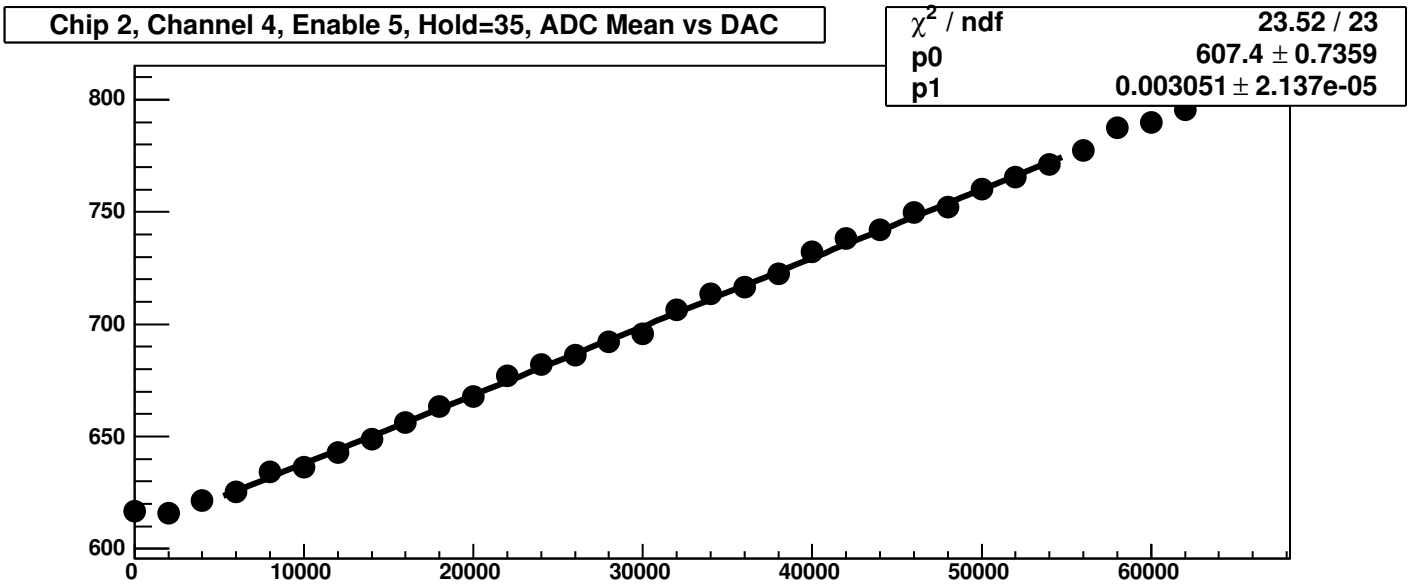


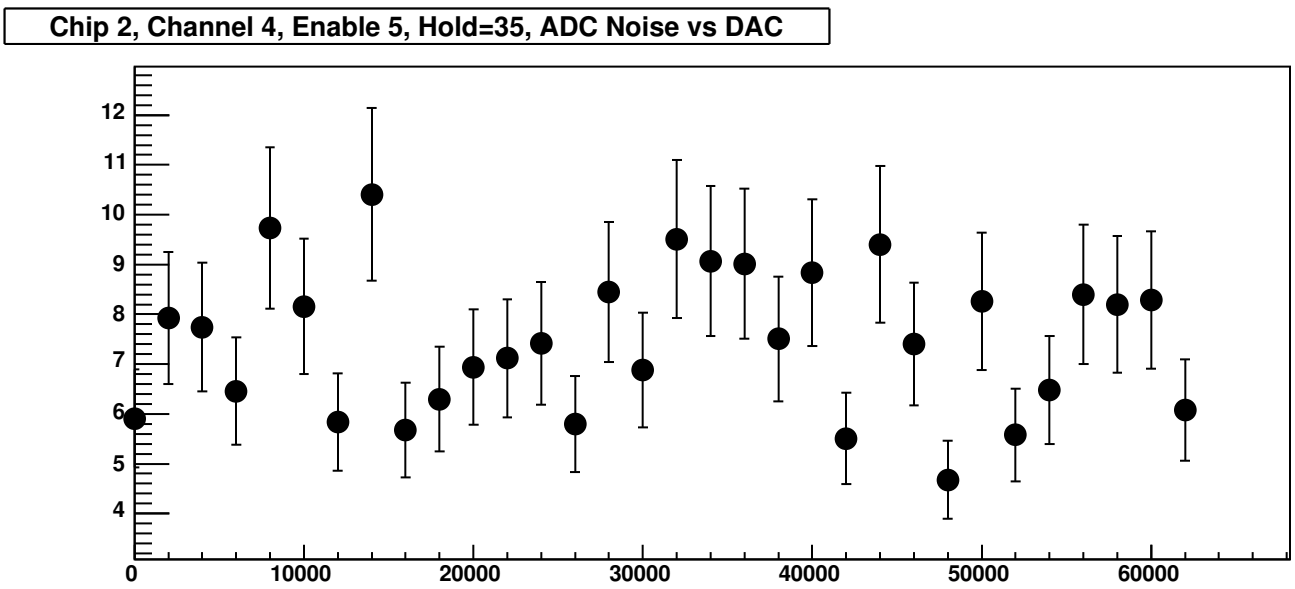
Chip 2, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC

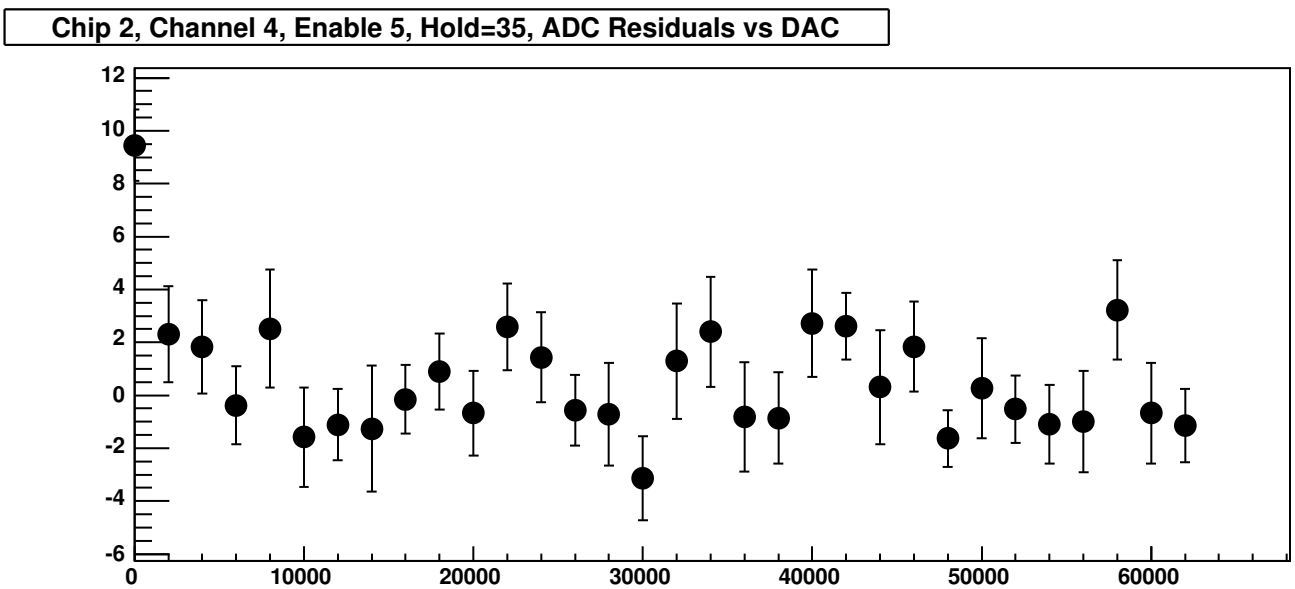


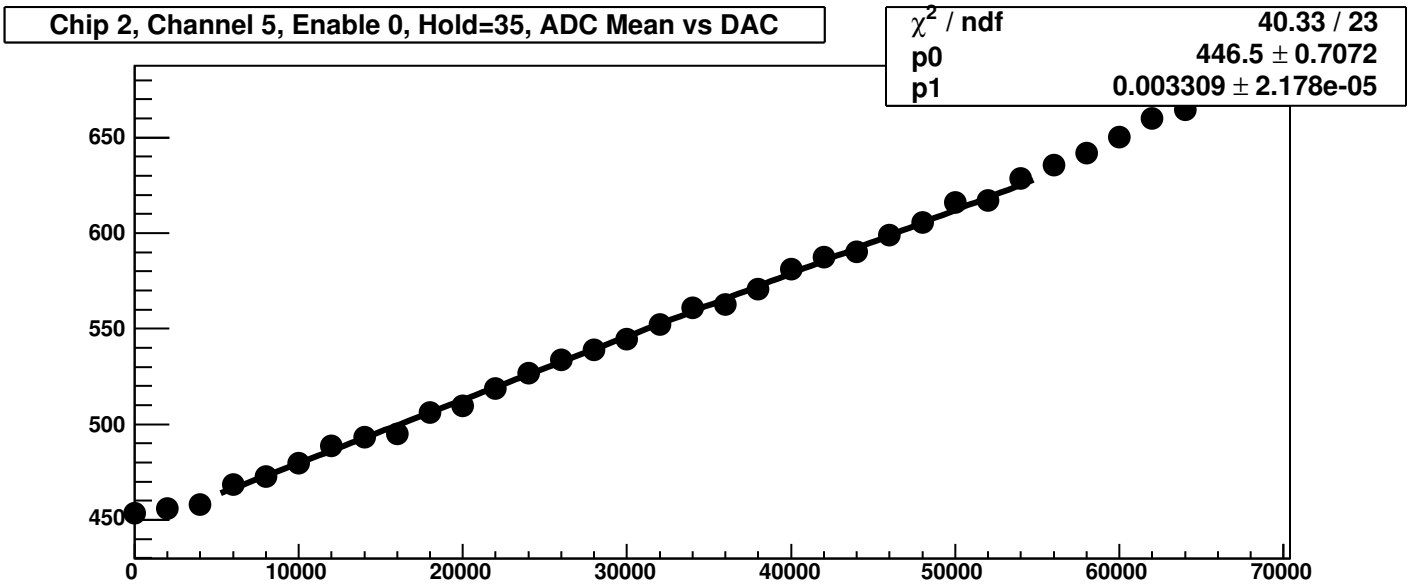
Chip 2, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC

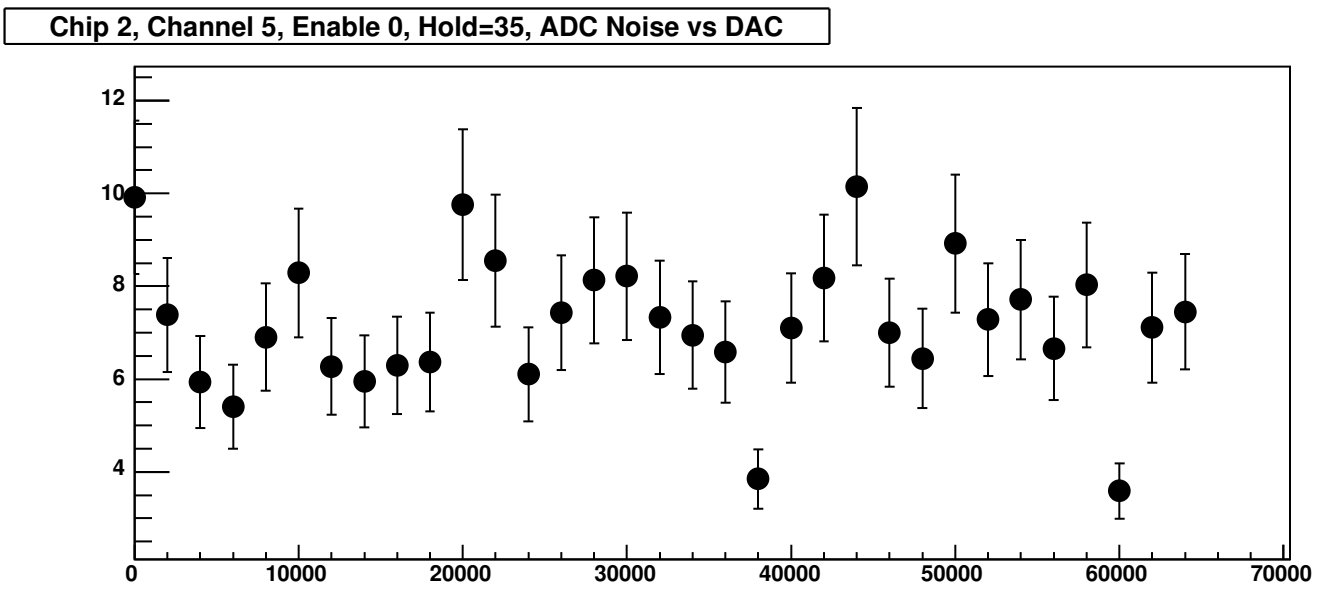


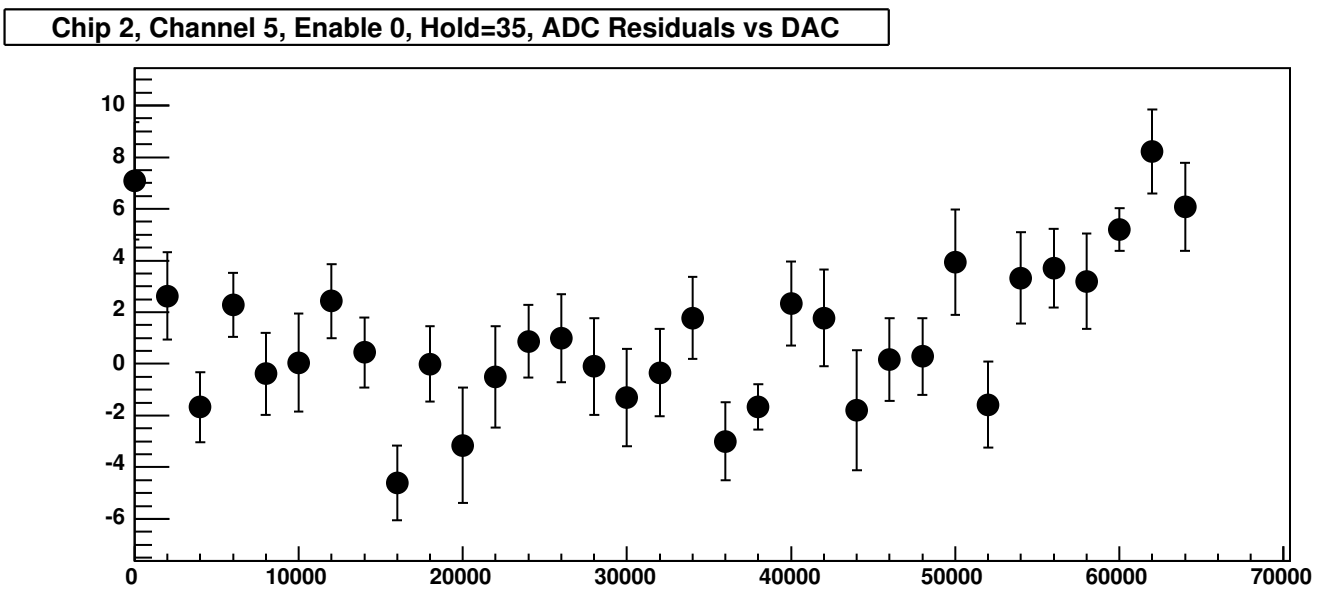

Chip 2, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 23.52 / 23
p0 607.4 ± 0.7359
p1 0.003051 ± 2.137e-05
800
750
700
650
600
0
10000
20000
30000
40000
50000
60000

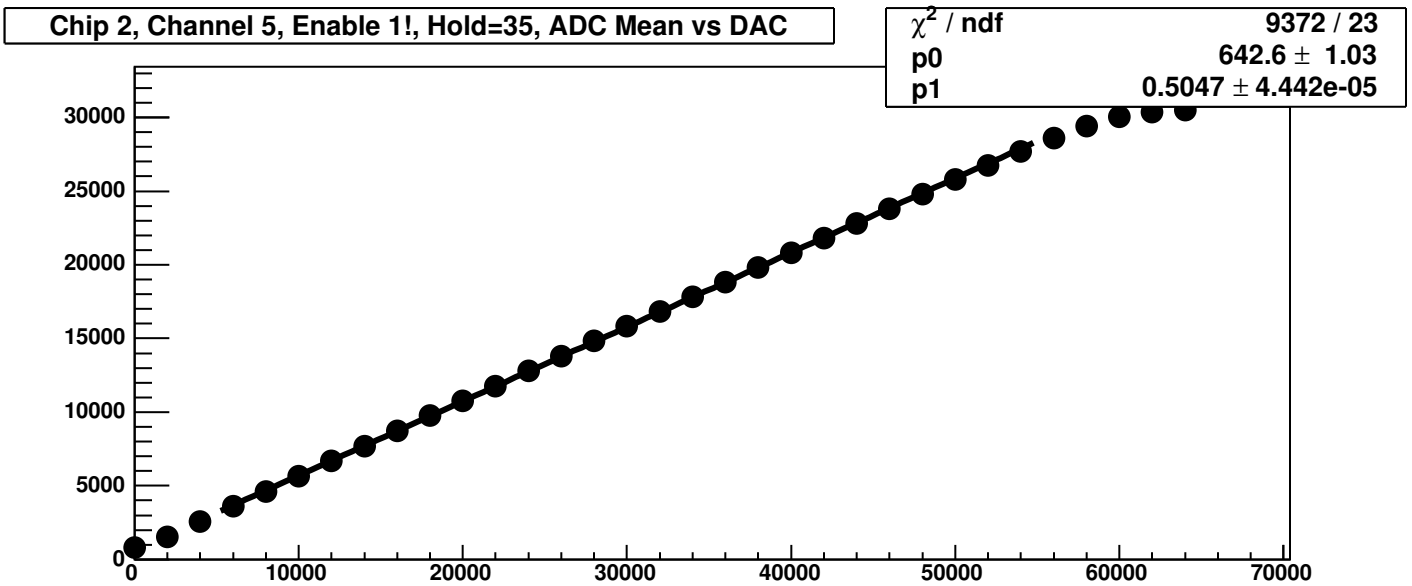

Chip 2, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

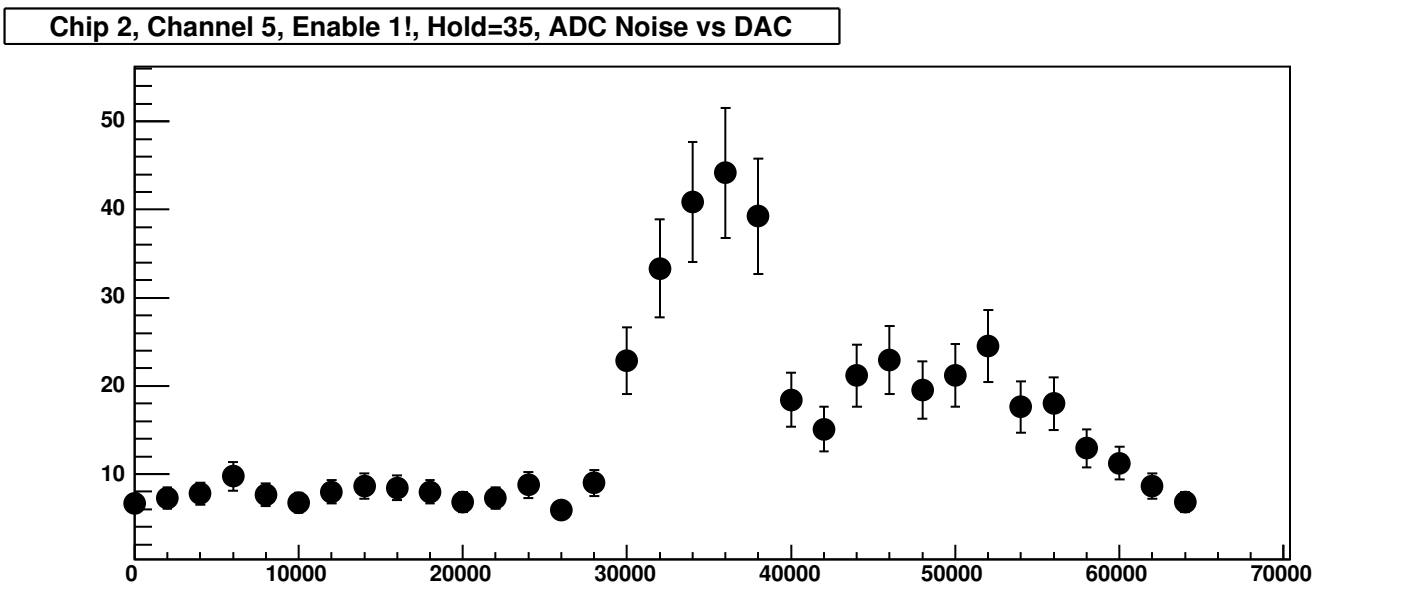

Chip 2, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000

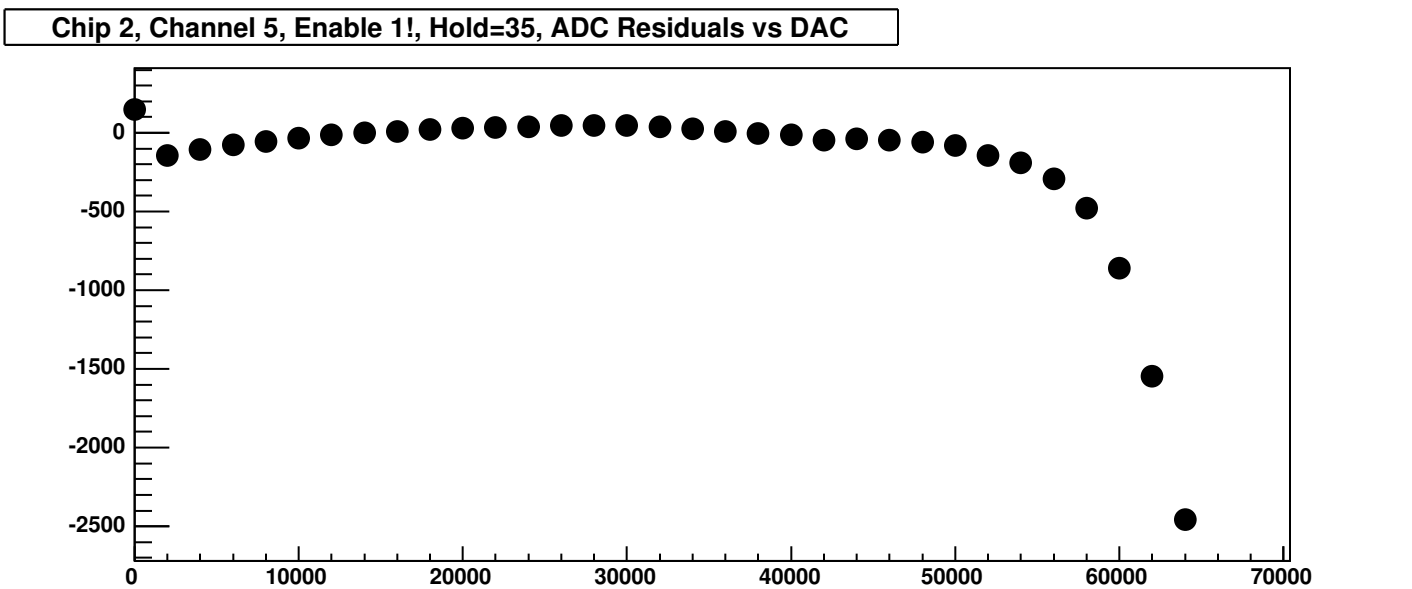

Chip 2, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 40.33 / 23
p0 446.5 ± 0.7072
p1 0.003309 ± 2.178e-05
650
600
550
500
450
0
10000
20000
30000
40000
50000
60000
70000

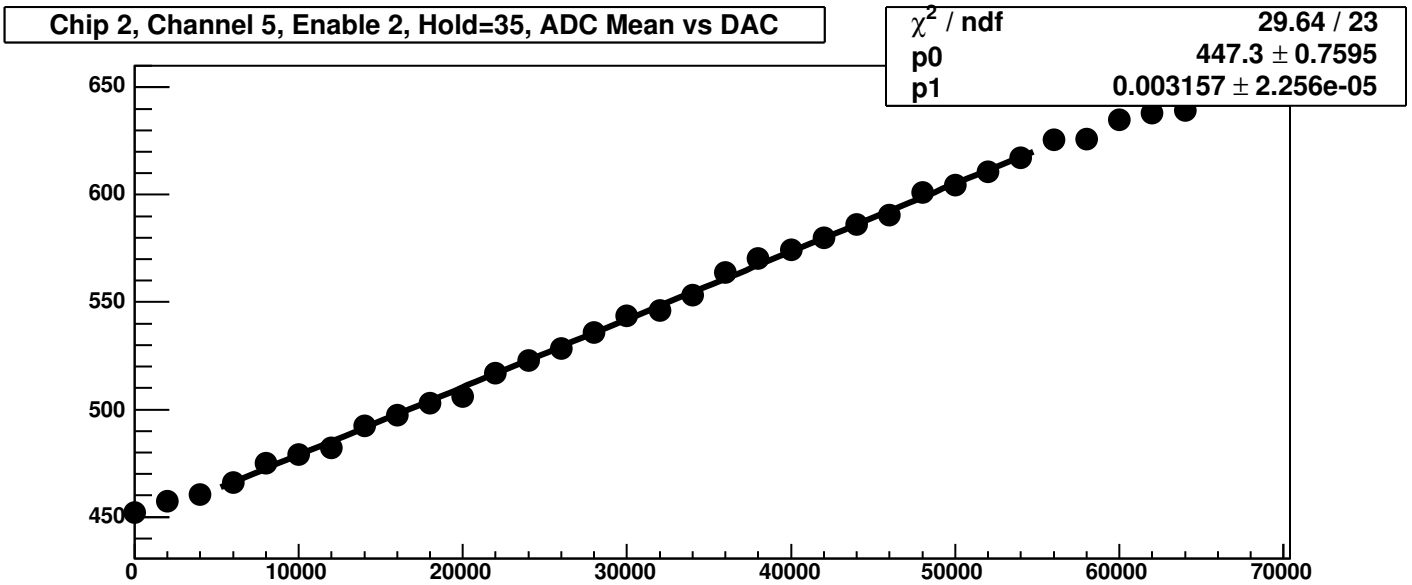

Chip 2, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

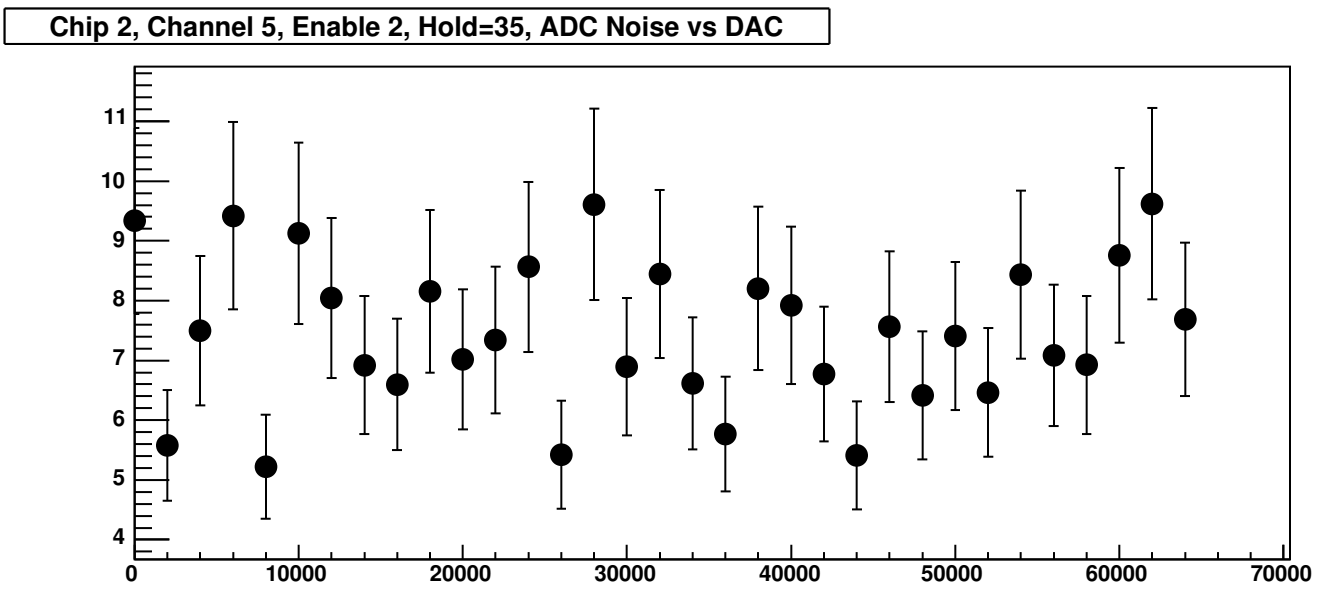

Chip 2, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

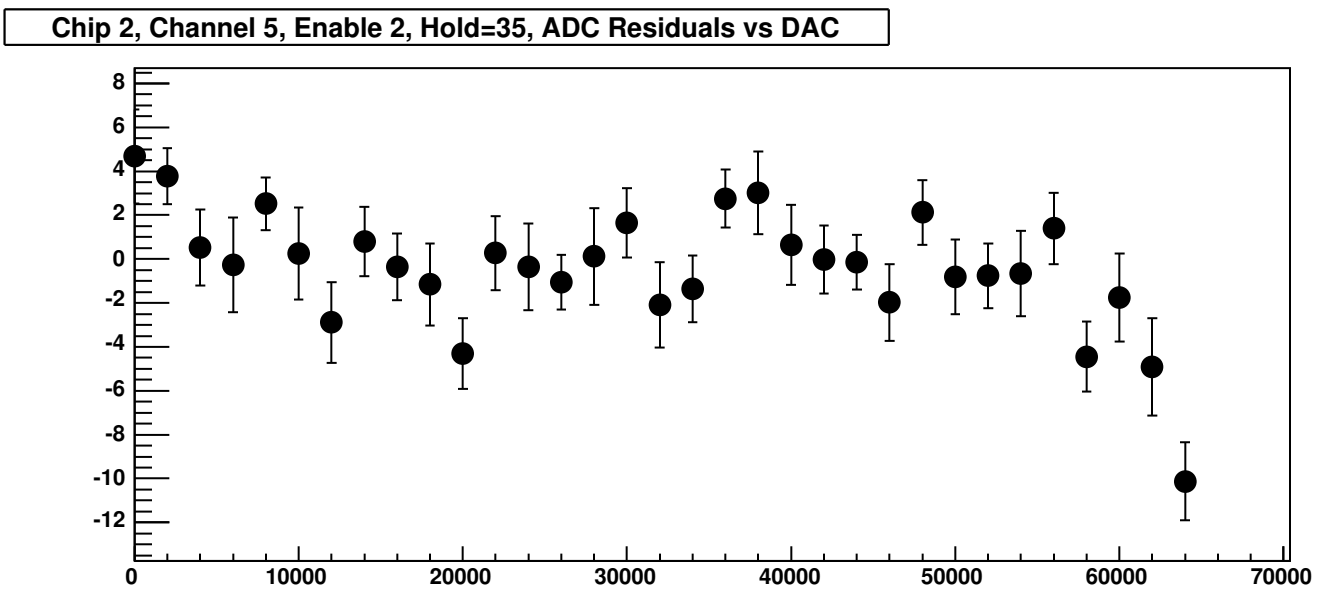

Chip 2, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 9372 / 23
p0 642.6 ± 1.03
p1 0.5047 ± 4.442e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

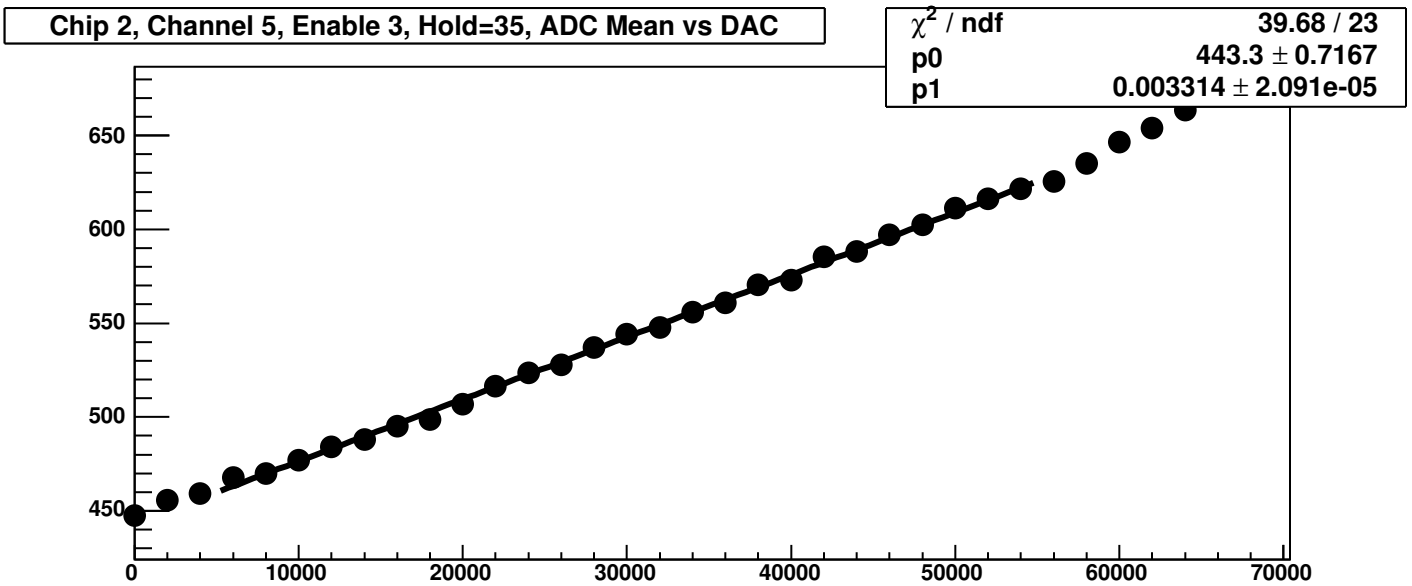

Chip 2, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000

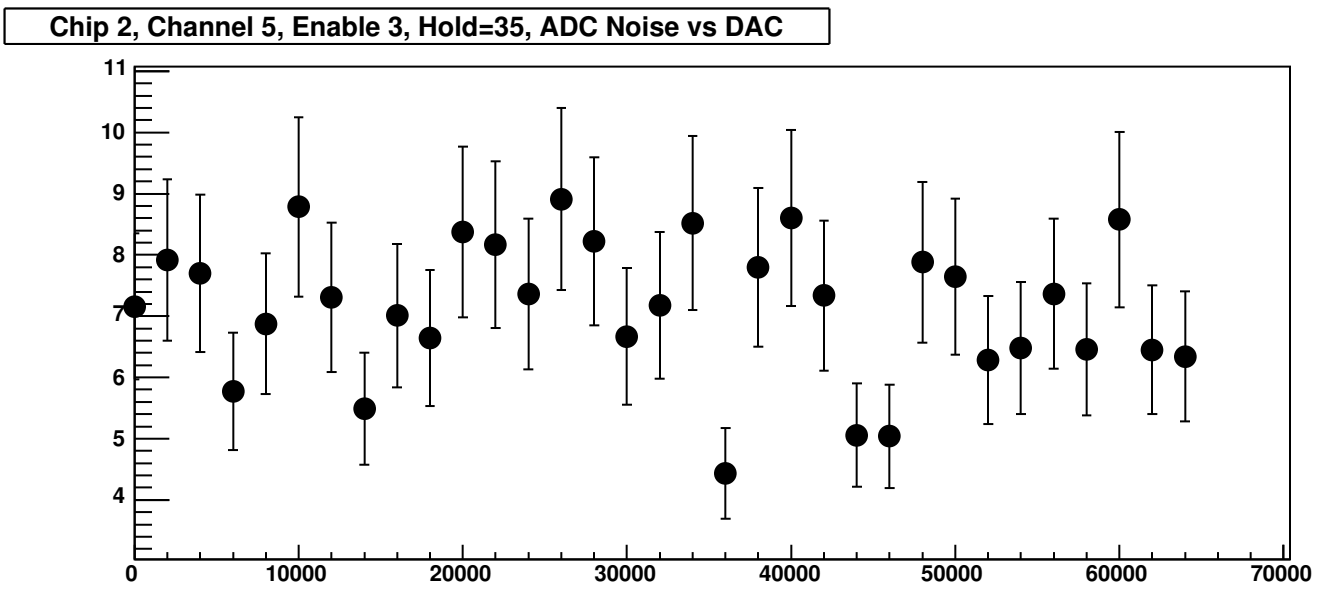

Chip 2, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
0
10000
20000
30000
40000
50000
60000
70000

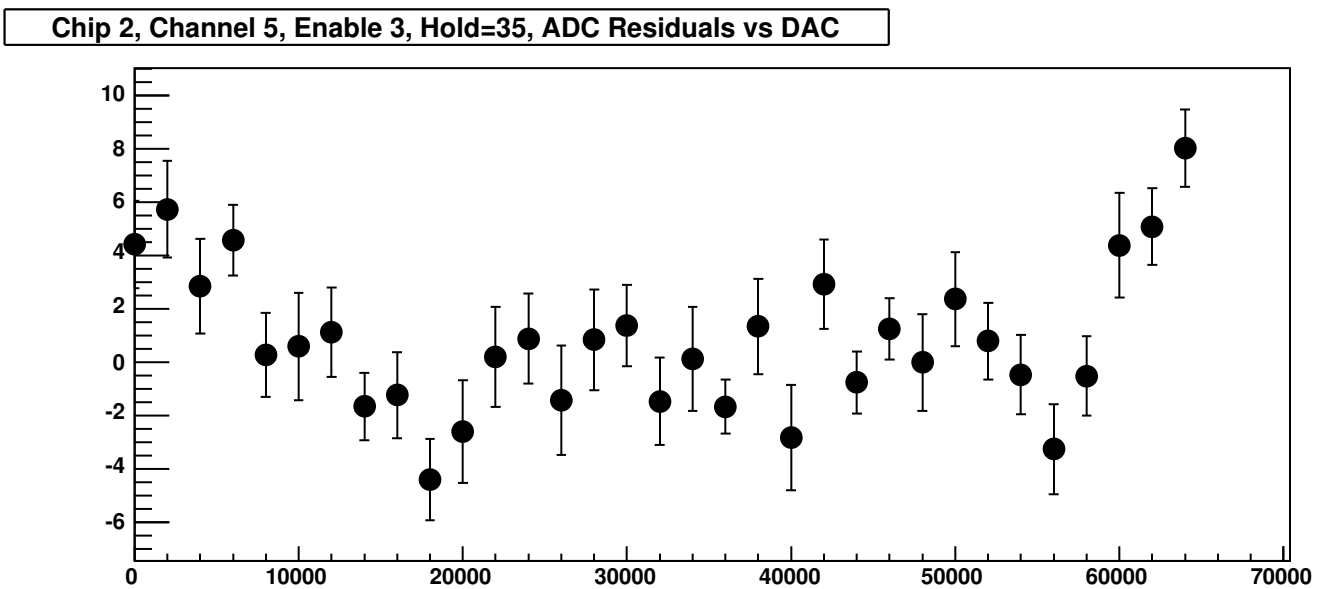

Chip 2, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 29.64 / 23
p0 447.3 ± 0.7595
p1 0.003157 ± 2.256e-05
650
600
550
500
450
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

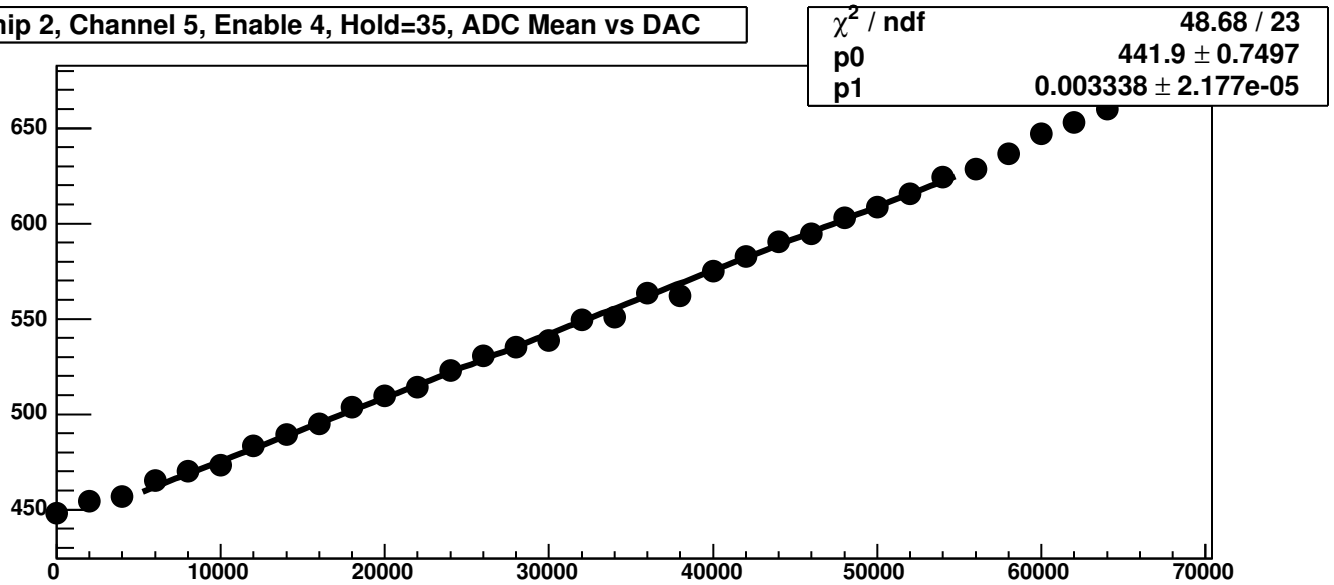

Chip 2, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 39.68 / 23
p0 443.3 ± 0.7167
p1 0.003314 ± 2.091e-05
650
600
550
500
450
0
10000
20000
30000
40000
50000
60000
70000

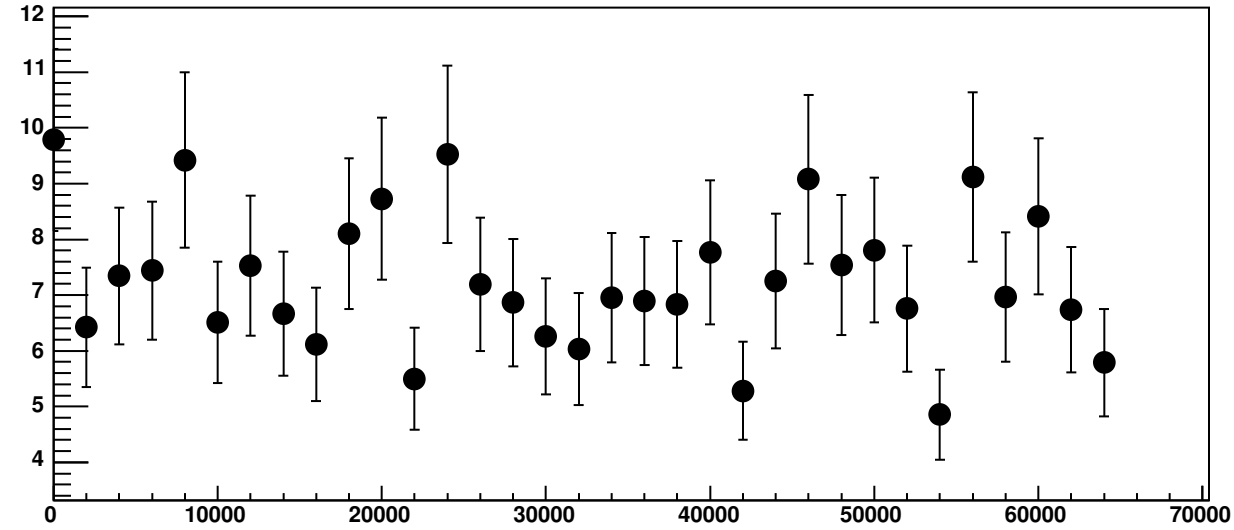

Chip 2, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

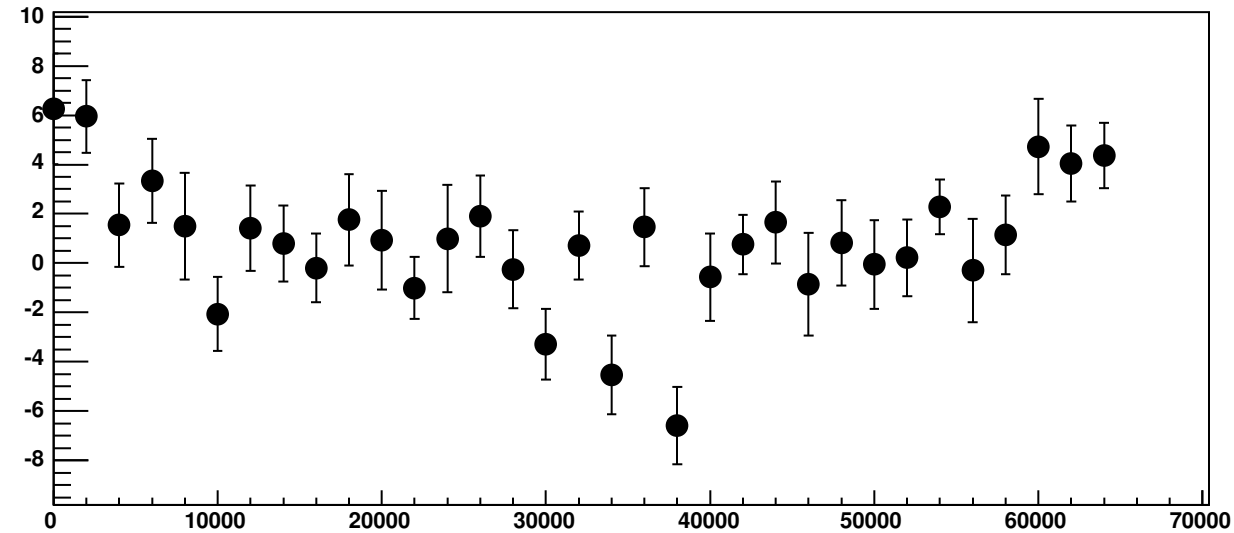
Chip 2, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC



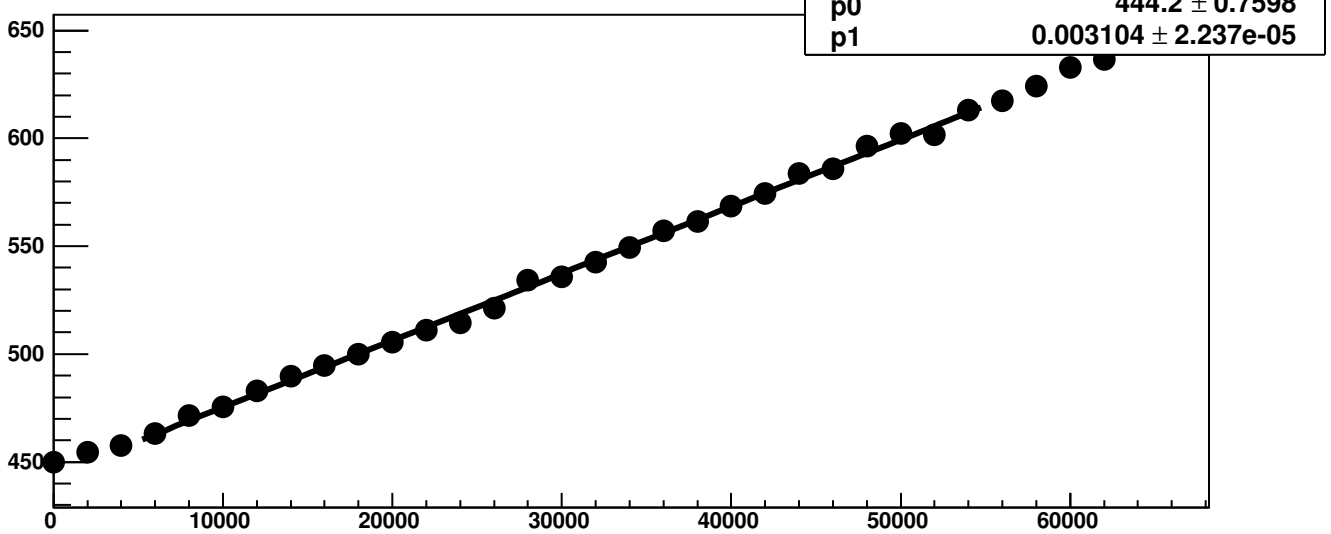
Chip 2, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



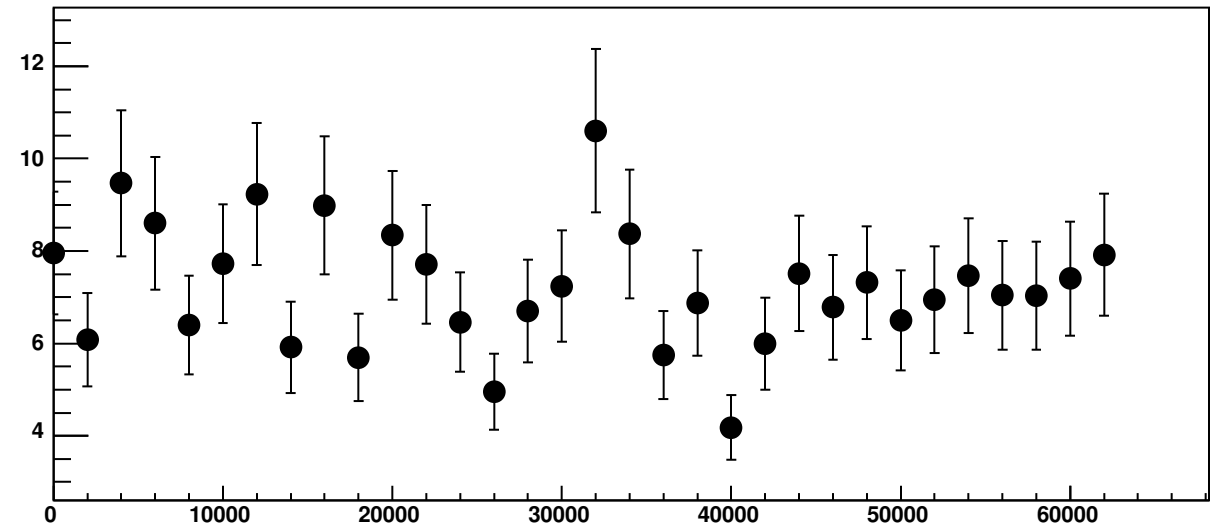
Chip 2, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



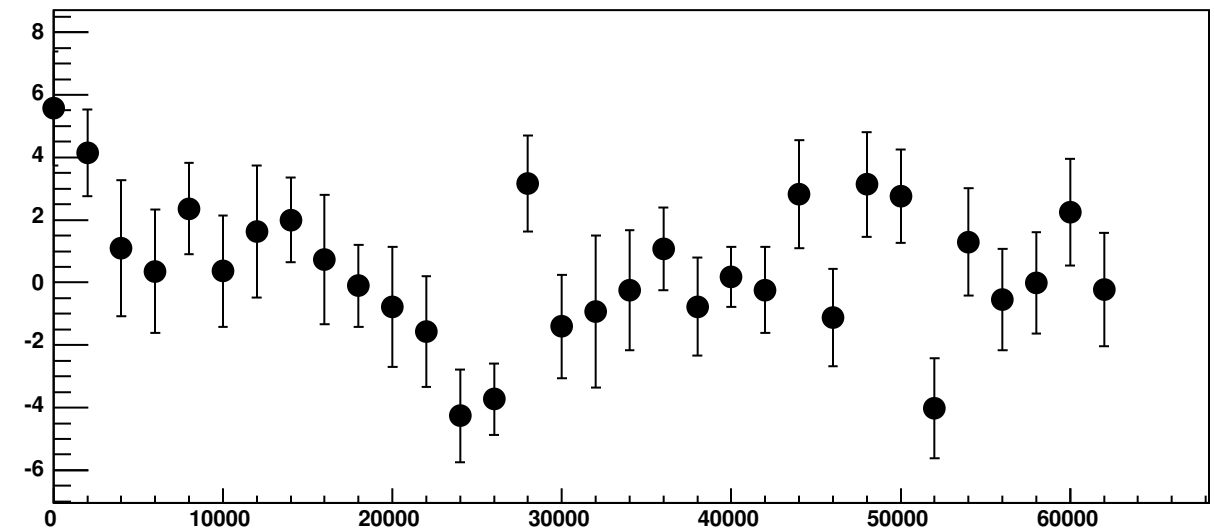
Chip 2, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

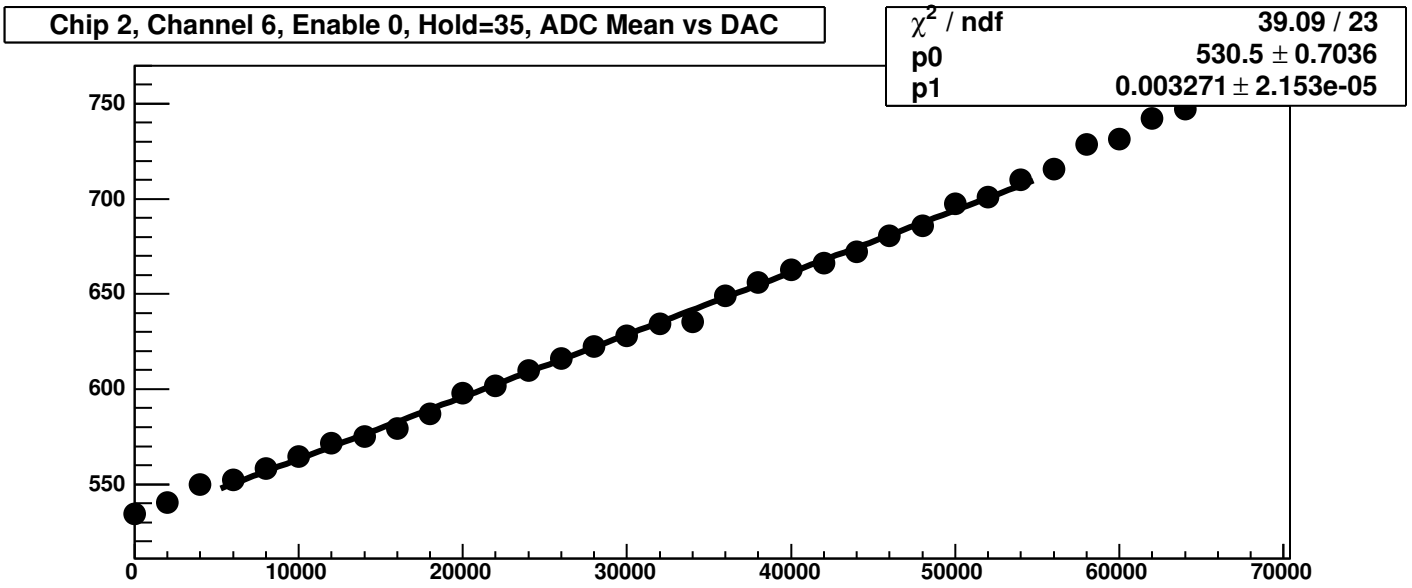


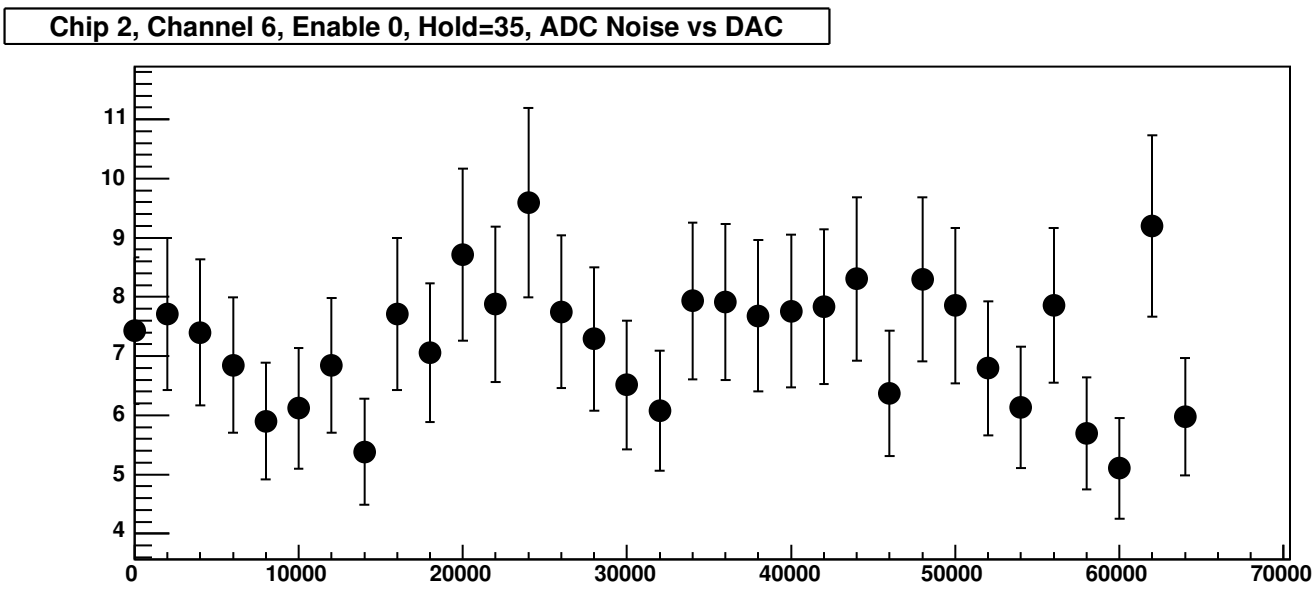
Chip 2, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC

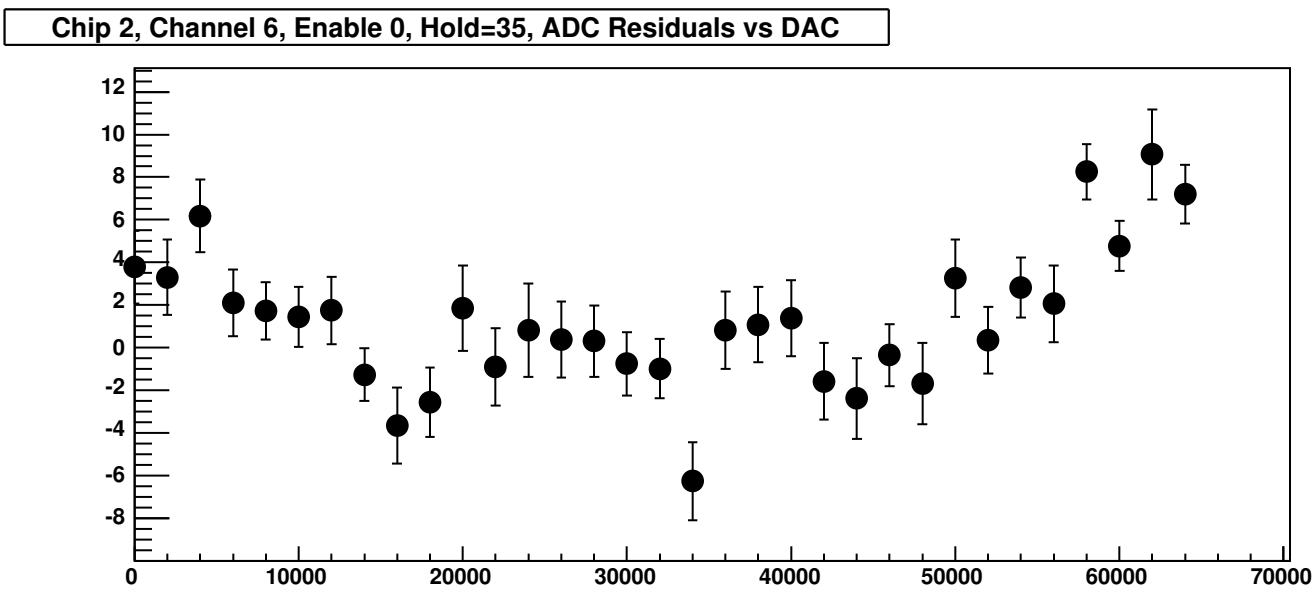


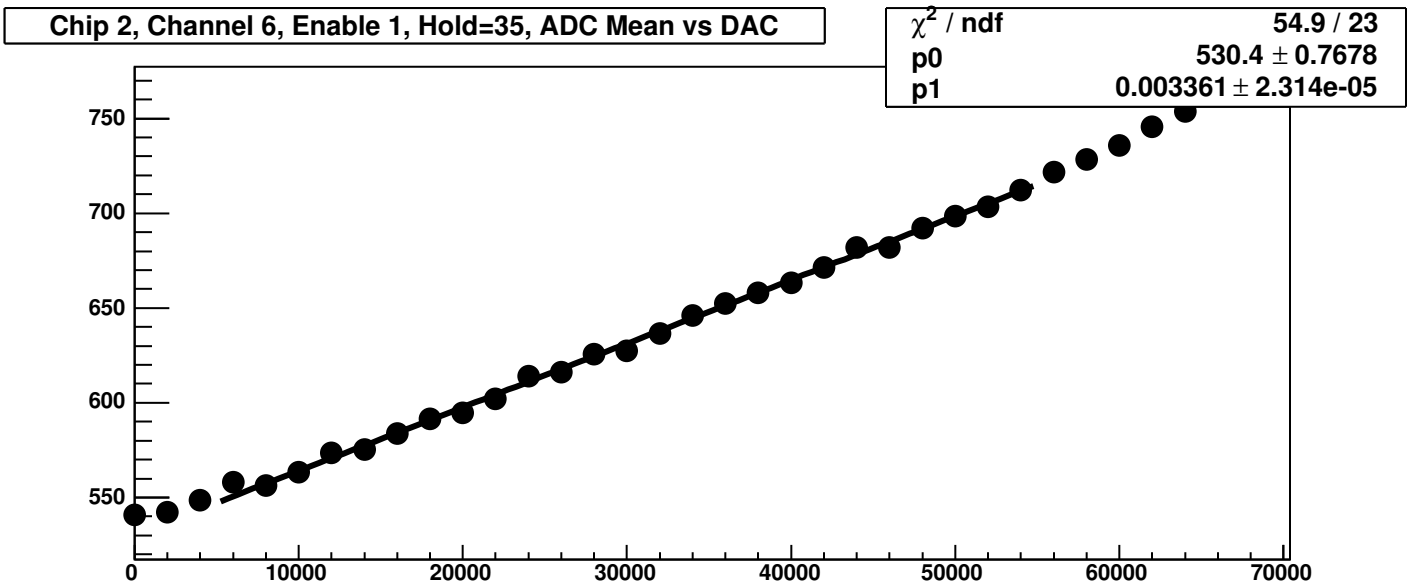
Chip 2, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

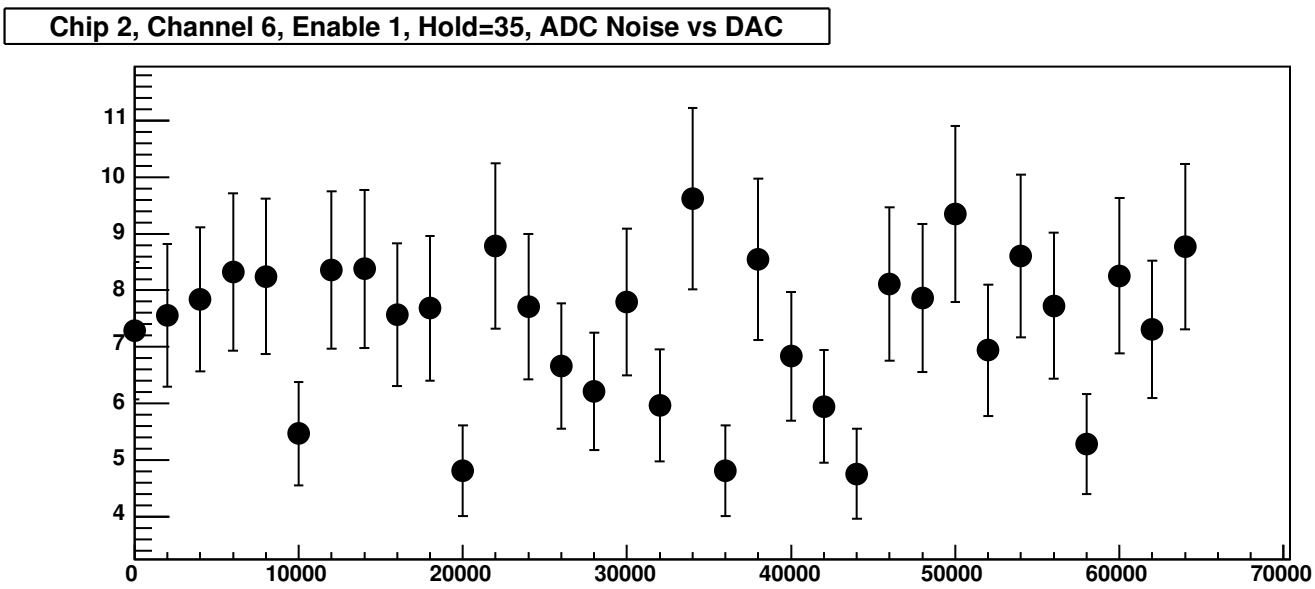


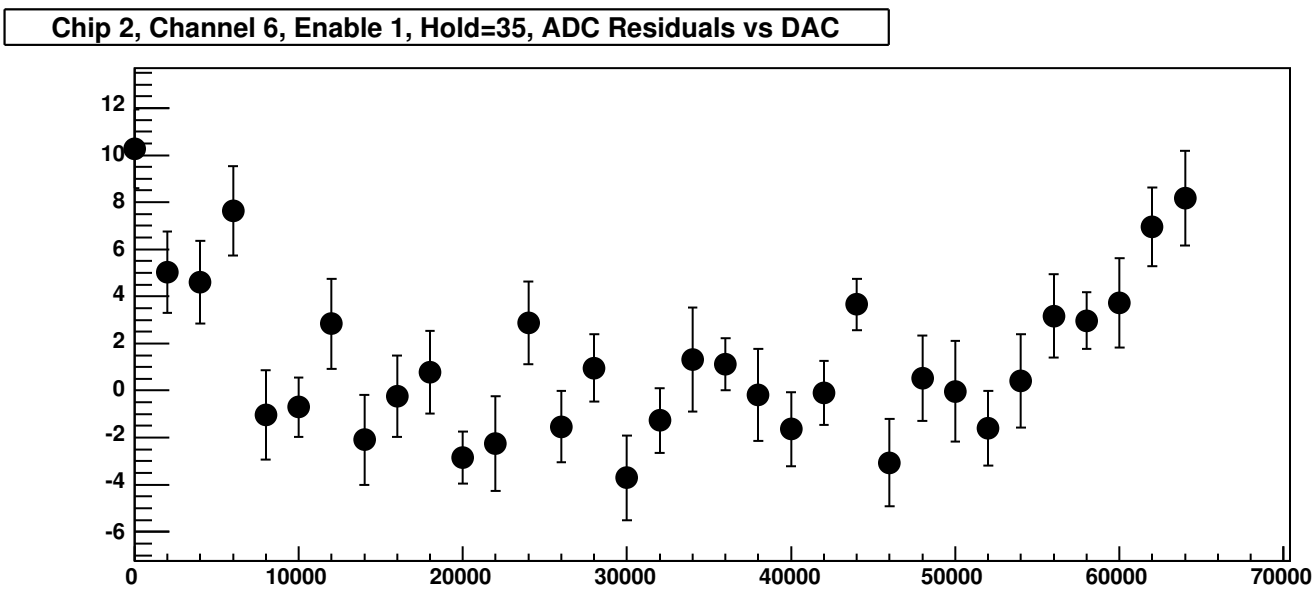

Chip 2, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 39.09 / 23
p0 530.5 ± 0.7036
p1 0.003271 ± 2.153e-05
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000
70000

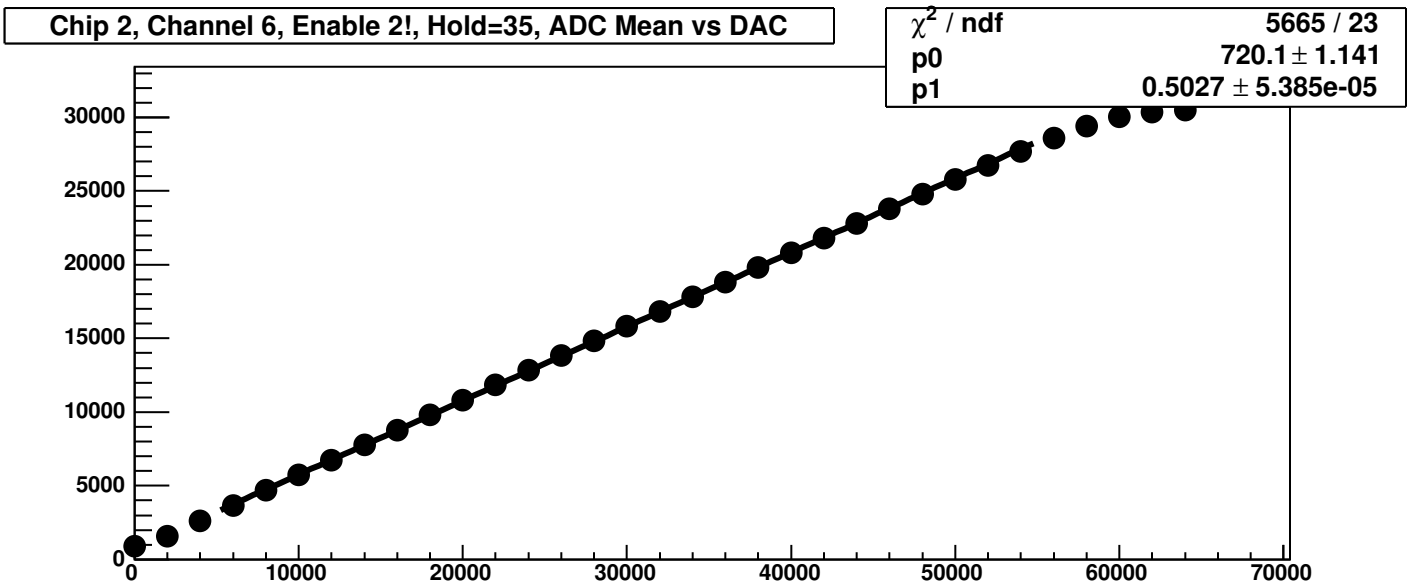

Chip 2, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

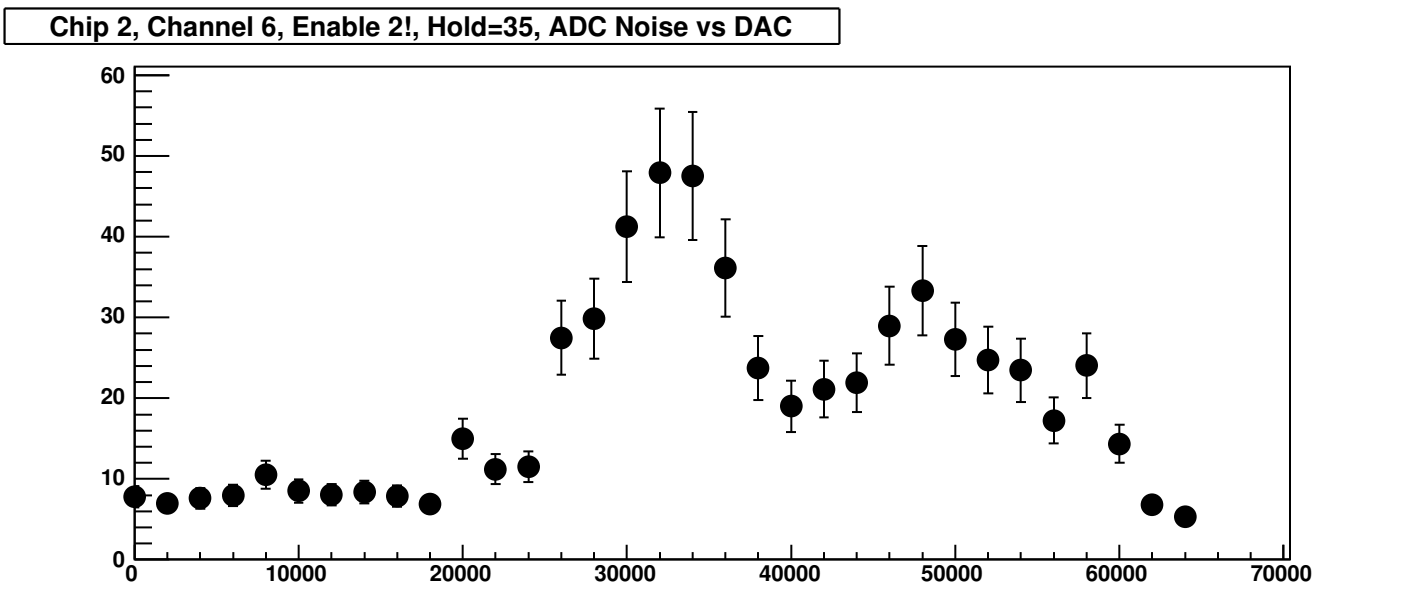

Chip 2, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

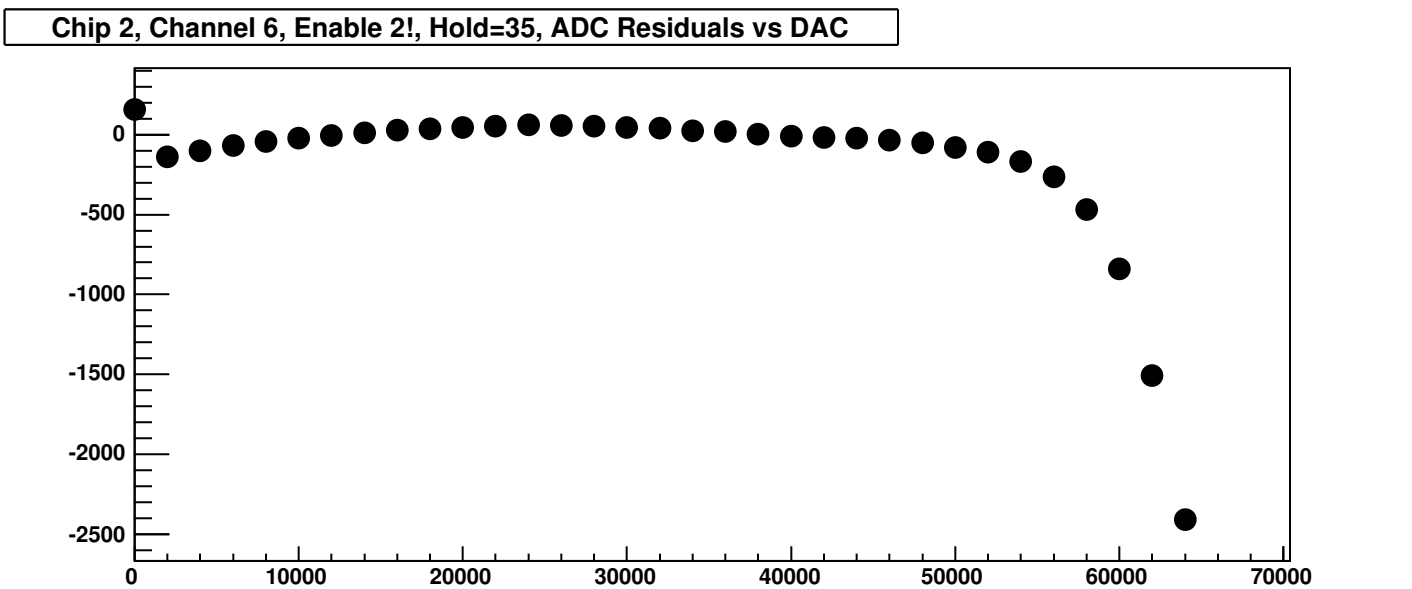

Chip 2, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 54.9 / 23
p0 530.4 ± 0.7678
p1 0.003361 ± 2.314e-05
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000
70000

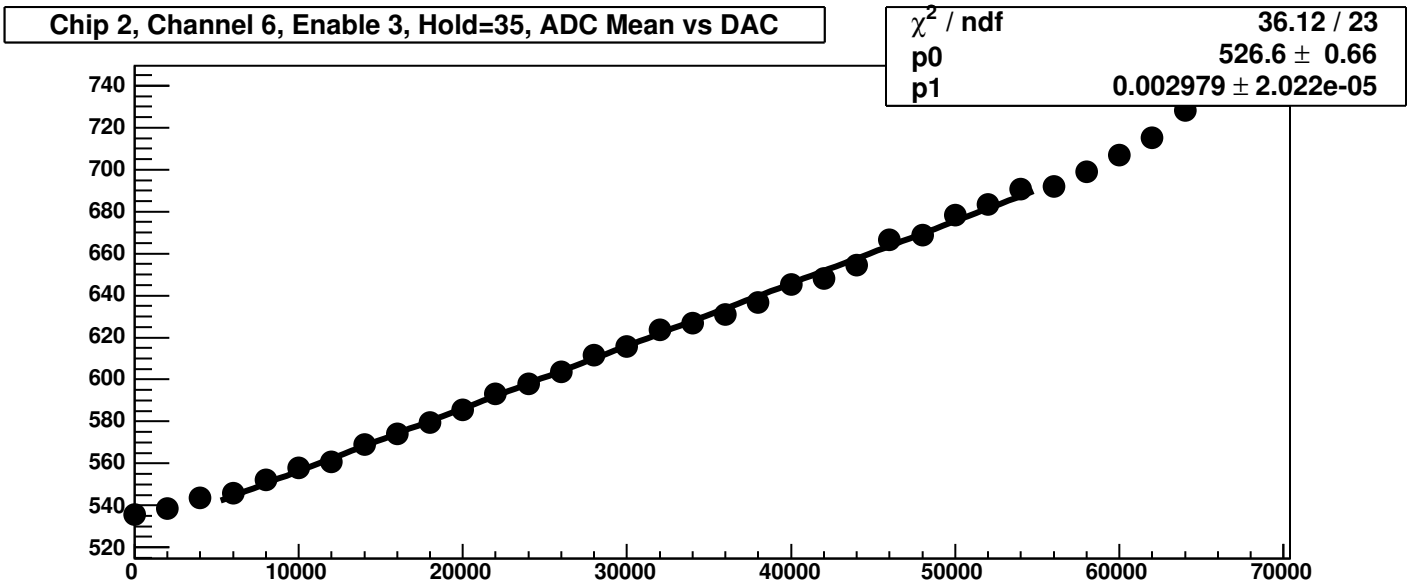

Chip 2, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

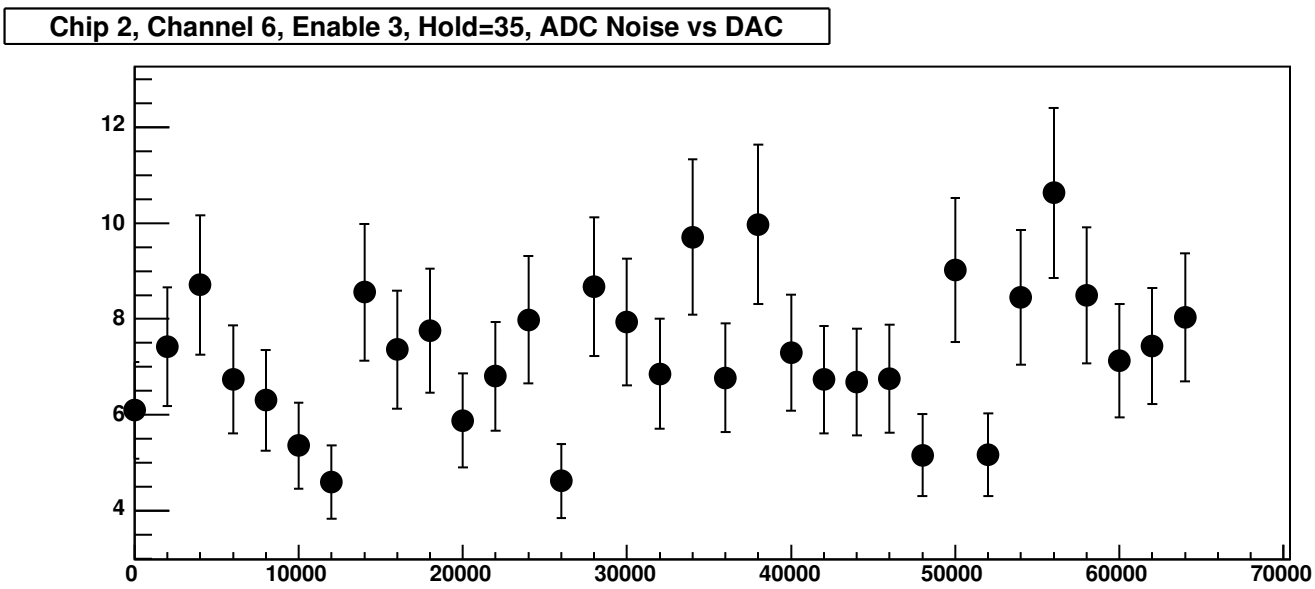

Chip 2, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

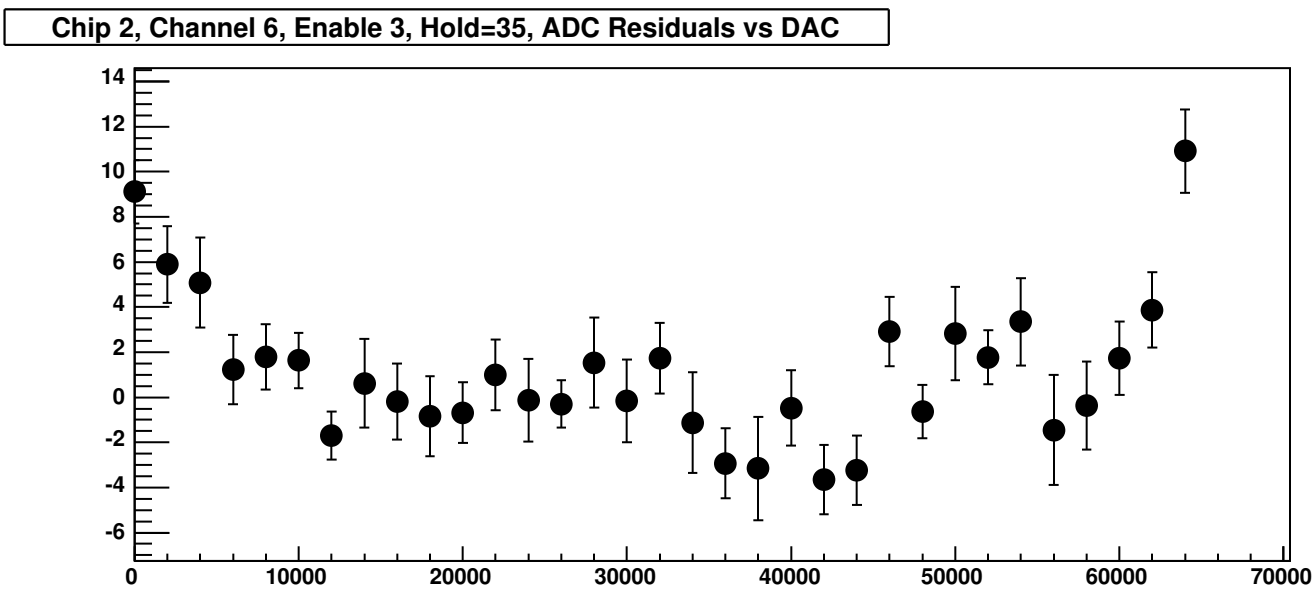

Chip 2, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 5665 / 23
p0 720.1 ± 1.141
p1 0.5027 ± 5.385e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

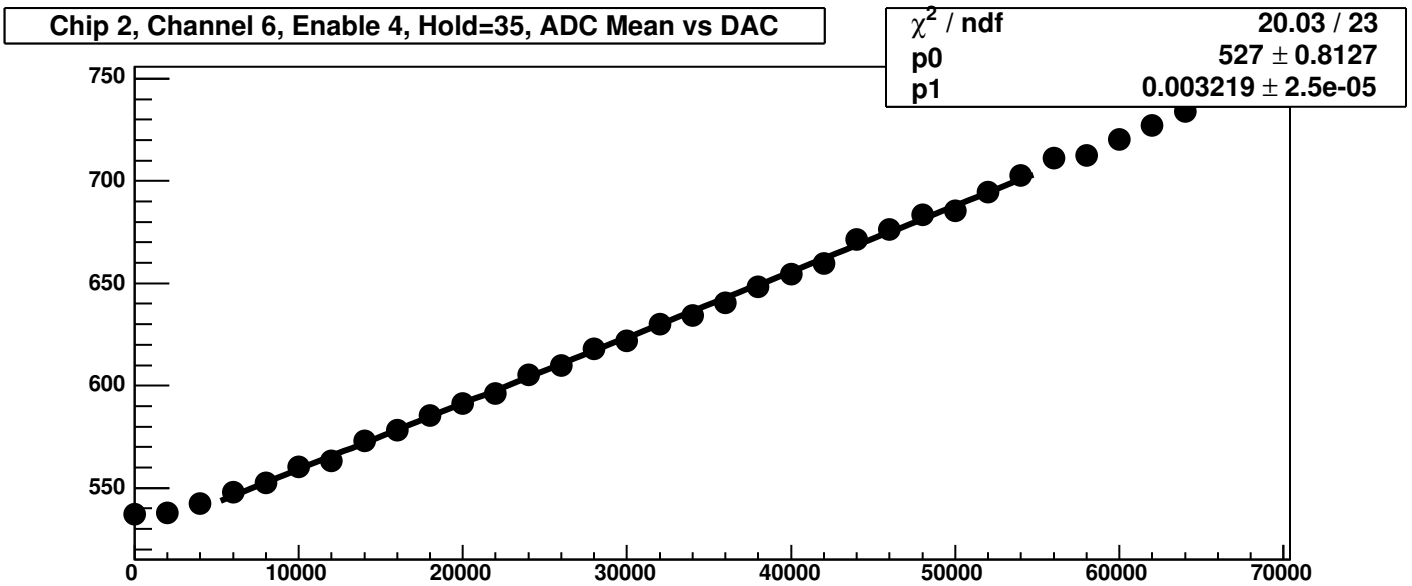

Chip 2, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

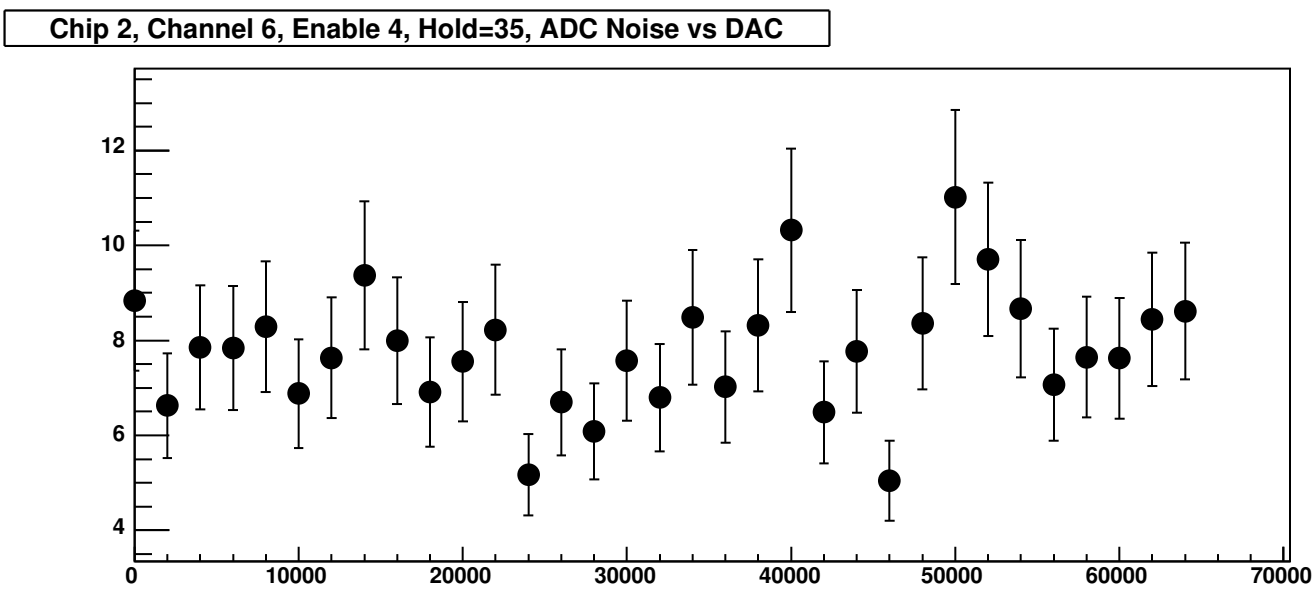

Chip 2, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
0
10000
20000
30000
40000
50000
60000
70000

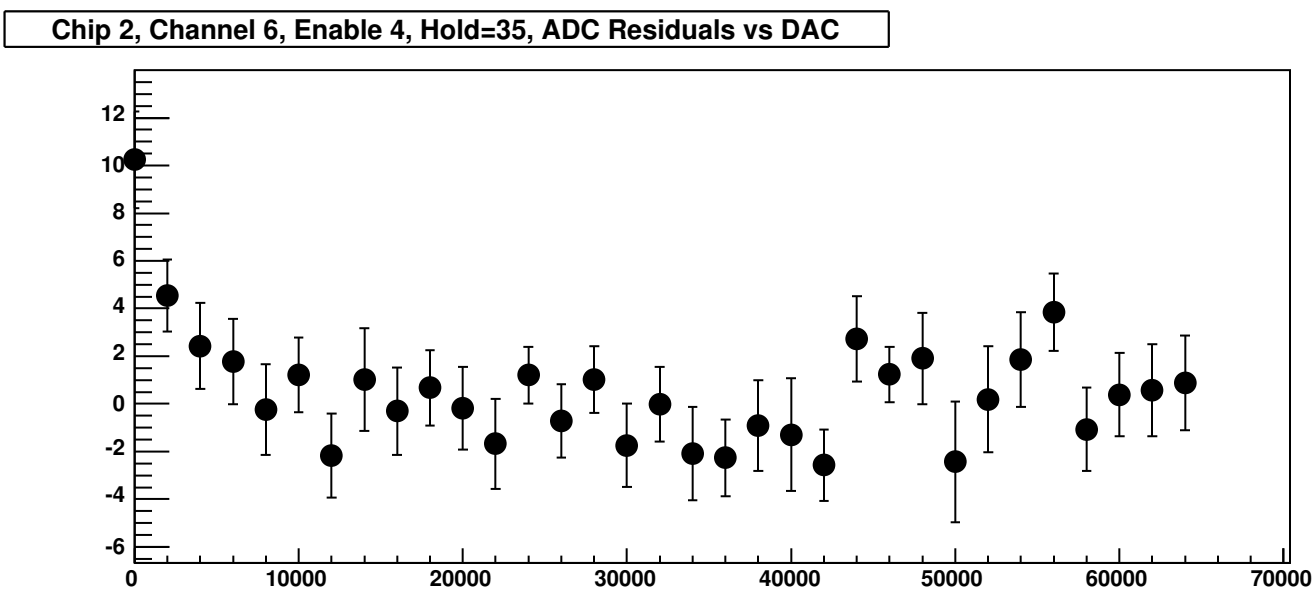

Chip 2, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.12 / 23
p0 526.6 ± 0.66
p1 0.002979 ± 2.022e-05
740
720
700
680
660
640
620
600
580
560
540
520
0
10000
20000
30000
40000
50000
60000
70000

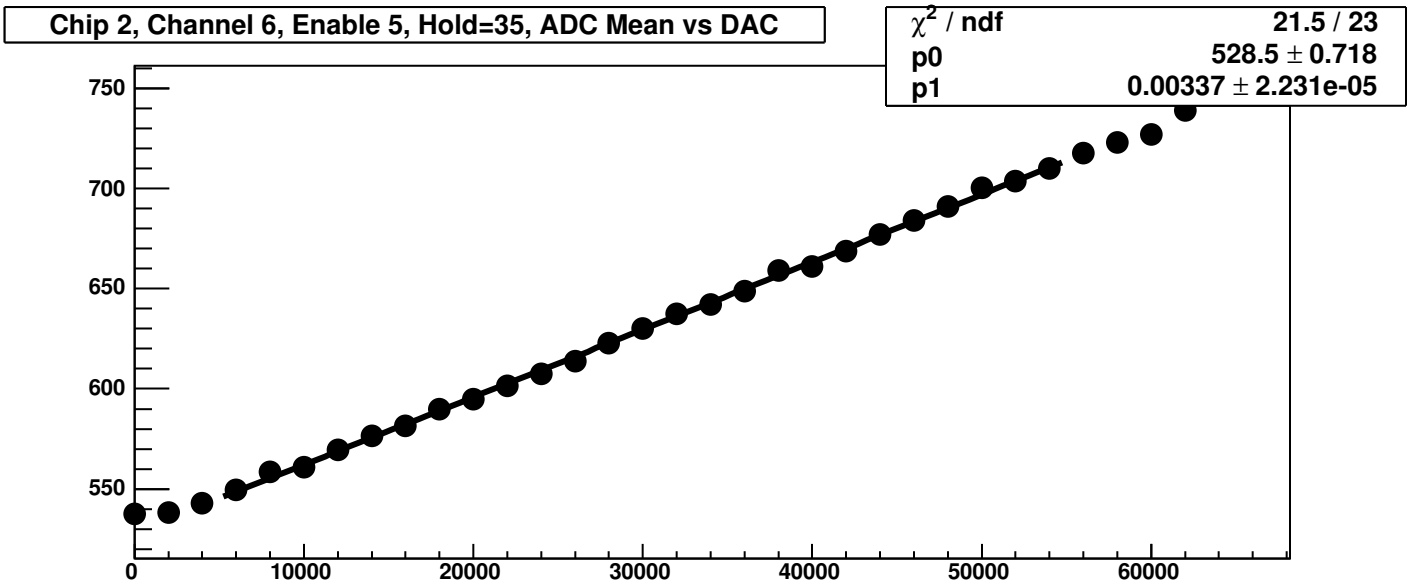

Chip 2, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

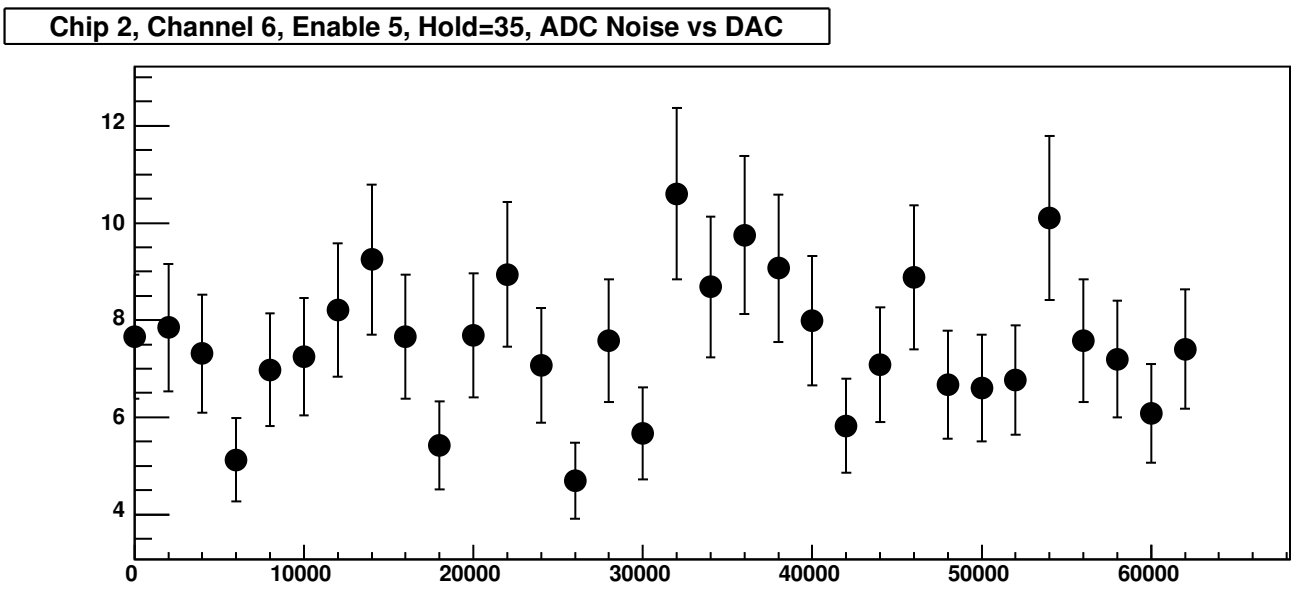

Chip 2, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

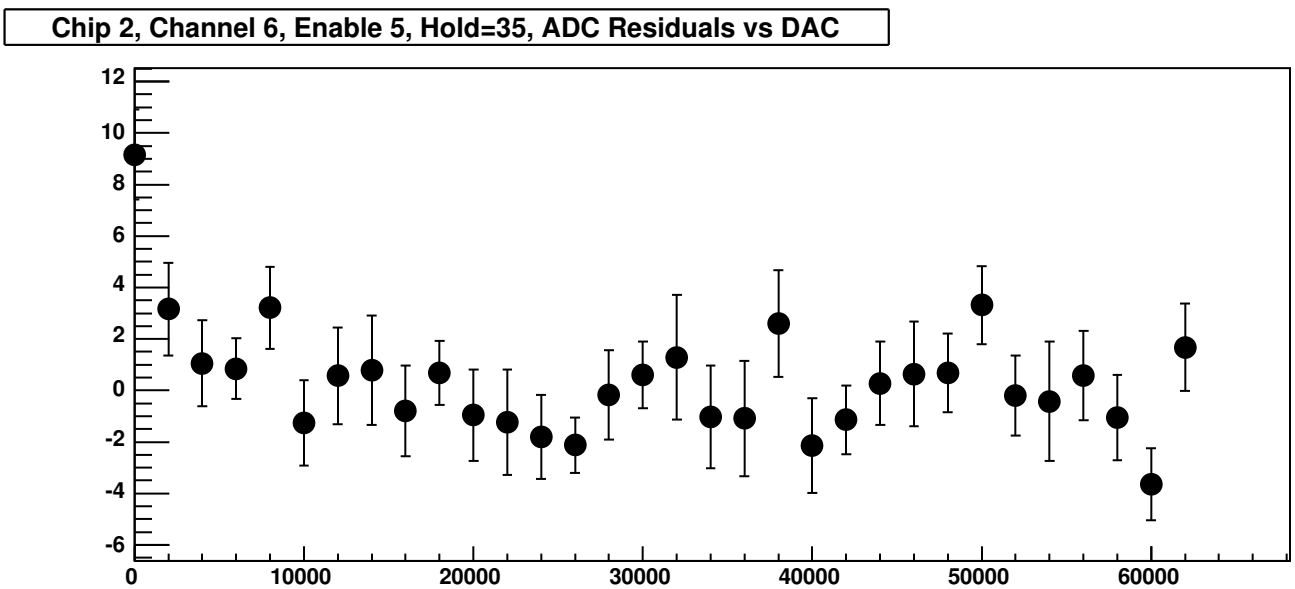

Chip 2, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 20.03 / 23
p0 527 ± 0.8127
p1 0.003219 ± 2.5e-05
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000
70000

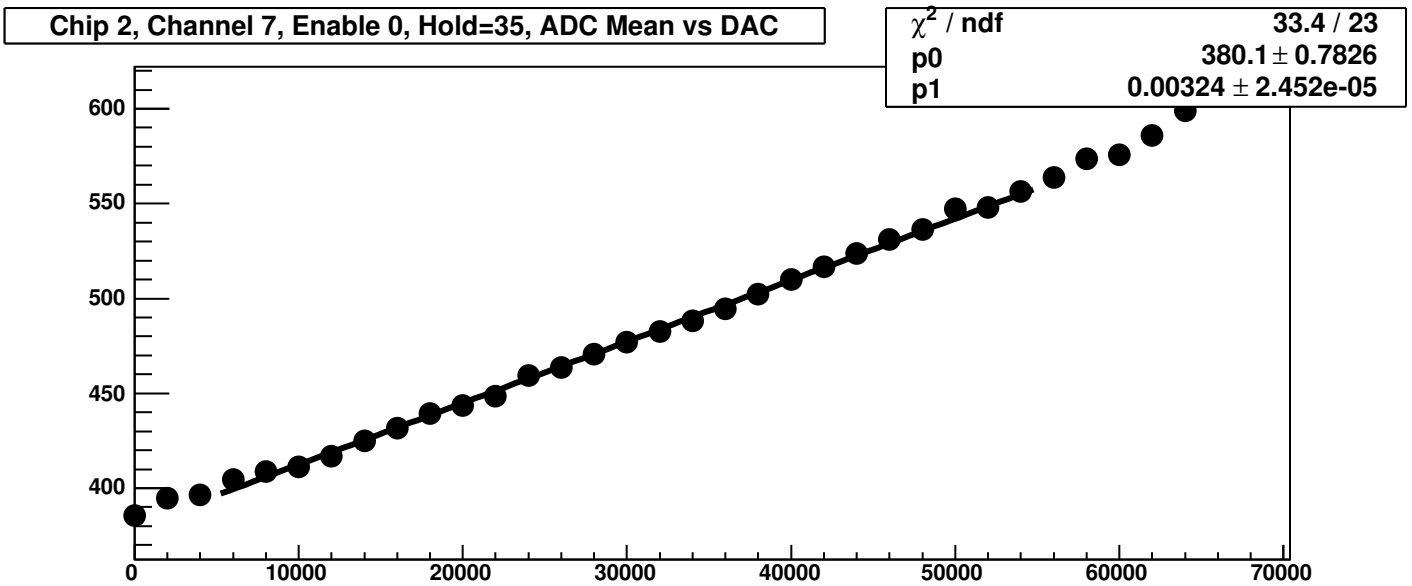

Chip 2, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

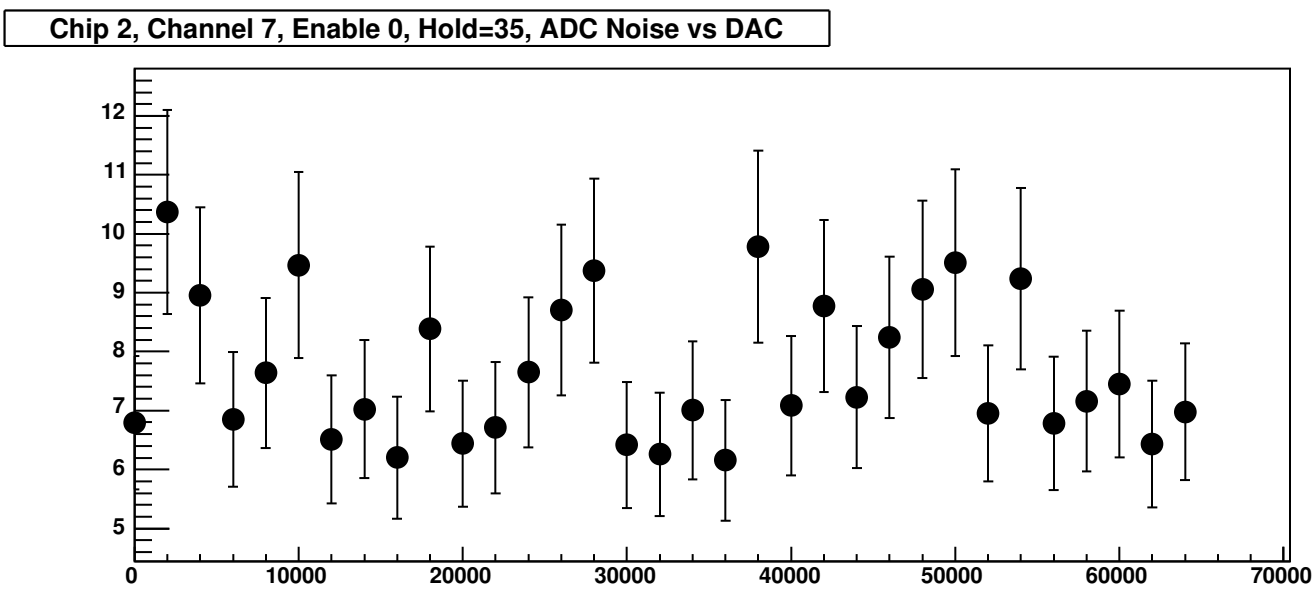

Chip 2, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

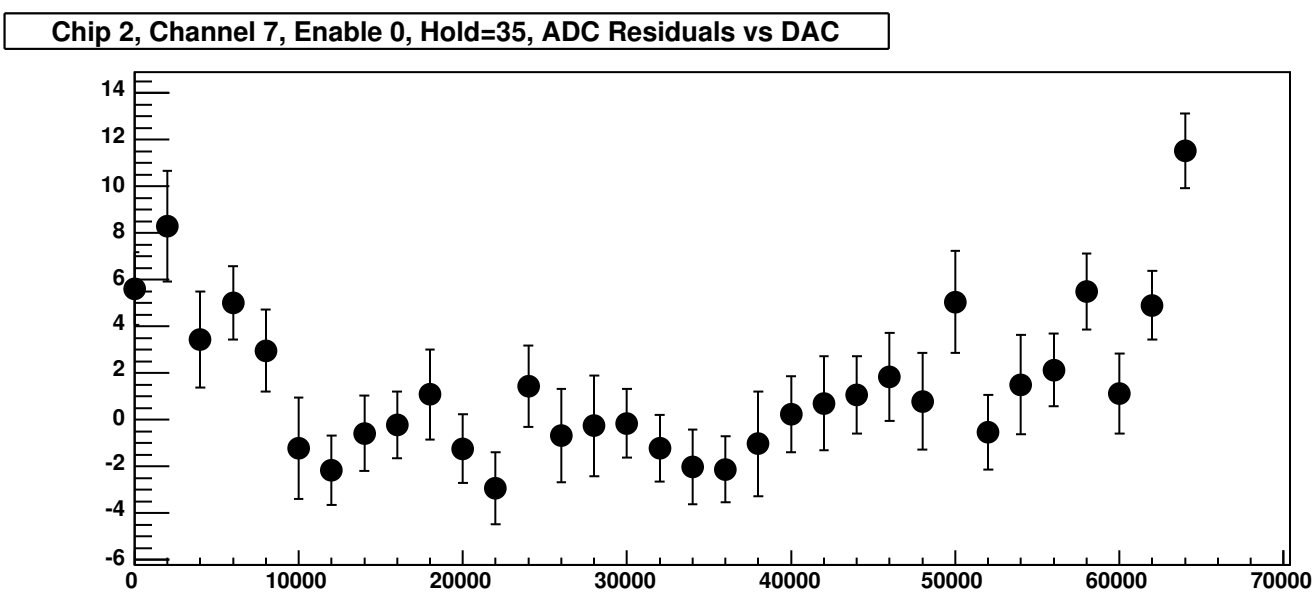

Chip 2, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 21.5 / 23
p0 528.5 ± 0.718
p1 0.00337 ± 2.231e-05
750
700
650
600
550
0
10000
20000
30000
40000
50000
60000

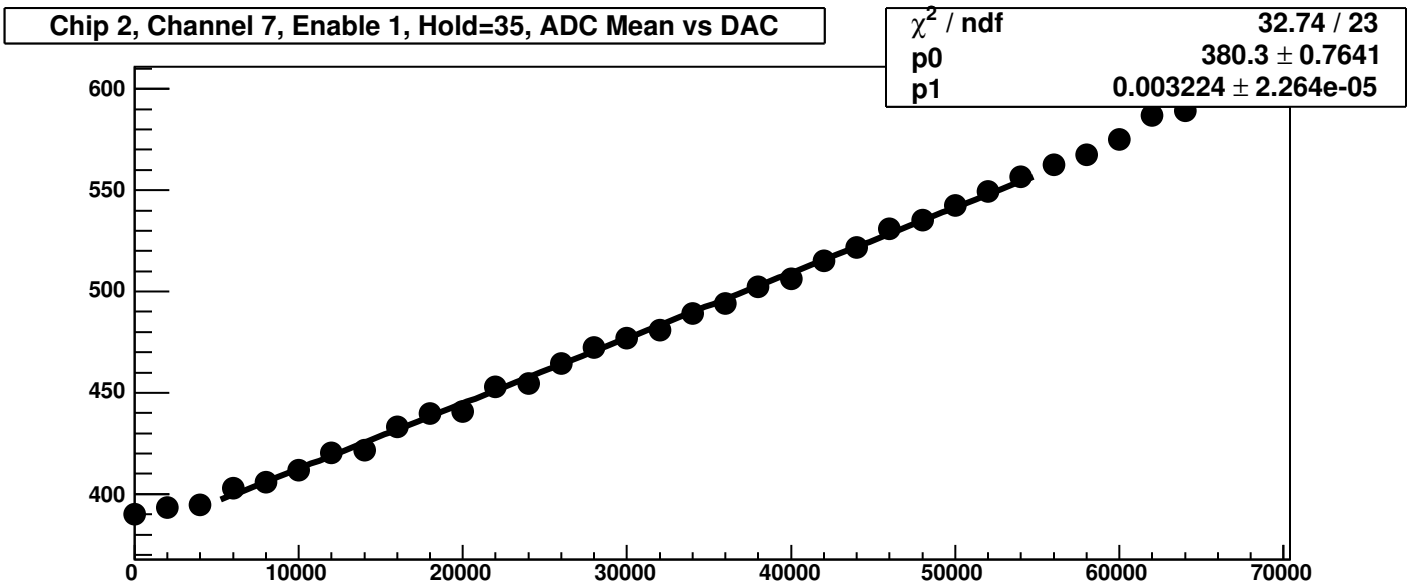

Chip 2, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000

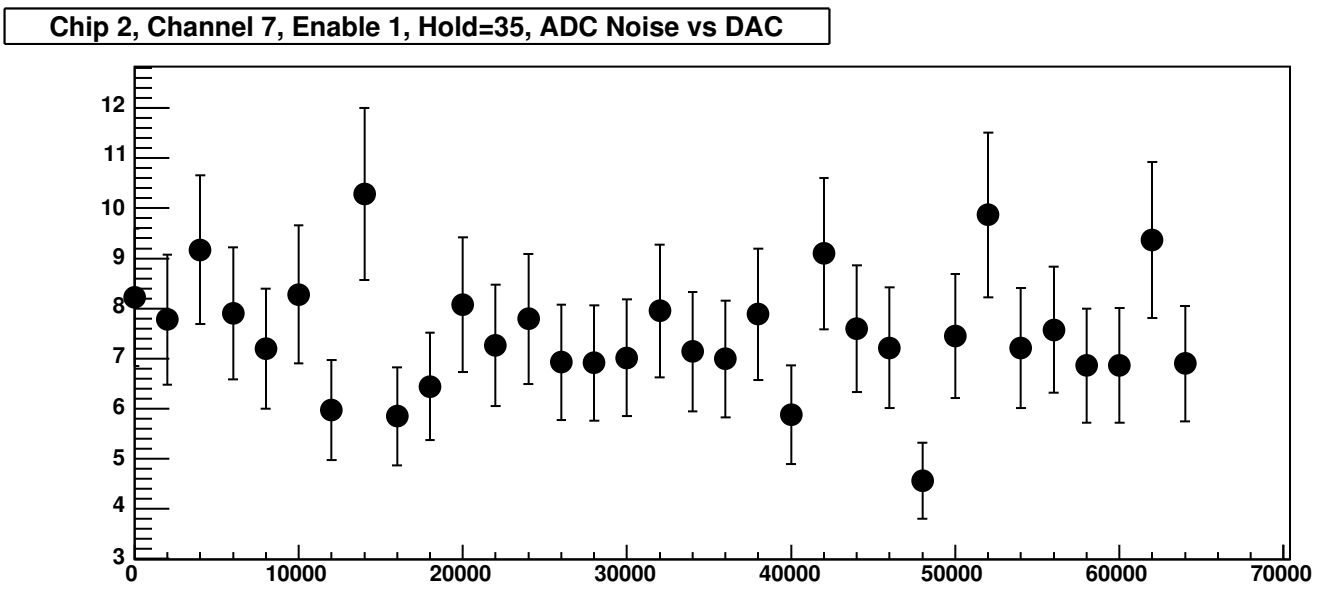

Chip 2, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000

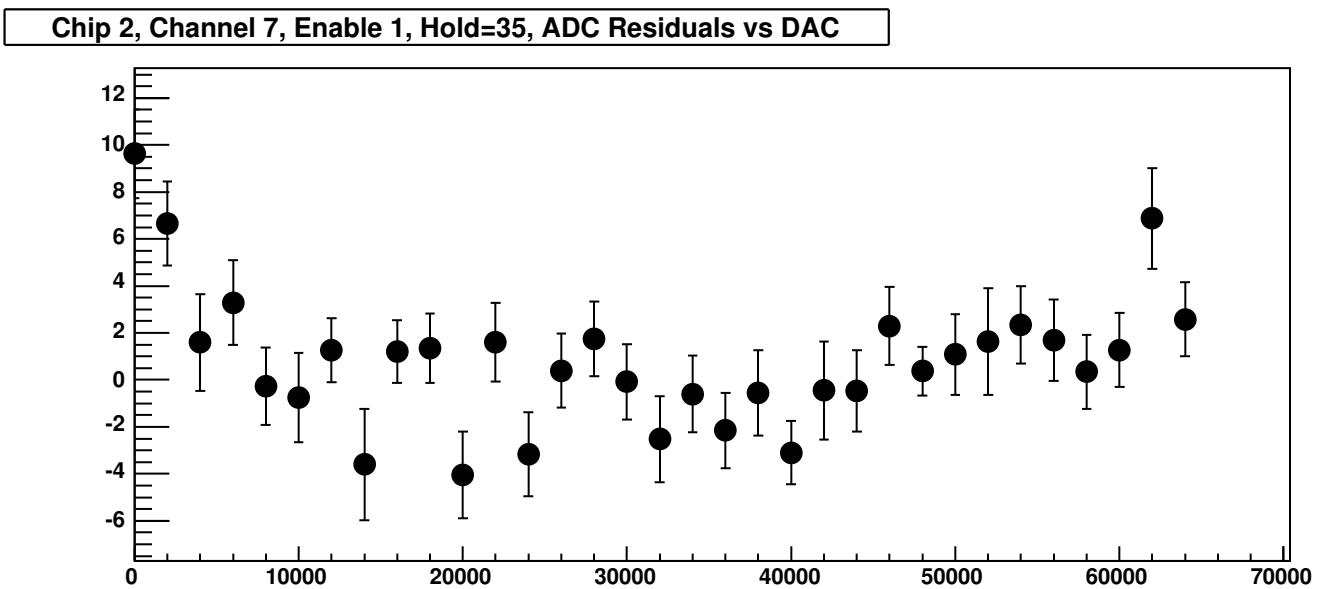

Chip 2, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 33.4 / 23
p0 380.1 ± 0.7826
p1 0.00324 ± 2.452e-05
600
550
500
450
400
0
10000
20000
30000
40000
50000
60000
70000

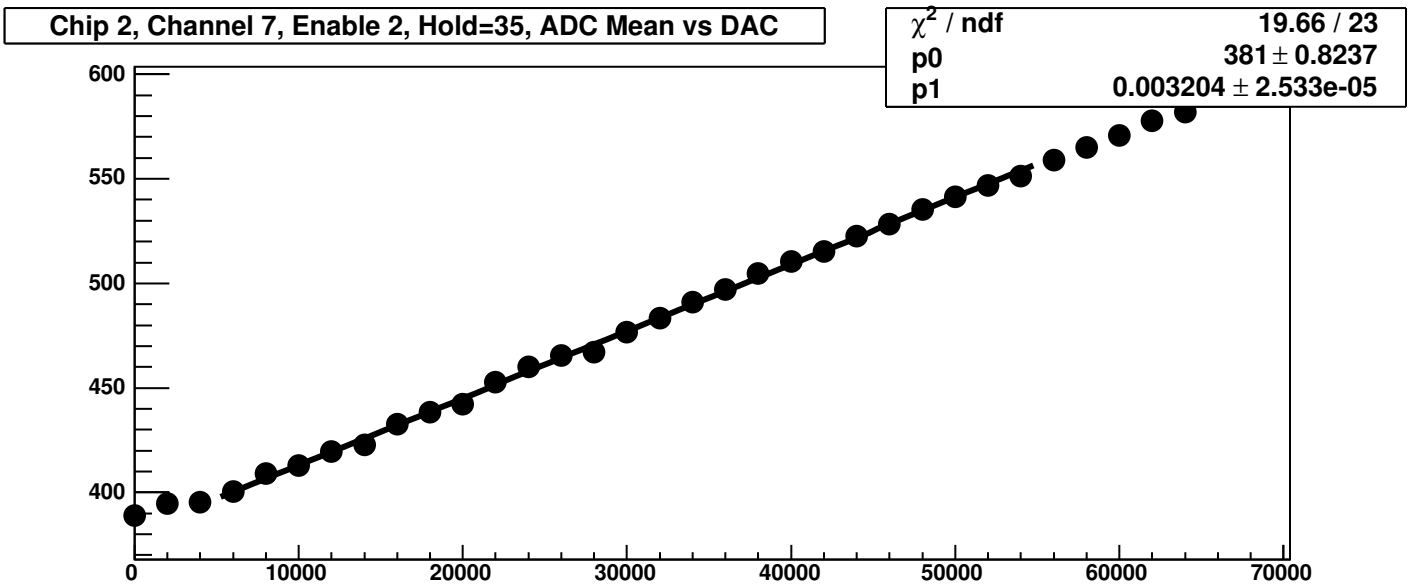

Chip 2, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000

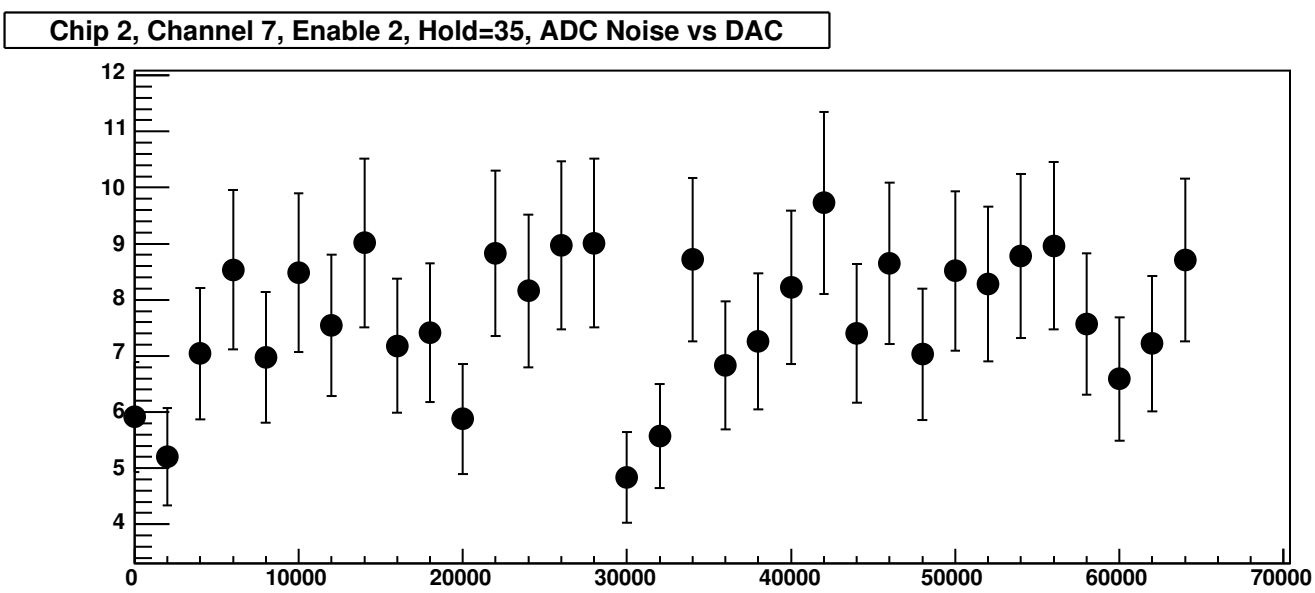

Chip 2, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

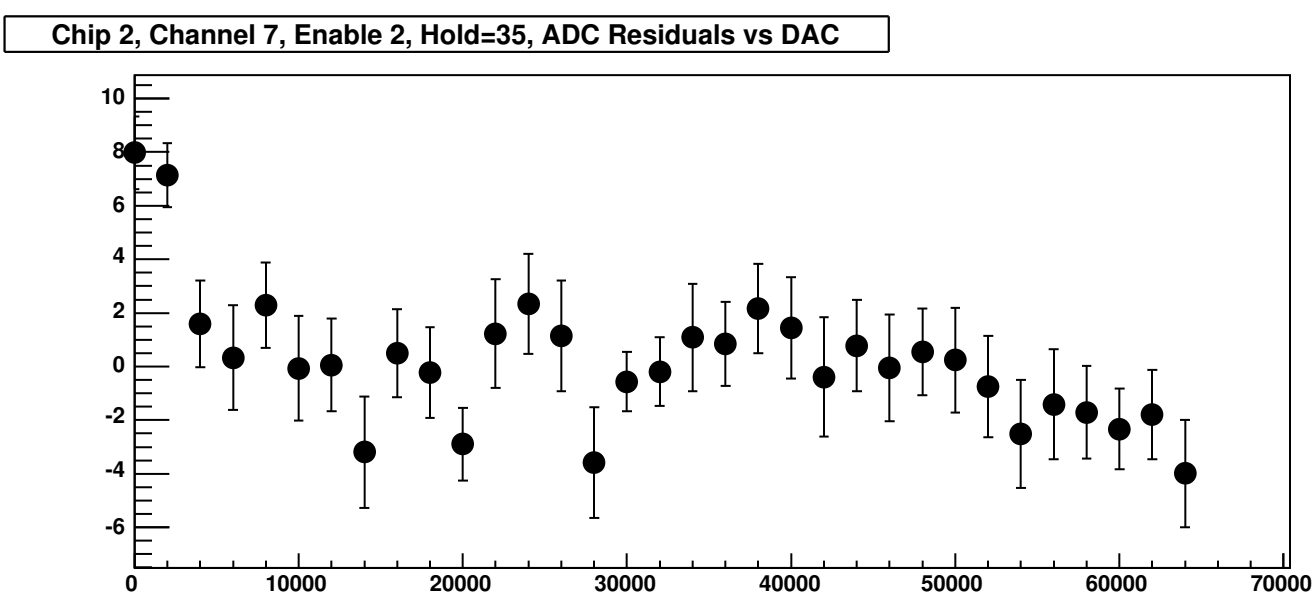

Chip 2, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 32.74 / 23
p0 380.3 ± 0.7641
p1 0.003224 ± 2.264e-05
600
550
500
450
400
0
10000
20000
30000
40000
50000
60000
70000

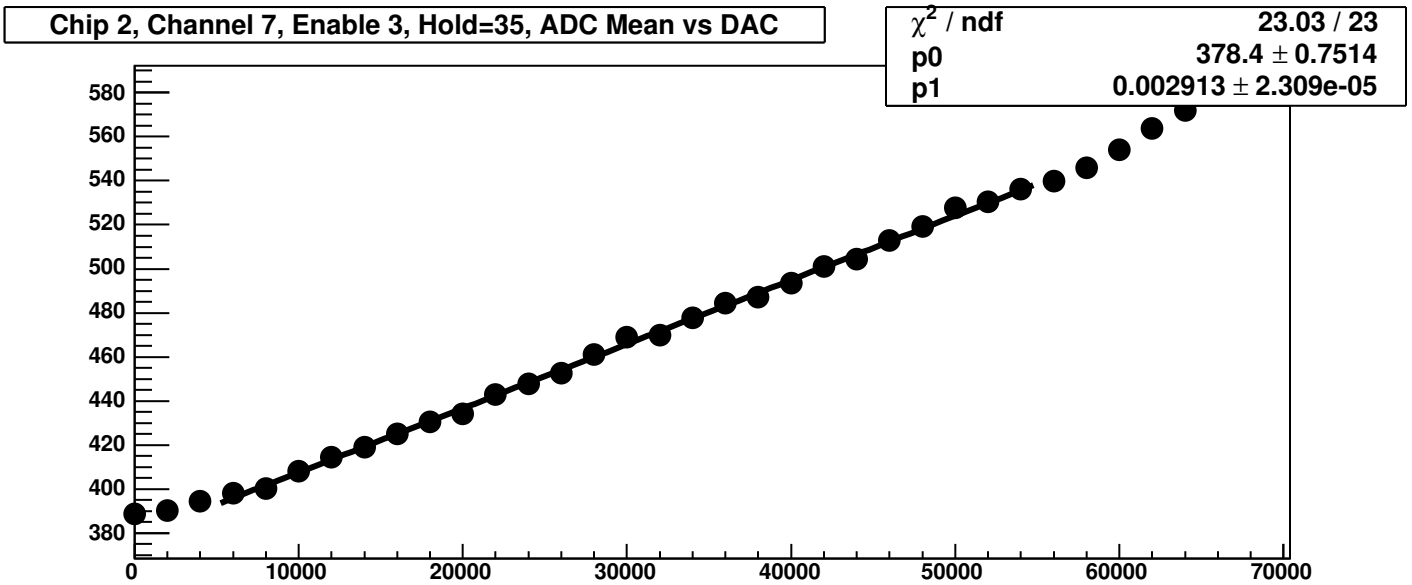

Chip 2, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

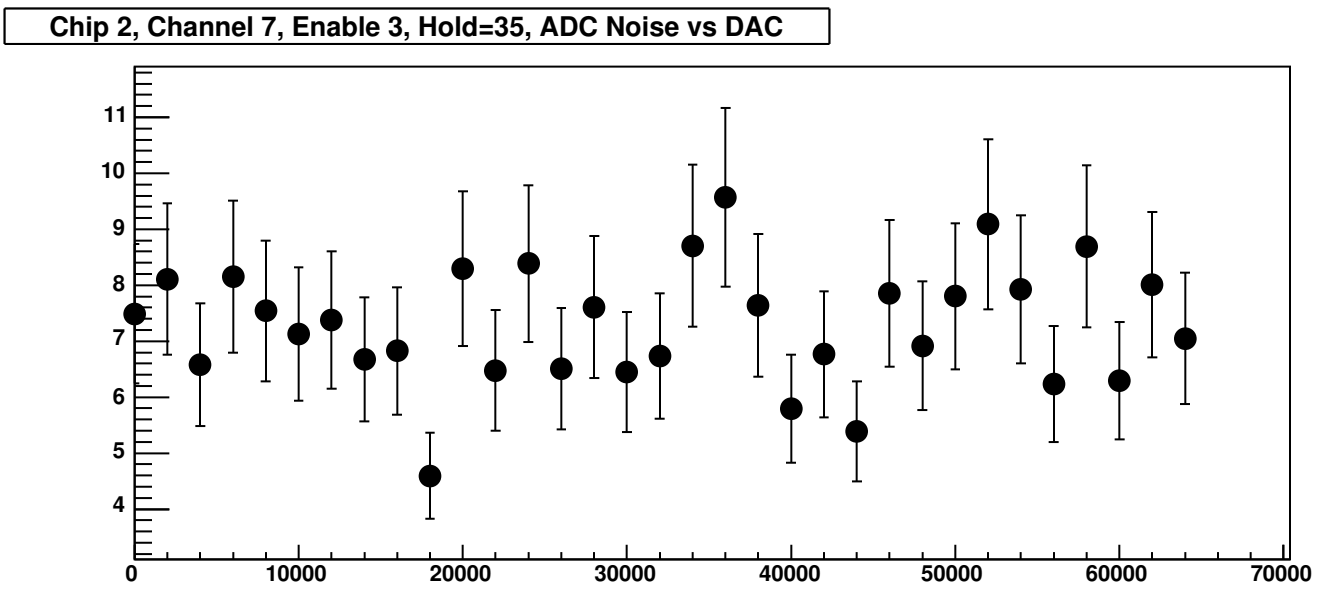

Chip 2, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

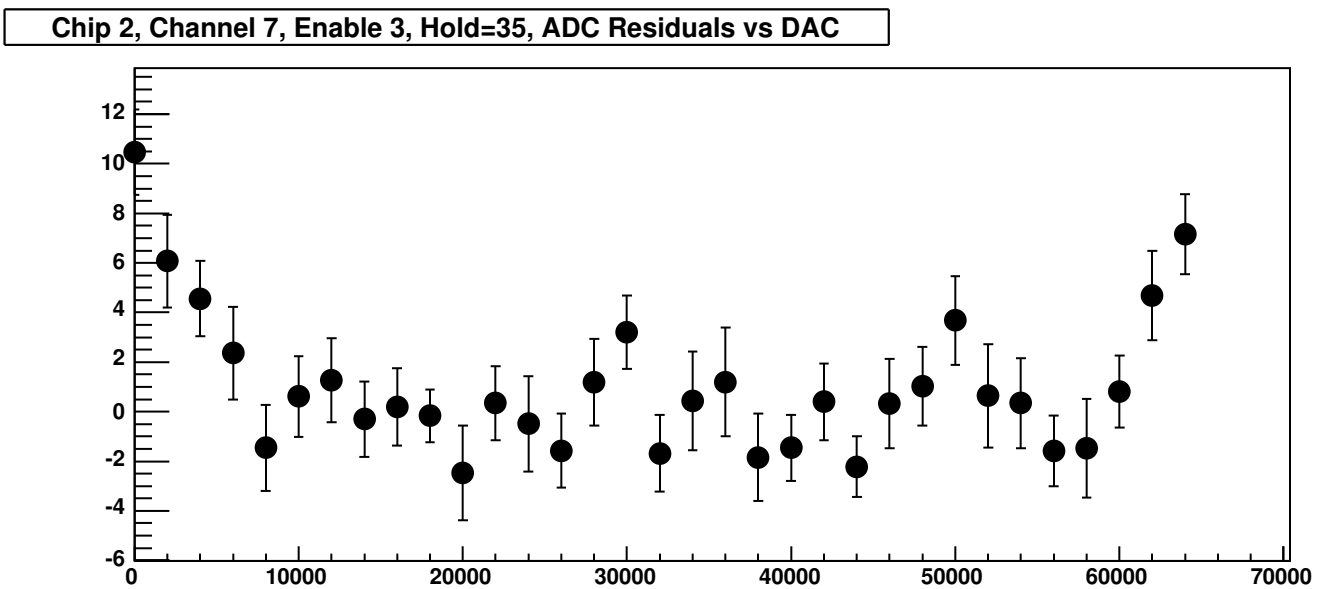

Chip 2, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 19.66 / 23
p0 381 ± 0.8237
p1 0.003204 ± 2.533e-05
600
550
500
450
400
0
10000
20000
30000
40000
50000
60000
70000

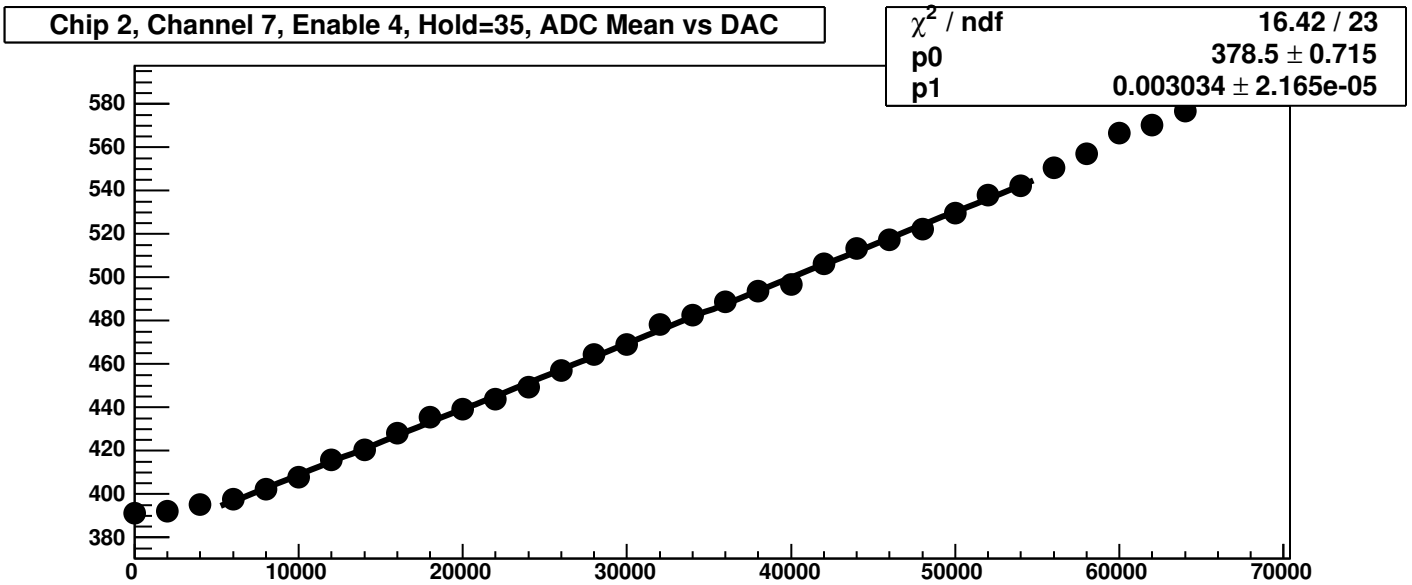

Chip 2, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

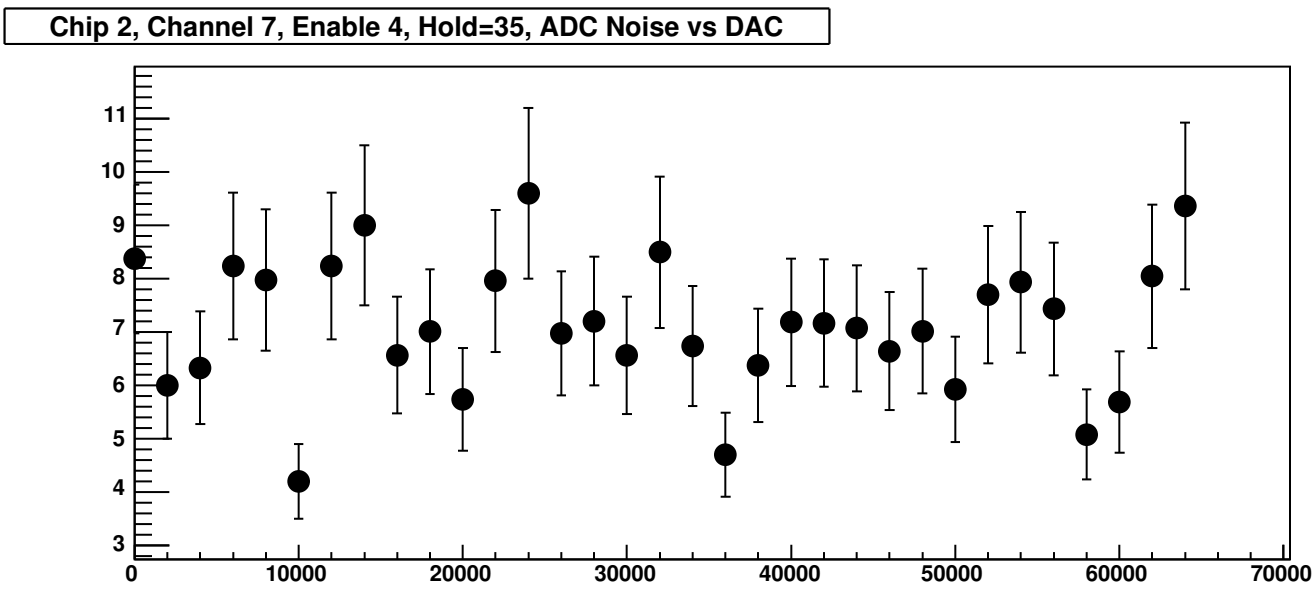

Chip 2, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

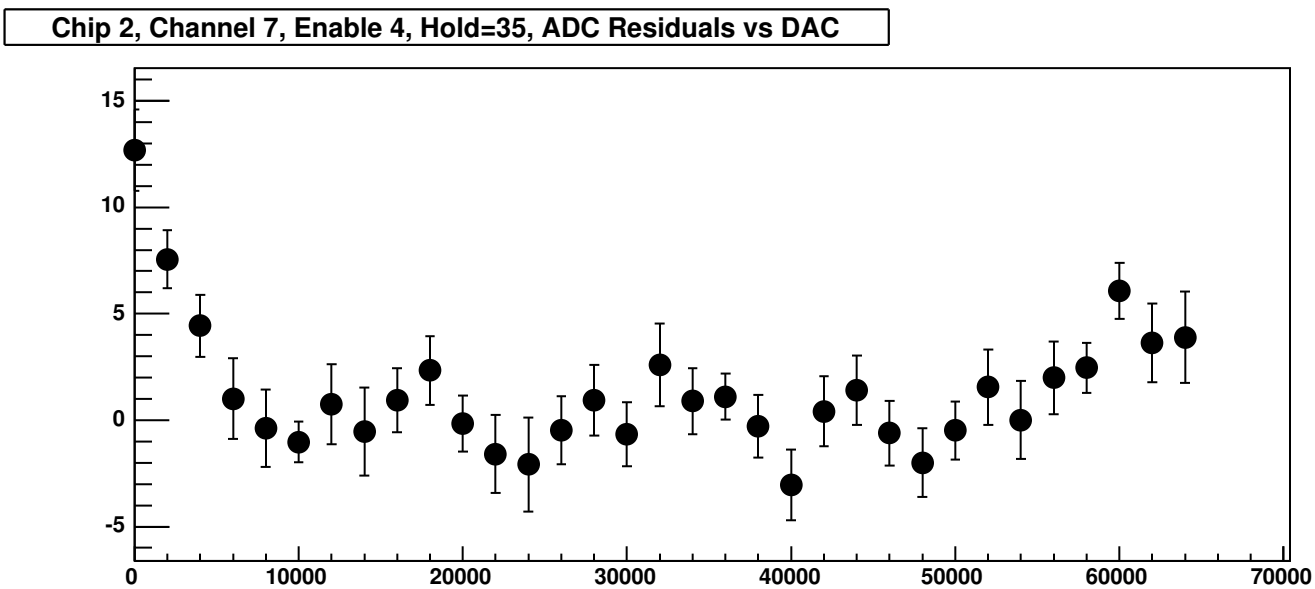

Chip 2, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.03 / 23
p0 378.4 ± 0.7514
p1 0.002913 ± 2.309e-05
580
560
540
520
500
480
460
440
420
400
380
0
10000
20000
30000
40000
50000
60000
70000

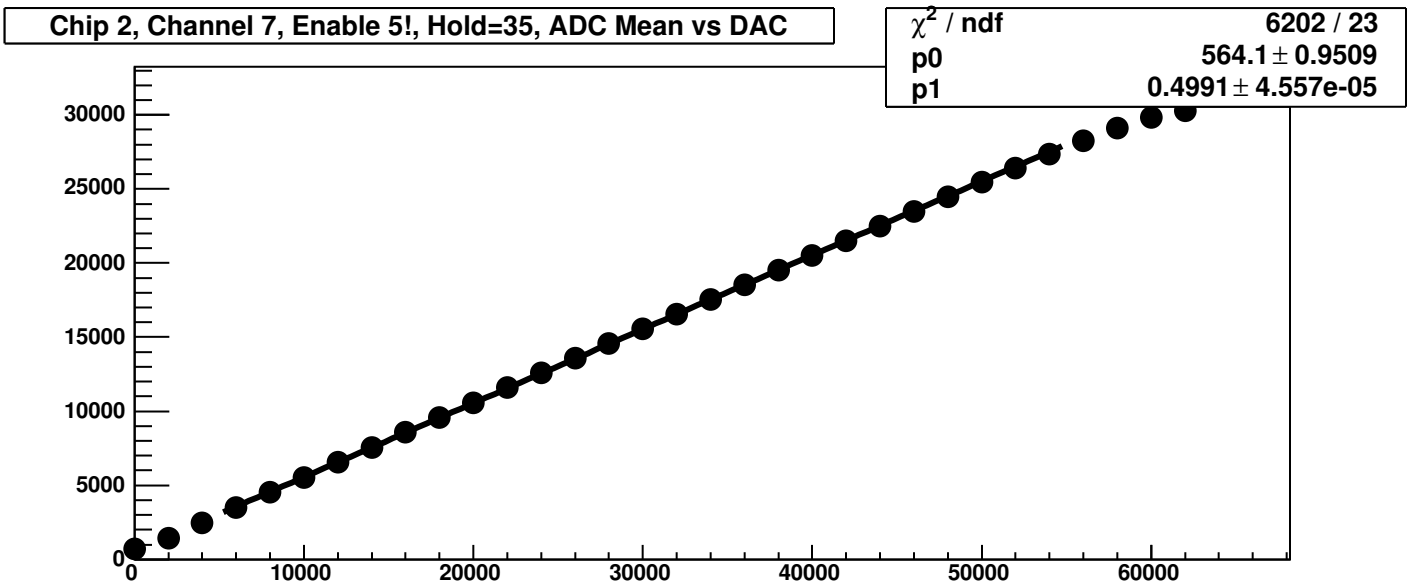

Chip 2, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

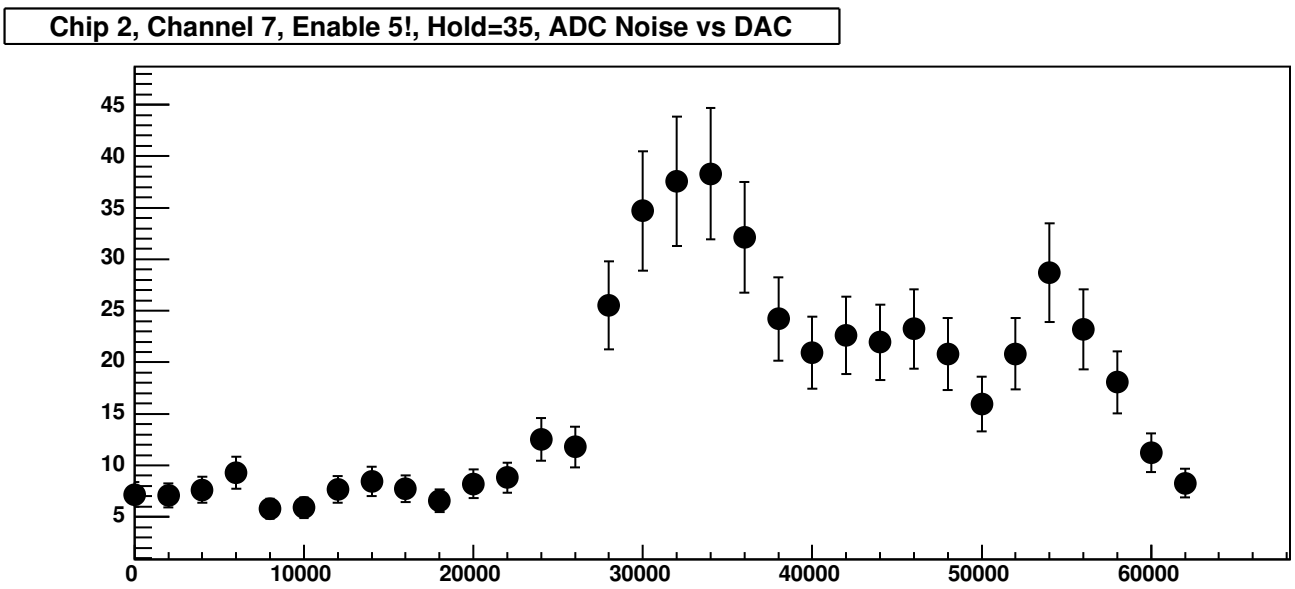

Chip 2, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

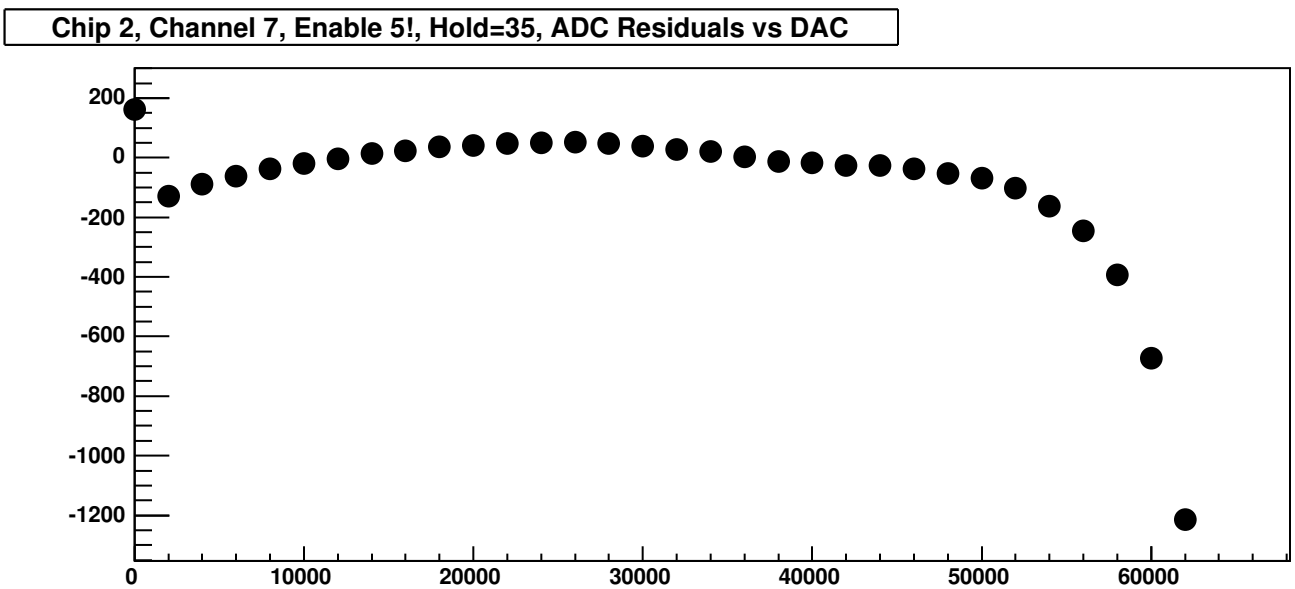

Chip 2, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 16.42 / 23
p0 378.5 ± 0.715
p1 0.003034 ± 2.165e-05
580
560
540
520
500
480
460
440
420
400
380
0
10000
20000
30000
40000
50000
60000
70000

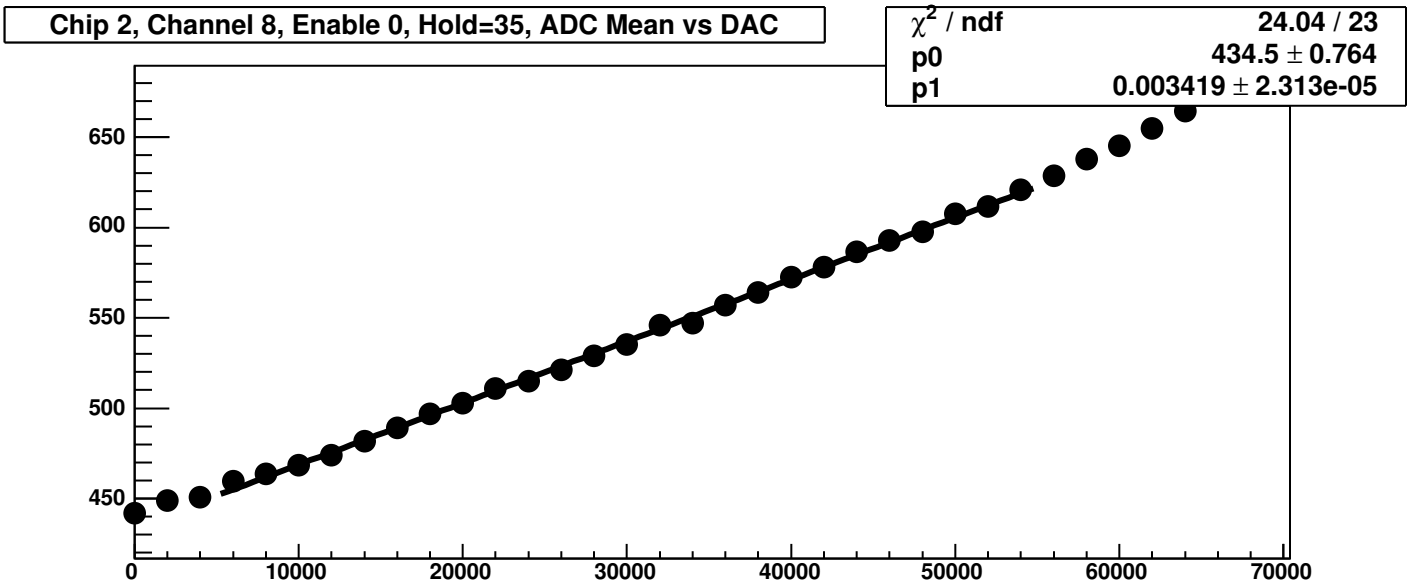

Chip 2, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

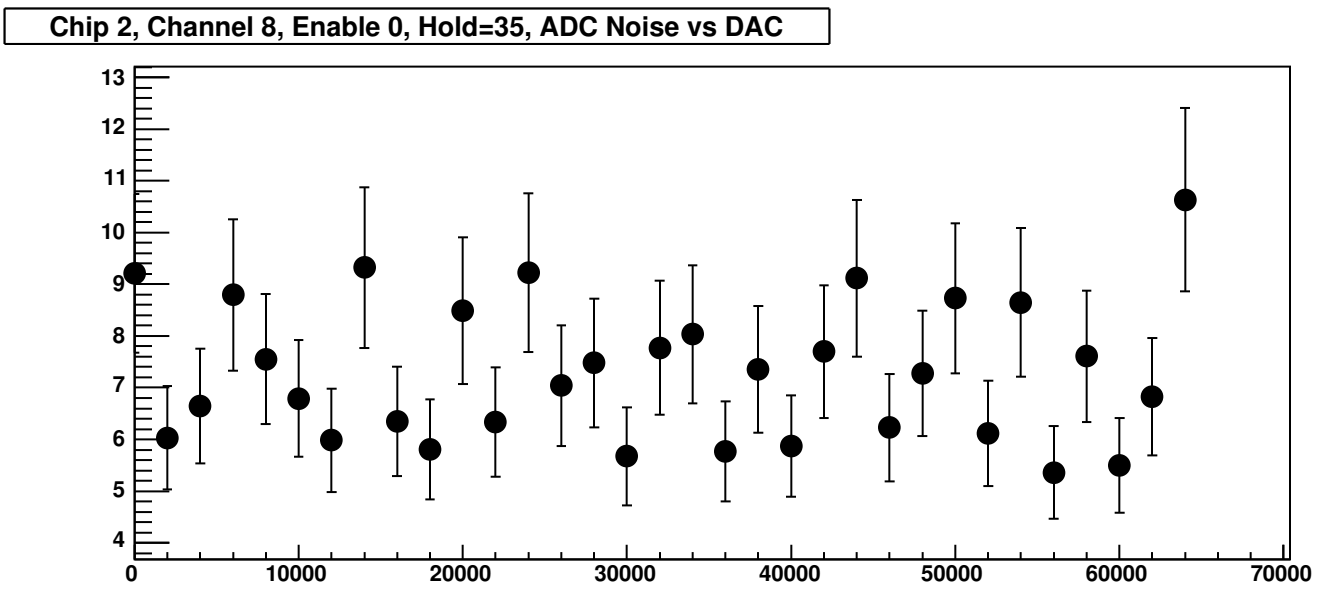

Chip 2, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

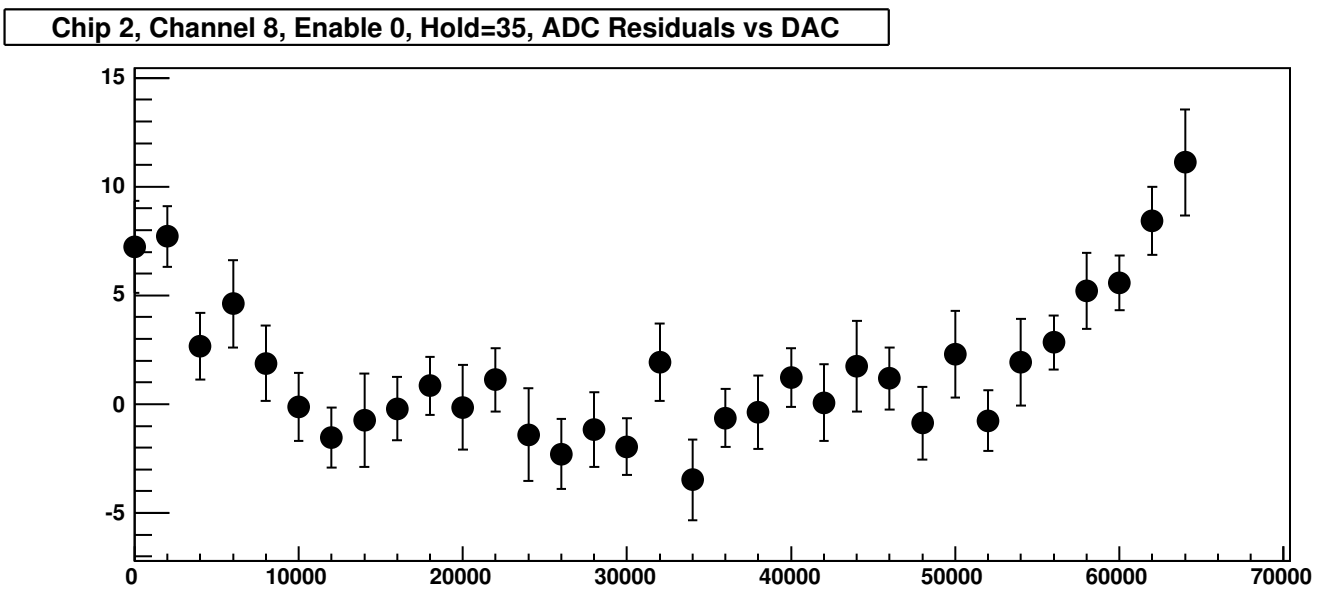

Chip 2, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 6202 / 23
p0 564.1 ± 0.9509
p1 0.4991 ± 4.557e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

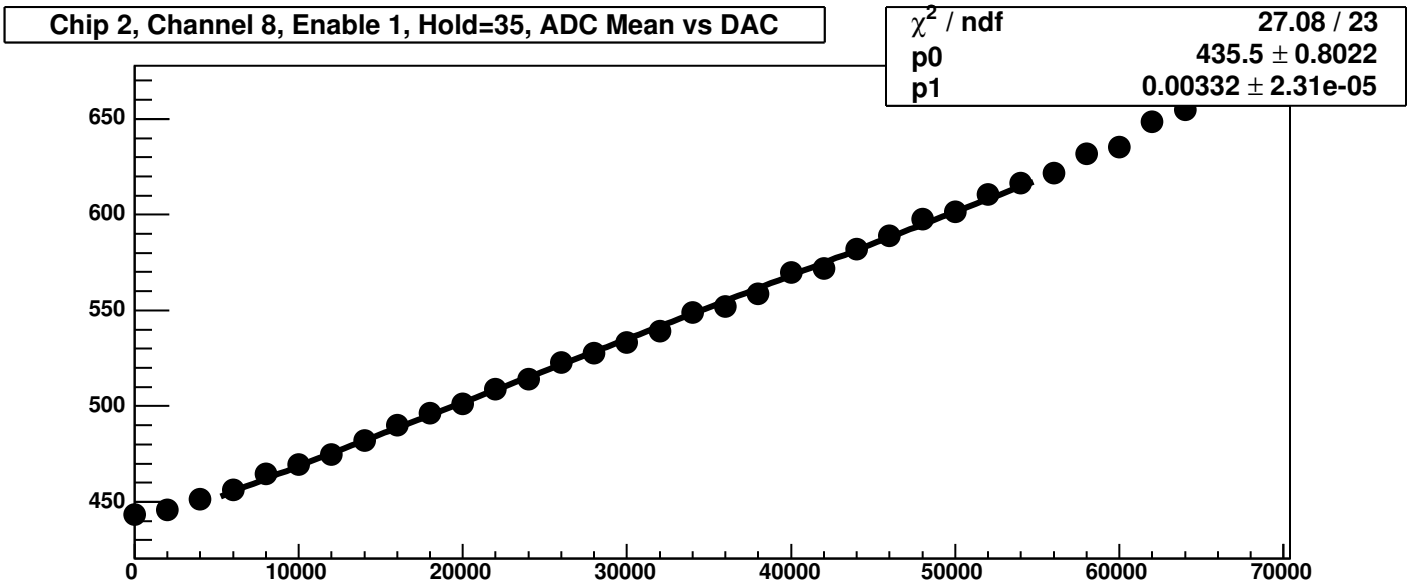

Chip 2, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000

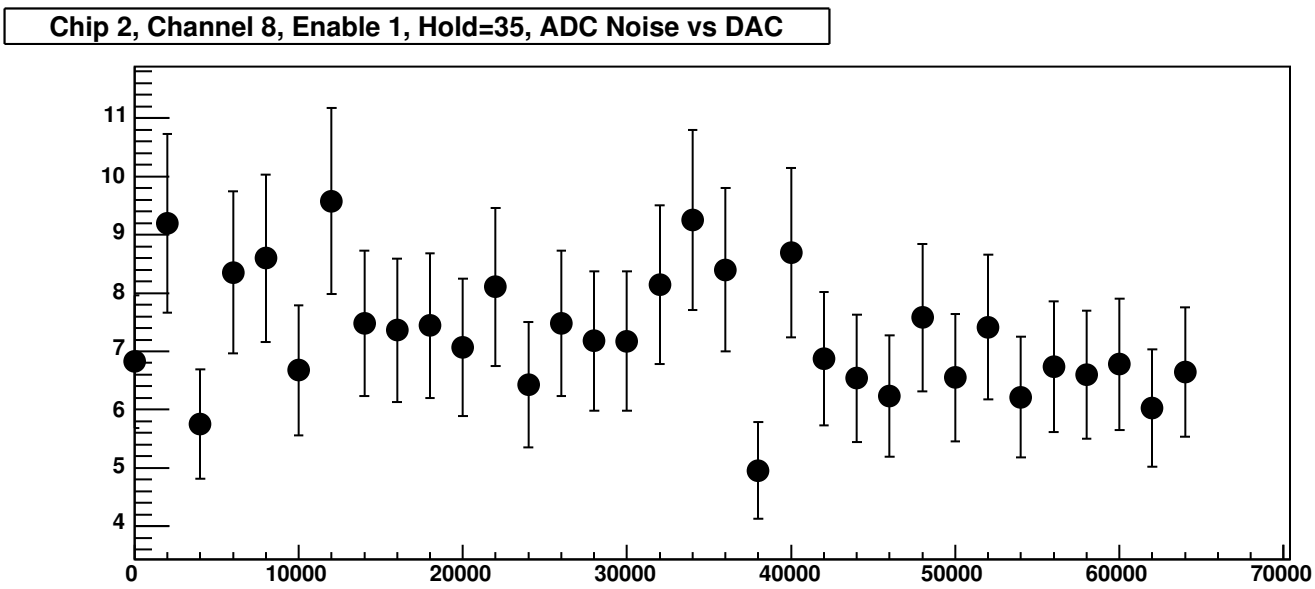

Chip 2, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
0
10000
20000
30000
40000
50000
60000

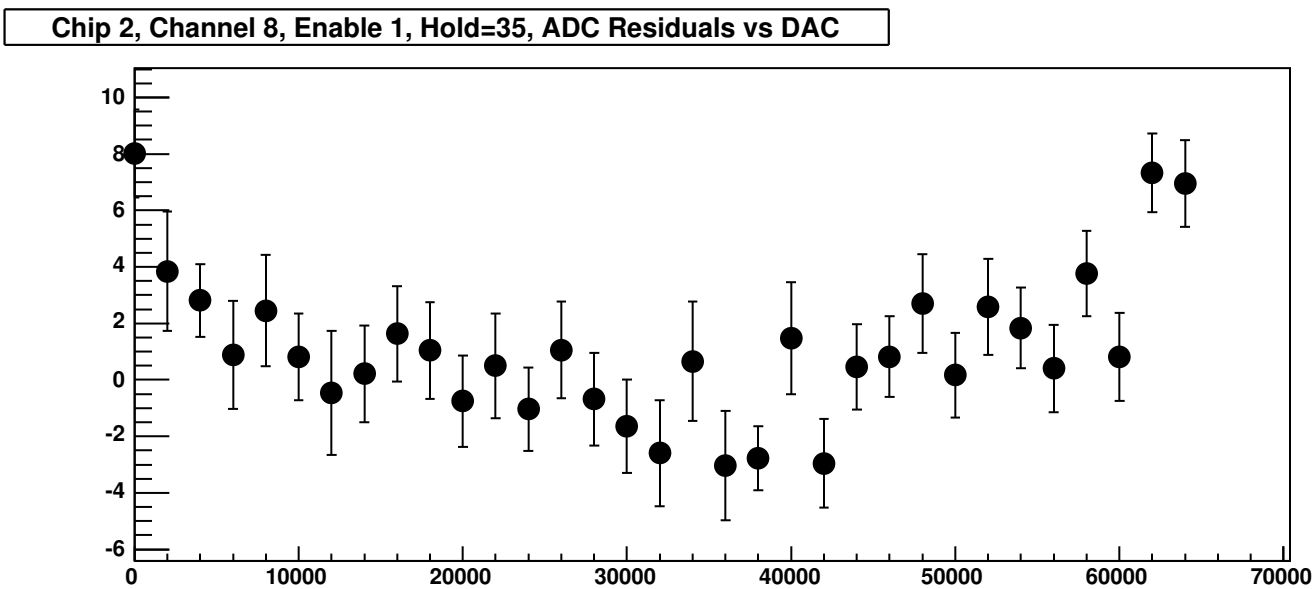

Chip 2, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 24.04 / 23
p0 434.5 ± 0.764
p1 0.003419 ± 2.313e-05
650
600
550
500
450
0
10000
20000
30000
40000
50000
60000
70000

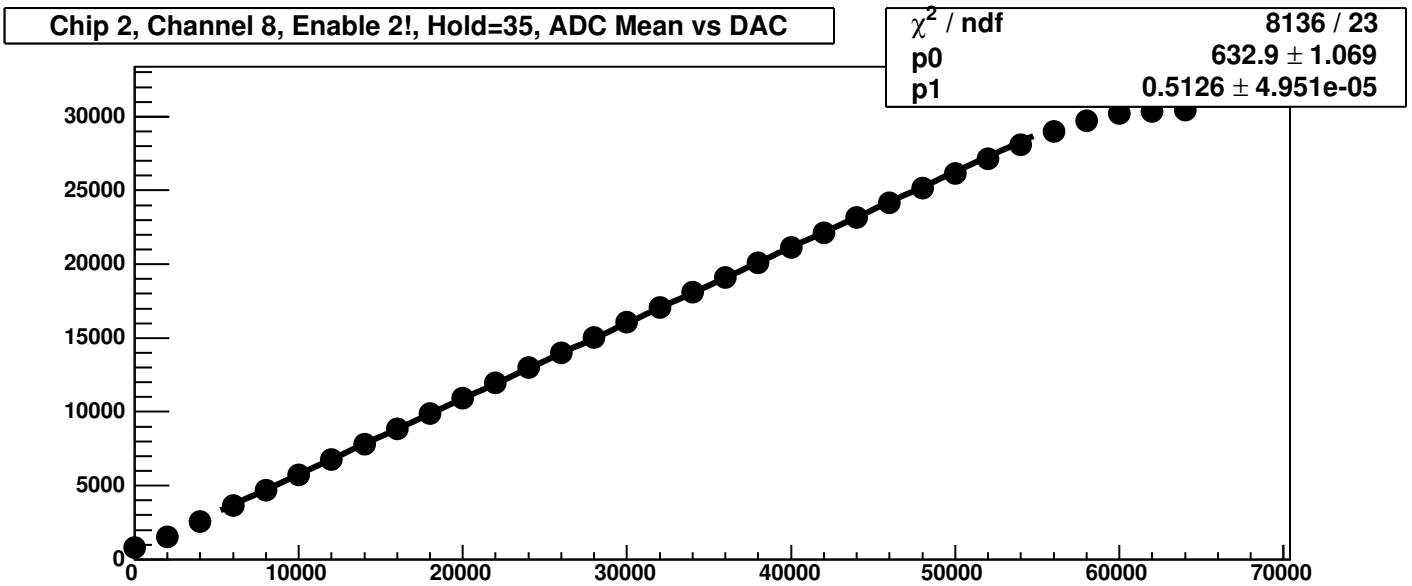

Chip 2, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

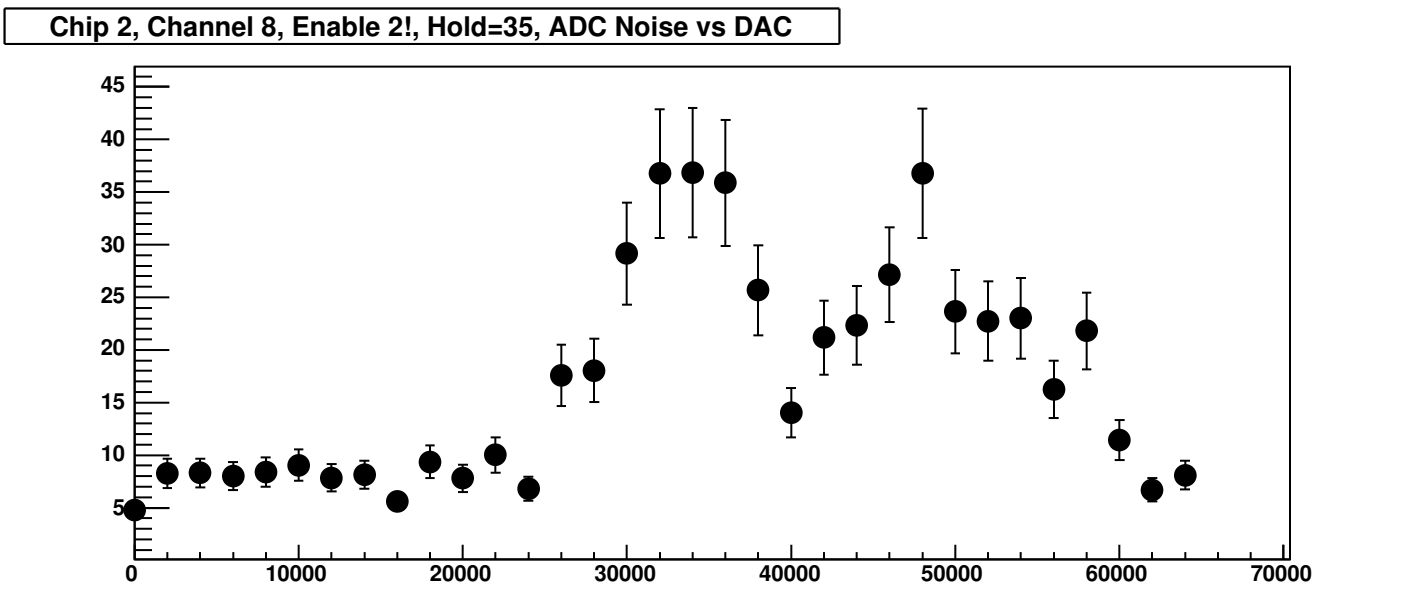

Chip 2, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

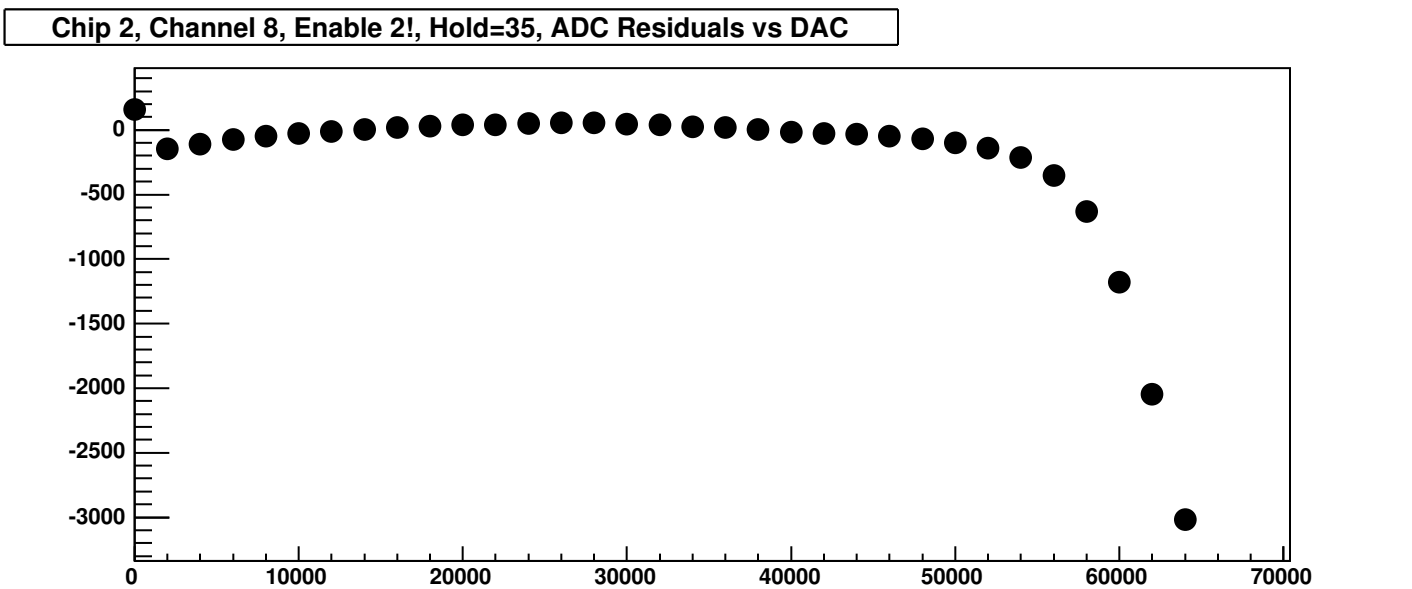

Chip 2, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 27.08 / 23
p0 435.5 ± 0.8022
p1 0.00332 ± 2.31e-05
650
600
550
500
450
0
10000
20000
30000
40000
50000
60000
70000

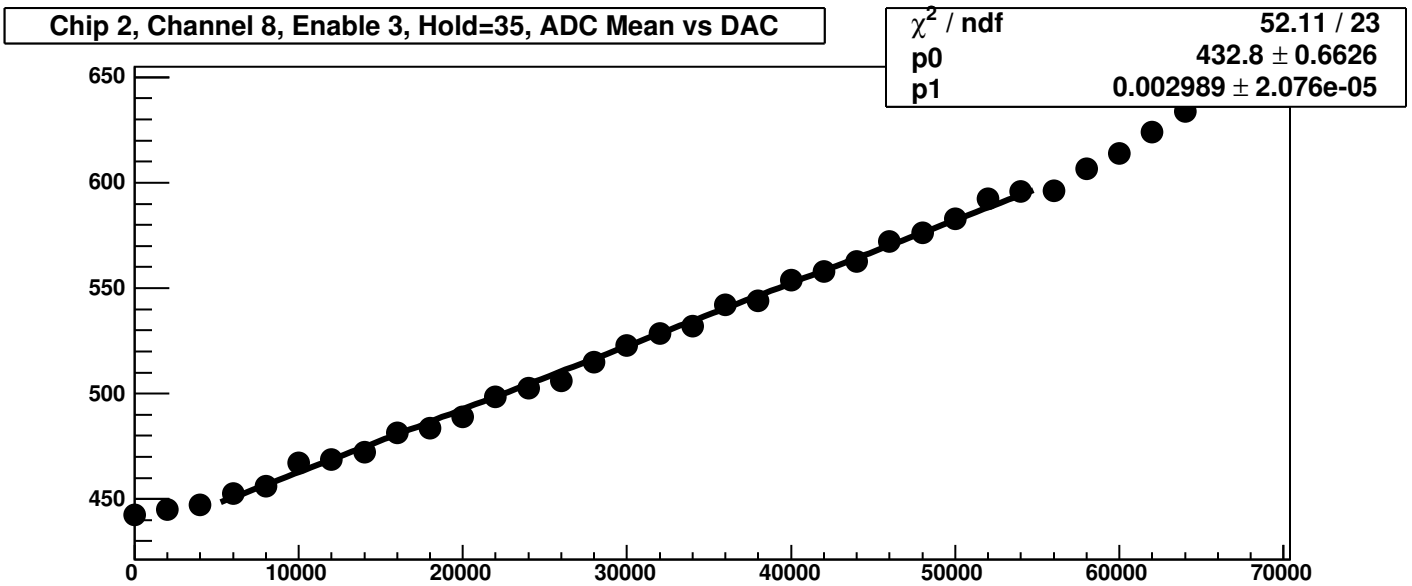

Chip 2, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

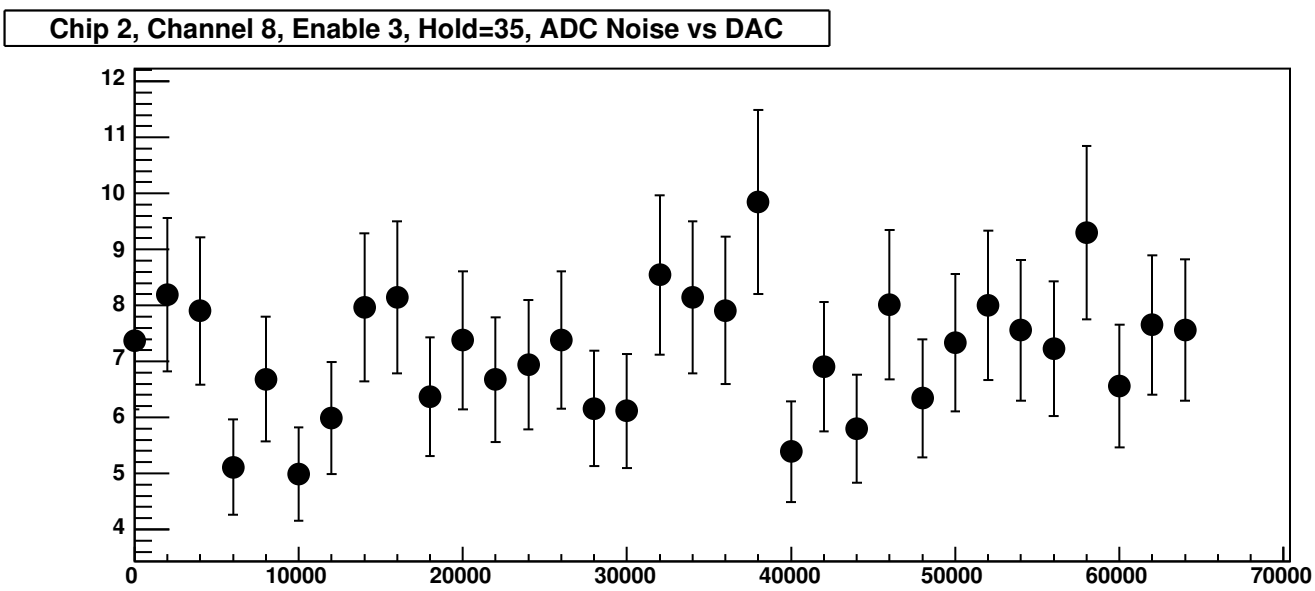

Chip 2, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

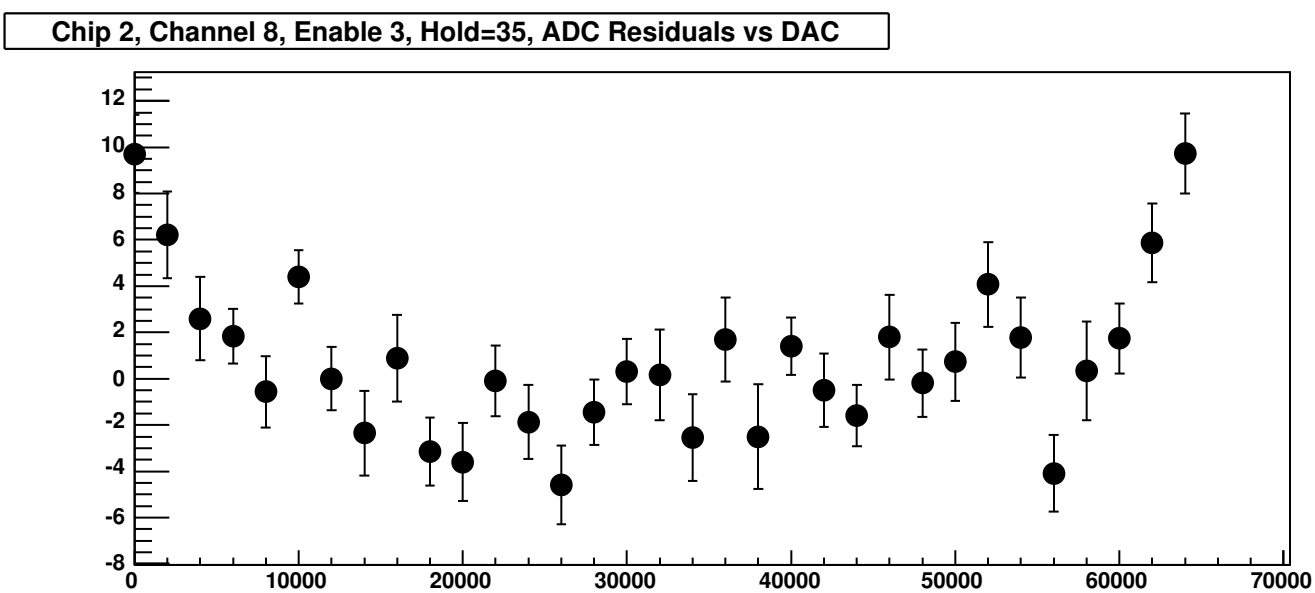

Chip 2, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 8136 / 23
p0 632.9 ± 1.069
p1 0.5126 ± 4.951e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

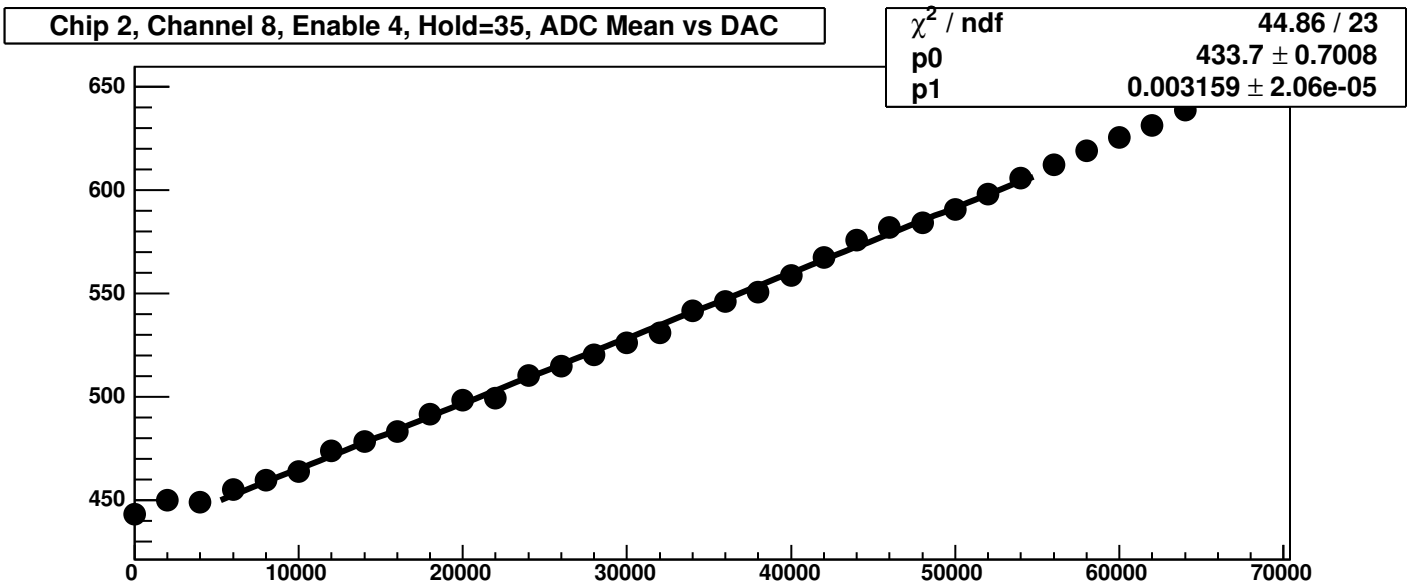

Chip 2, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000

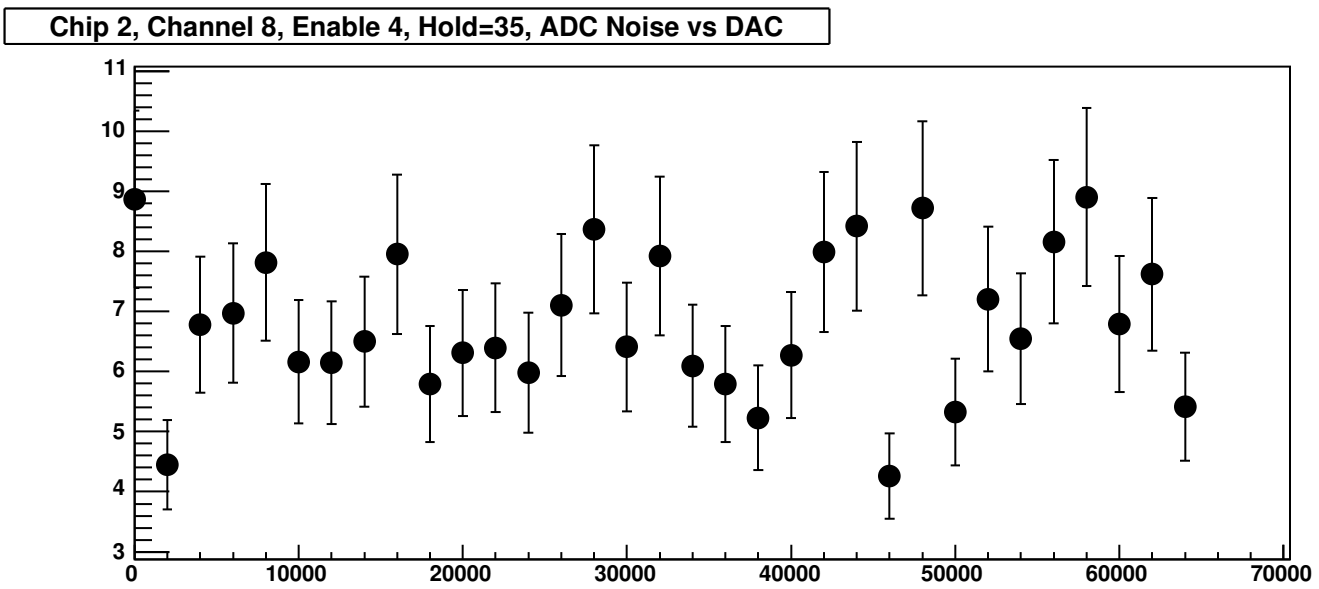

Chip 2, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
0
10000
20000
30000
40000
50000
60000
70000

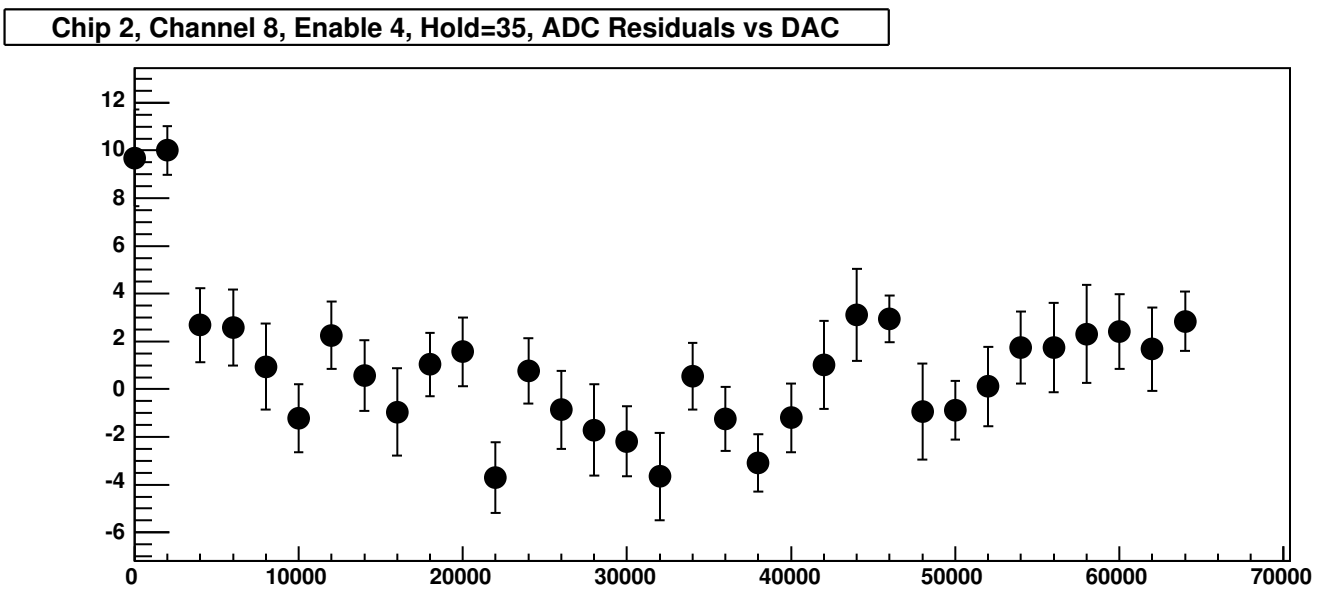

Chip 2, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 52.11 / 23
p0 432.8 ± 0.6626
p1 0.002989 ± 2.076e-05
650
600
550
500
450
0
10000
20000
30000
40000
50000
60000
70000

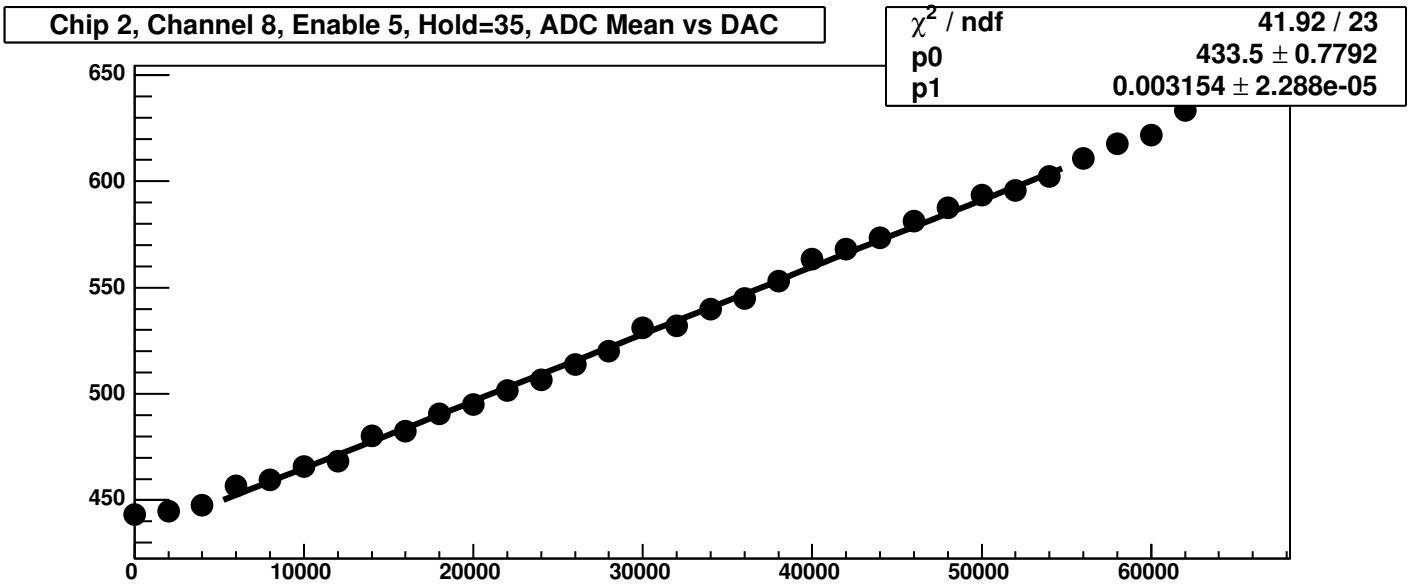

Chip 2, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

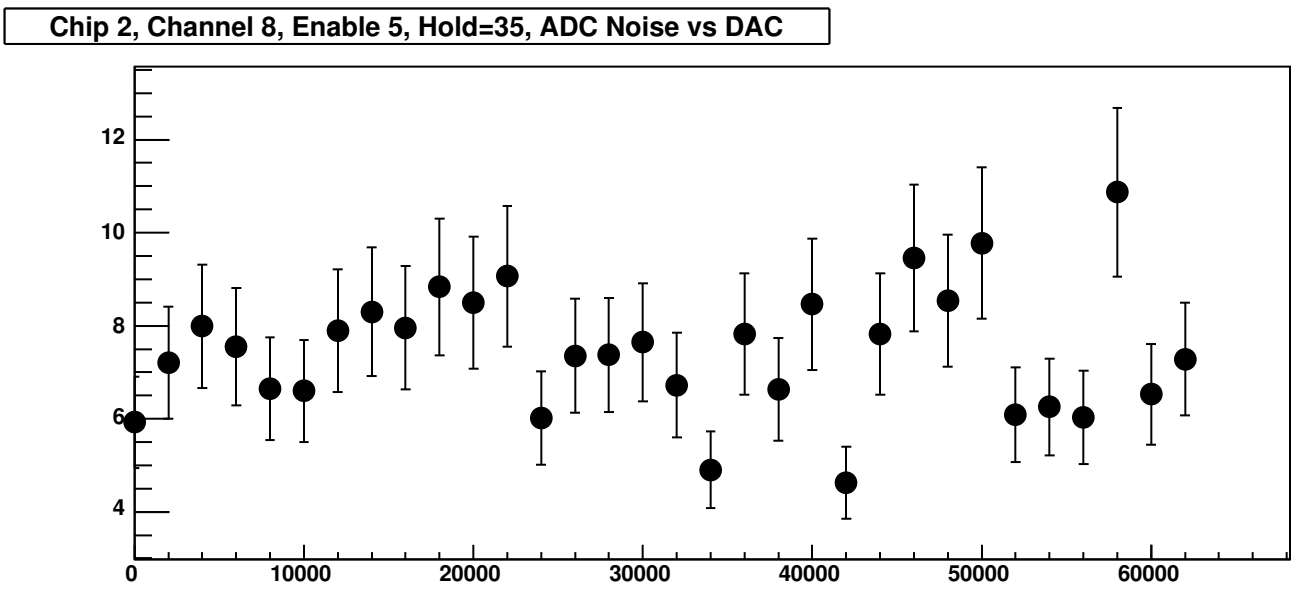

Chip 2, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

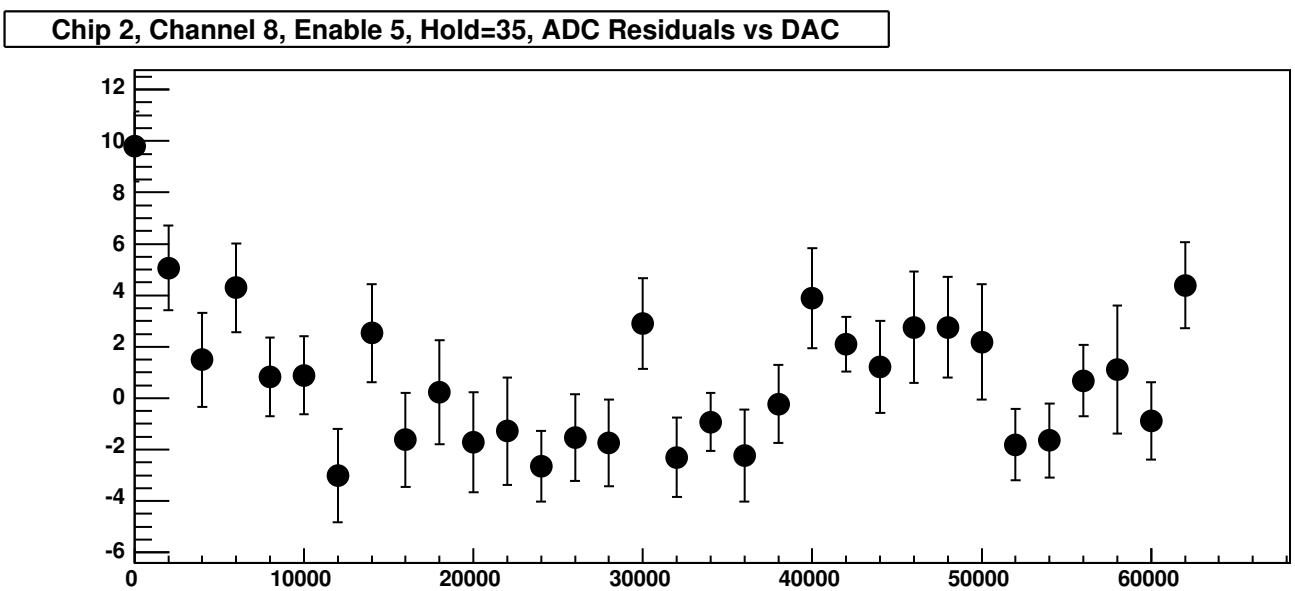

Chip 2, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 44.86 / 23
p0 433.7 ± 0.7008
p1 0.003159 ± 2.06e-05
650
600
550
500
450
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

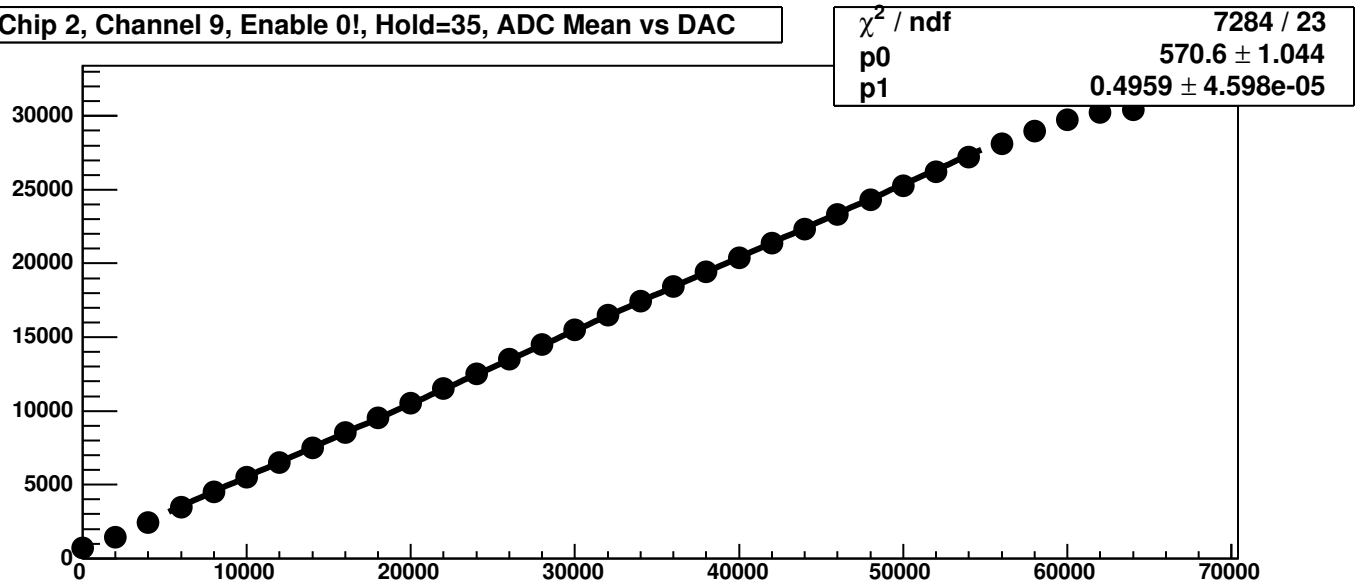

Chip 2, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 41.92 / 23
p0 433.5 ± 0.7792
p1 0.003154 ± 2.288e-05
650
600
550
500
450
0
10000
20000
30000
40000
50000
60000

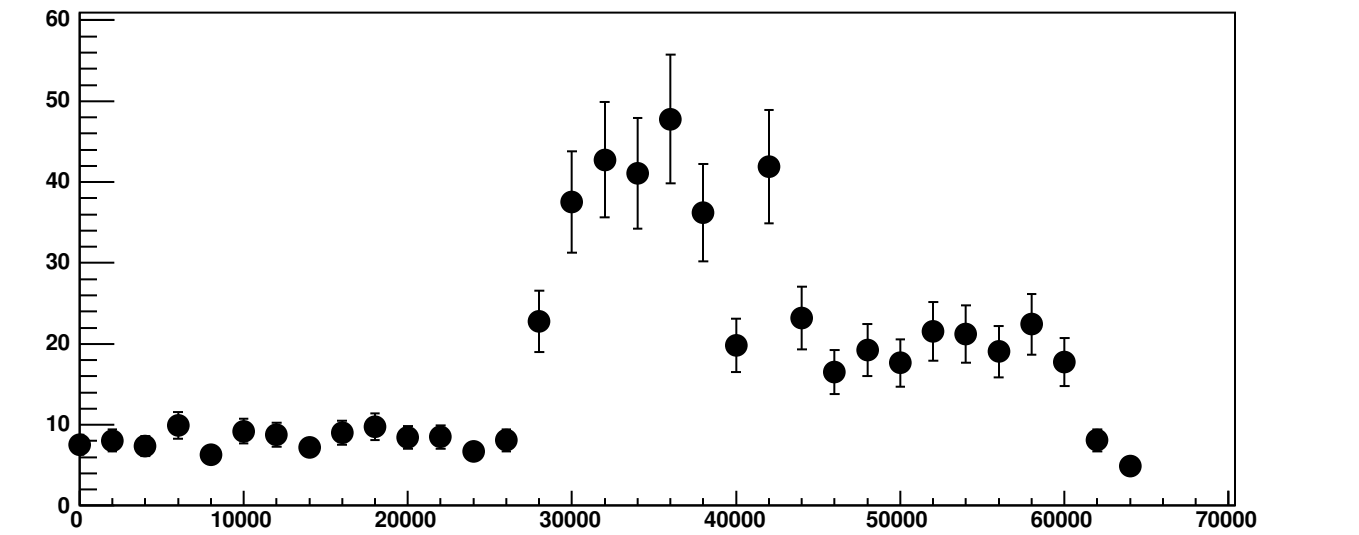

Chip 2, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000

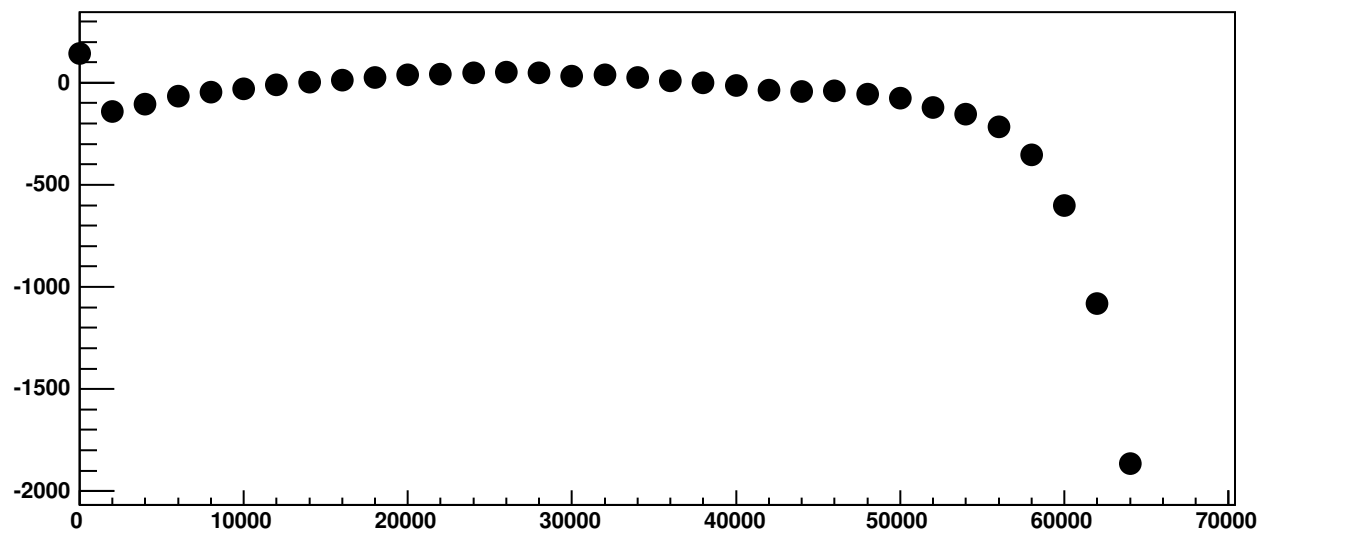
Chip 2, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC



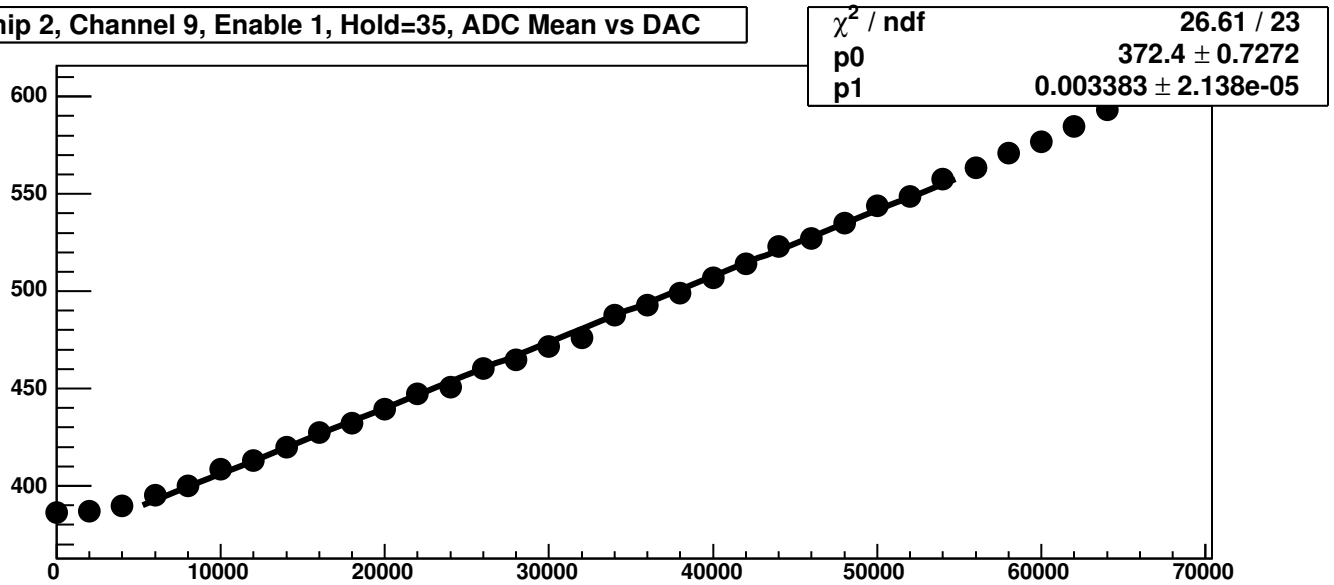
Chip 2, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC



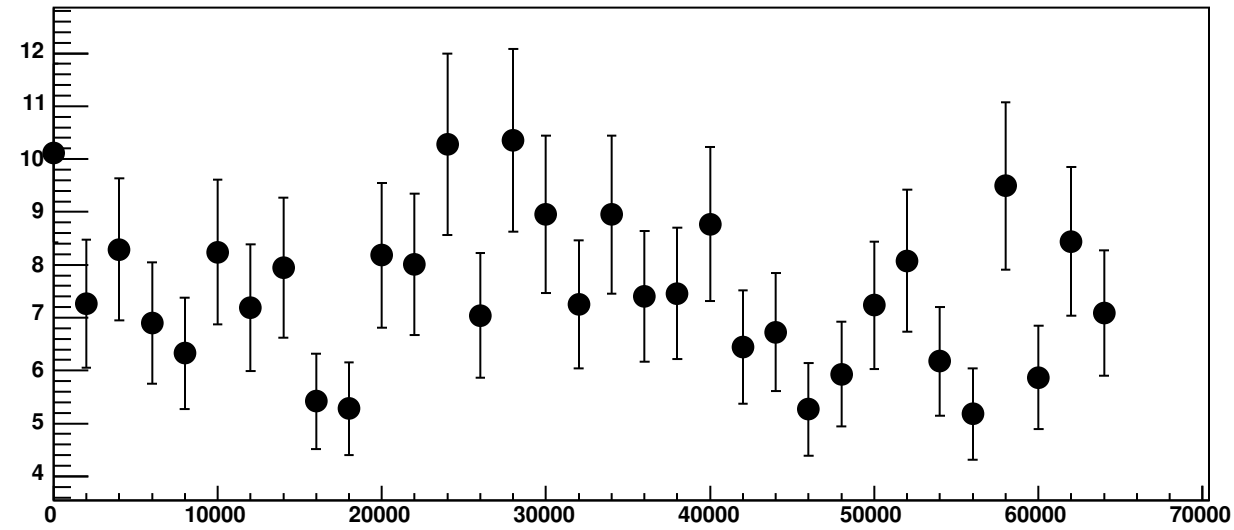
Chip 2, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC



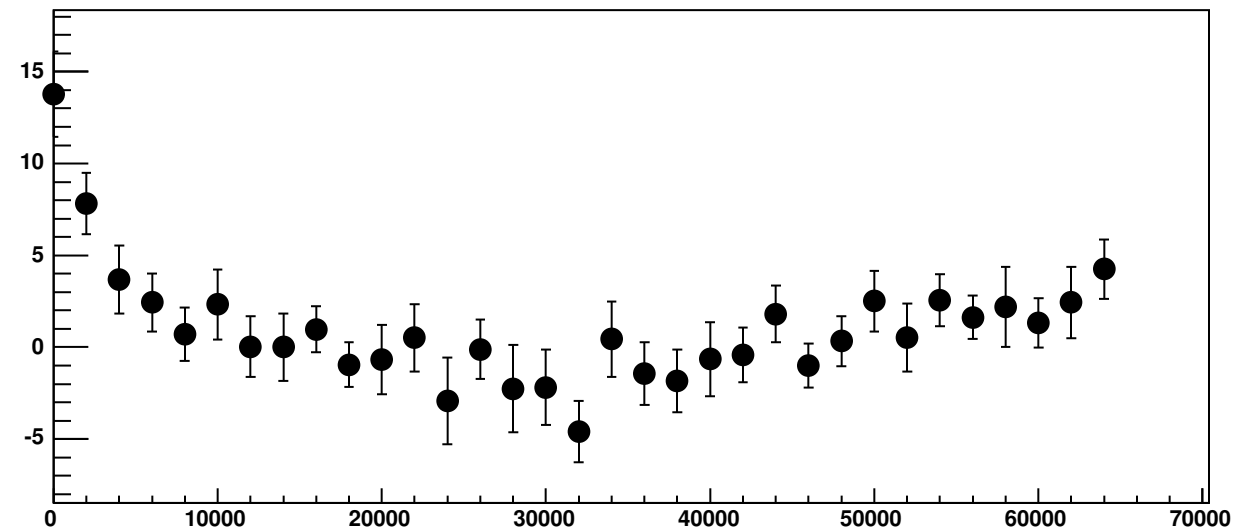
Chip 2, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC

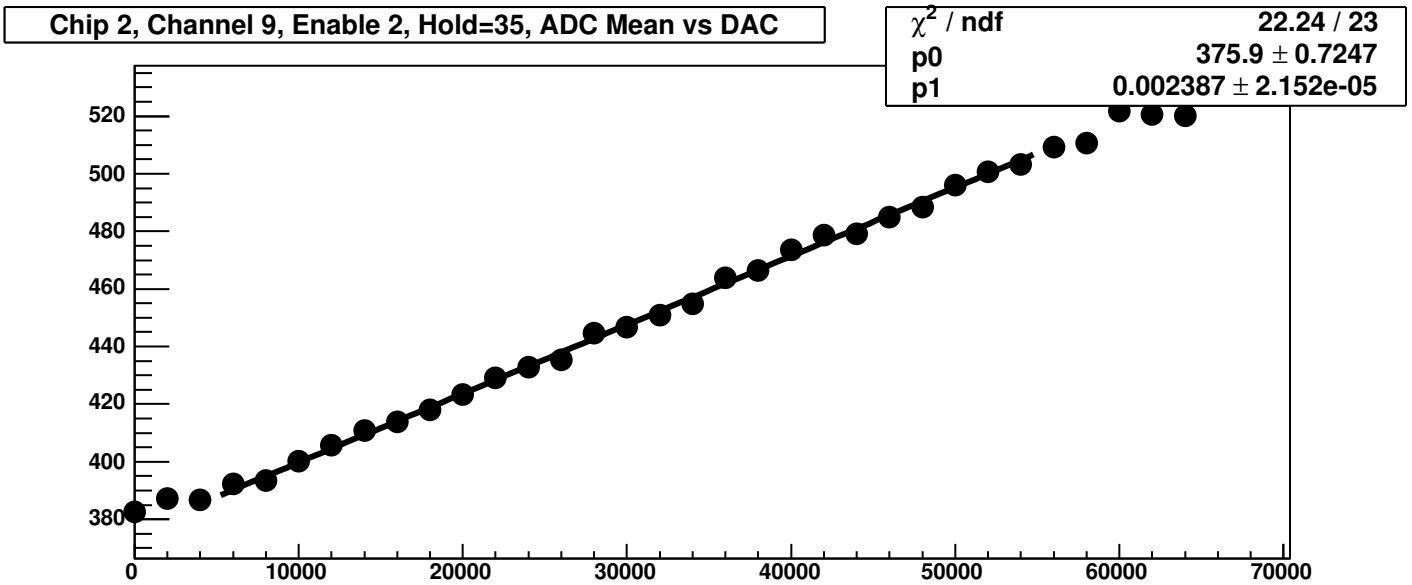


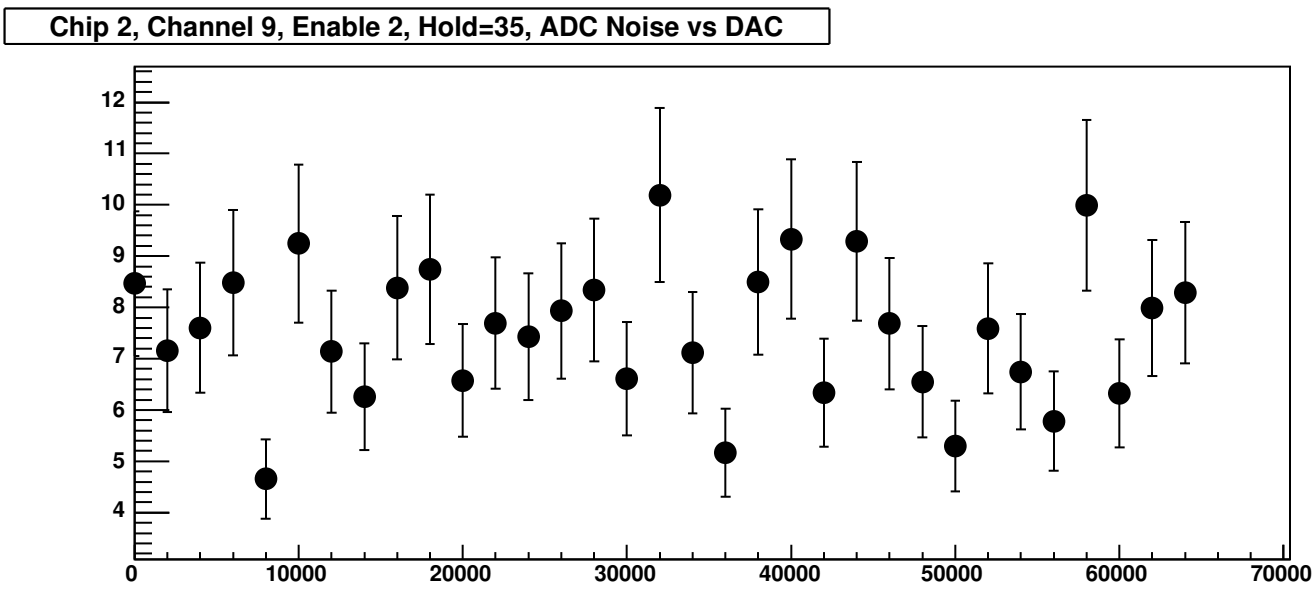
Chip 2, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC

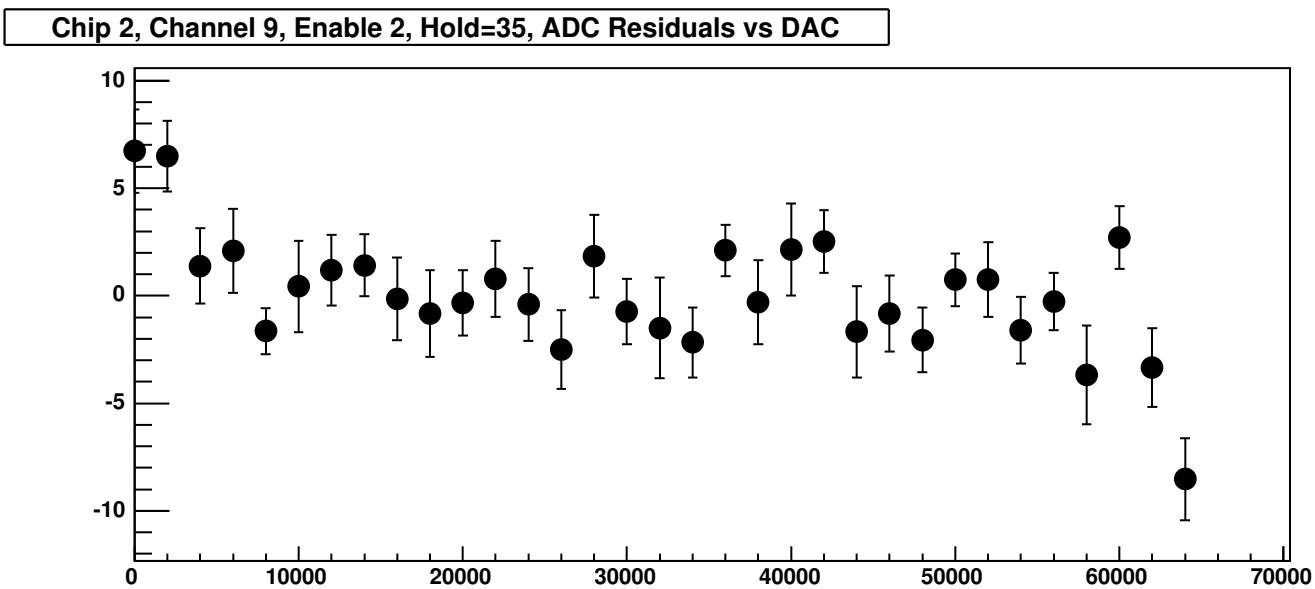


Chip 2, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC

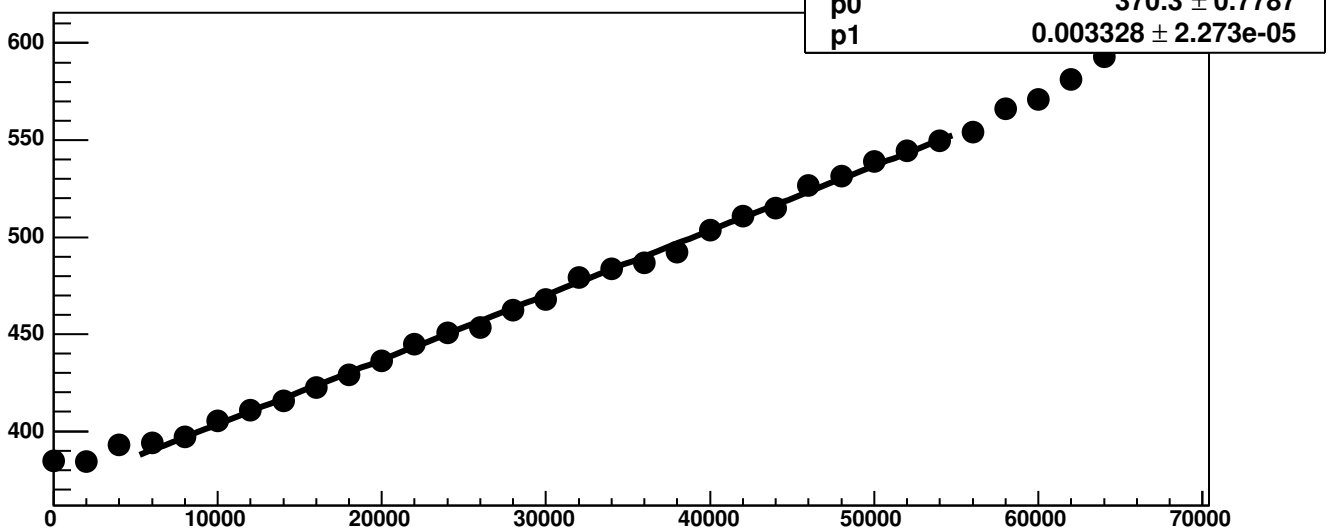



Chip 2, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 22.24 / 23
p0 375.9 ± 0.7247
p1 0.002387 ± 2.152e-05
520
500
480
460
440
420
400
380
0
10000
20000
30000
40000
50000
60000
70000

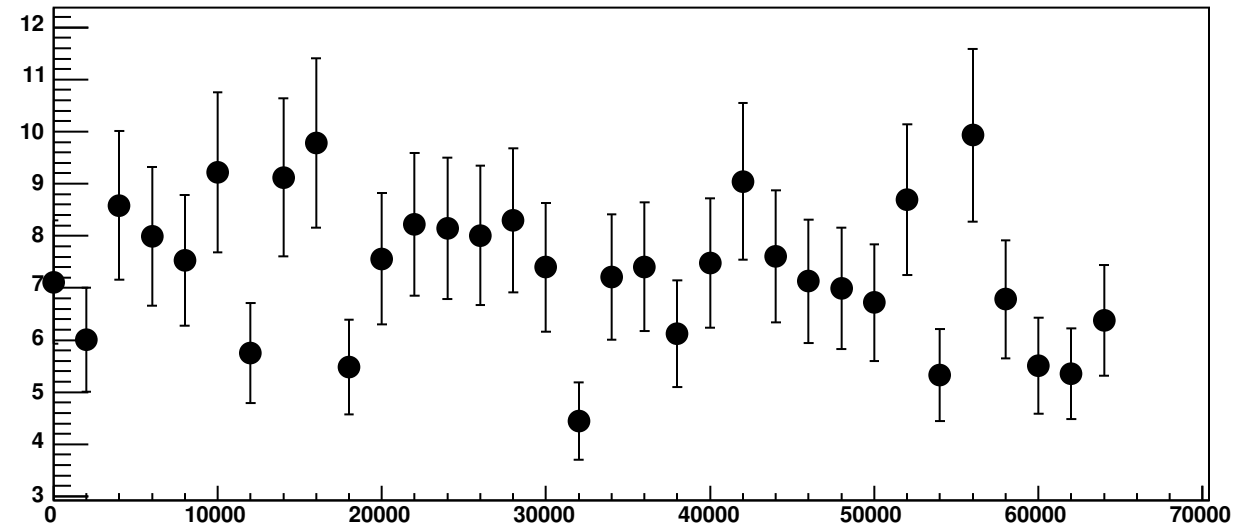

Chip 2, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

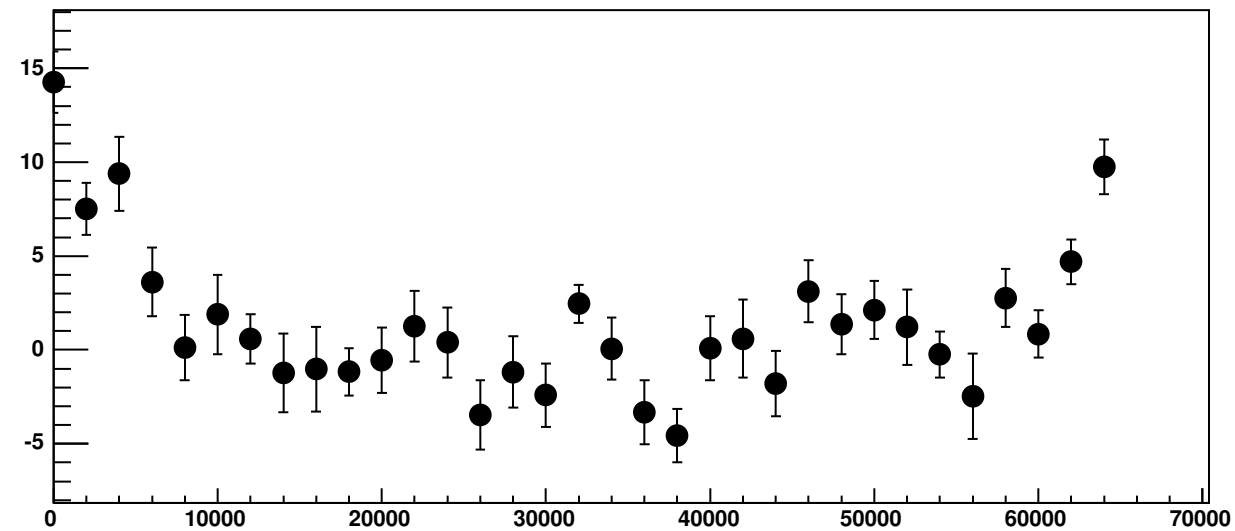
Chip 2, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC

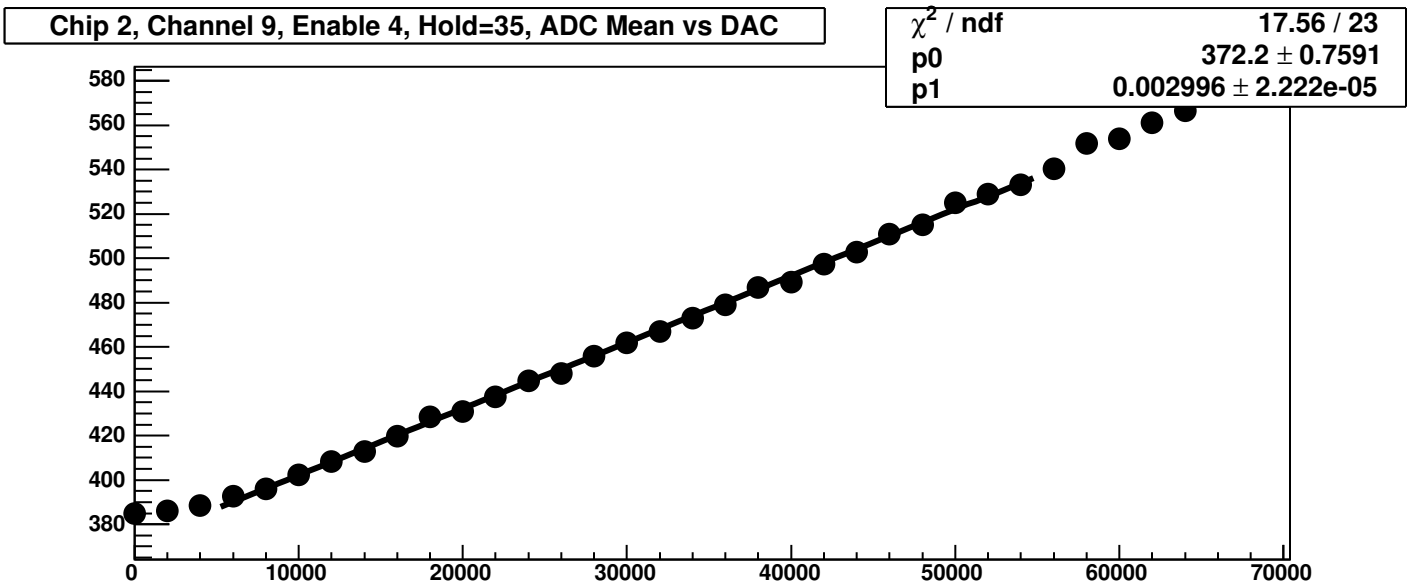


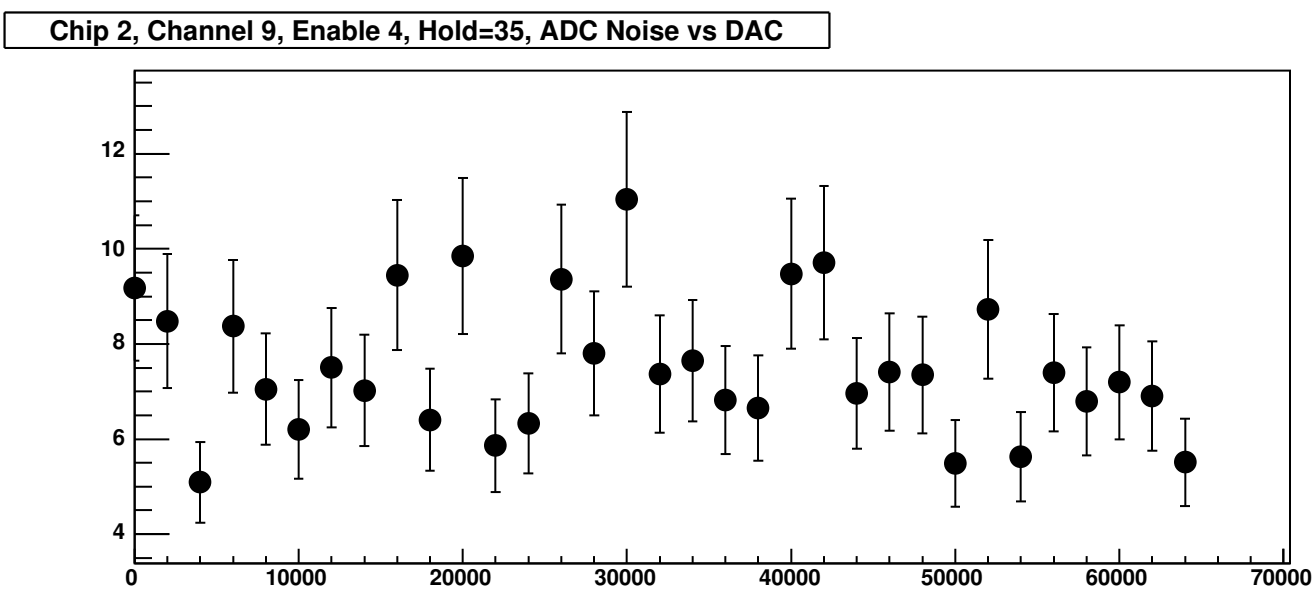
Chip 2, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC

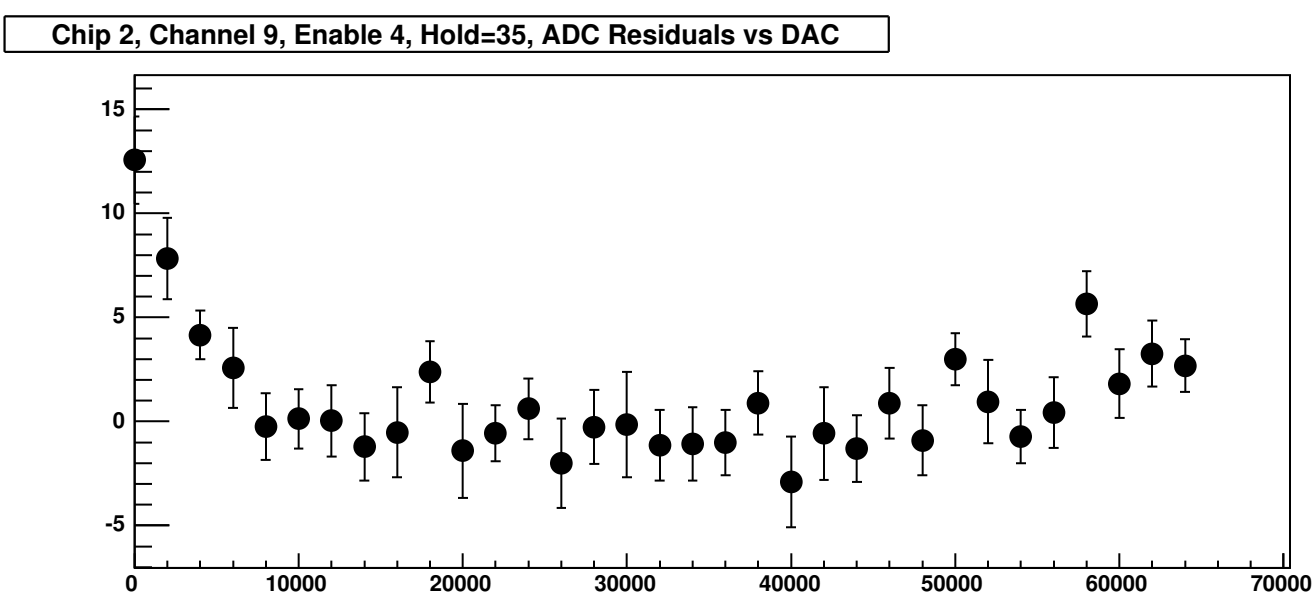


Chip 2, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC

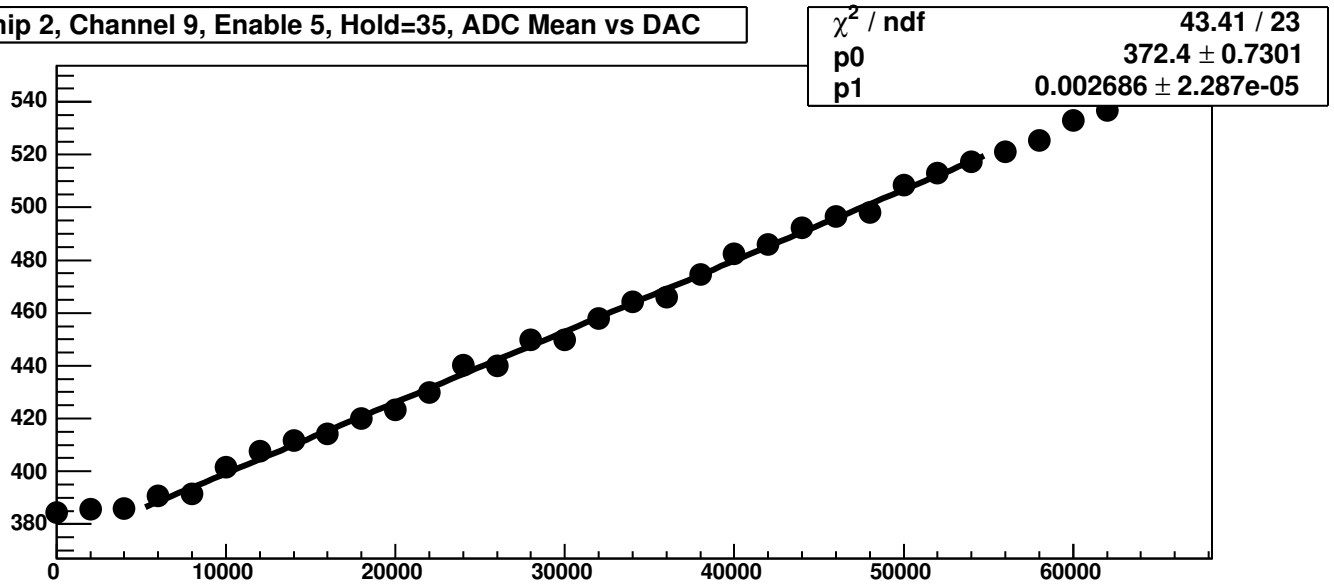



Chip 2, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 17.56 / 23
p0 372.2 ± 0.7591
p1 0.002996 ± 2.222e-05
580
560
540
520
500
480
460
440
420
400
380
0
10000
20000
30000
40000
50000
60000
70000

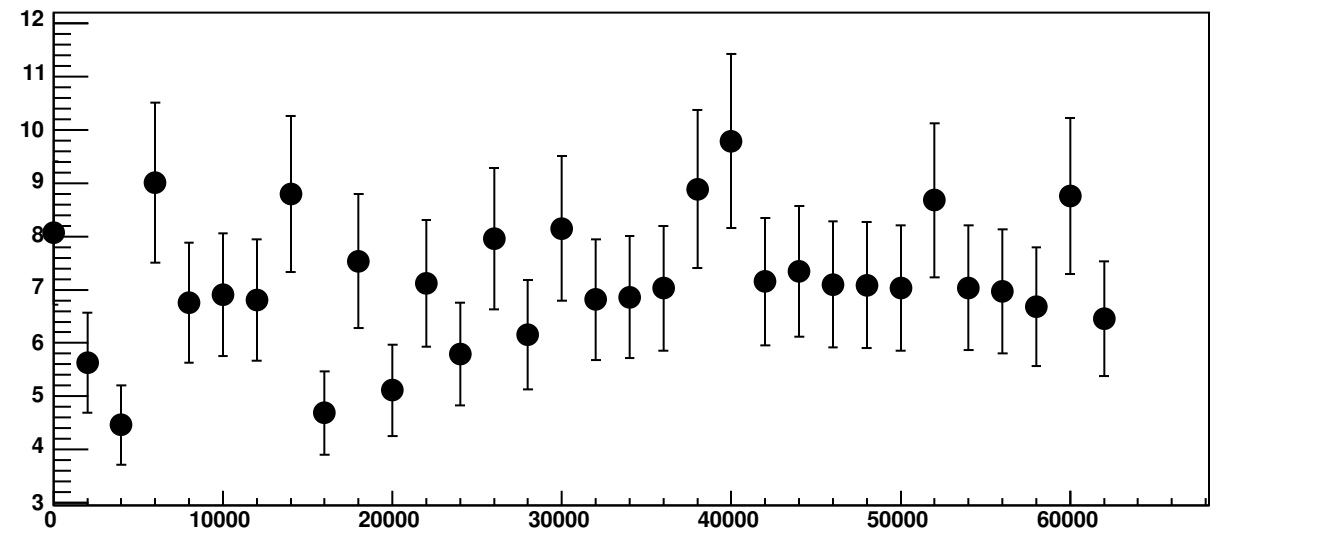

Chip 2, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

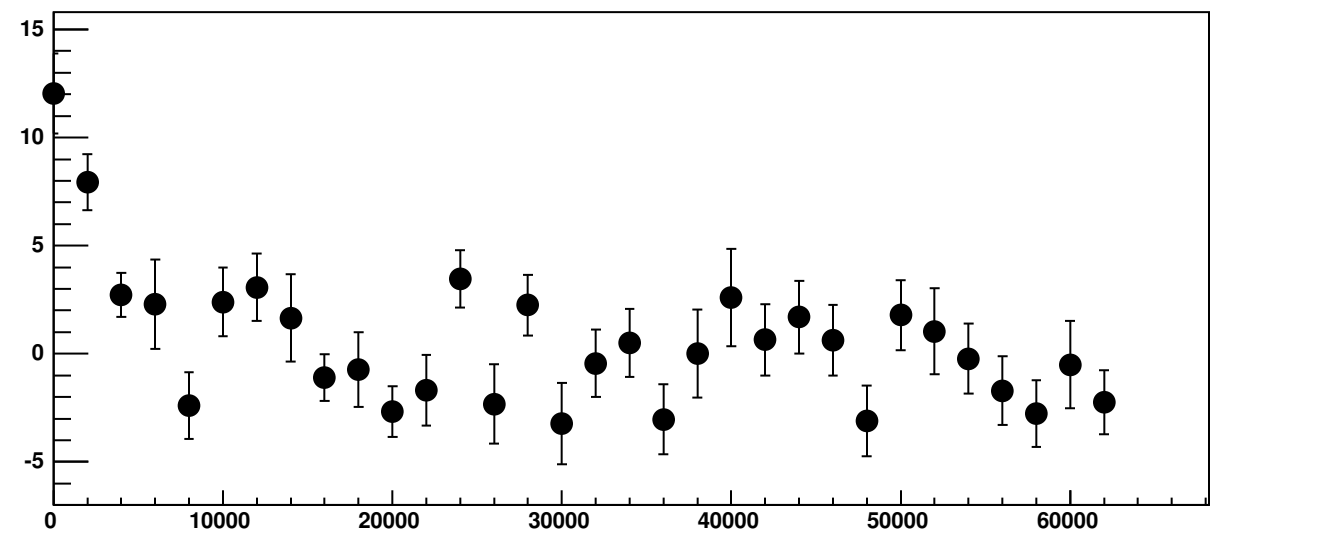
Chip 2, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

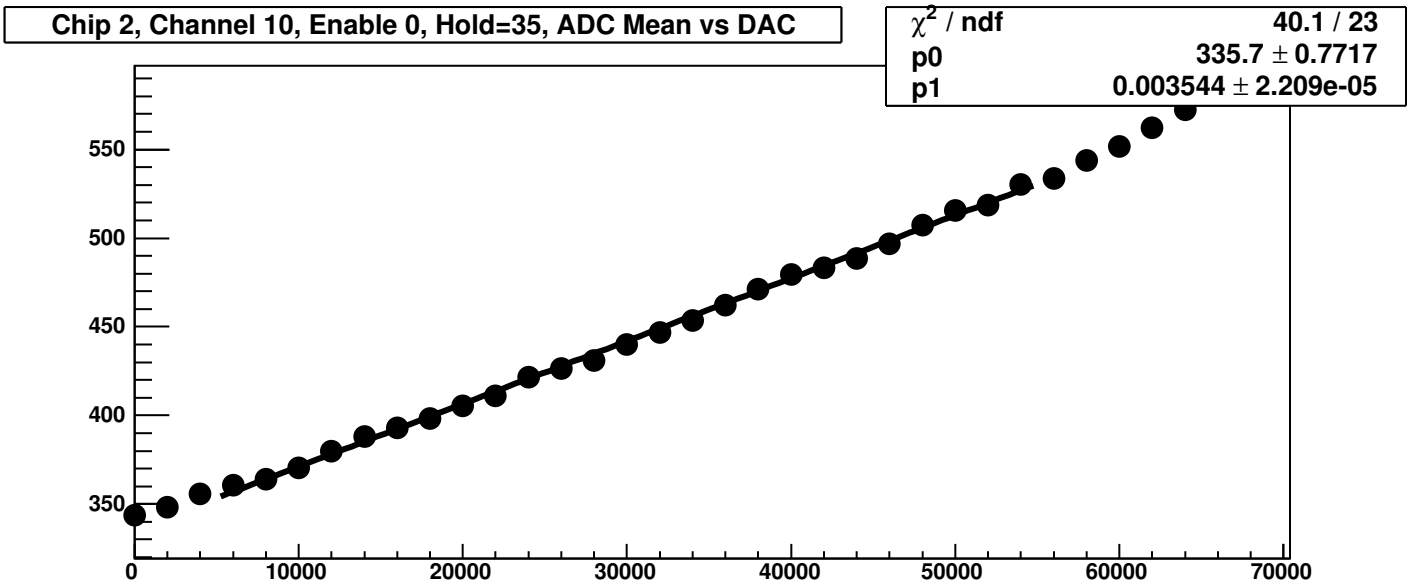


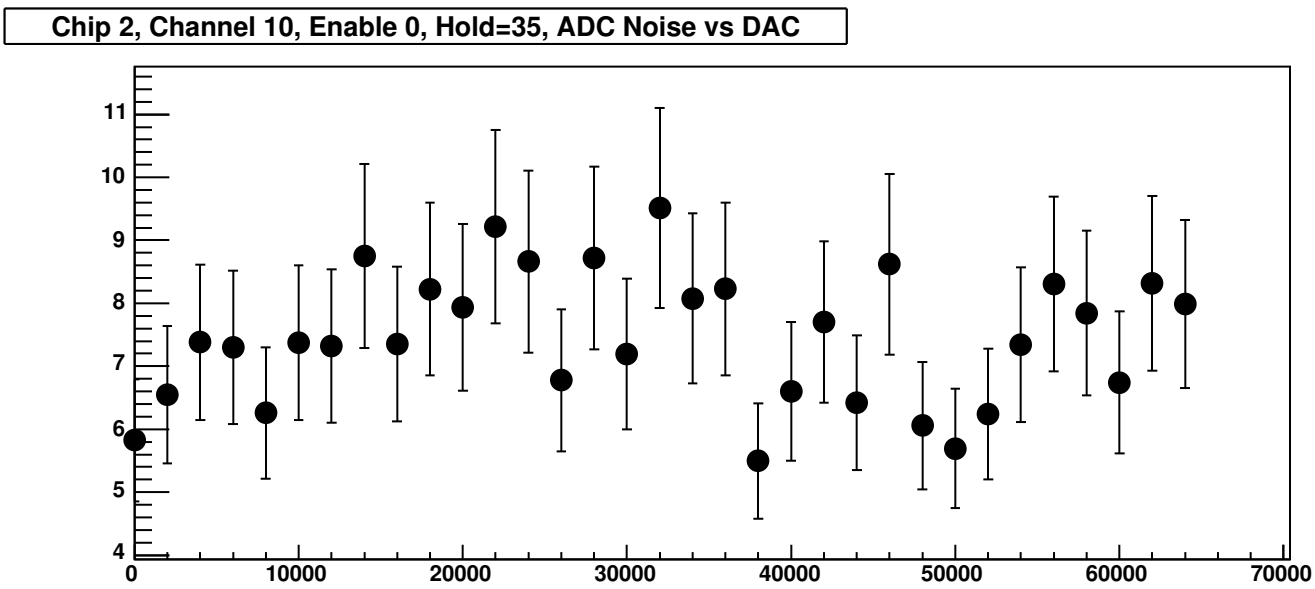
Chip 2, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

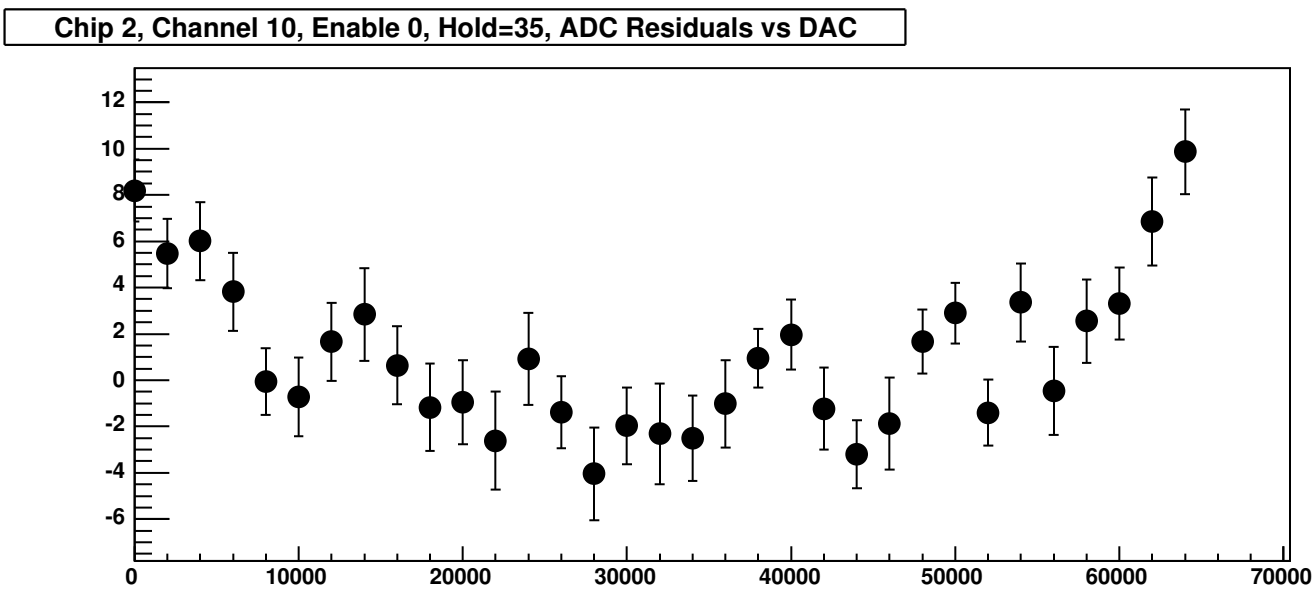


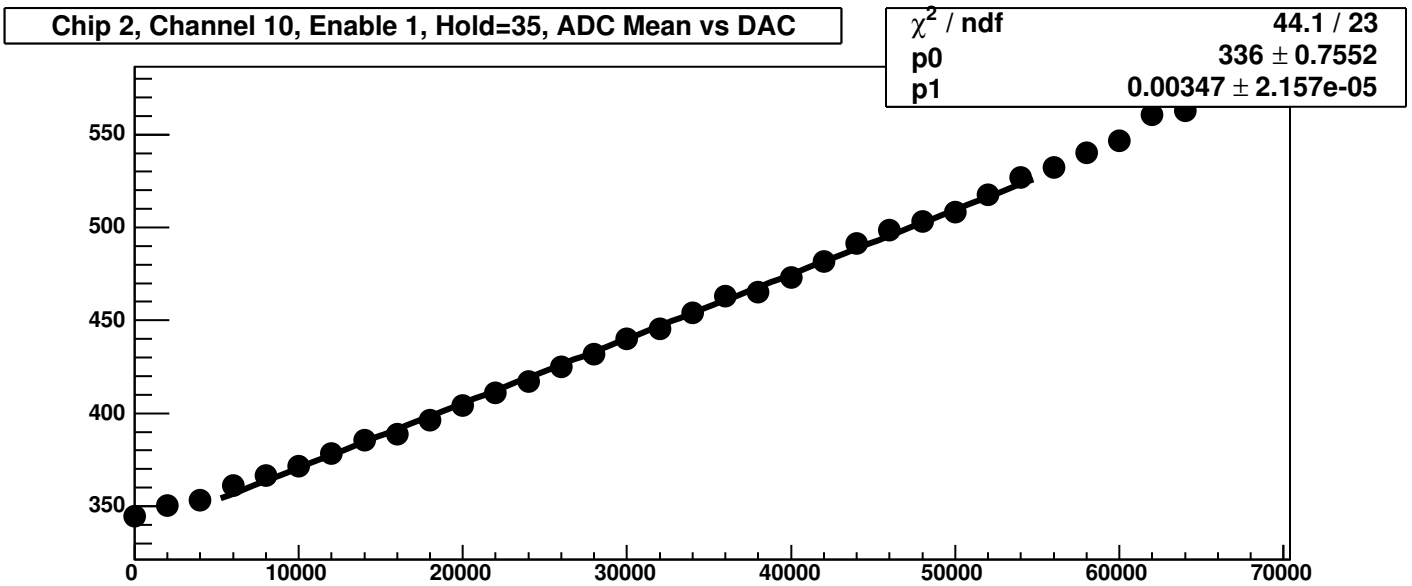
Chip 2, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

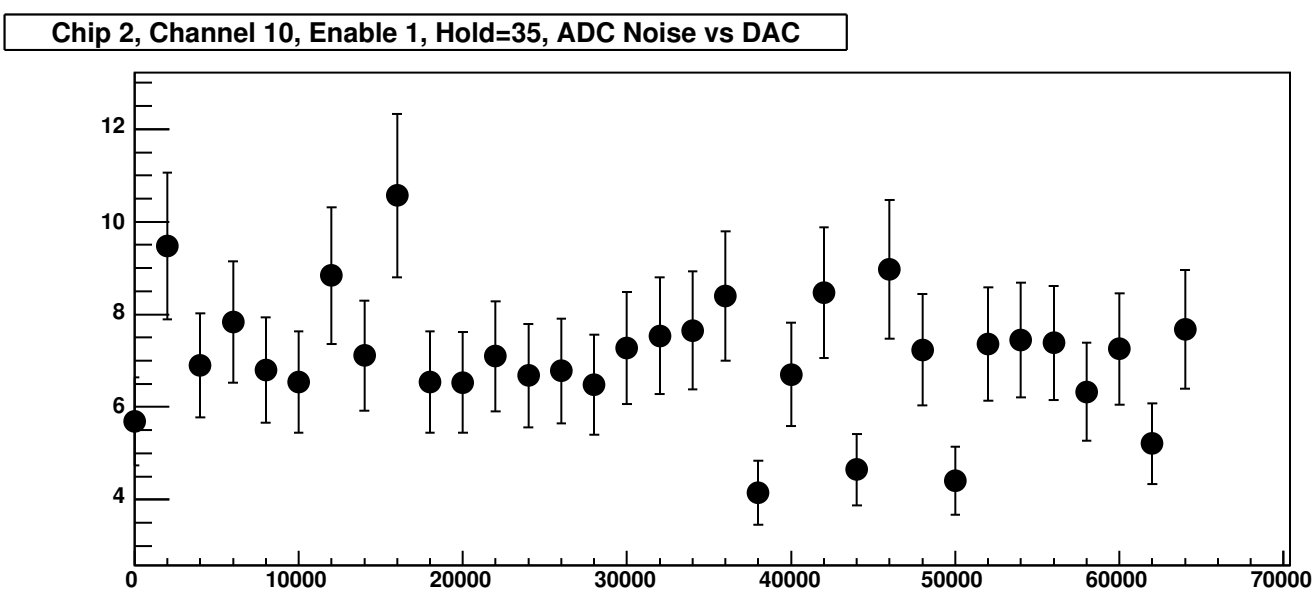


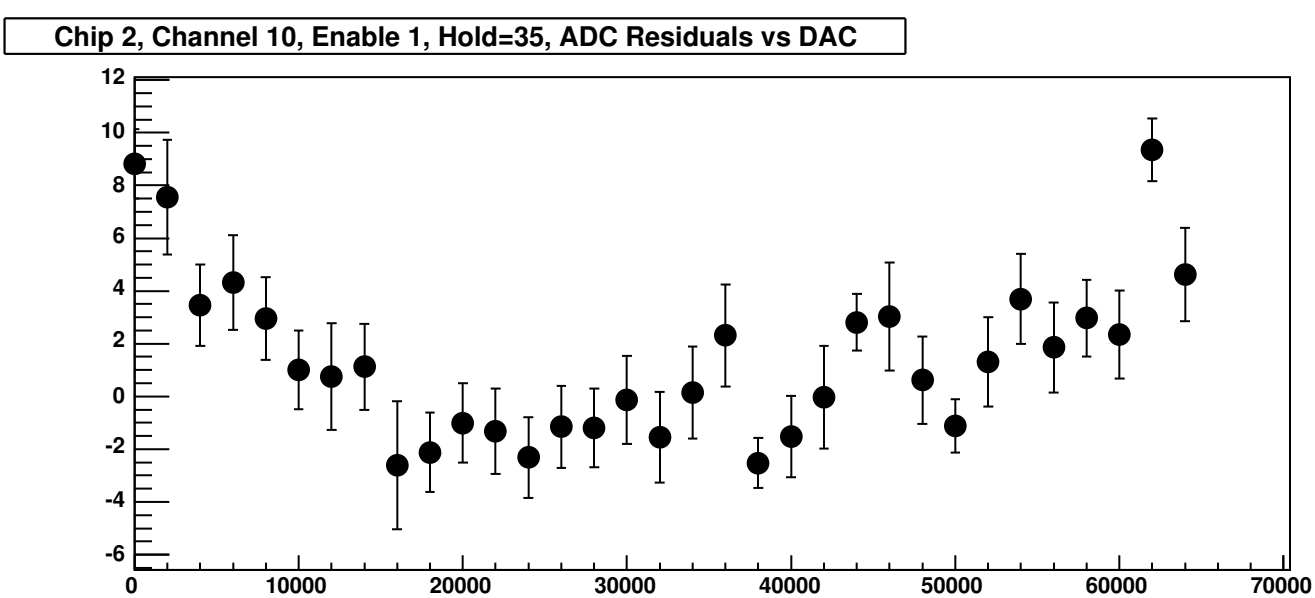

Chip 2, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 40.1 / 23
p0 335.7 ± 0.7717
p1 0.003544 ± 2.209e-05
550
500
450
400
350
0
10000
20000
30000
40000
50000
60000
70000

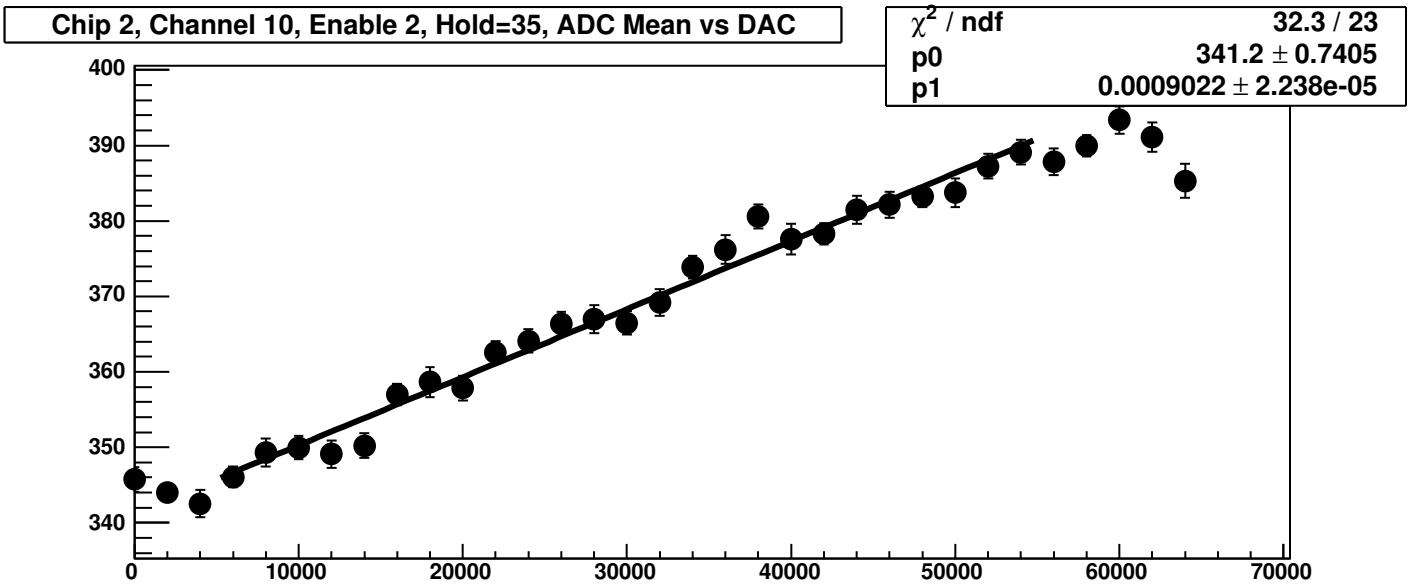

Chip 2, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

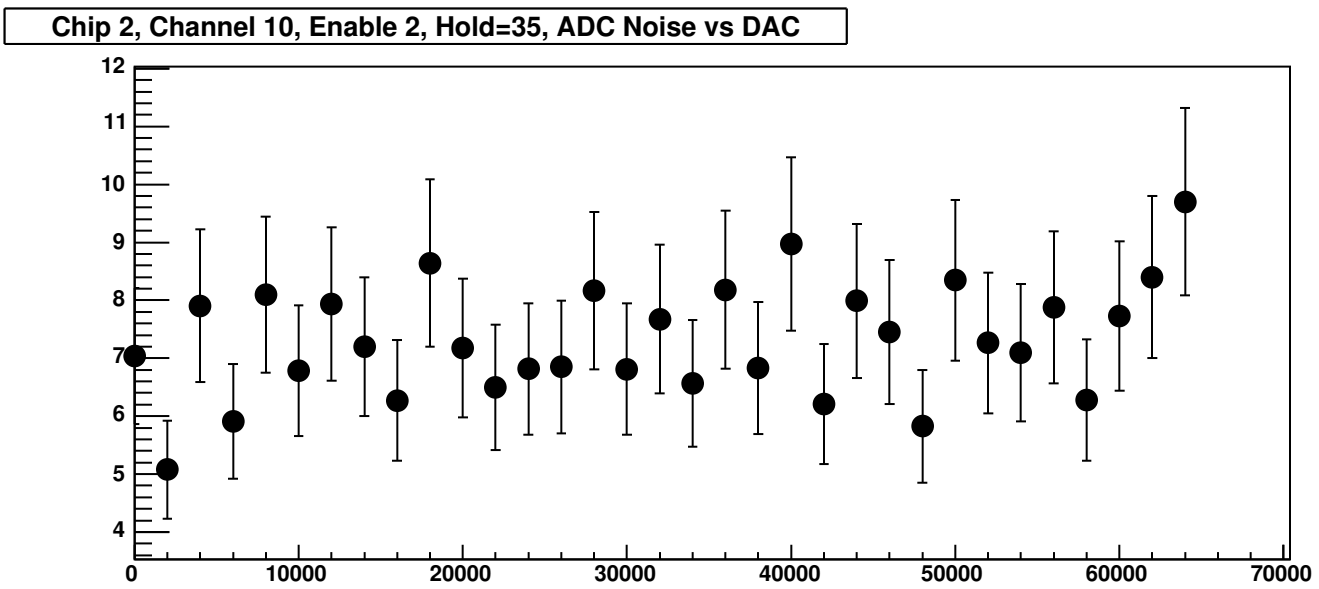

Chip 2, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

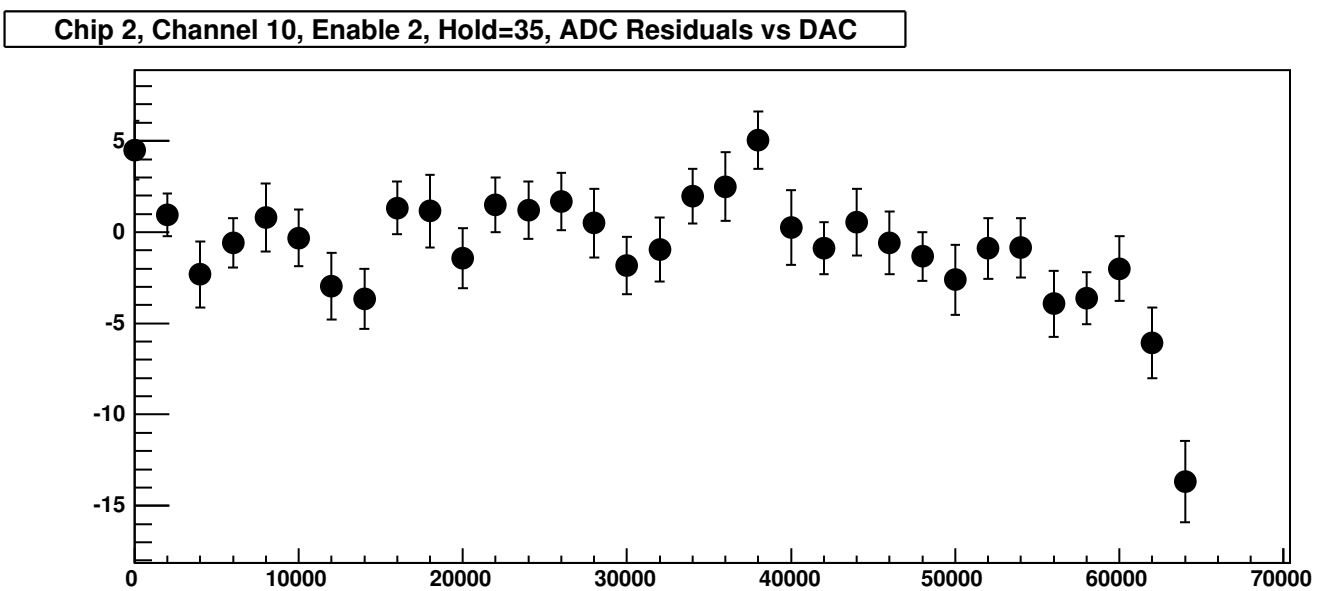

Chip 2, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 44.1 / 23
p0 336 ± 0.7552
p1 0.00347 ± 2.157e-05
550
500
450
400
350
0
10000
20000
30000
40000
50000
60000
70000

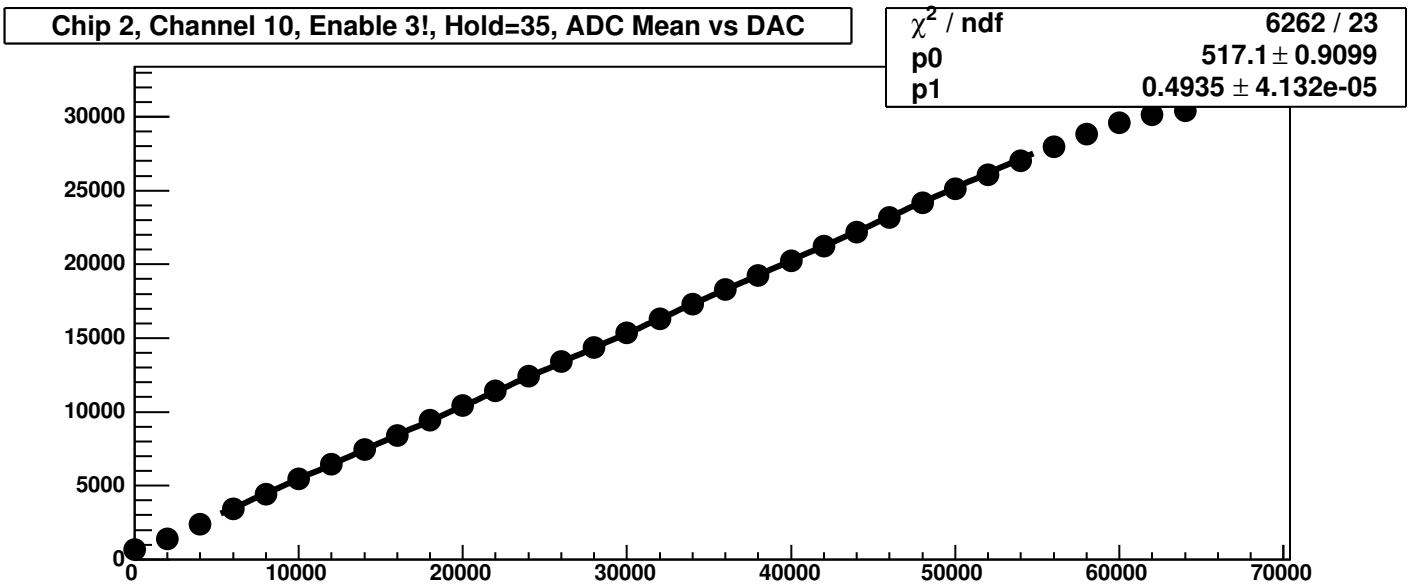

Chip 2, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

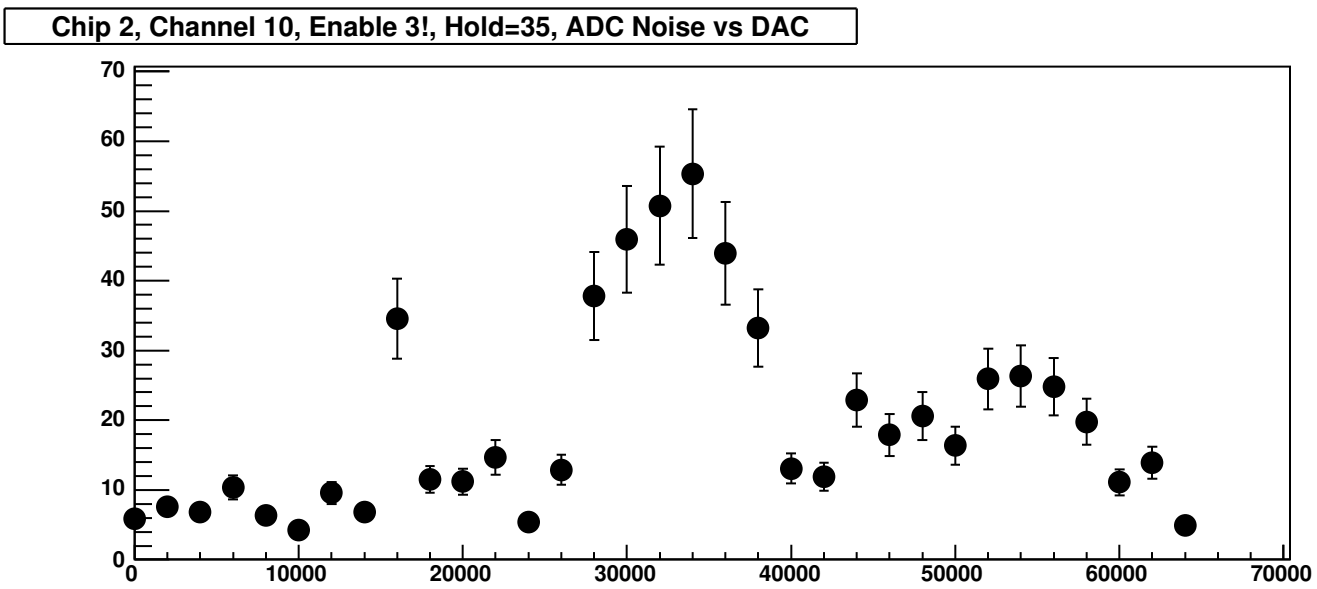

Chip 2, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

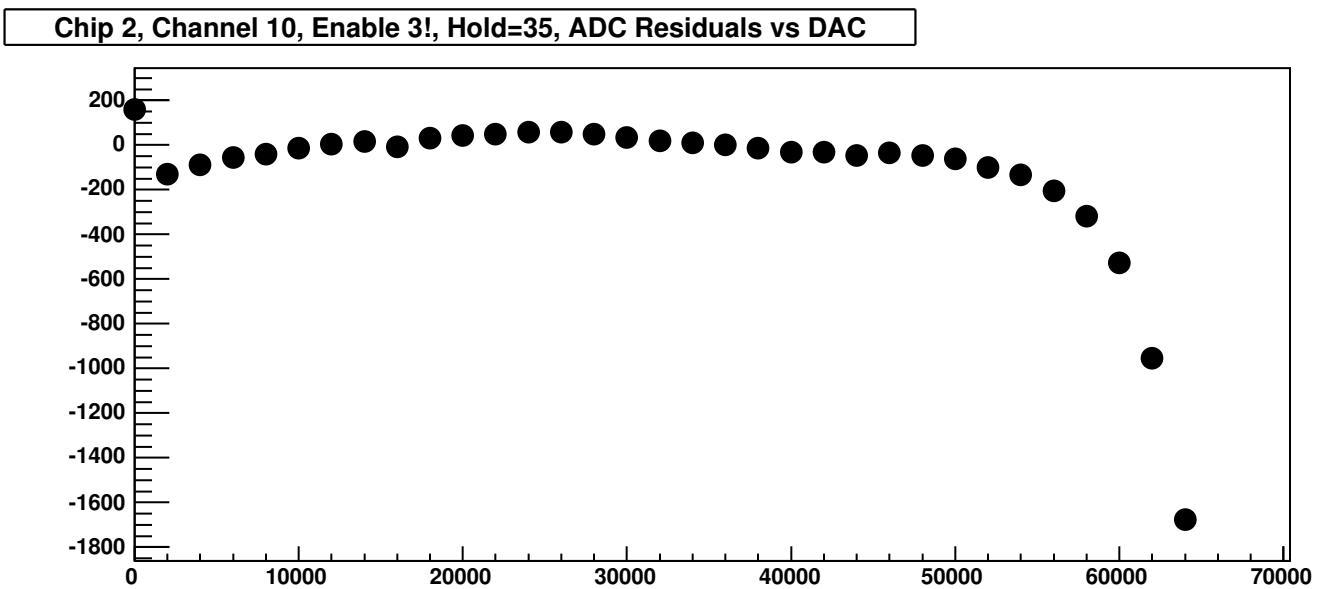

Chip 2, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 32.3 / 23
p0 341.2 ± 0.7405
p1 0.0009022 ± 2.238e-05
400
390
380
370
360
350
340
0
10000
20000
30000
40000
50000
60000
70000

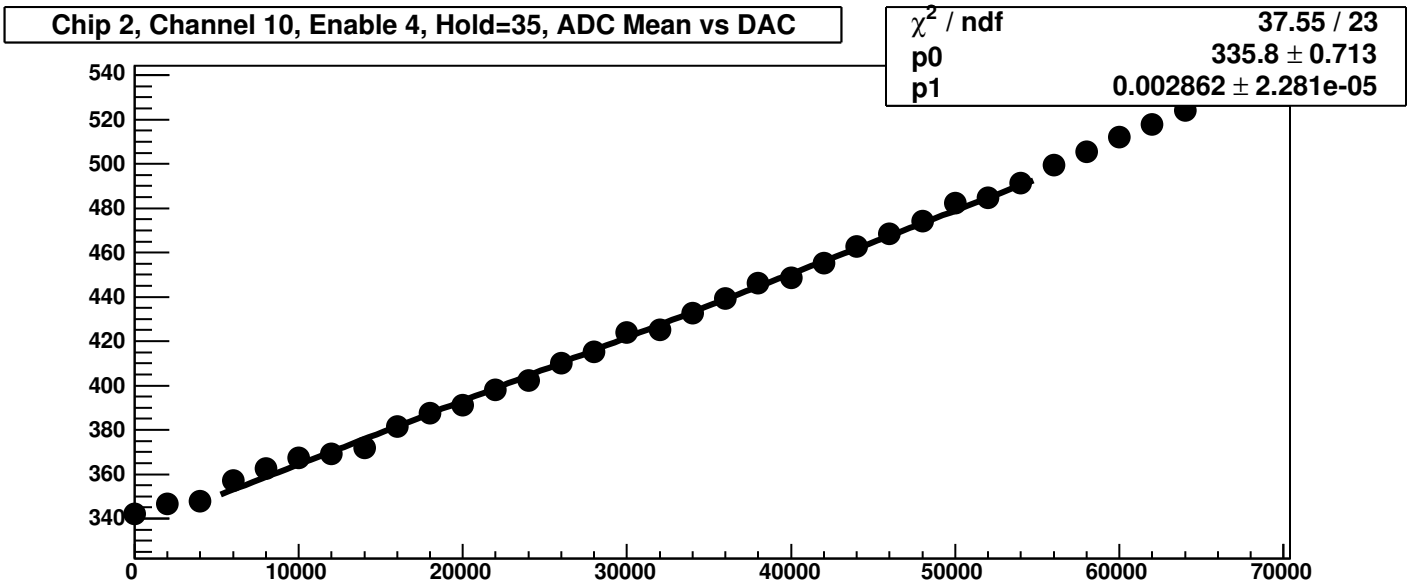

Chip 2, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

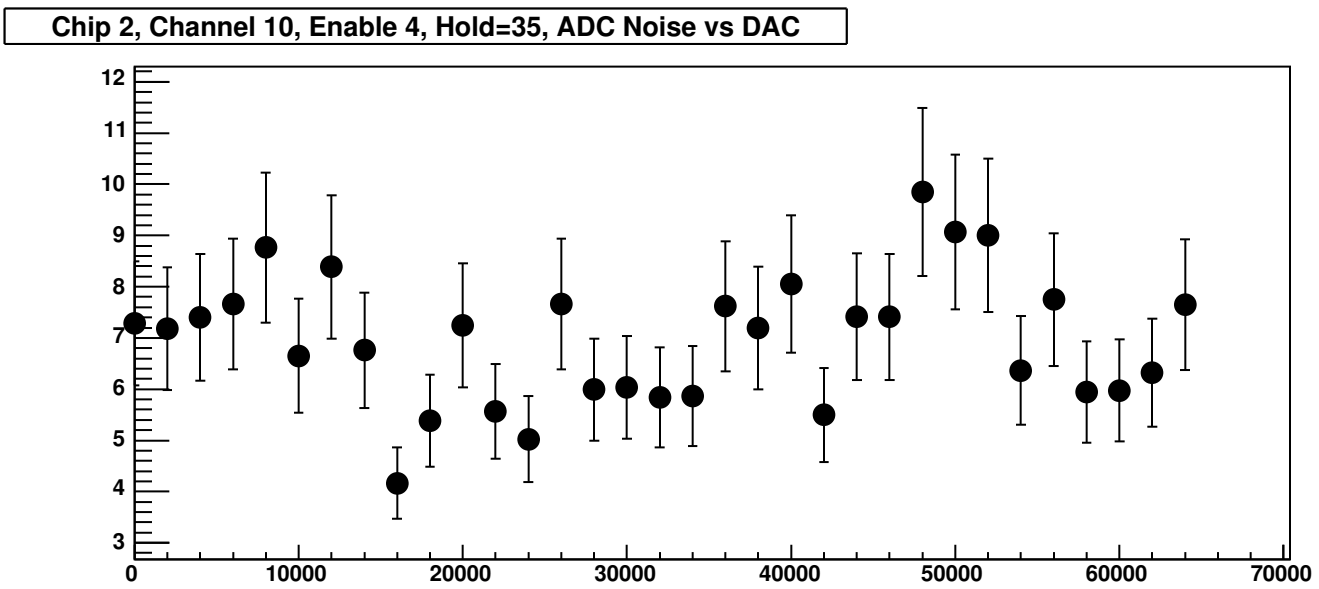

Chip 2, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

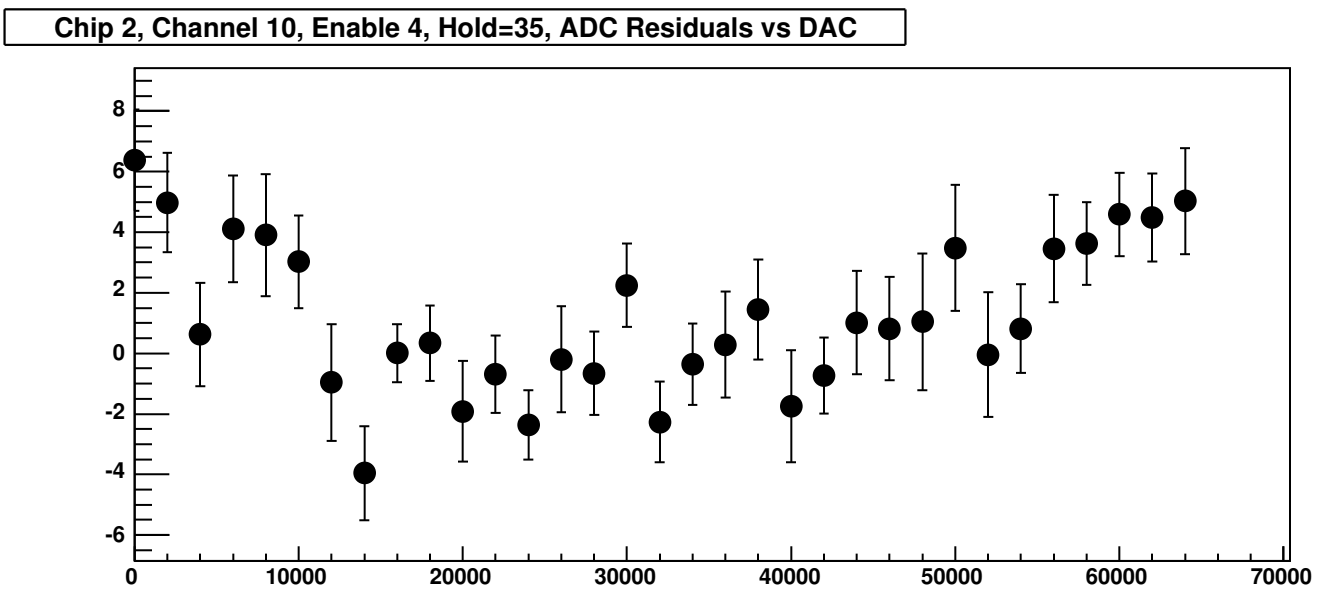

Chip 2, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 6262 / 23
p0 517.1 ± 0.9099
p1 0.4935 ± 4.132e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

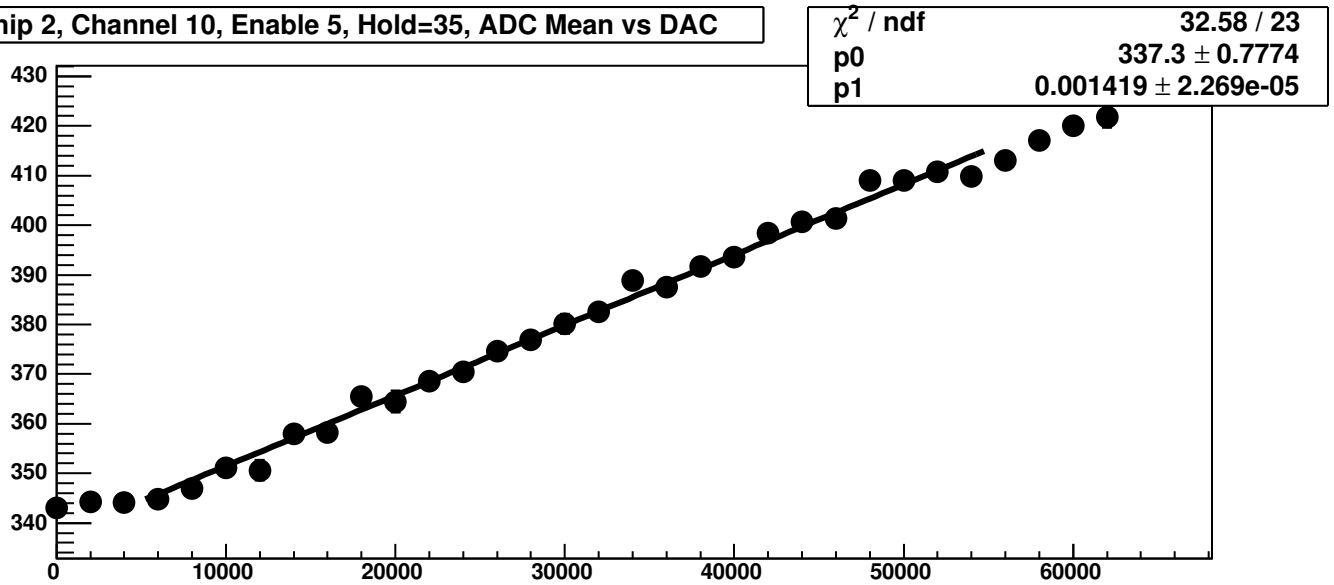

Chip 2, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 37.55 / 23
p0 335.8 ± 0.713
p1 0.002862 ± 2.281e-05
540
520
500
480
460
440
420
400
380
360
340
0
10000
20000
30000
40000
50000
60000
70000

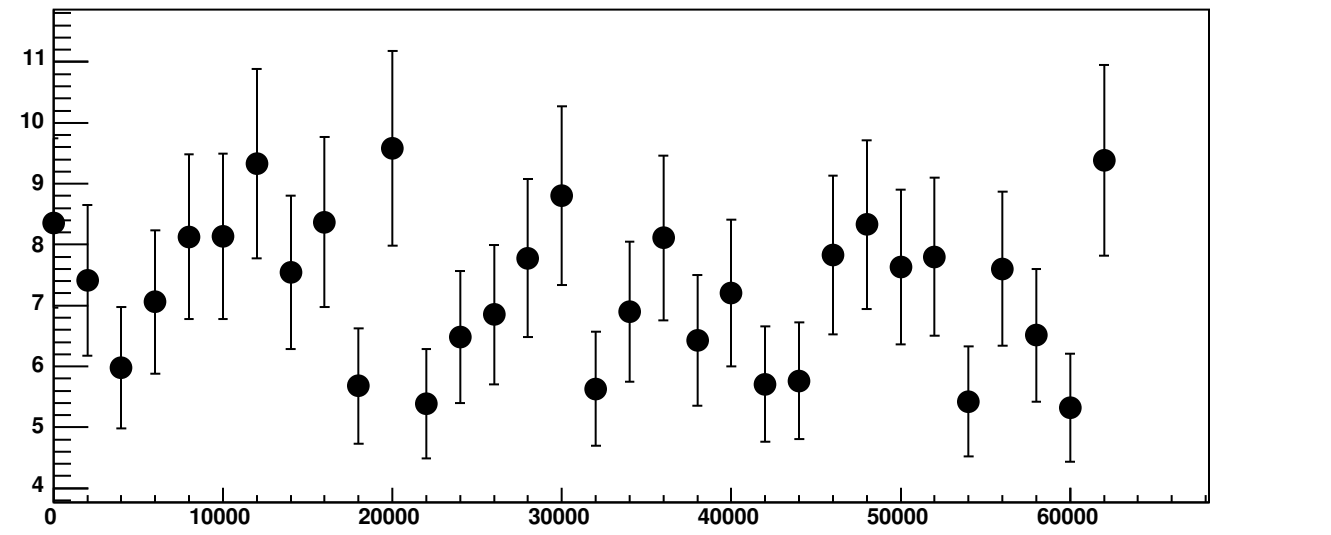

Chip 2, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

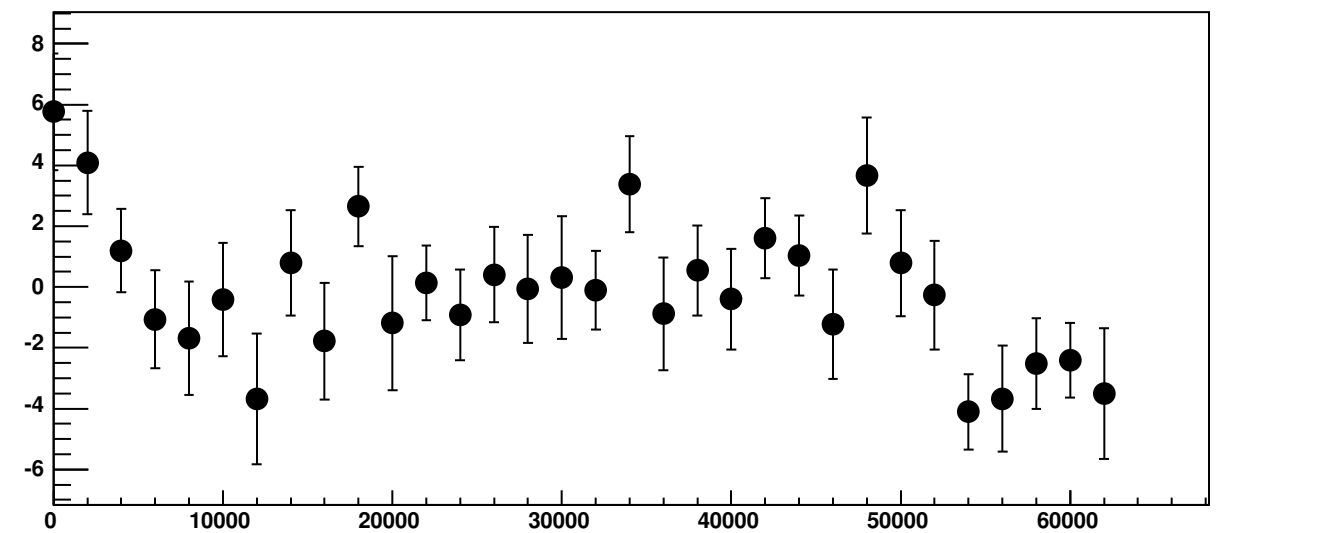
Chip 2, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

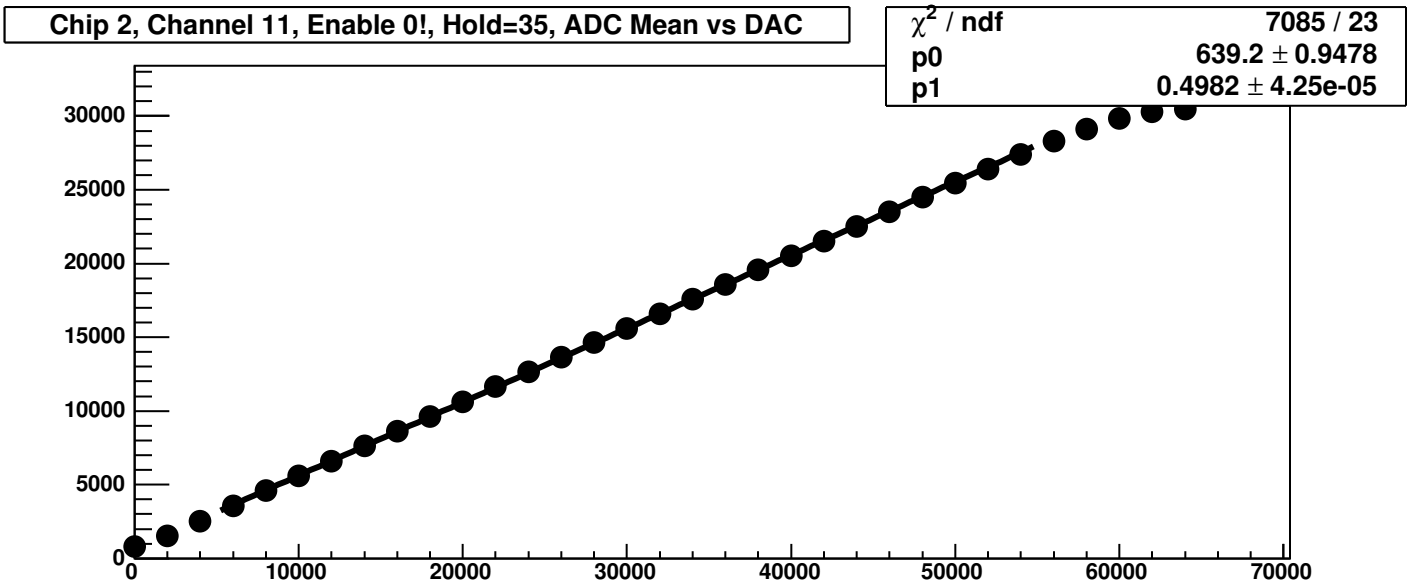


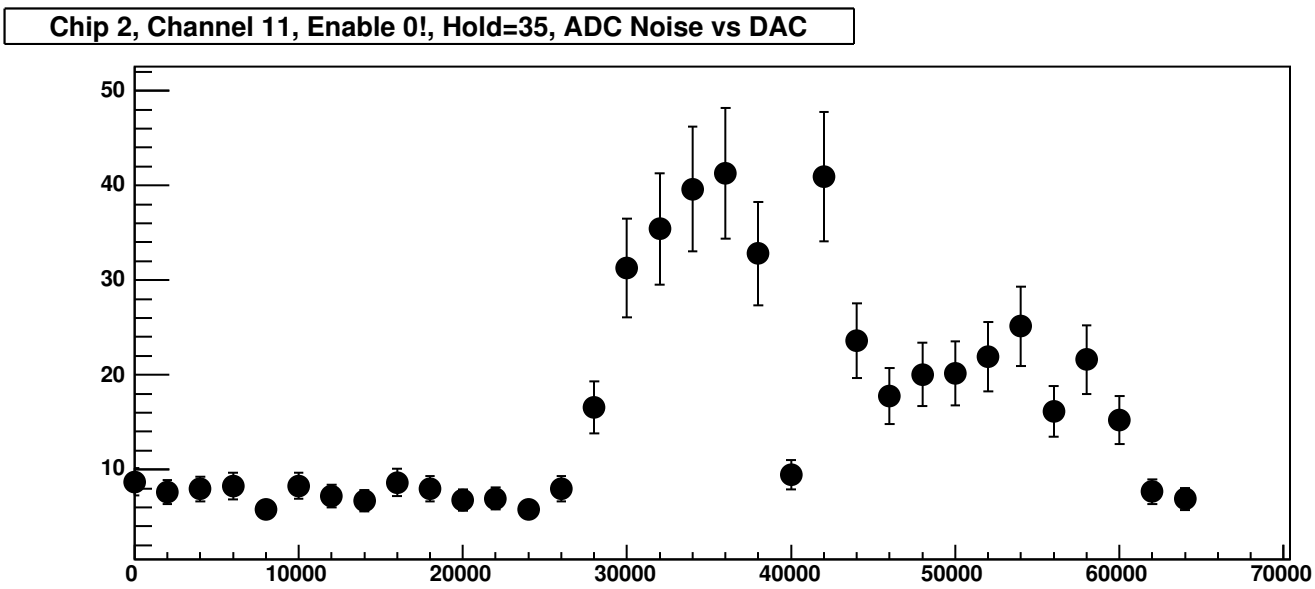
Chip 2, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

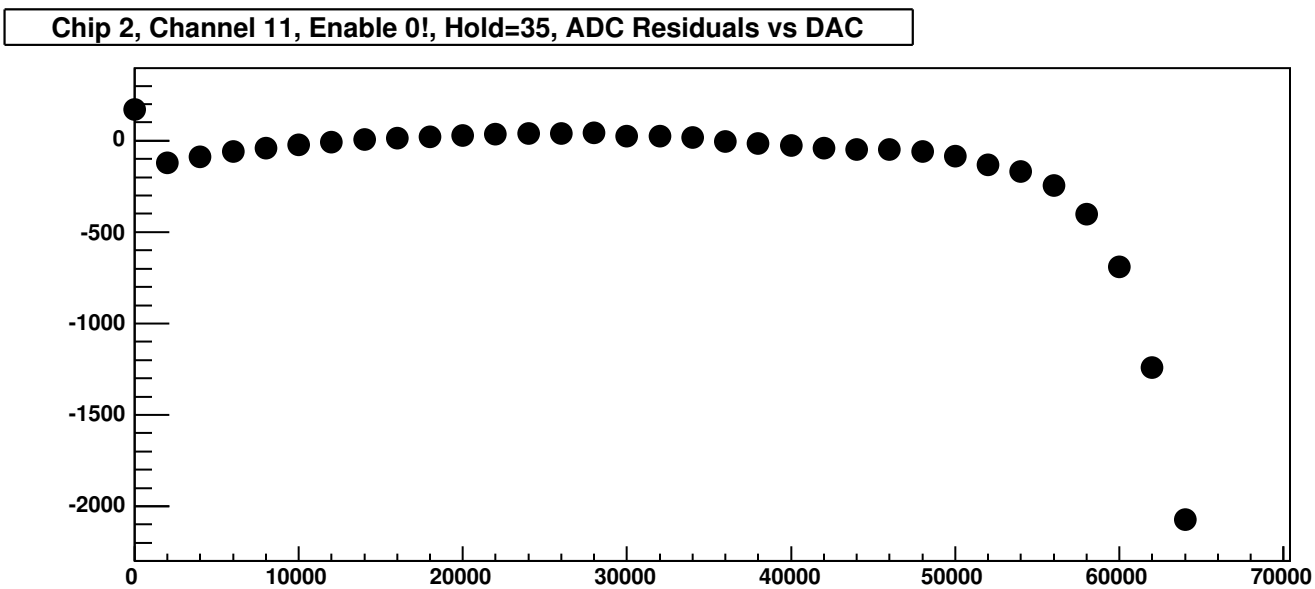


Chip 2, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



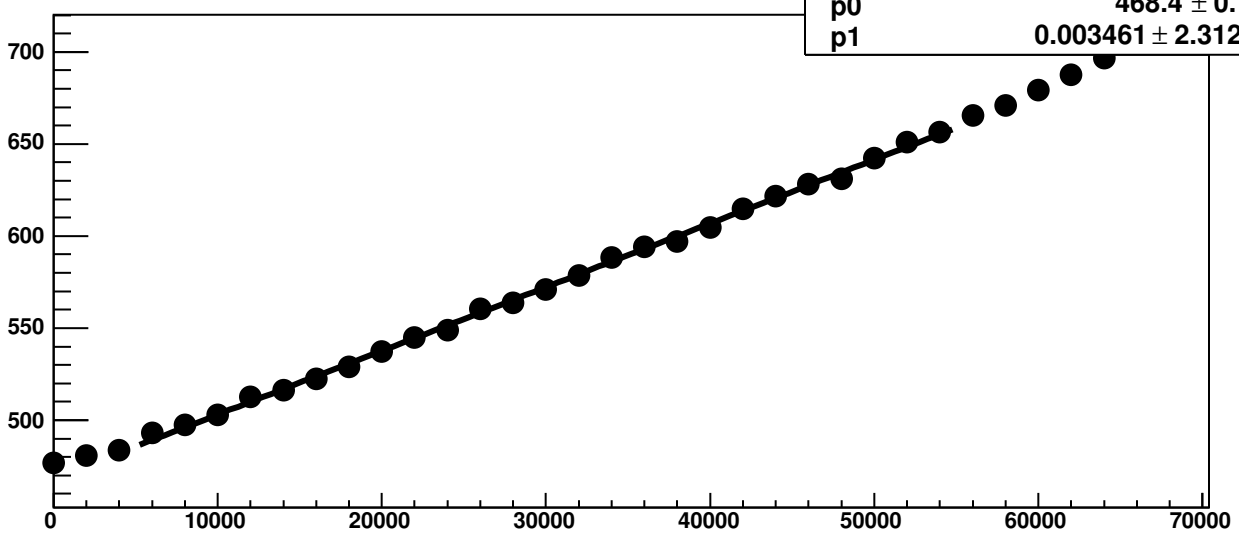

Chip 2, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 7085 / 23
p0 639.2 ± 0.9478
p1 0.4982 ± 4.25e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000

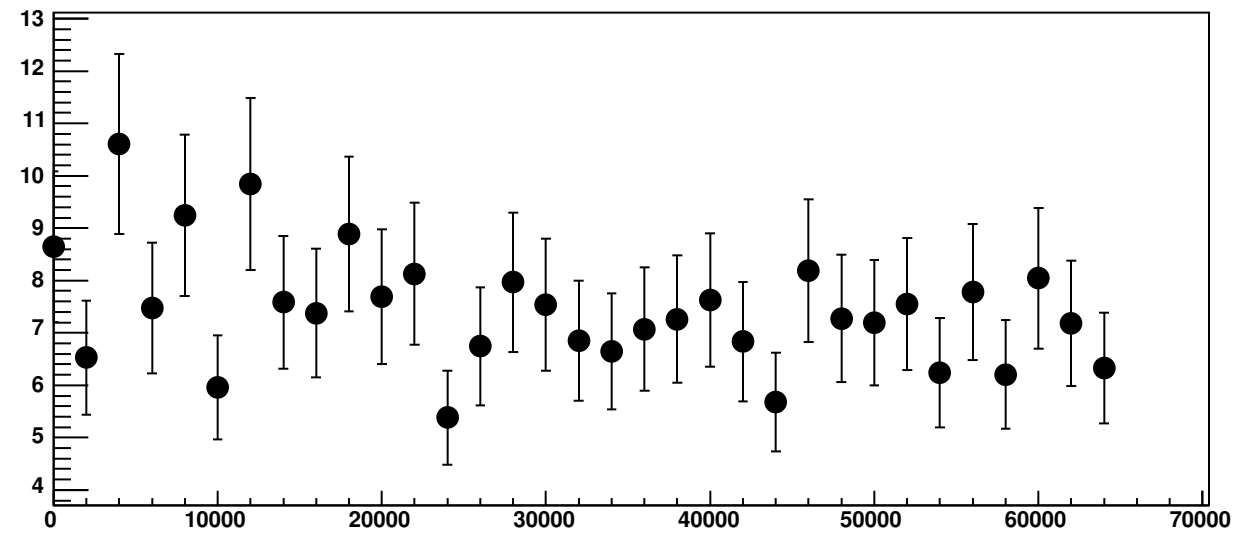

Chip 2, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000

Chip 2, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC

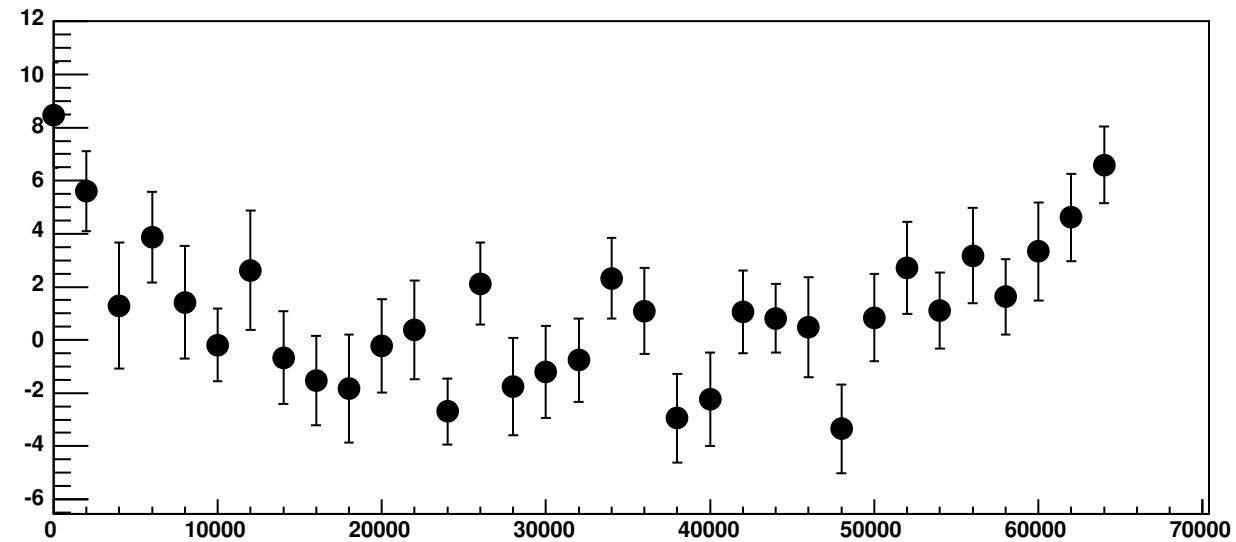
χ^2 / ndf	32.73 / 23
p0	468.4 ± 0.7888
p1	0.003461 ± 2.312e-05

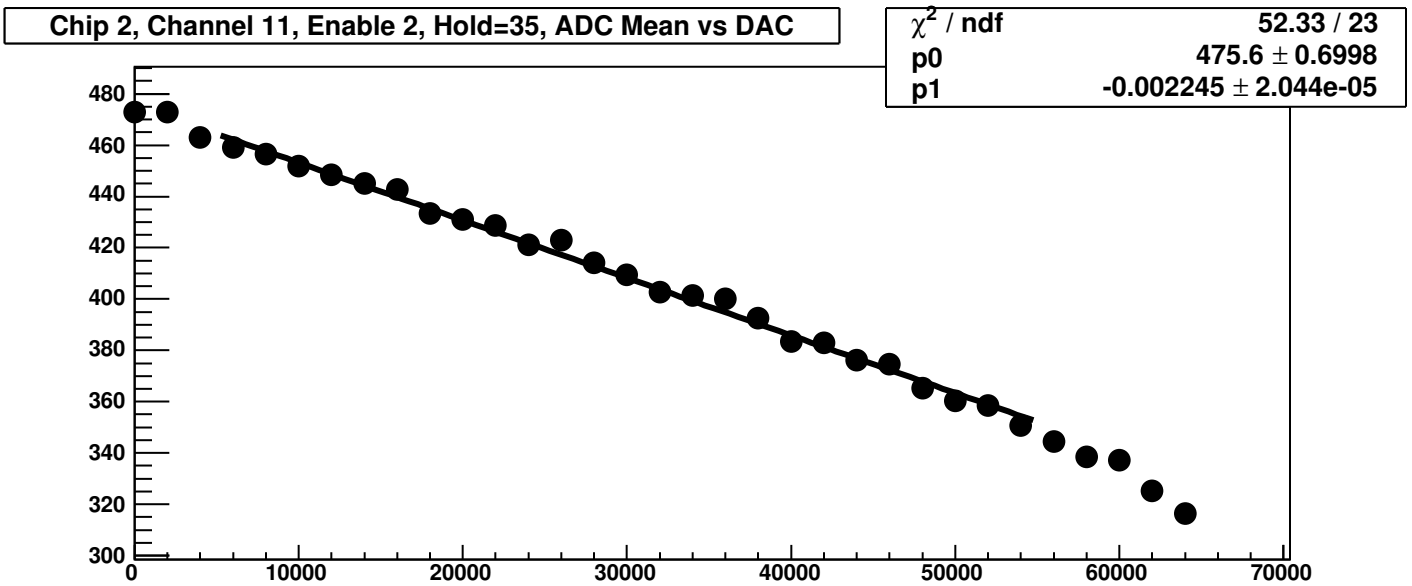


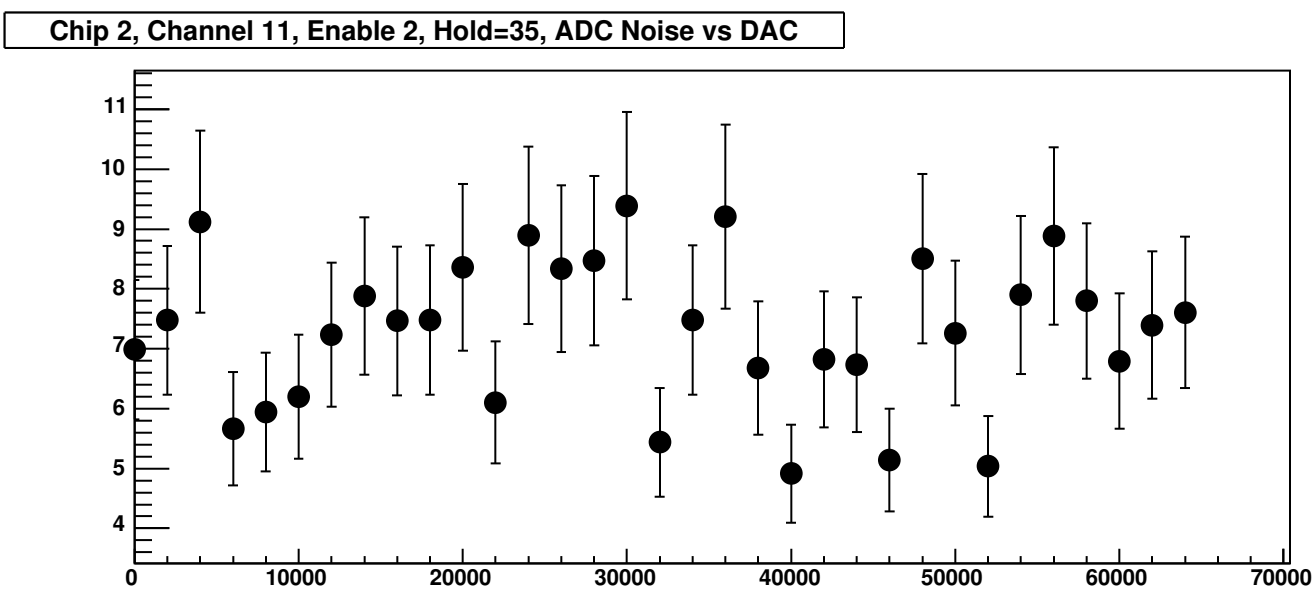
Chip 2, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC

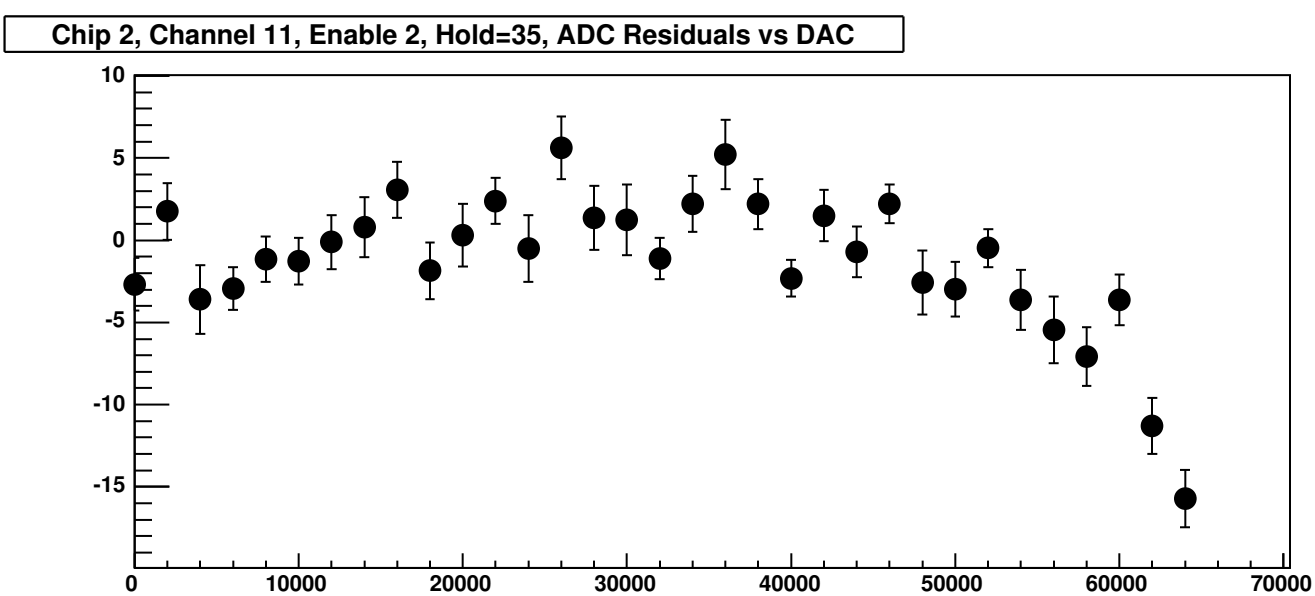


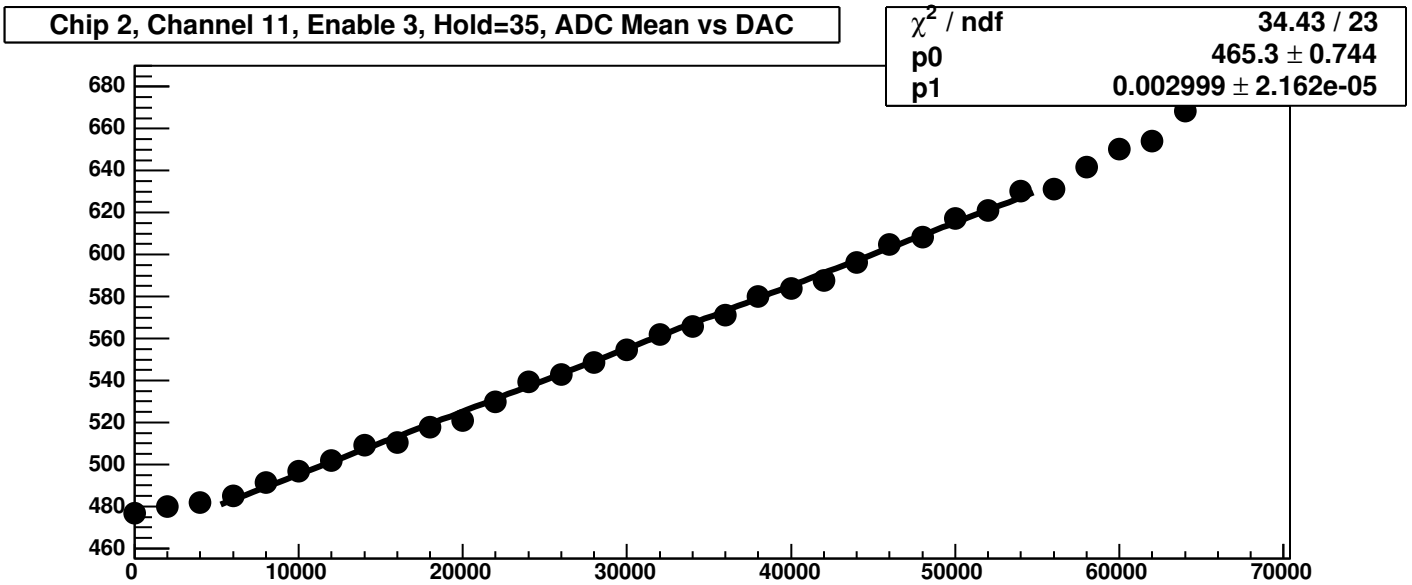
Chip 2, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC

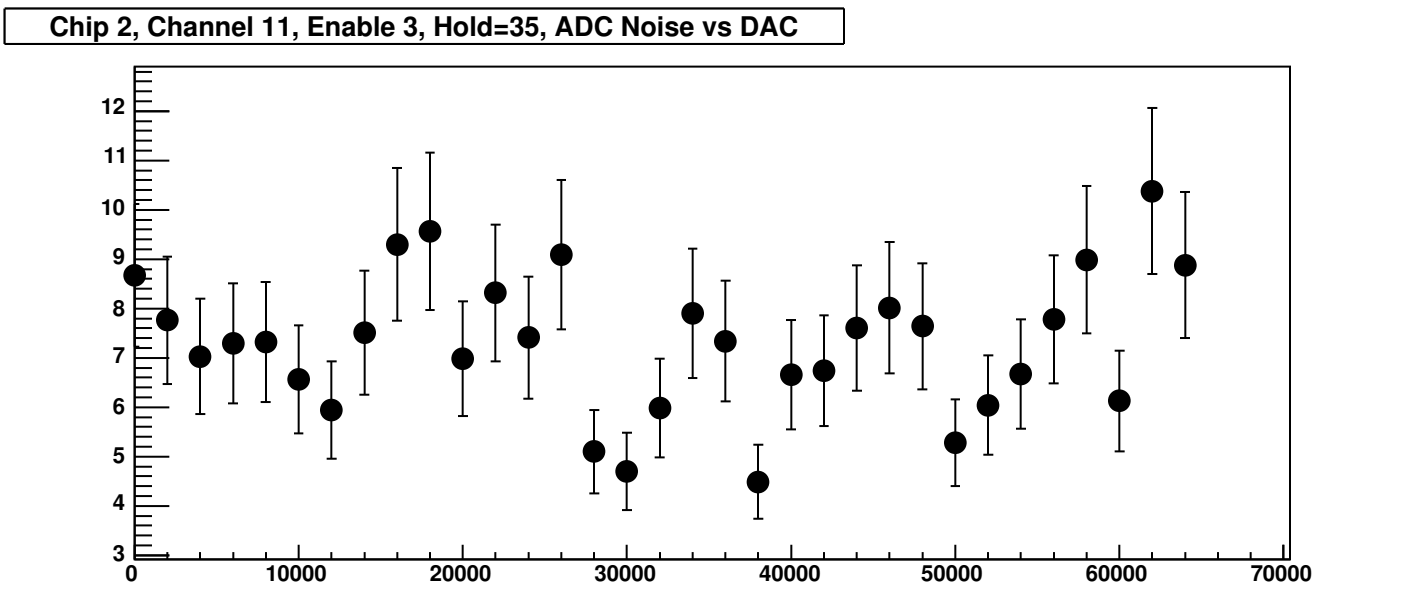


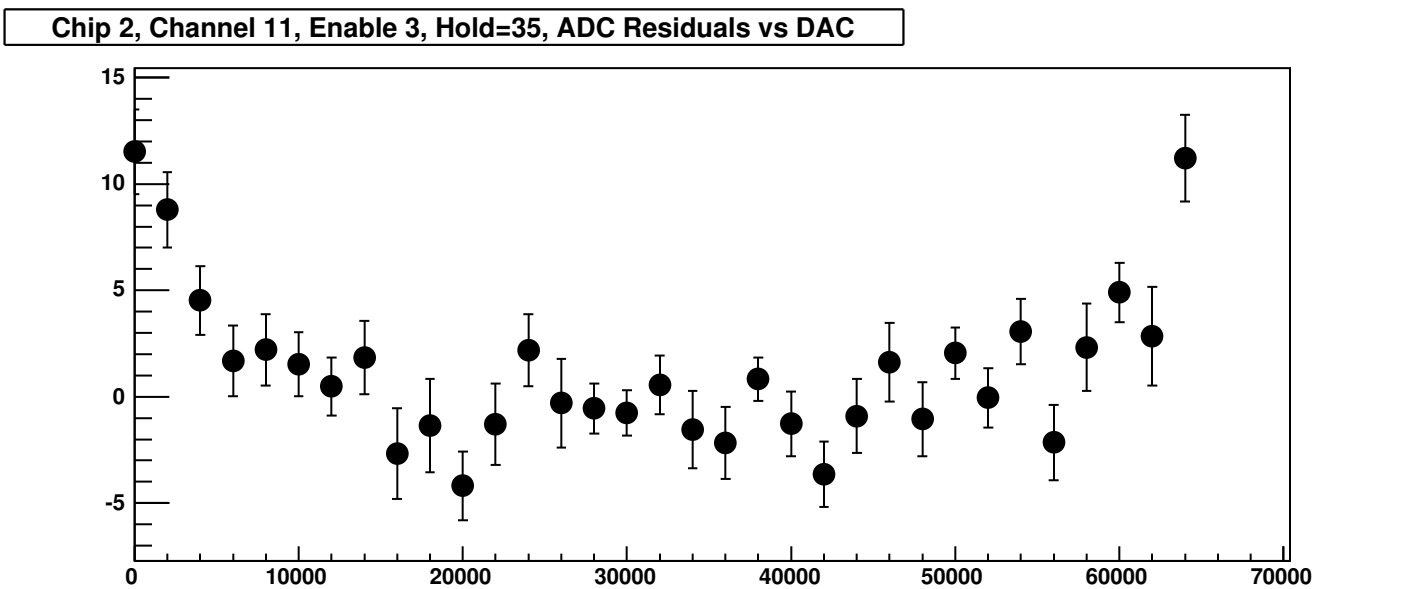

Chip 2, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 52.33 / 23
p0 475.6 ± 0.6998
p1 -0.002245 ± 2.044e-05
480
460
440
420
400
380
360
340
320
300
0
10000
20000
30000
40000
50000
60000
70000

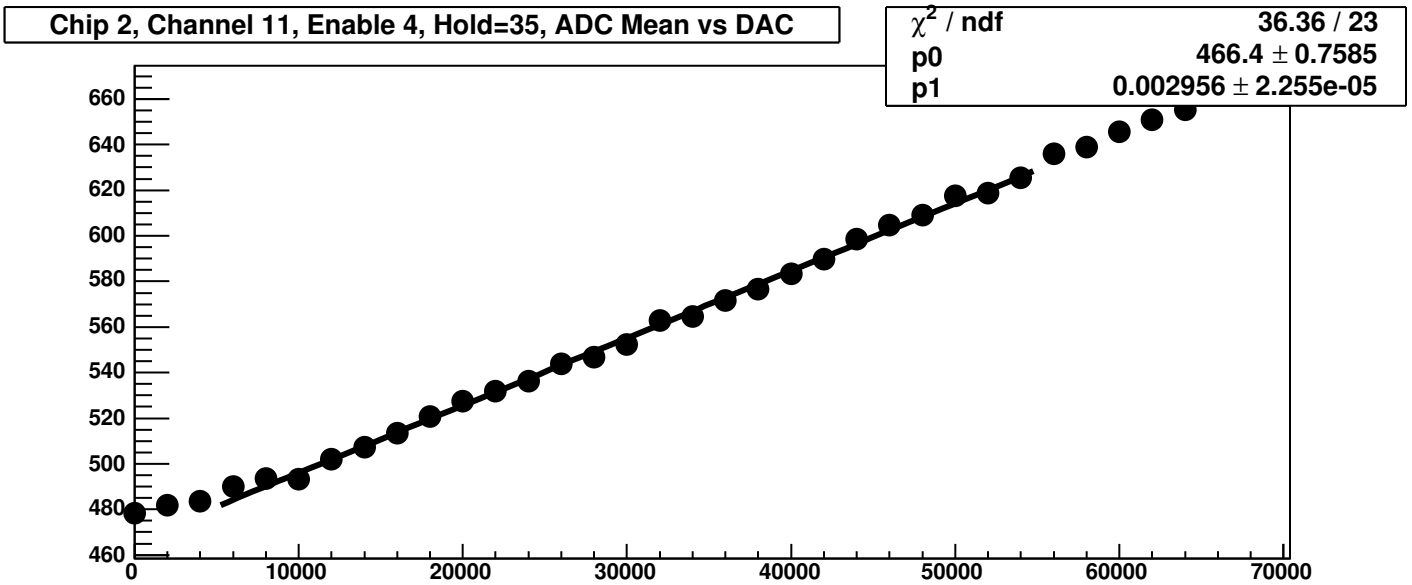

Chip 2, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

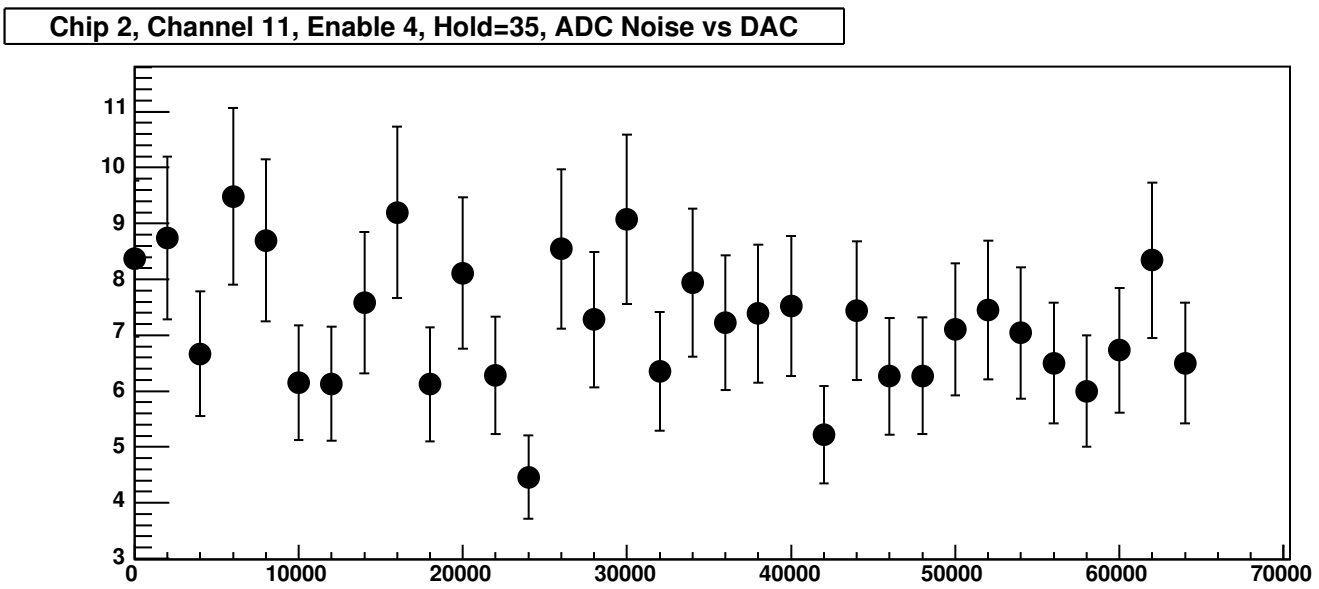

Chip 2, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

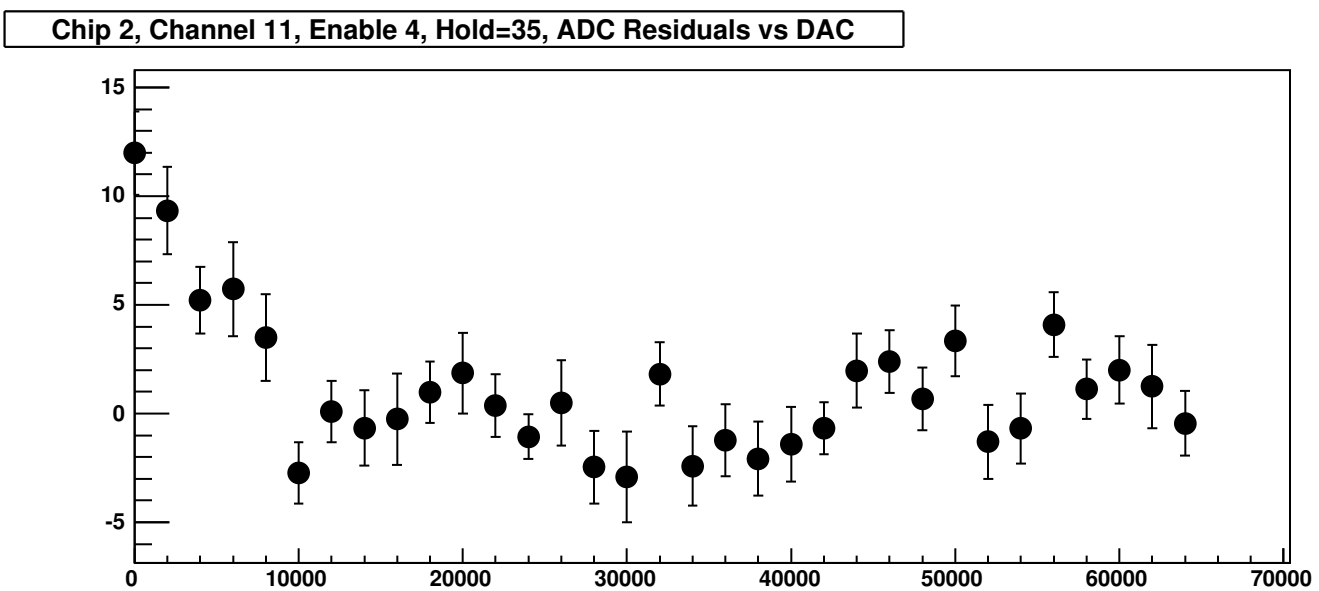

Chip 2, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 34.43 / 23
p0 465.3 ± 0.744
p1 0.002999 ± 2.162e-05
680
660
640
620
600
580
560
540
520
500
480
460
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

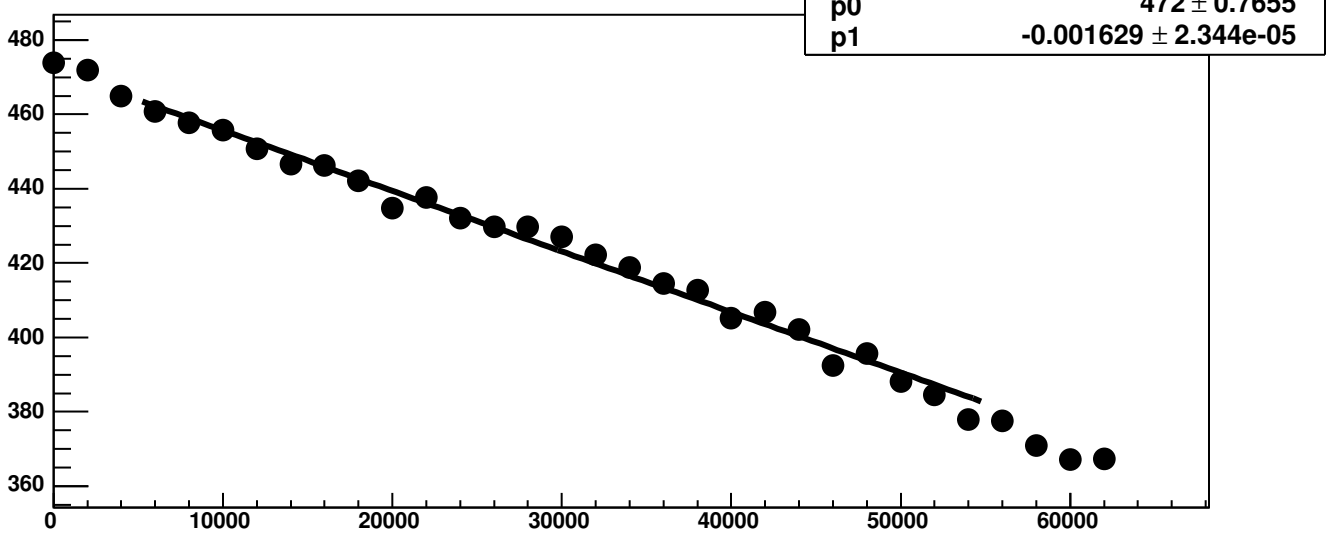

Chip 2, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 36.36 / 23
p0 466.4 ± 0.7585
p1 0.002956 ± 2.255e-05
660
640
620
600
580
560
540
520
500
480
460
0
10000
20000
30000
40000
50000
60000
70000

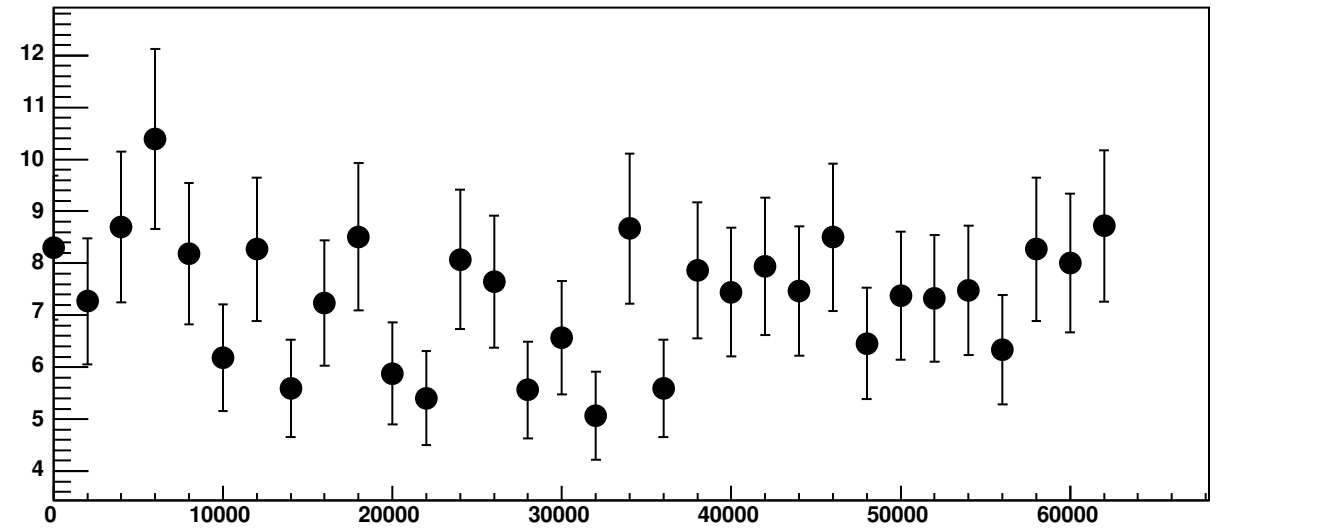

Chip 2, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

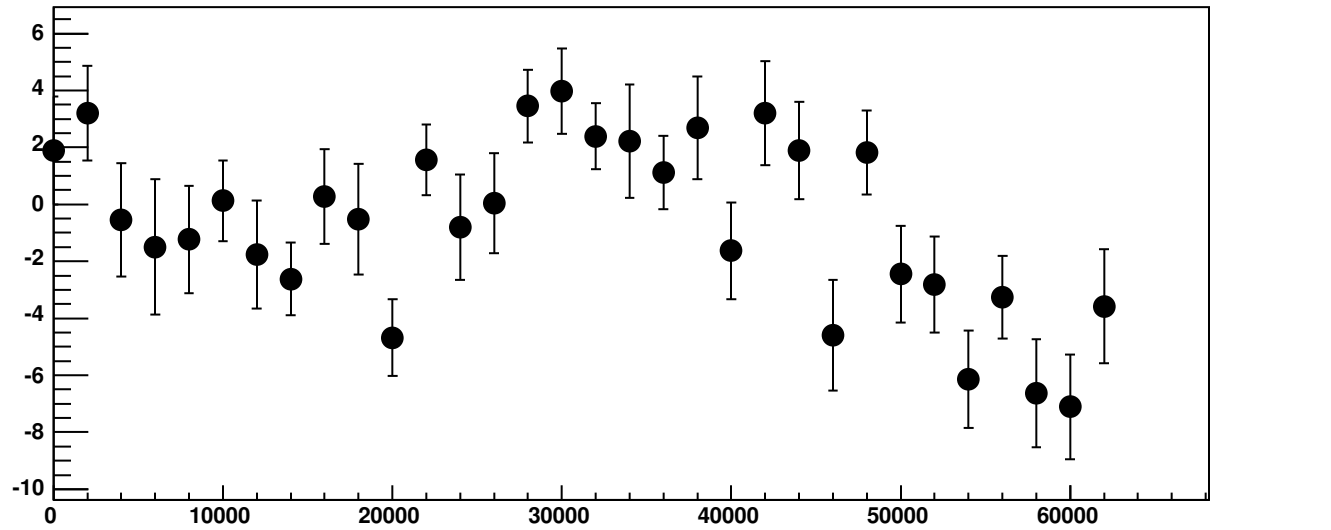
Chip 2, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

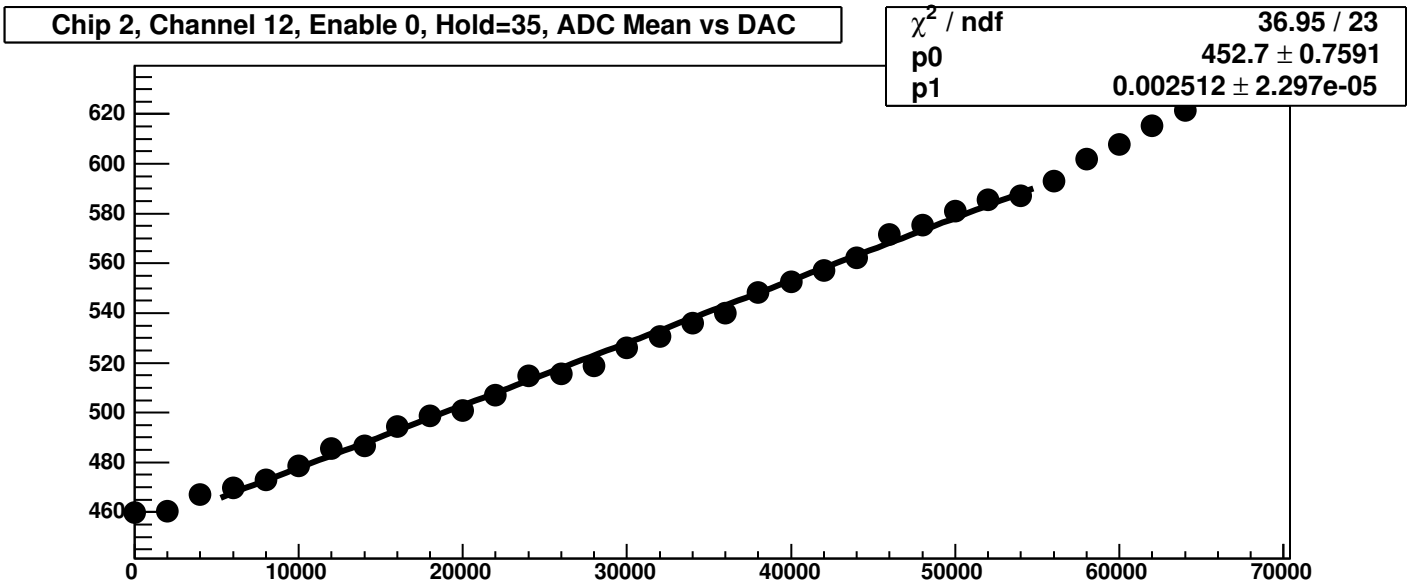


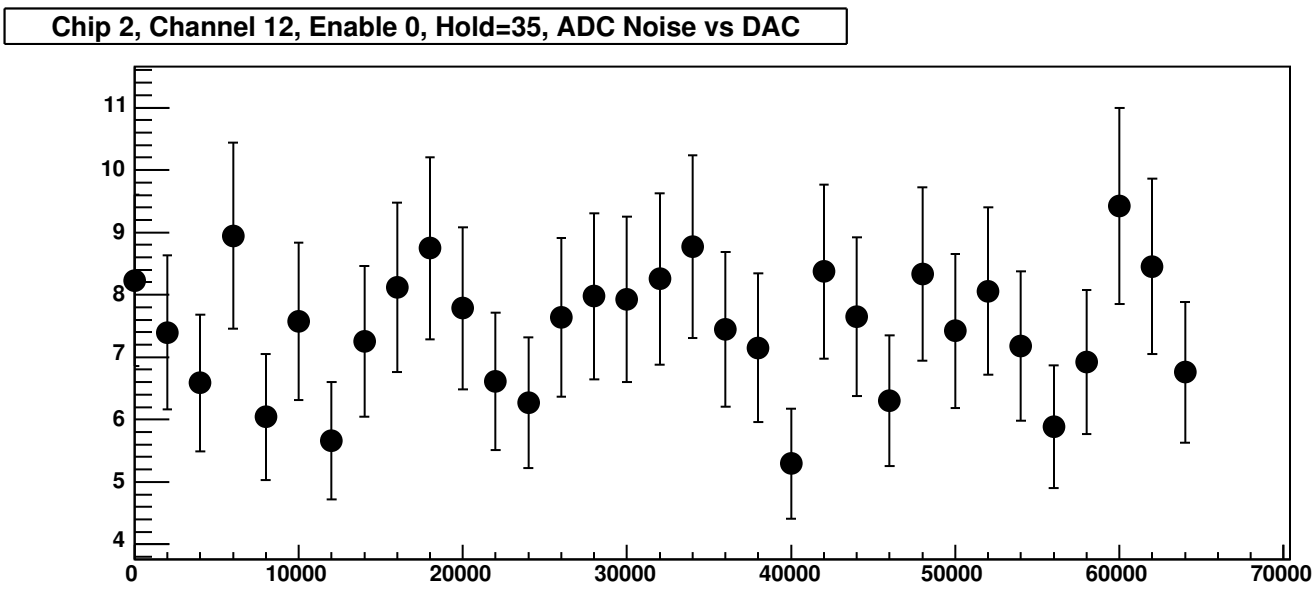
Chip 2, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

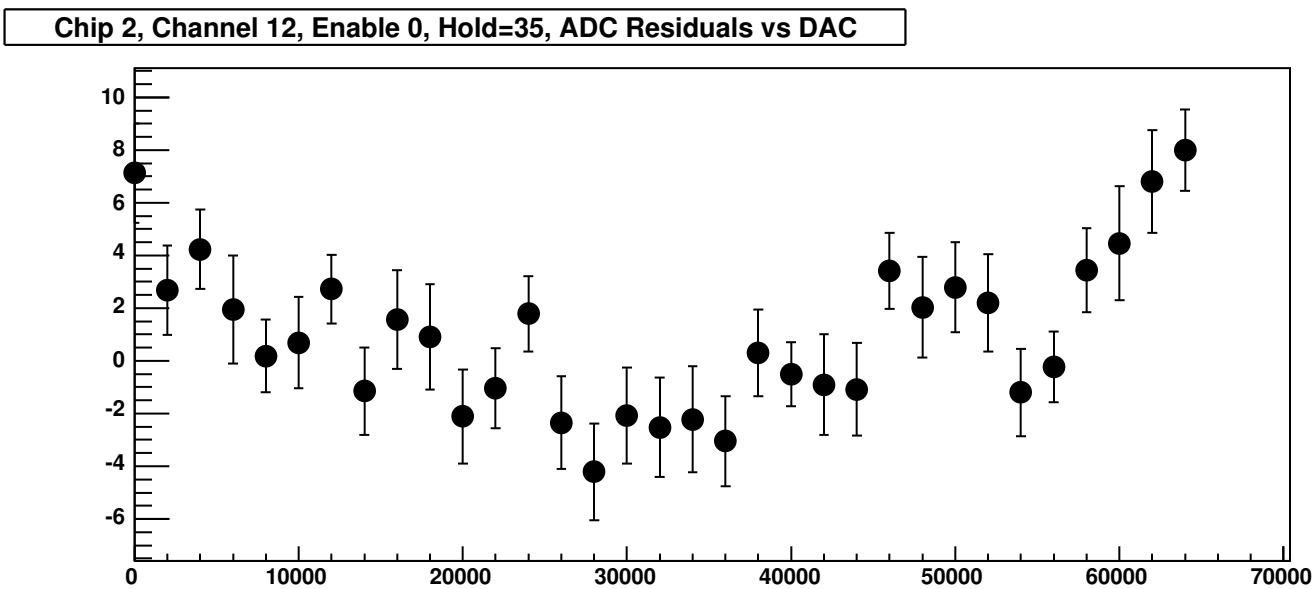


Chip 2, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

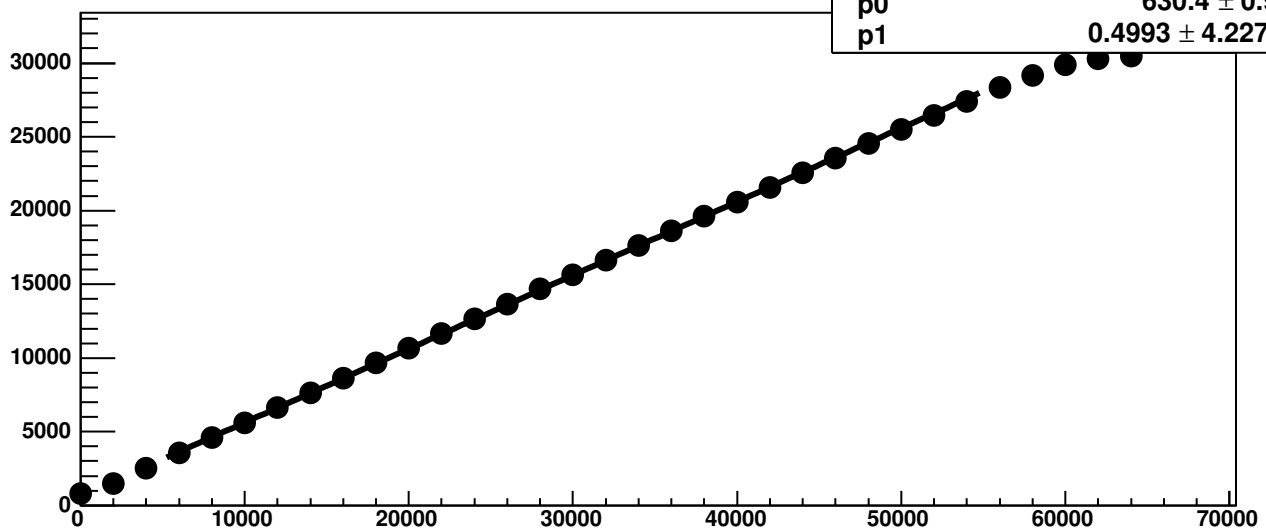



Chip 2, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 36.95 / 23
p0 452.7 ± 0.7591
p1 0.002512 ± 2.297e-05
620
600
580
560
540
520
500
480
460
0
10000
20000
30000
40000
50000
60000
70000

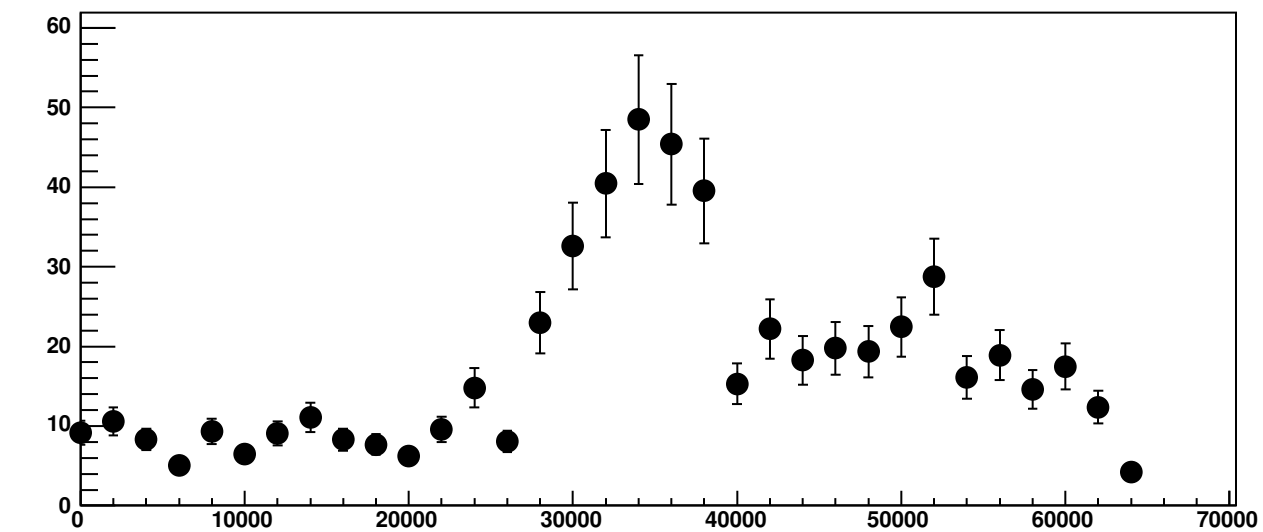

Chip 2, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

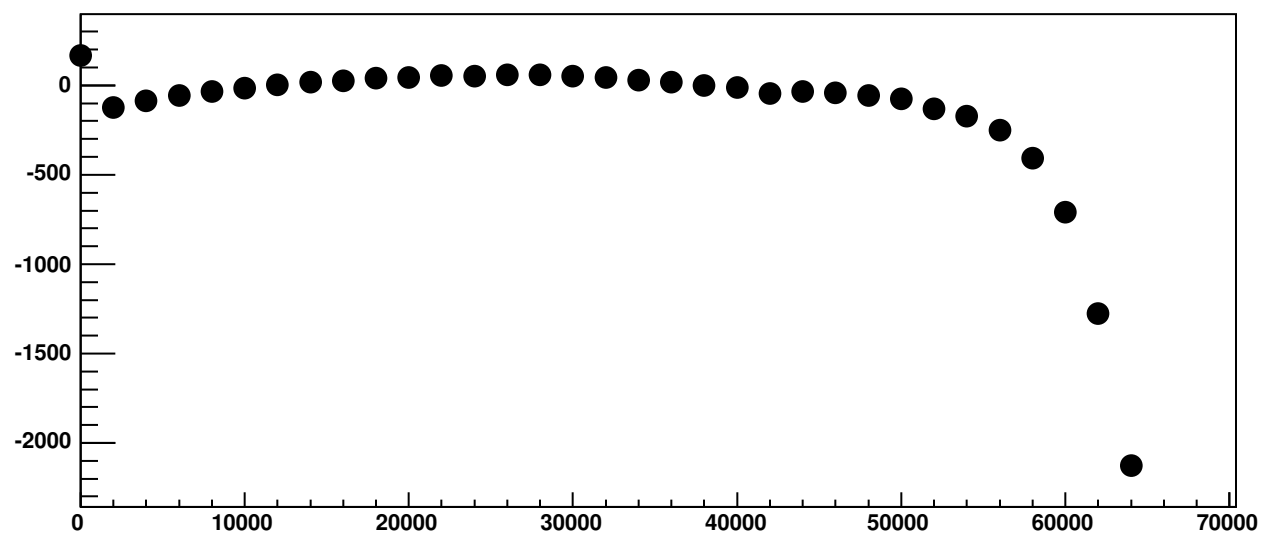
Chip 2, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC

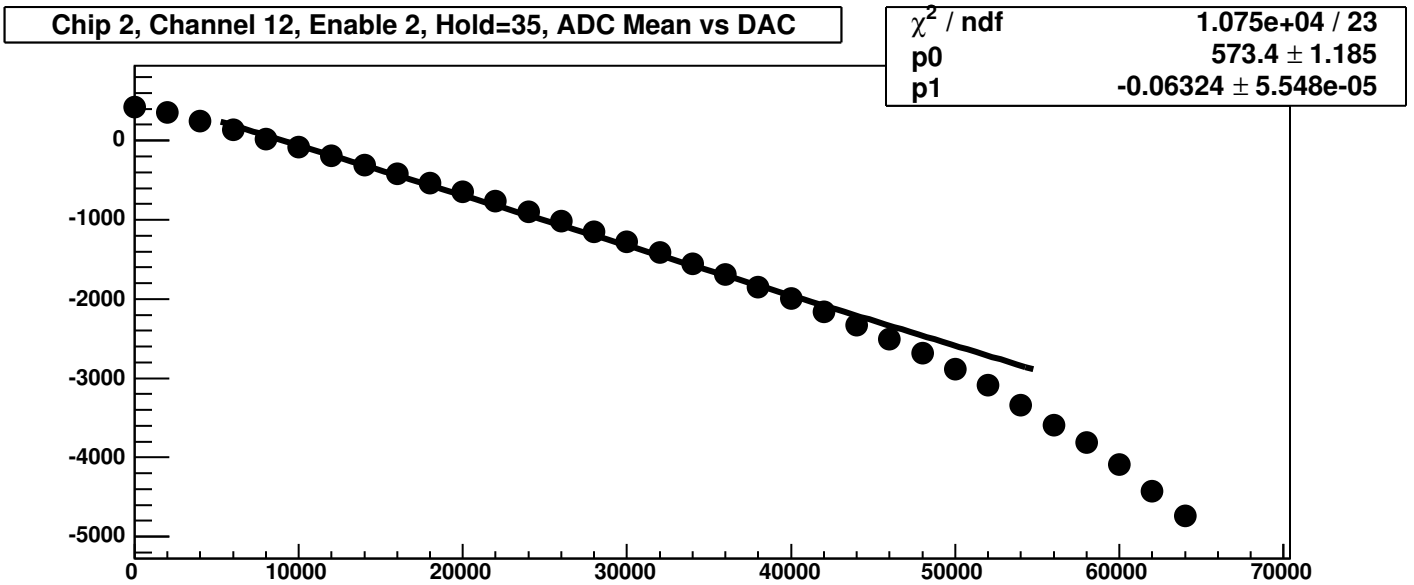


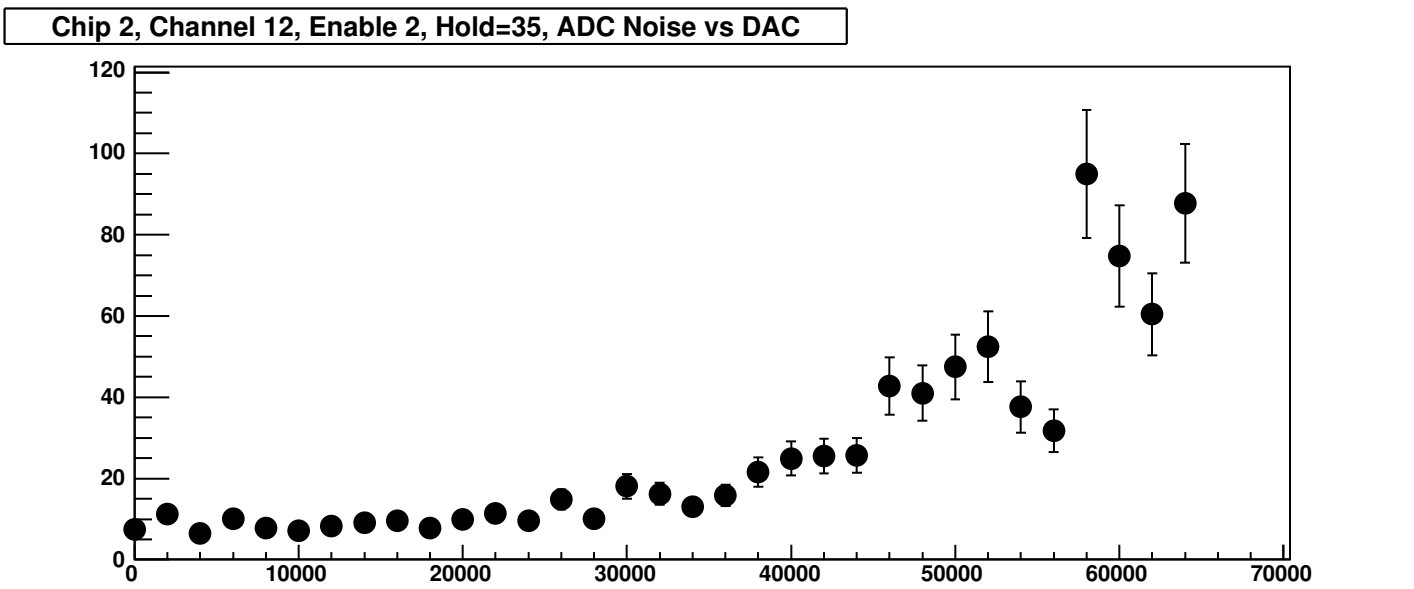
Chip 2, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC

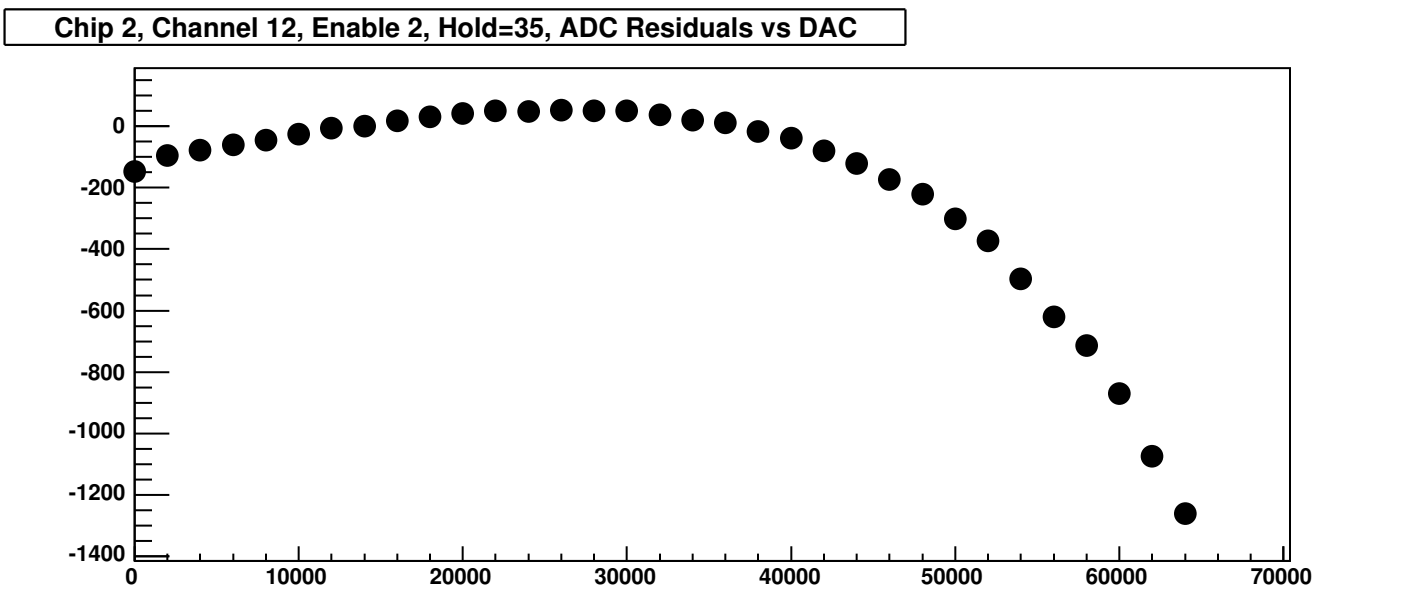


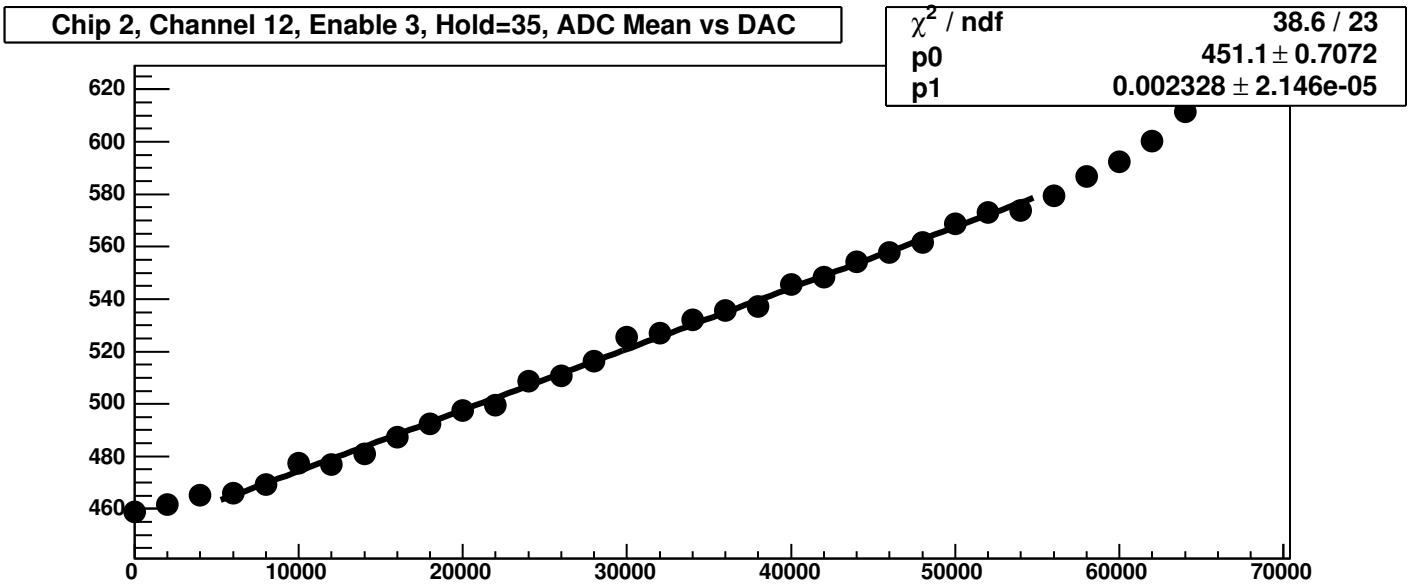
Chip 2, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC

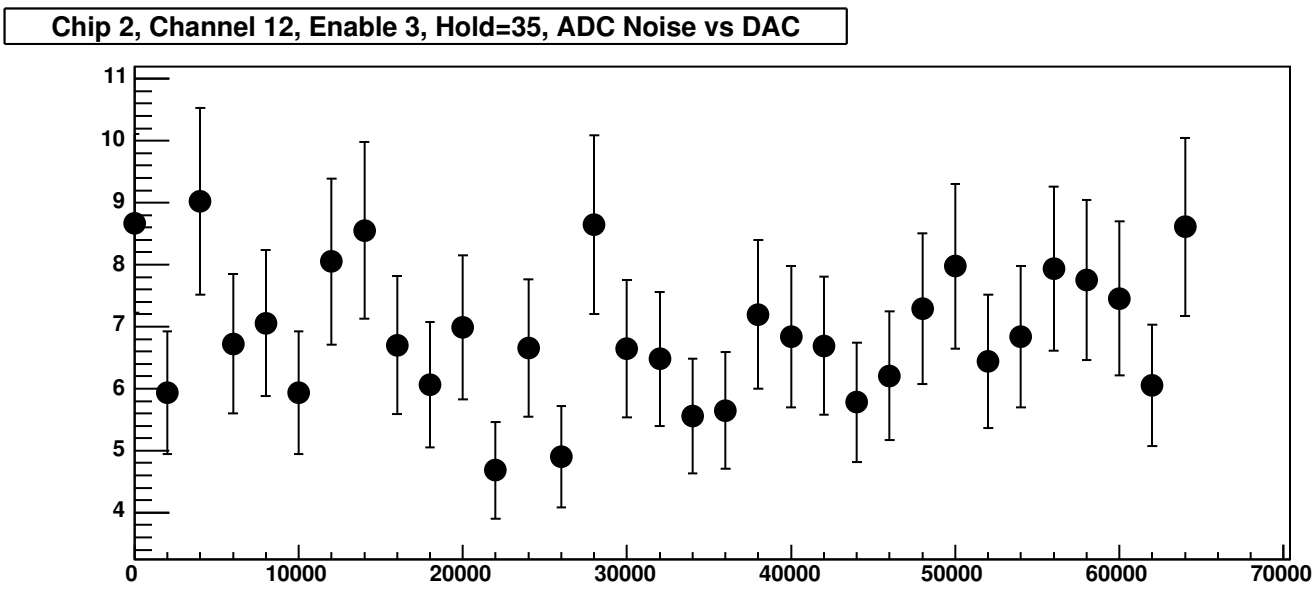


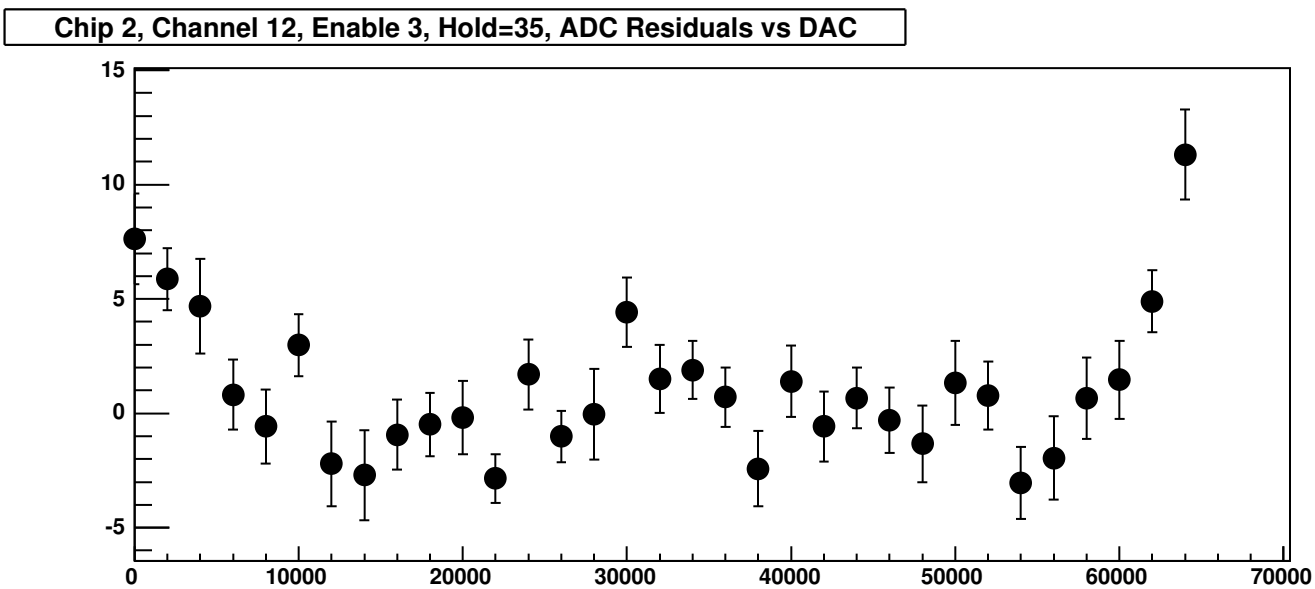

Chip 2, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1.075e+04 / 23
p0 573.4 ± 1.185
p1 -0.06324 ± 5.548e-05
0
-1000
-2000
-3000
-4000
-5000
0
10000
20000
30000
40000
50000
60000
70000

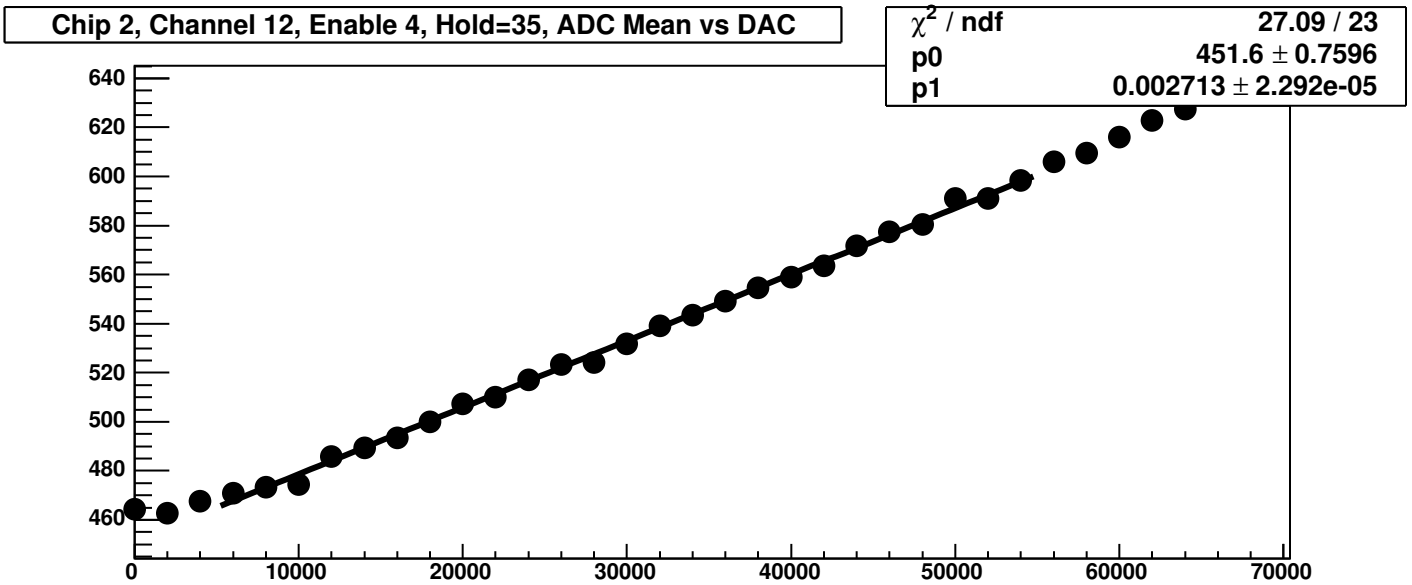

Chip 2, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000
70000

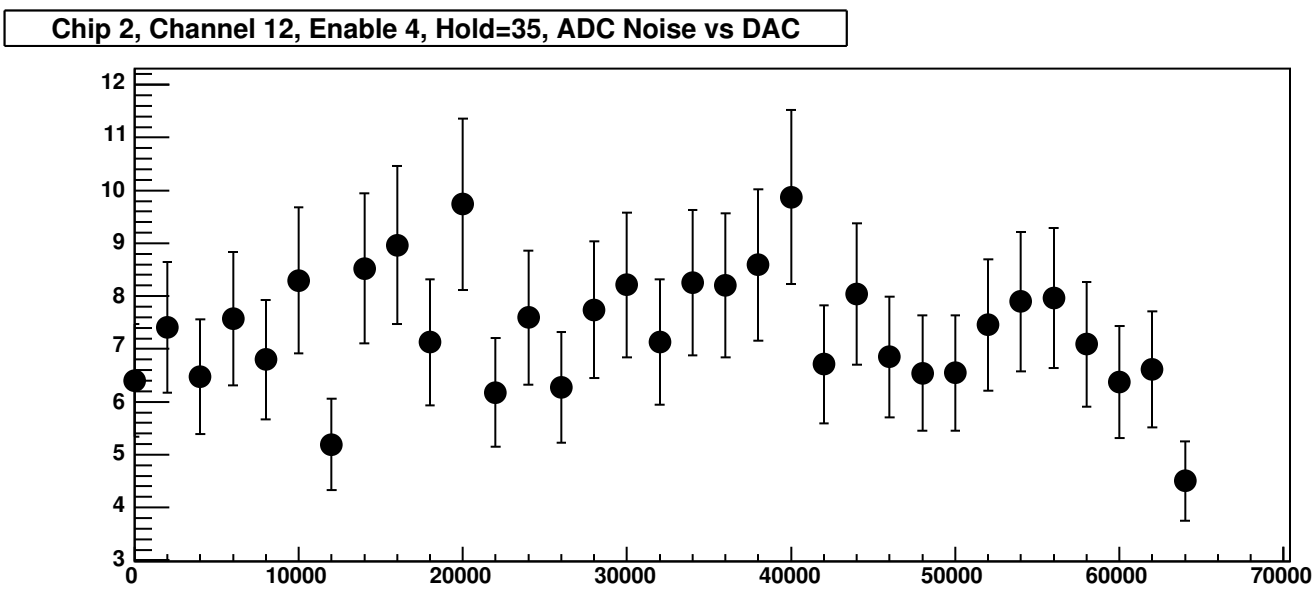

Chip 2, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
0
-200
-400
-600
-800
-1000
-1200
-1400
0
10000
20000
30000
40000
50000
60000
70000

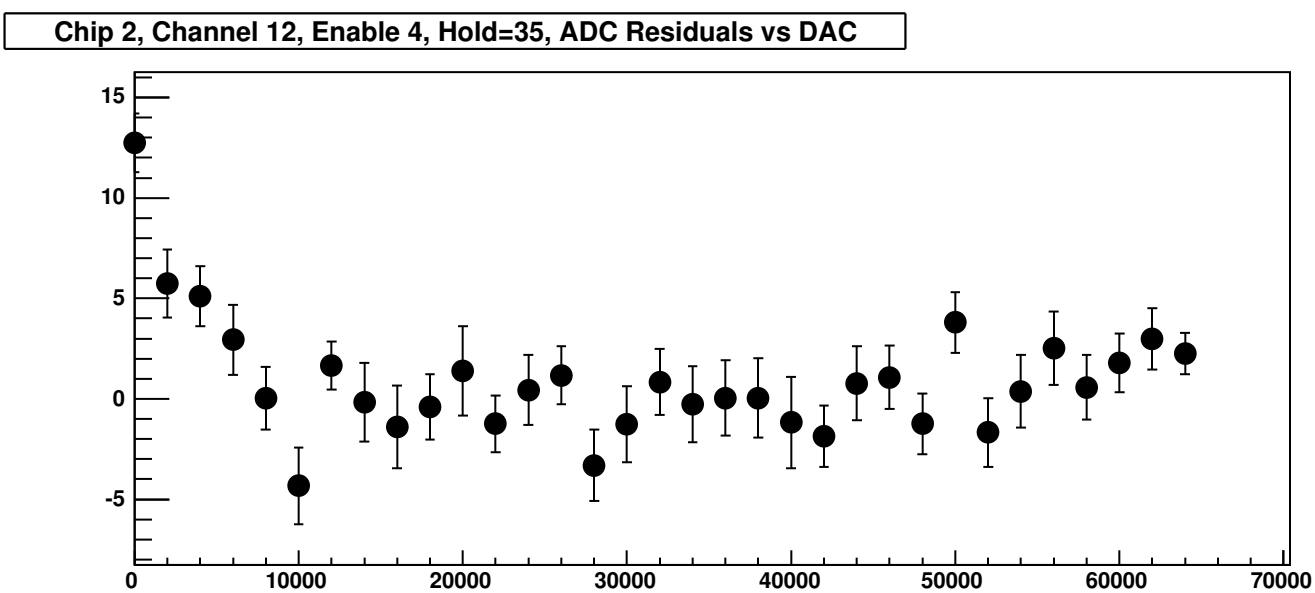

Chip 2, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.6 / 23
p0 451.1 ± 0.7072
p1 0.002328 ± 2.146e-05
620
600
580
560
540
520
500
480
460
0
10000
20000
30000
40000
50000
60000
70000

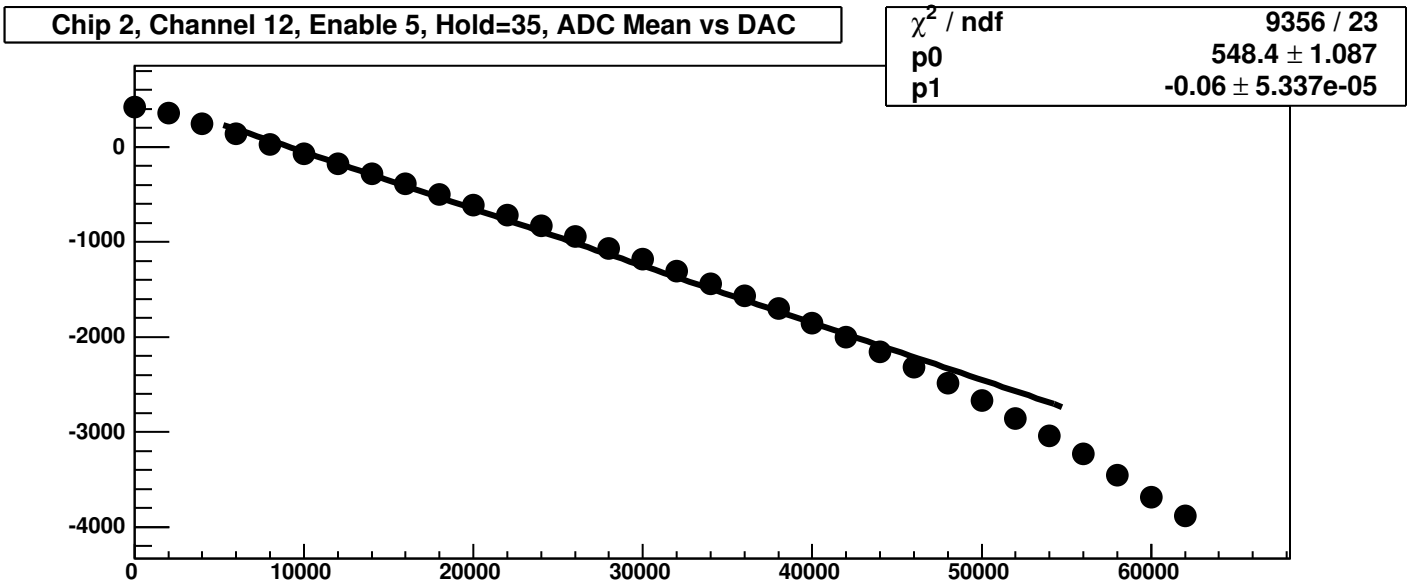

Chip 2, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

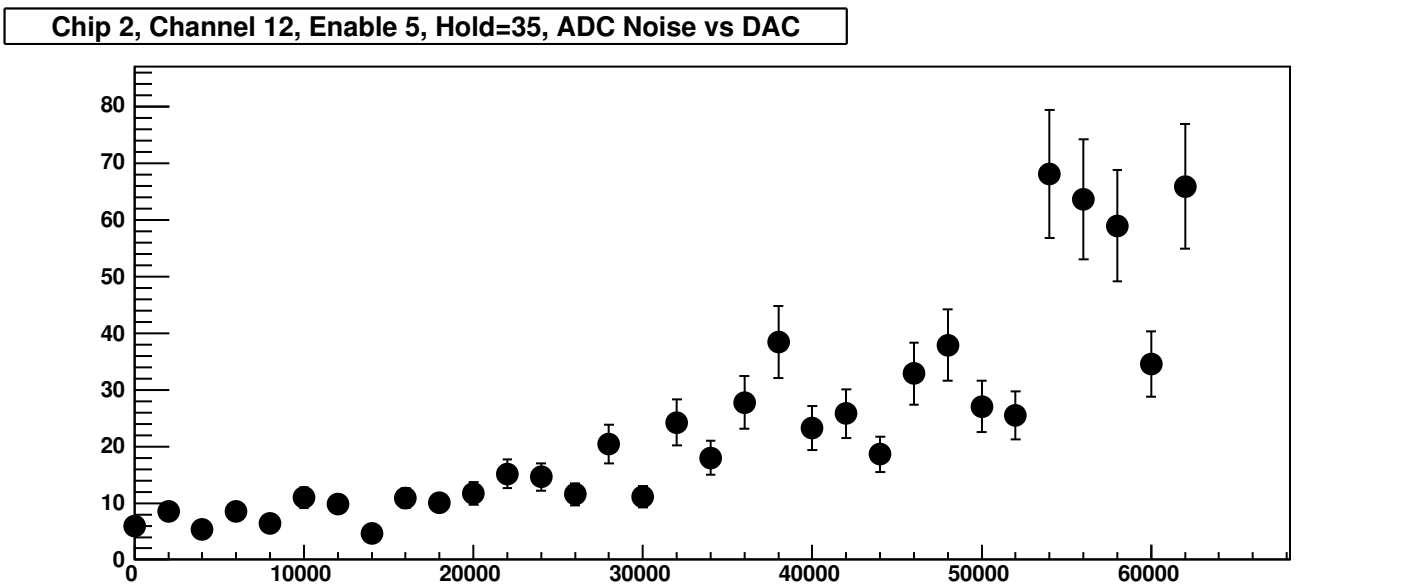

Chip 2, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

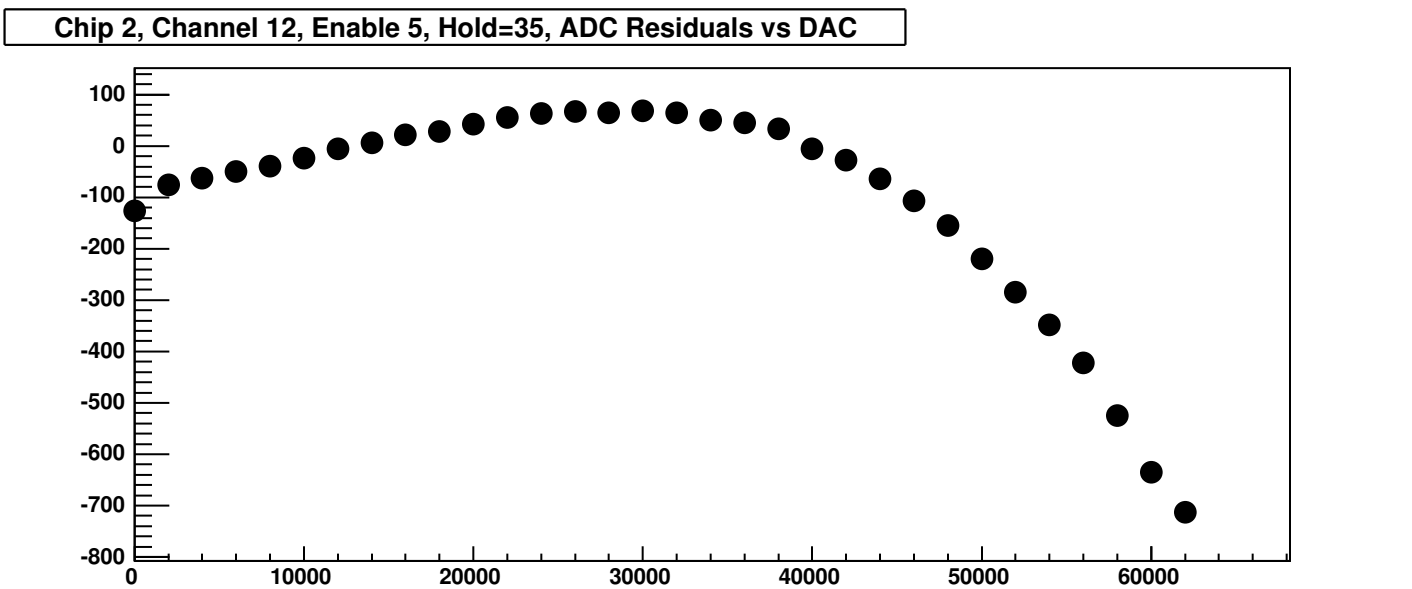

Chip 2, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 27.09 / 23
p0 451.6 ± 0.7596
p1 0.002713 ± 2.292e-05
640
620
600
580
560
540
520
500
480
460
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

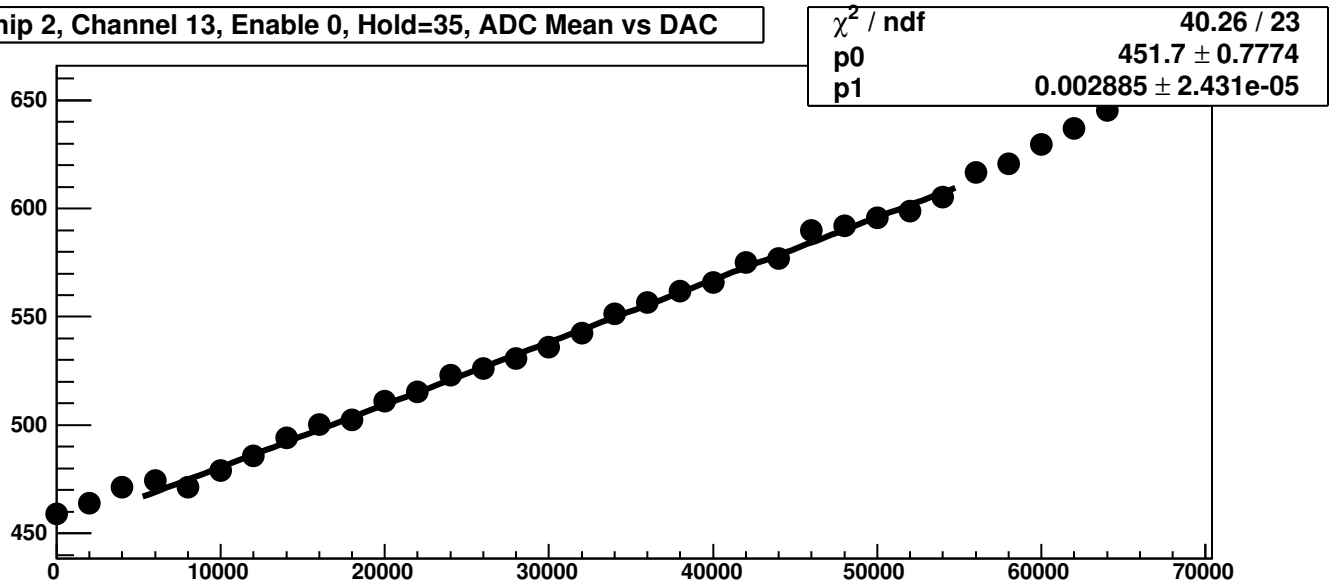

Chip 2, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 9356 / 23
p0 548.4 ± 1.087
p1 -0.06 ± 5.337e-05
0
-1000
-2000
-3000
-4000
0
10000
20000
30000
40000
50000
60000

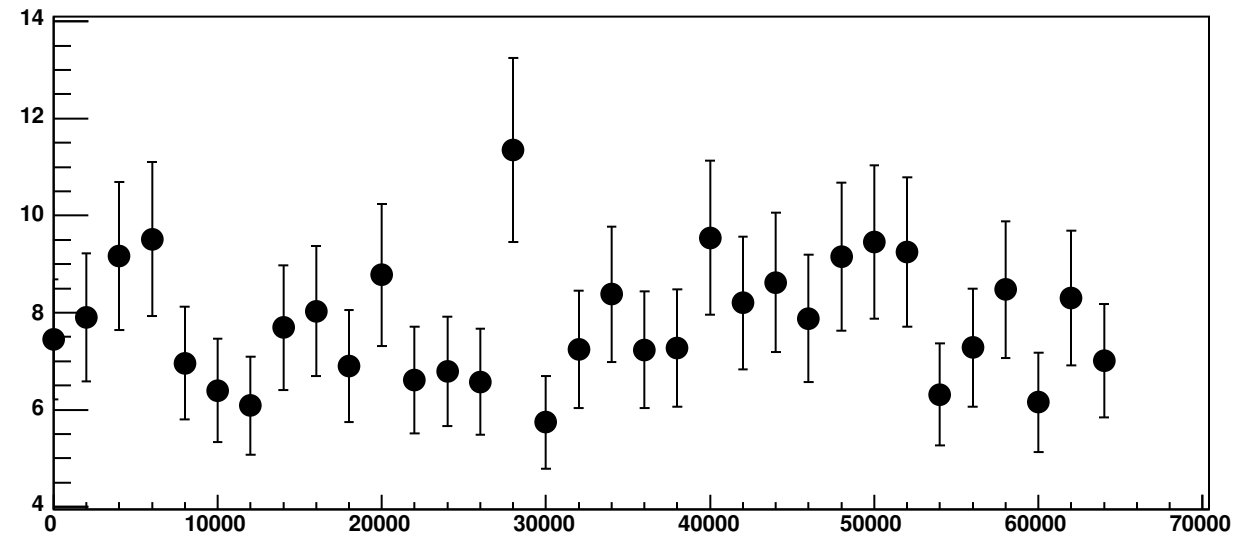

Chip 2, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
100
0
-100
-200
-300
-400
-500
-600
-700
-800
0
10000
20000
30000
40000
50000
60000

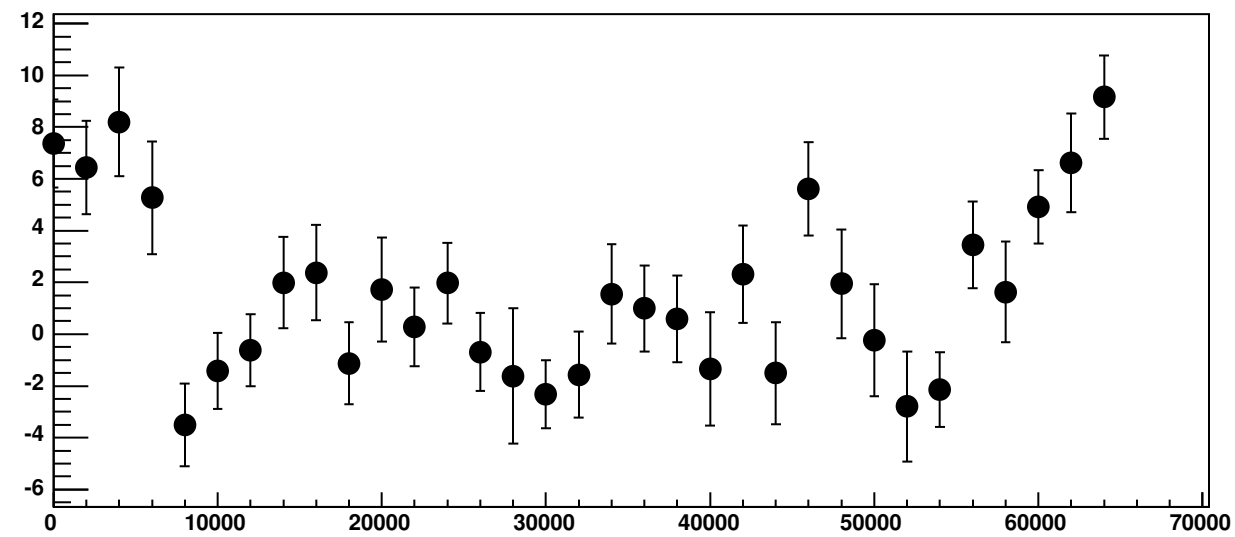
Chip 2, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC

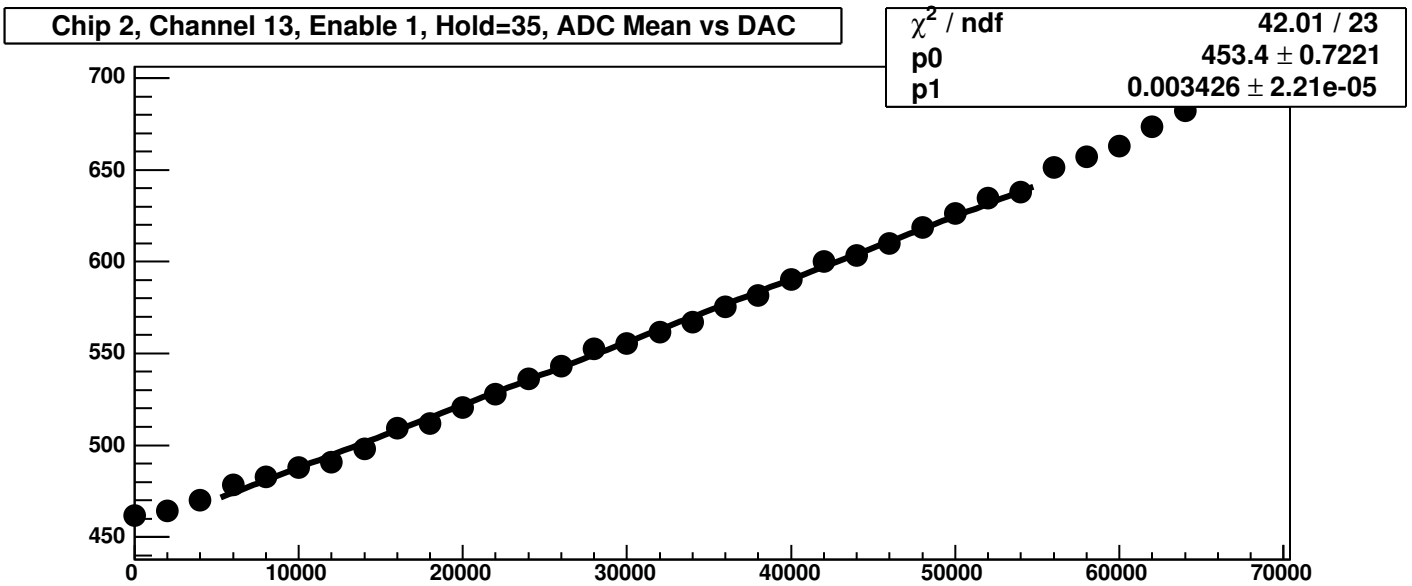


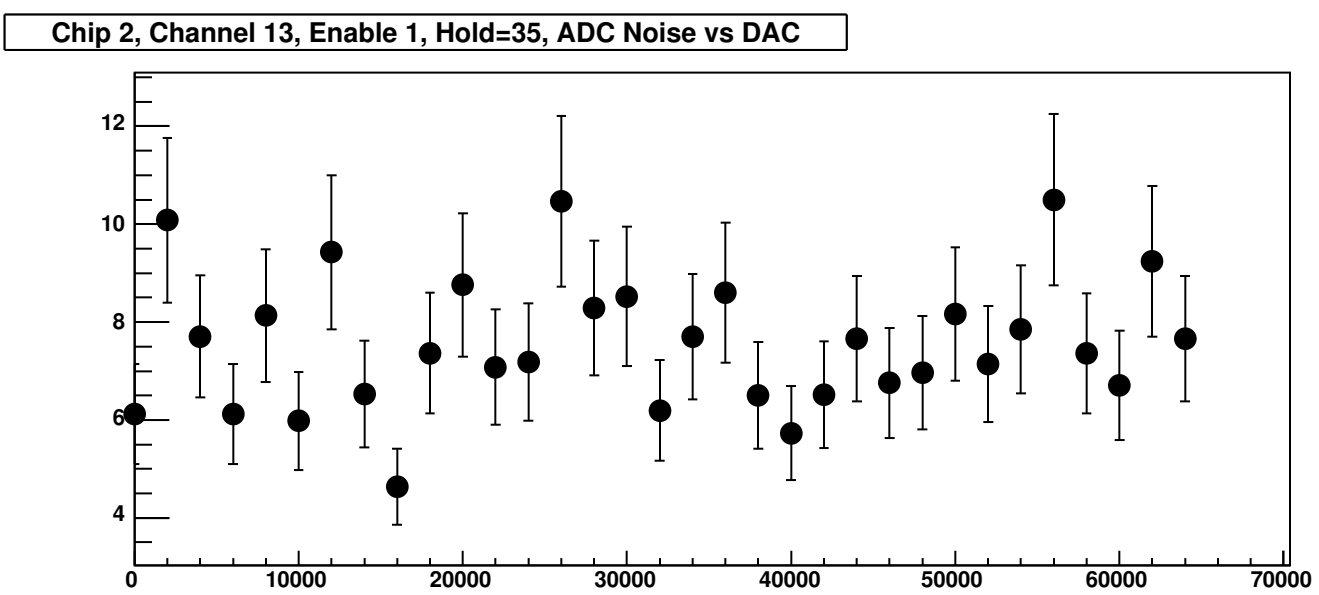
Chip 2, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC

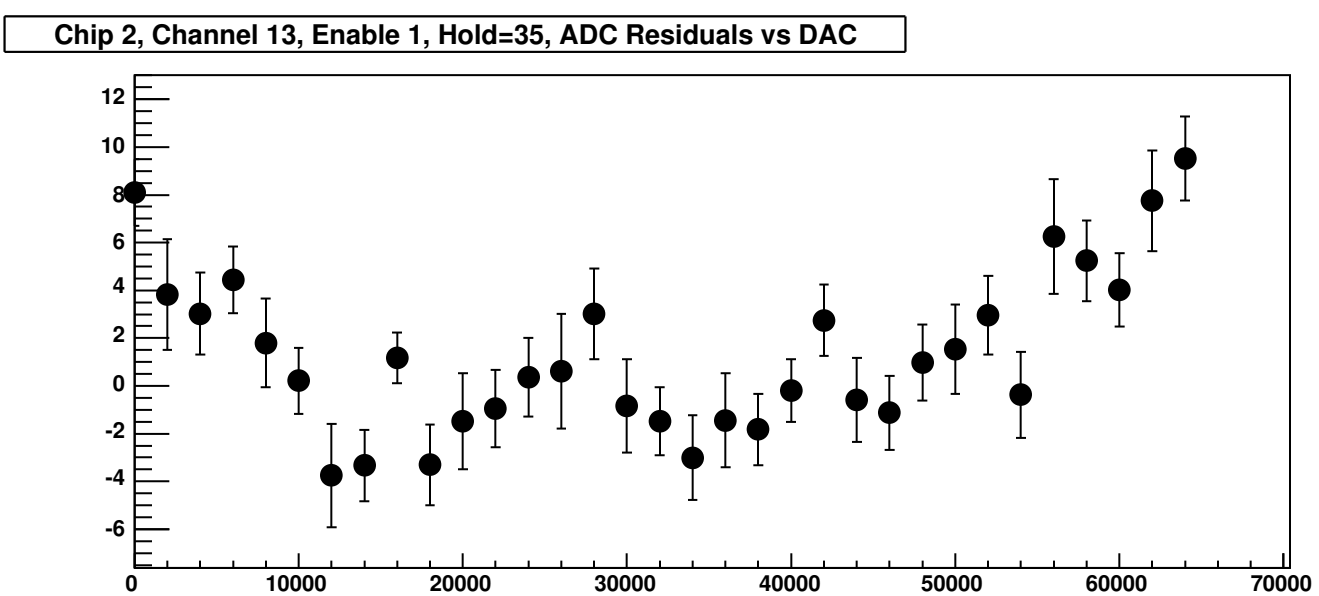


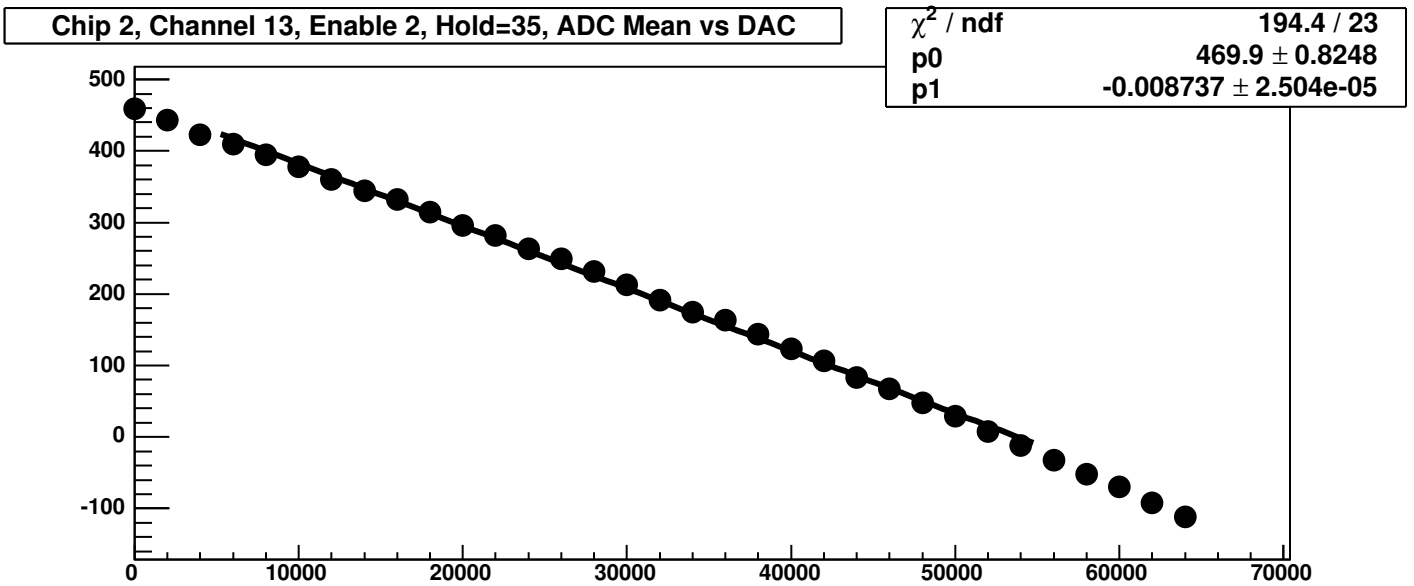
Chip 2, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC

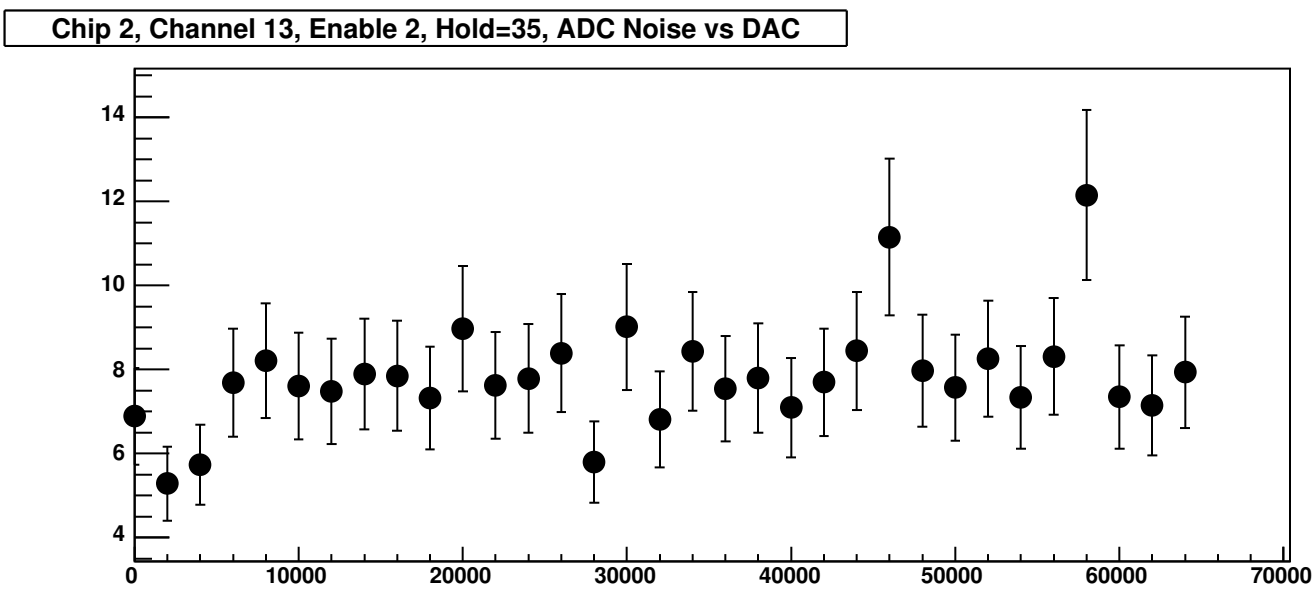


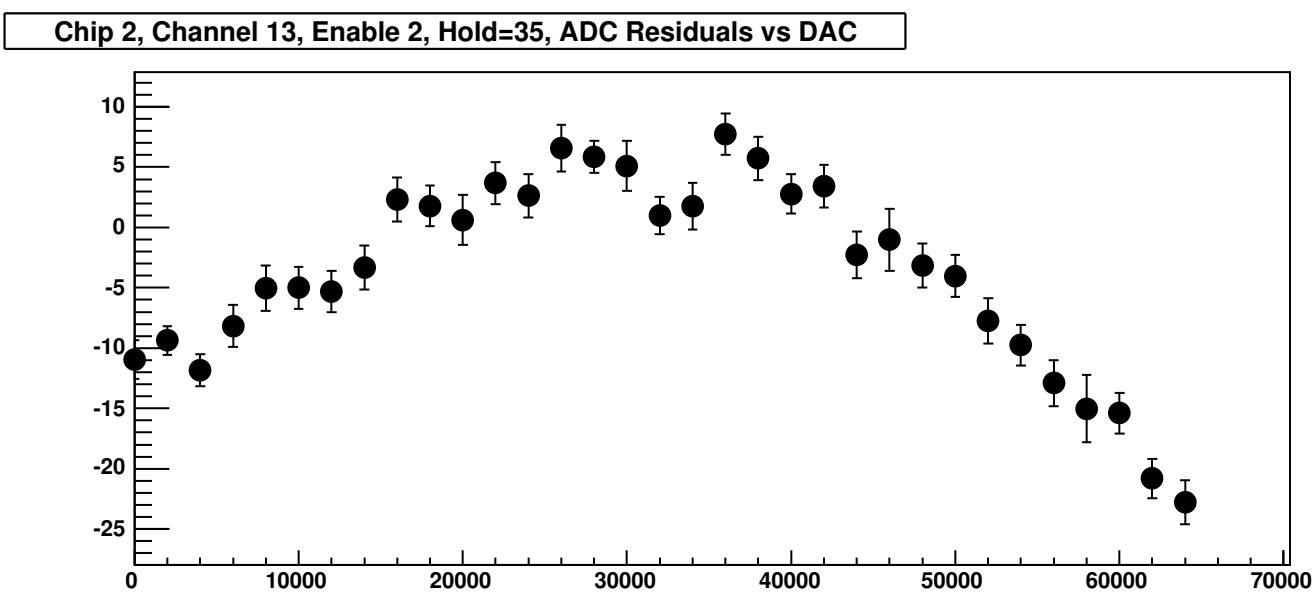

Chip 2, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 42.01 / 23
p0 453.4 ± 0.7221
p1 0.003426 ± 2.21e-05
700
650
600
550
500
450
0
10000
20000
30000
40000
50000
60000
70000

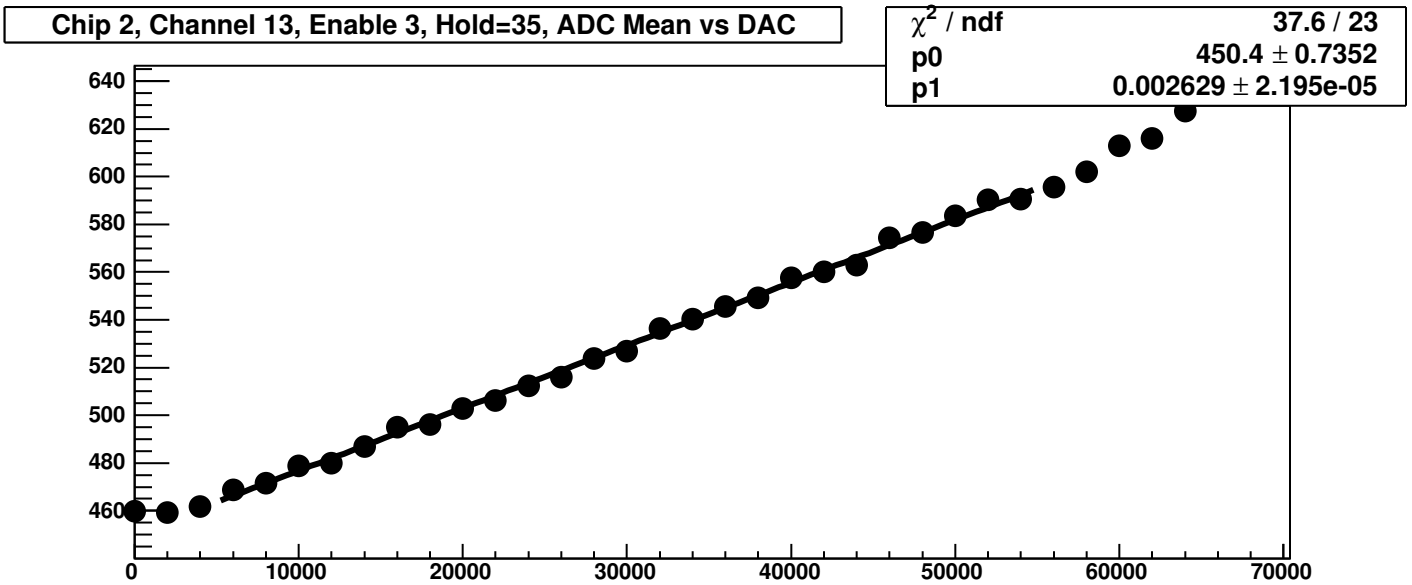

Chip 2, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

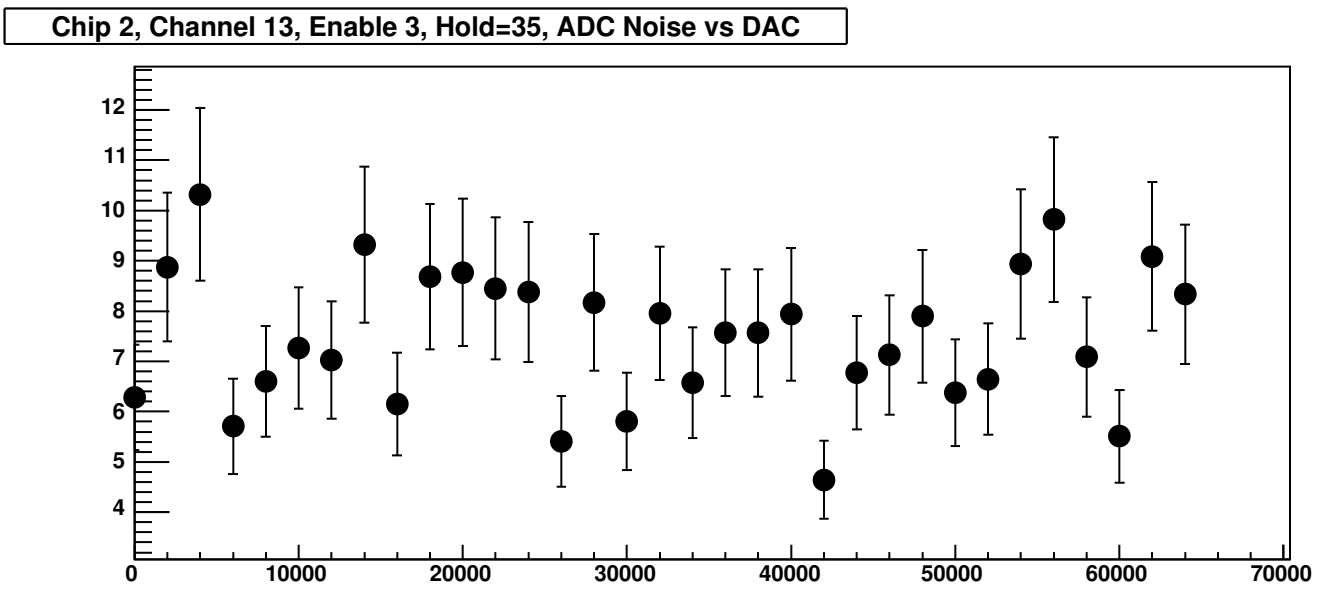

Chip 2, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

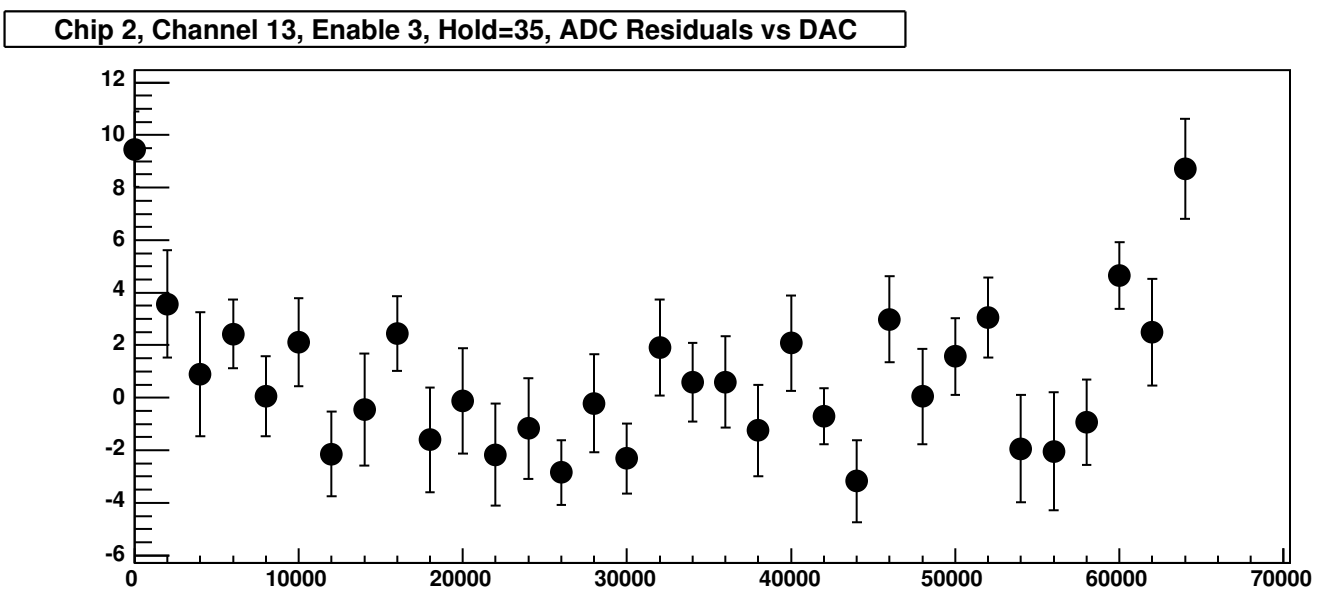

Chip 2, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 194.4 / 23
p0 469.9 ± 0.8248
p1 -0.008737 ± 2.504e-05
500
400
300
200
100
0
-100
0
10000
20000
30000
40000
50000
60000
70000

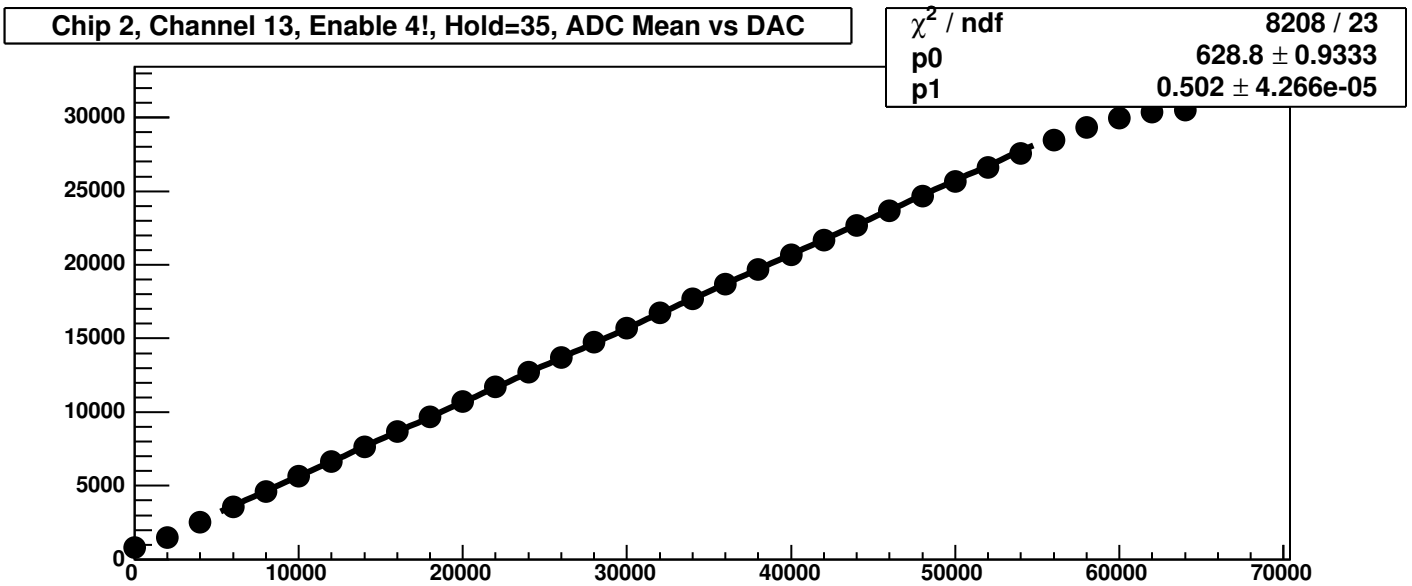

Chip 2, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

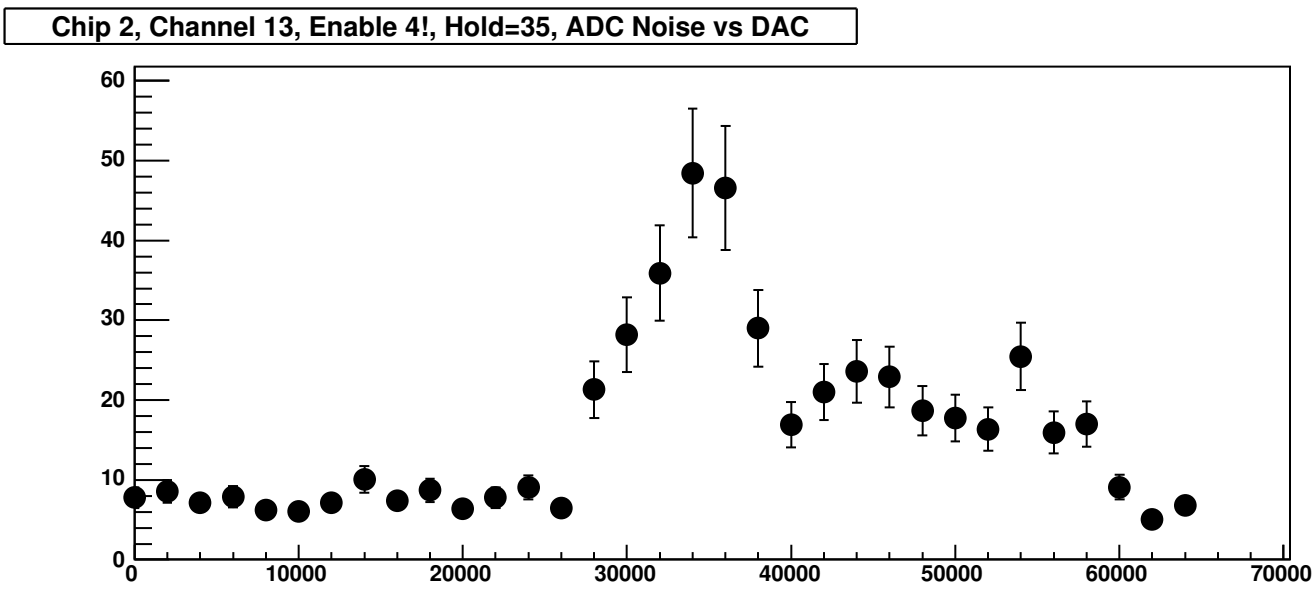

Chip 2, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
10000
20000
30000
40000
50000
60000
70000

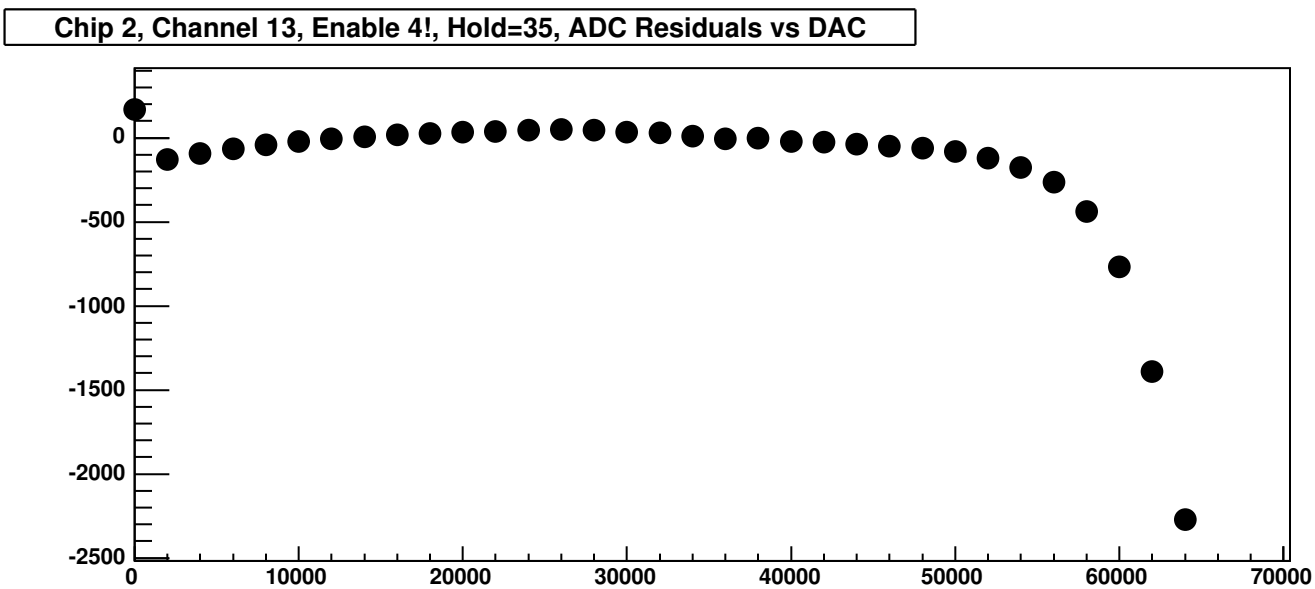

Chip 2, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 37.6 / 23
p0 450.4 ± 0.7352
p1 0.002629 ± 2.195e-05
640
620
600
580
560
540
520
500
480
460
0
10000
20000
30000
40000
50000
60000
70000

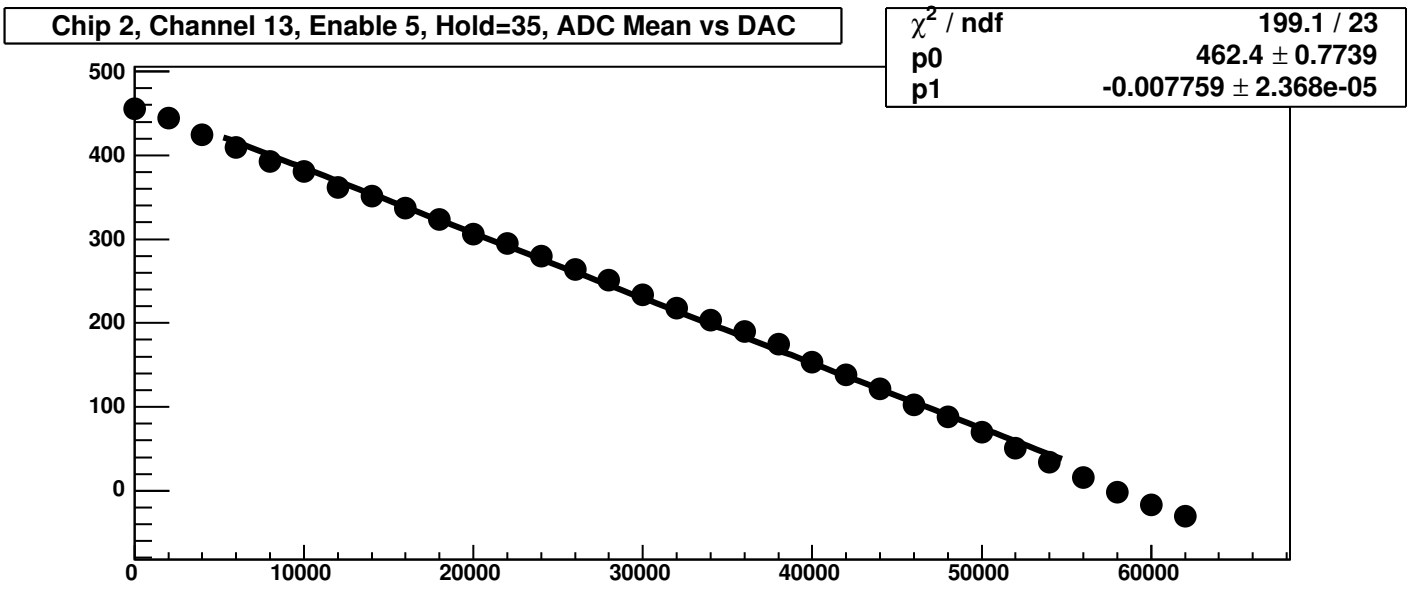

Chip 2, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

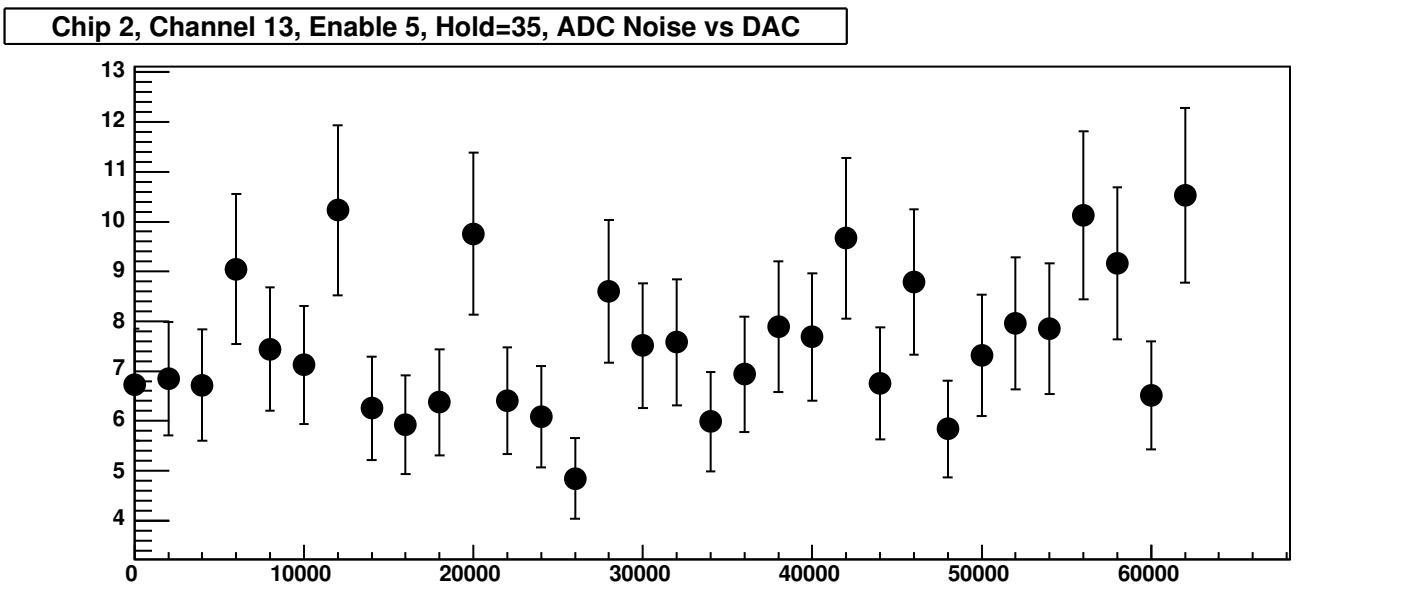

Chip 2, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

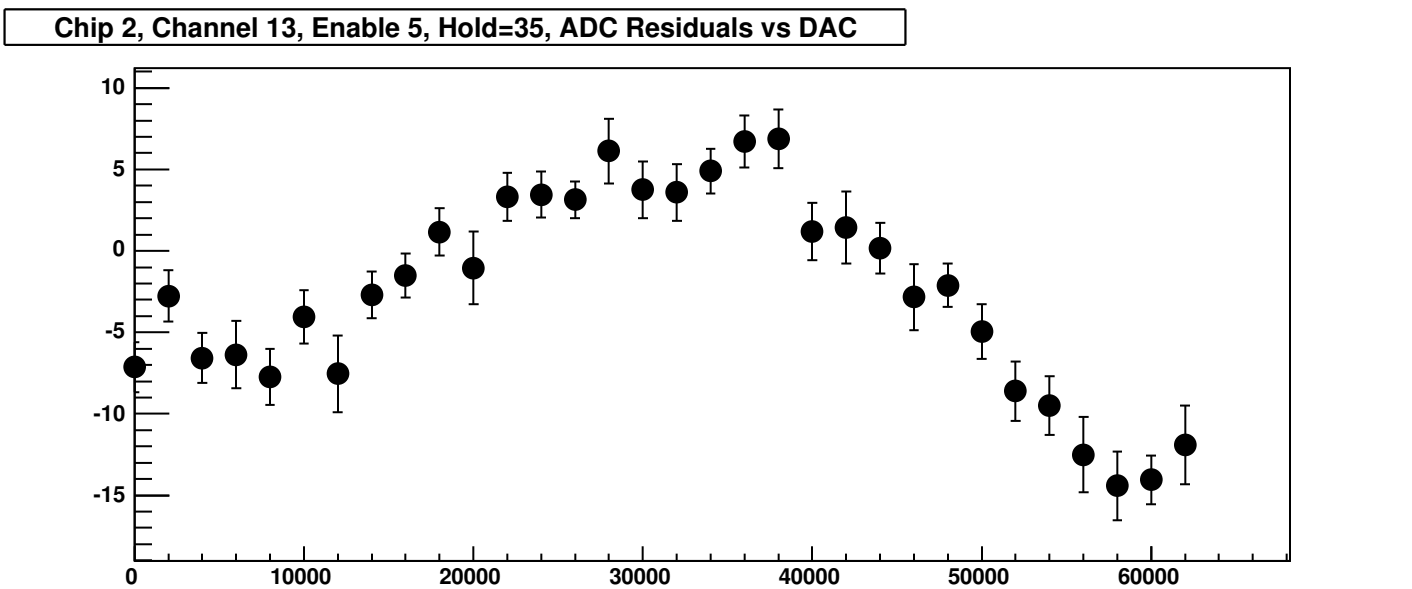

Chip 2, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 8208 / 23
p0 628.8 ± 0.9333
p1 0.502 ± 4.266e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

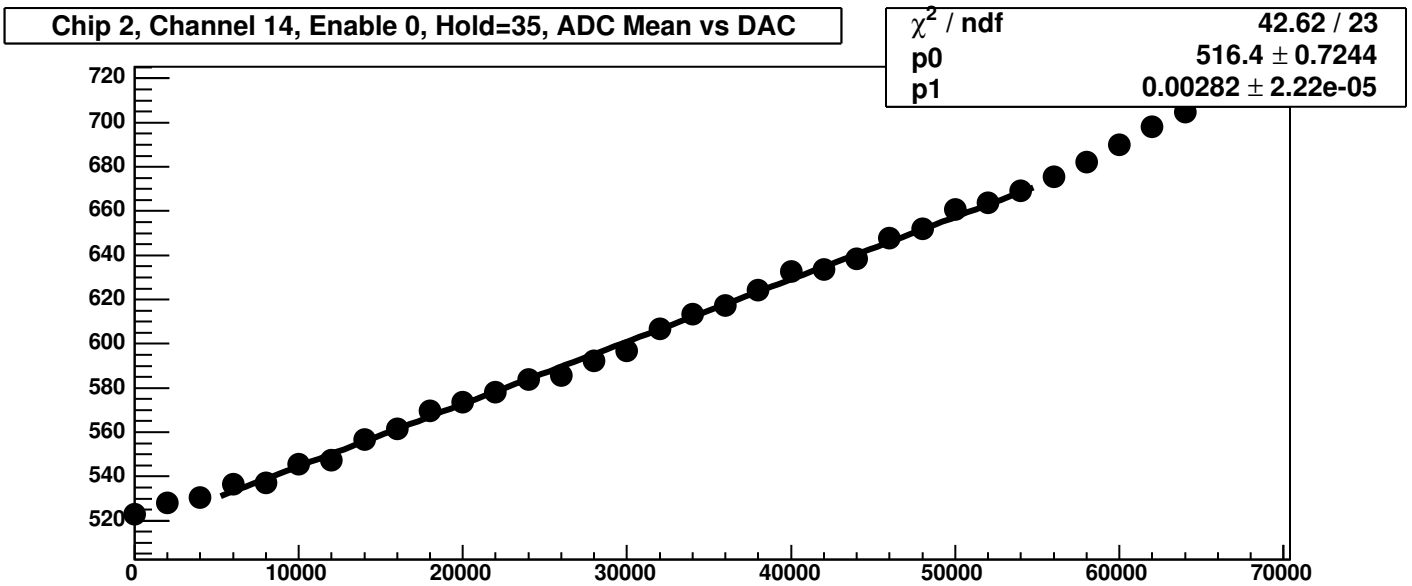

Chip 2, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

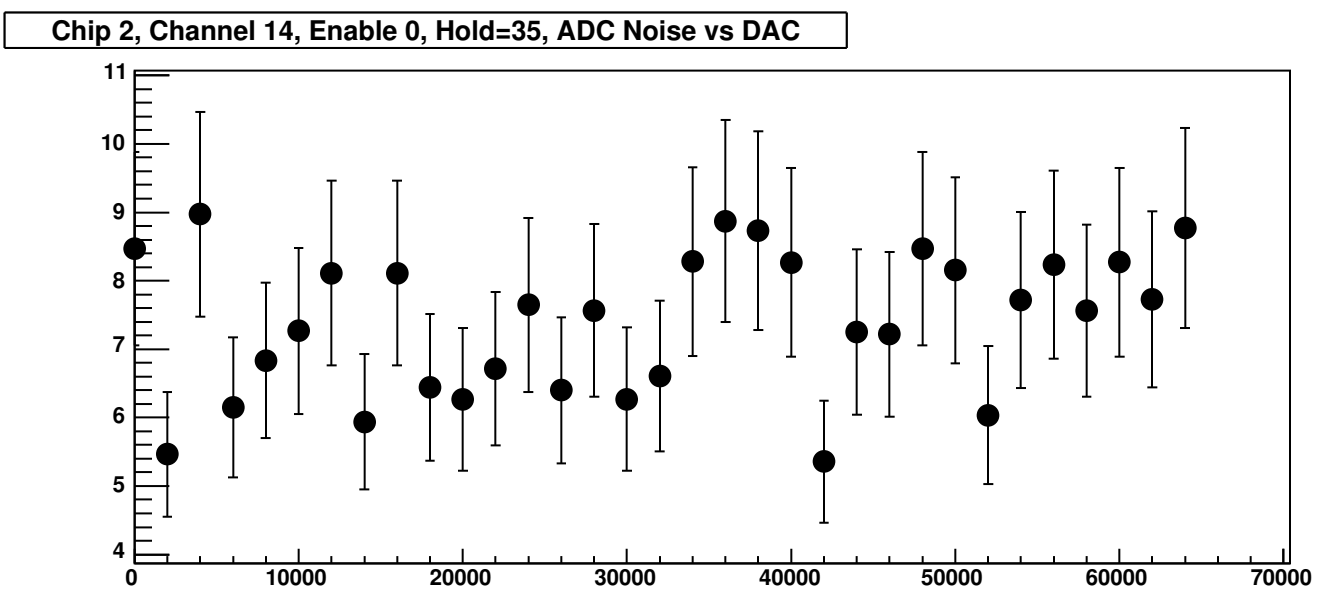

Chip 2, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
0
10000
20000
30000
40000
50000
60000
70000

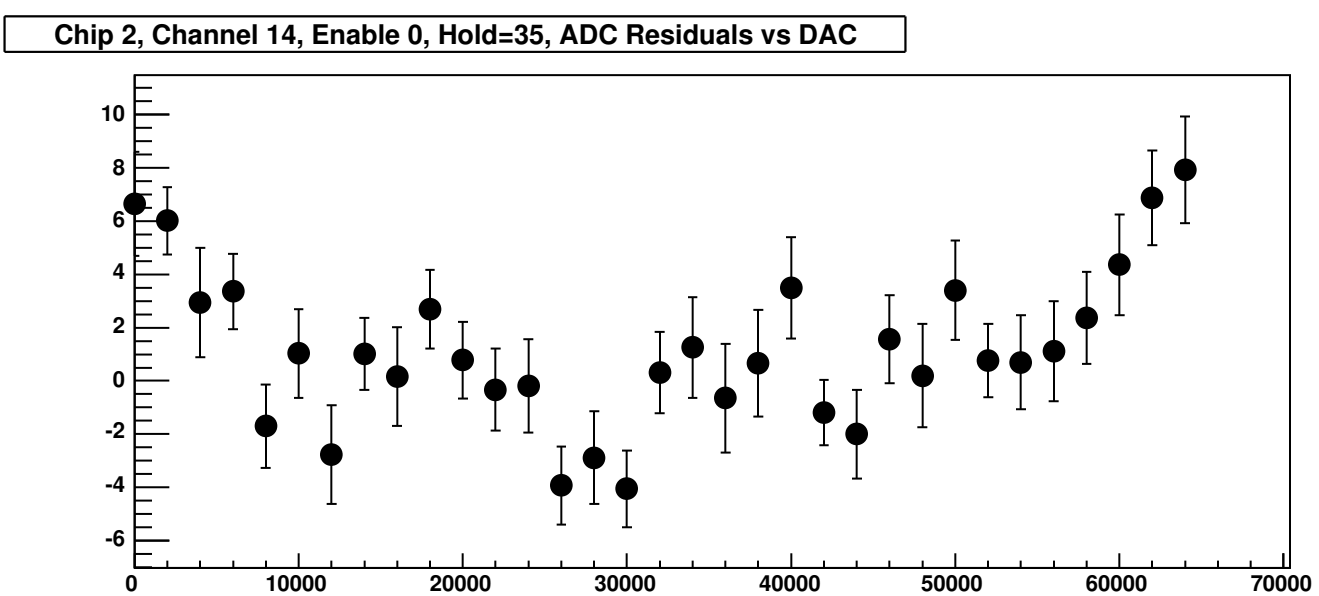

Chip 2, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 199.1 / 23
p0 462.4 ± 0.7739
p1 -0.007759 ± 2.368e-05
500
400
300
200
100
0
0
10000
20000
30000
40000
50000
60000

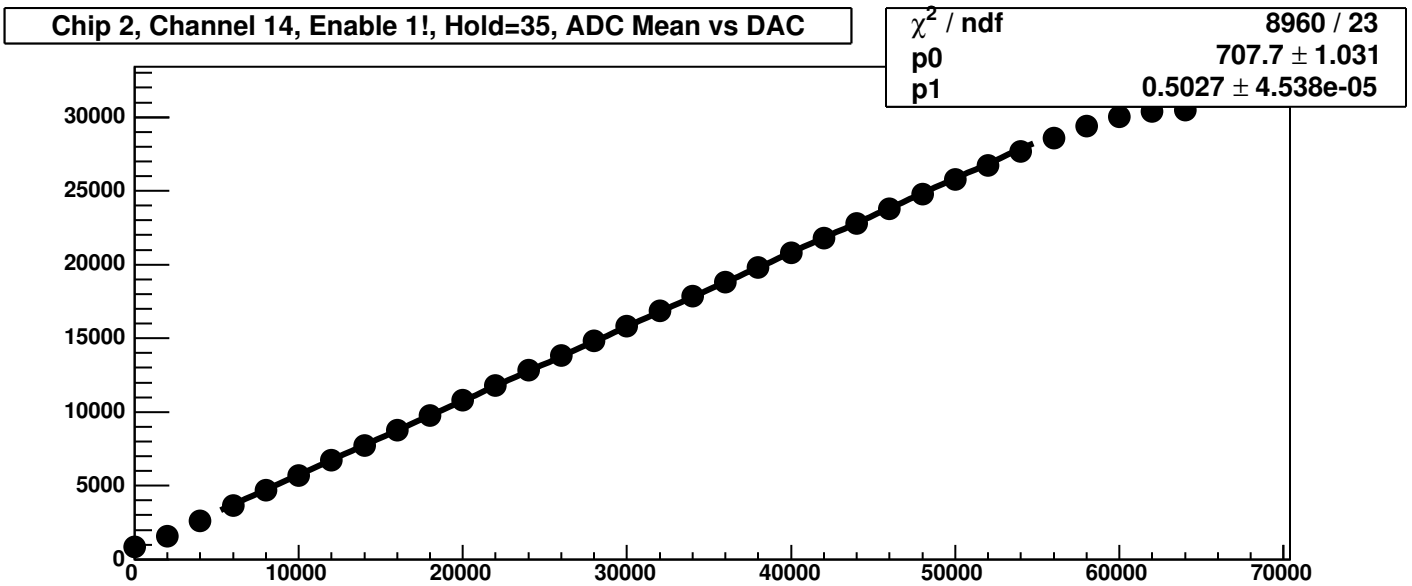

Chip 2, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

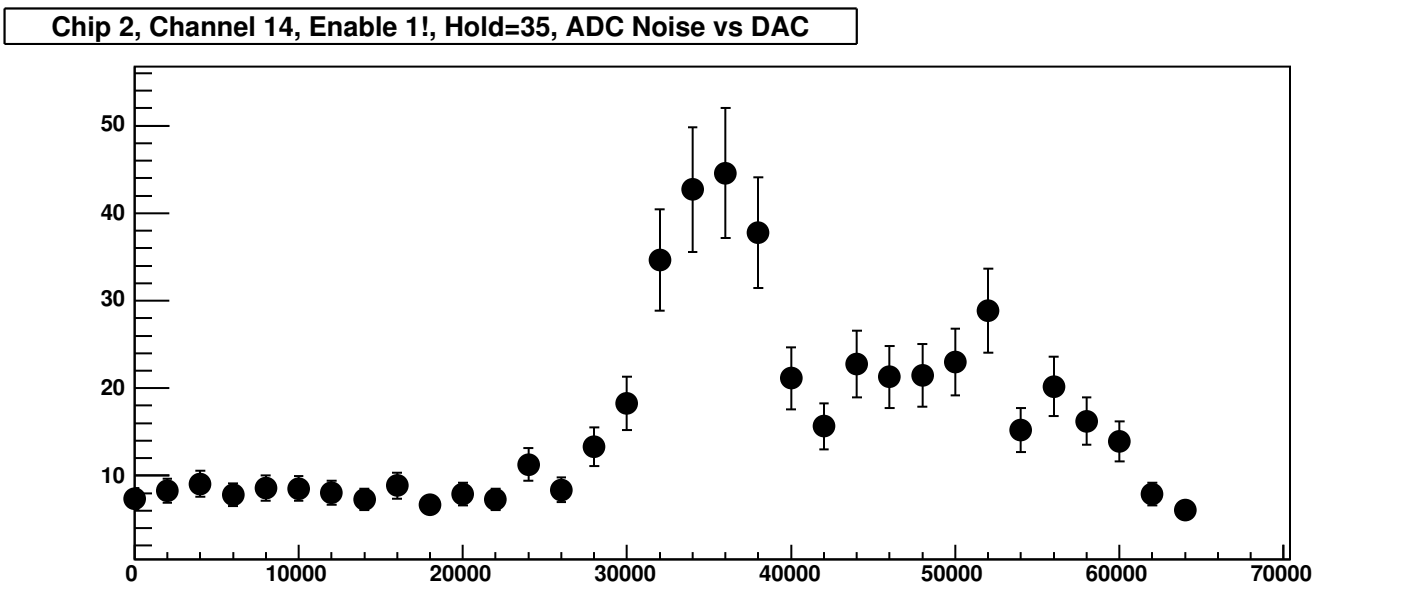

Chip 2, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000

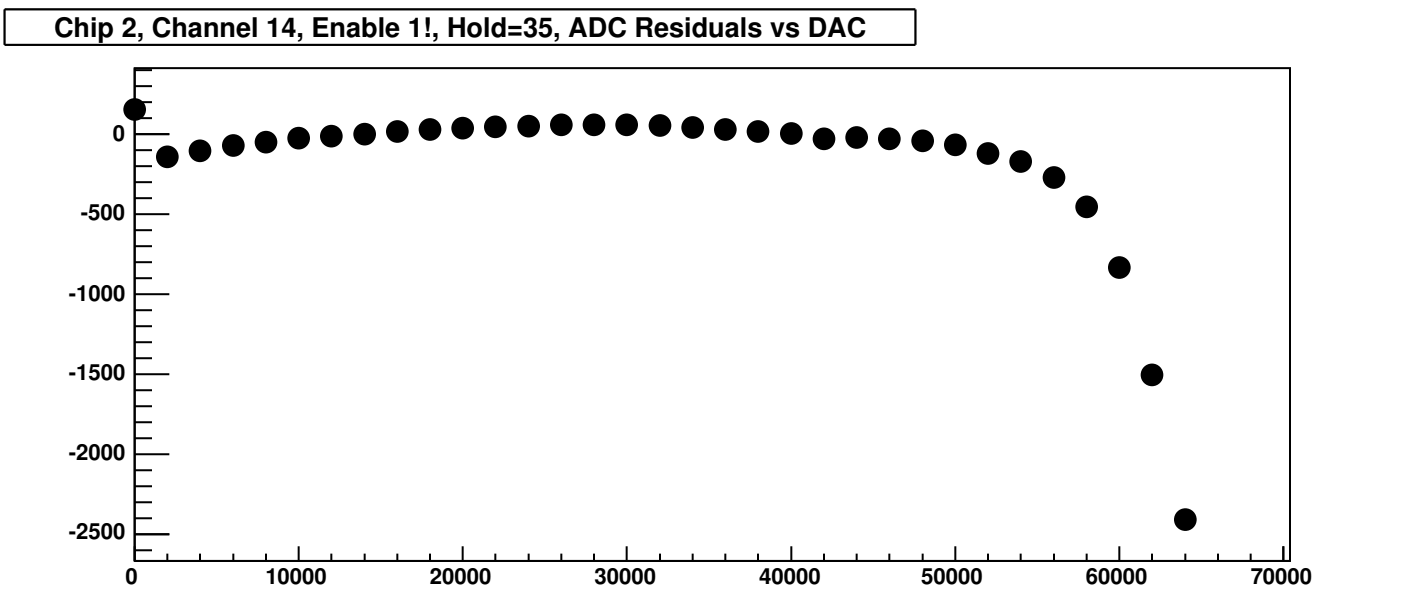

Chip 2, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 42.62 / 23
p0 516.4 ± 0.7244
p1 0.00282 ± 2.22e-05
720
700
680
660
640
620
600
580
560
540
520
0
10000
20000
30000
40000
50000
60000
70000

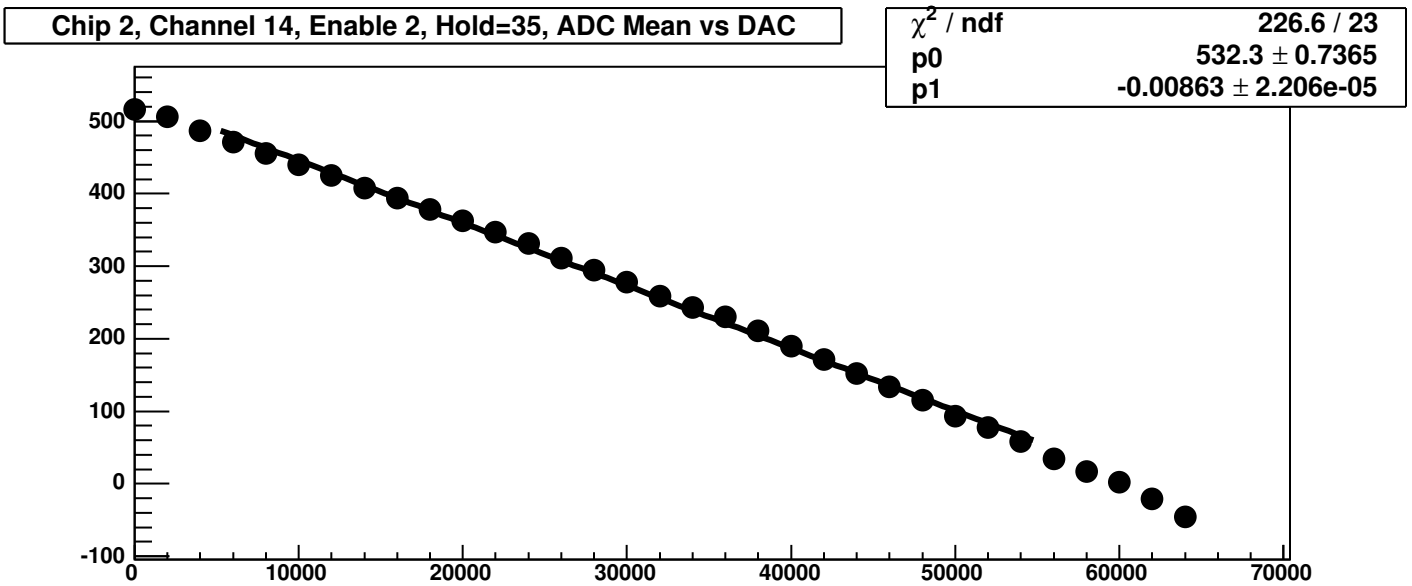

Chip 2, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

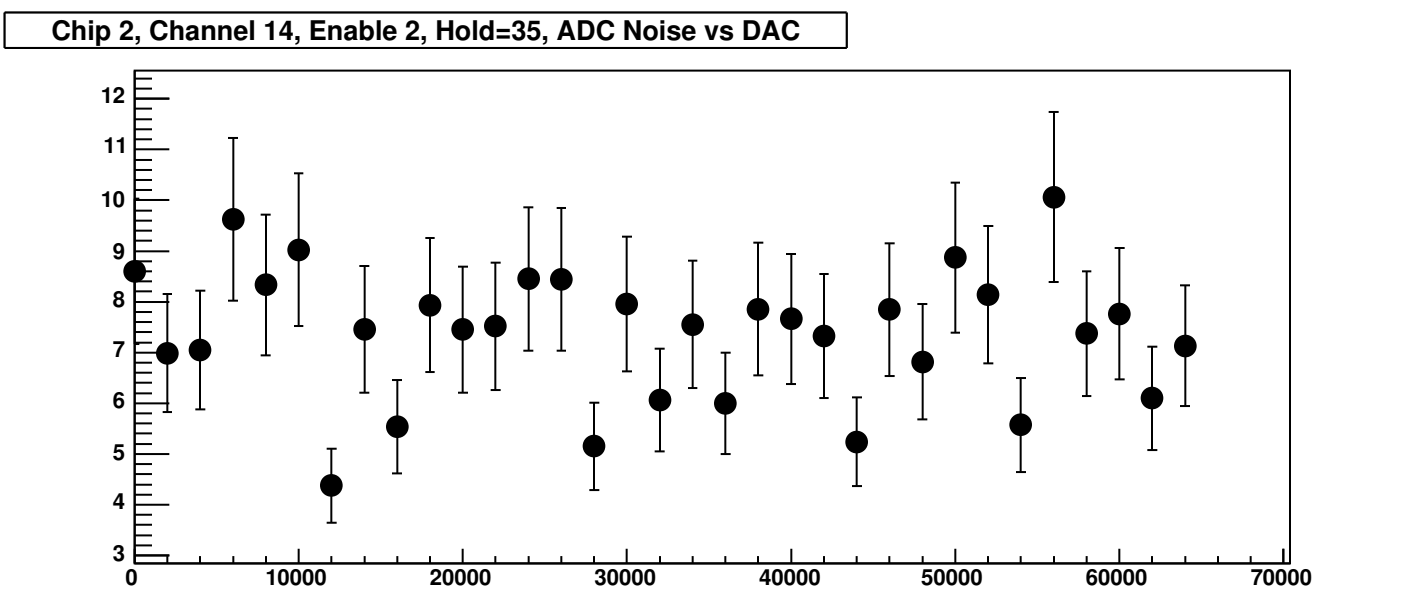

Chip 2, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

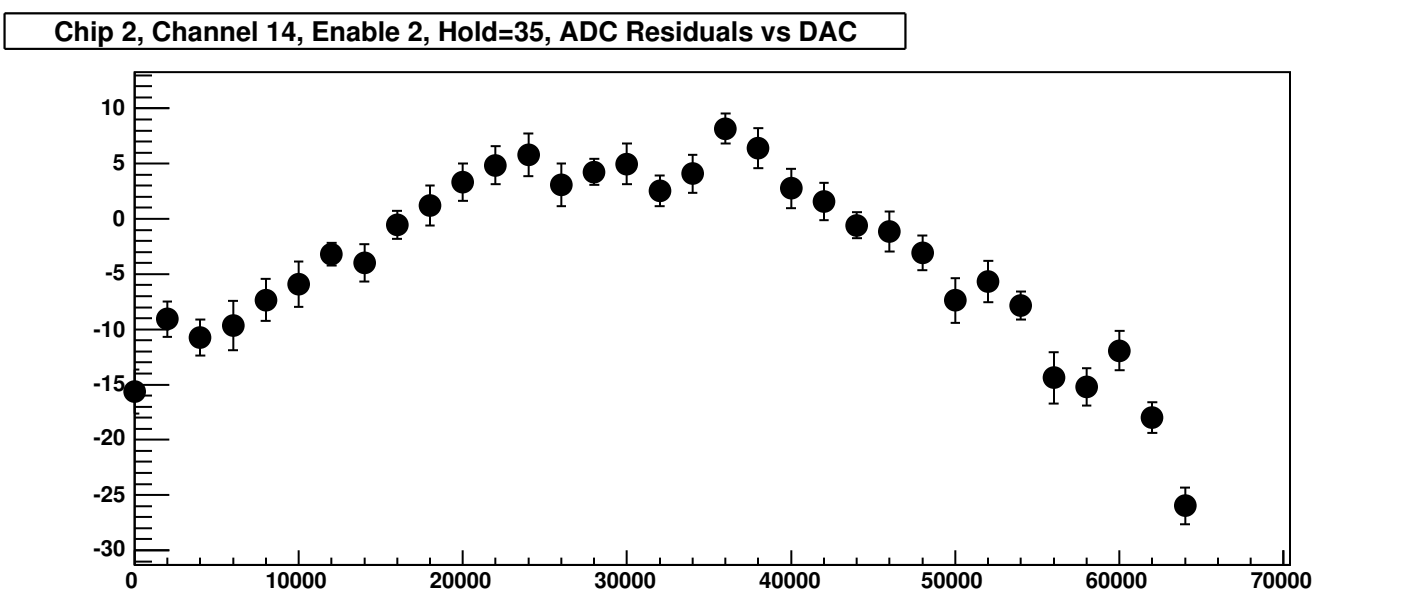

Chip 2, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 8960 / 23
p0 707.7 ± 1.031
p1 0.5027 ± 4.538e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

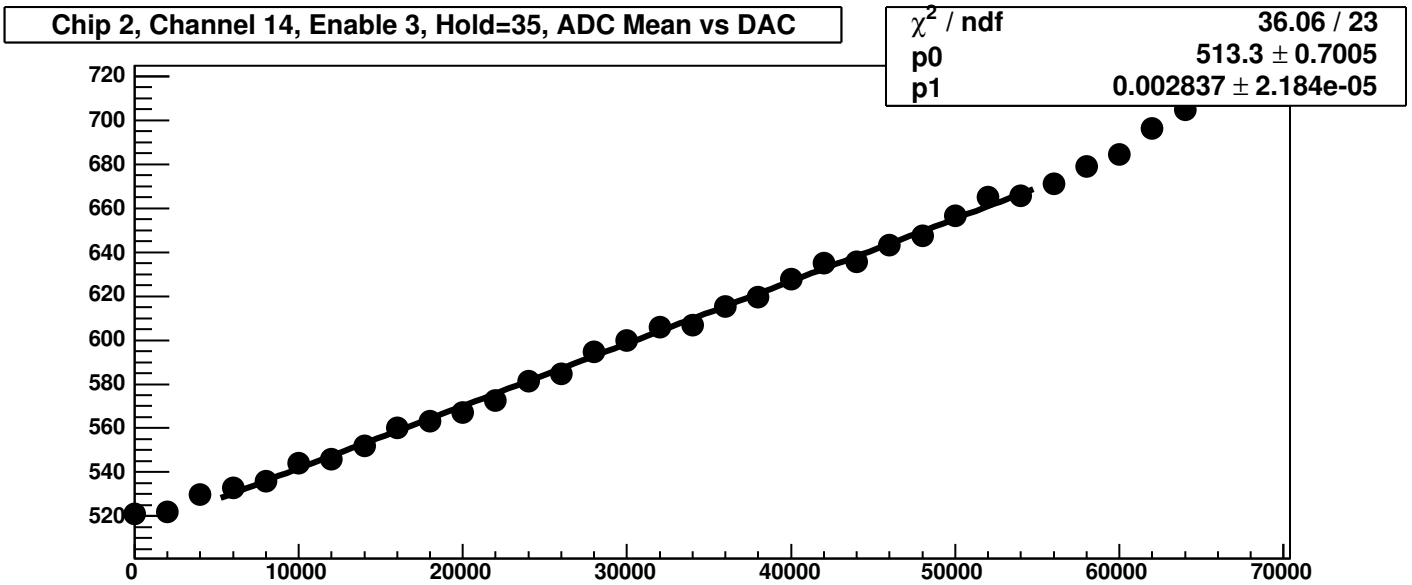

Chip 2, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000

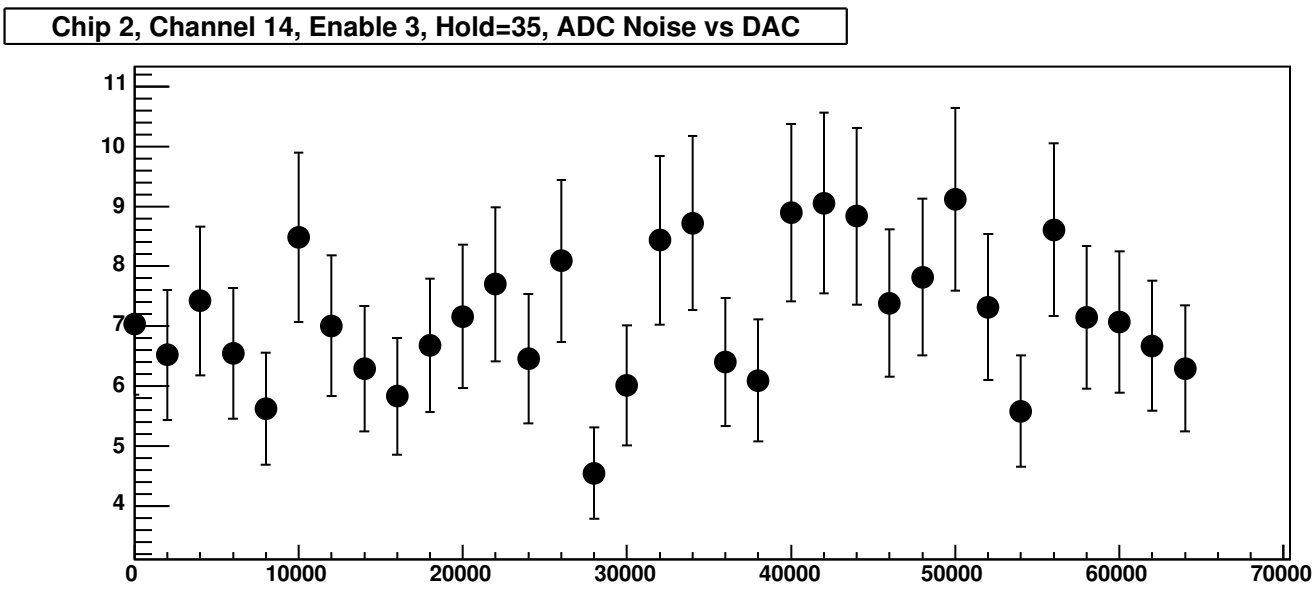

Chip 2, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
0
10000
20000
30000
40000
50000
60000
70000

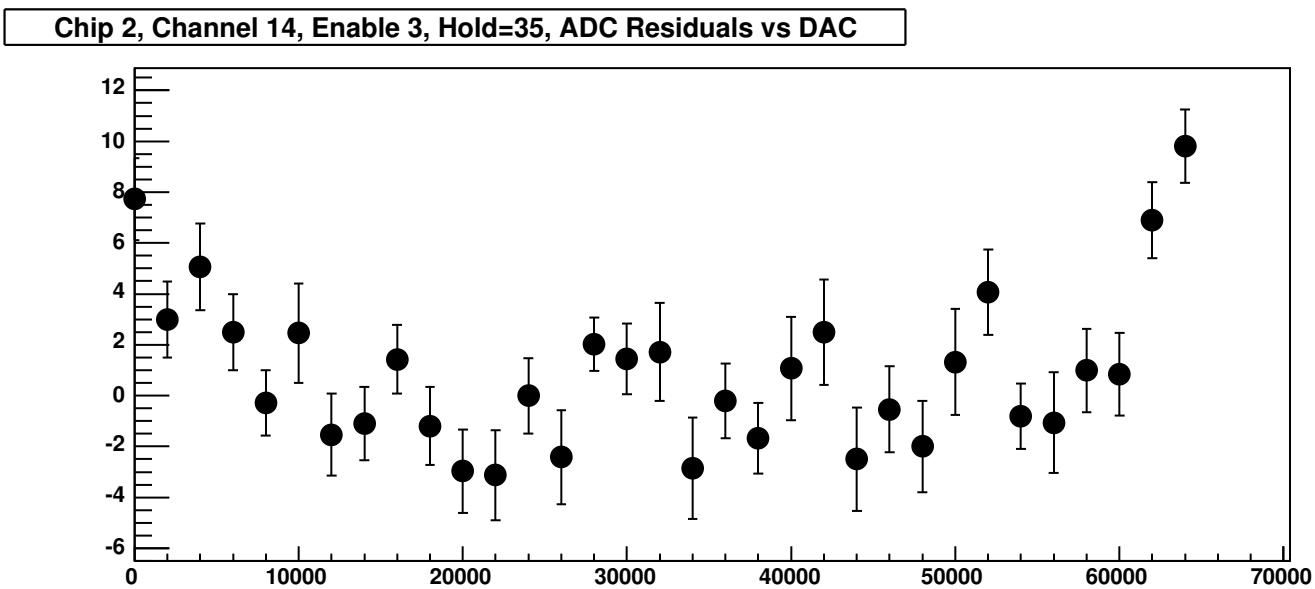

Chip 2, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 226.6 / 23
p0 532.3 ± 0.7365
p1 -0.00863 ± 2.206e-05
500
400
300
200
100
0
-100
0
10000
20000
30000
40000
50000
60000
70000

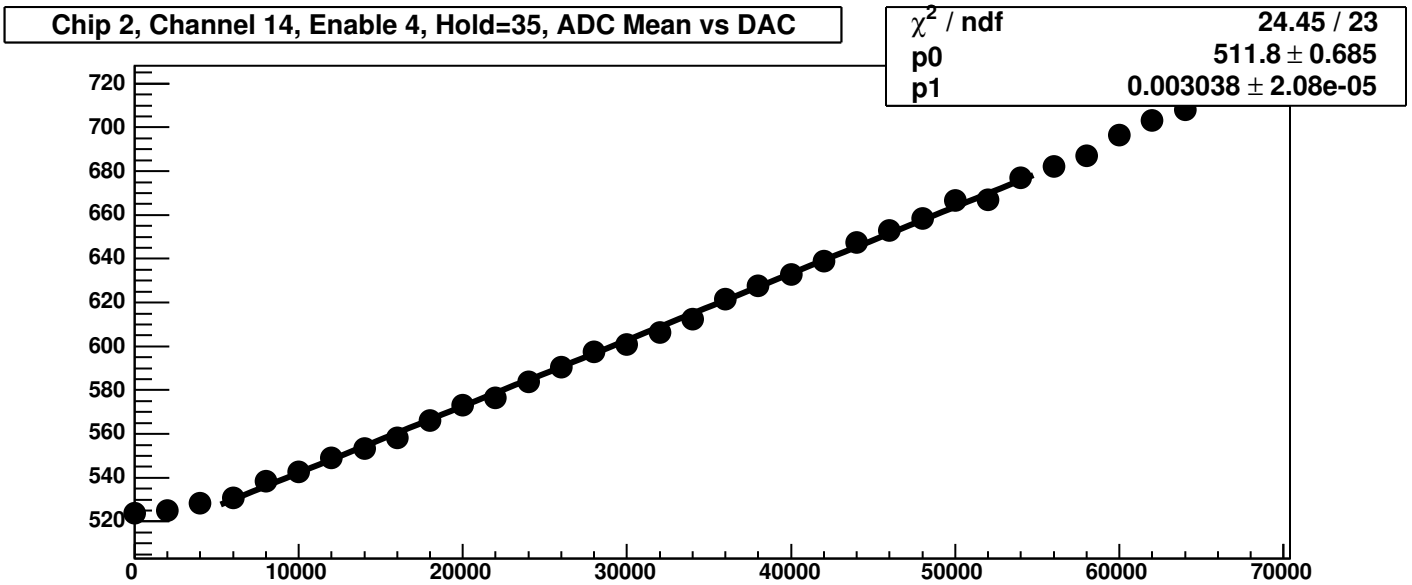

Chip 2, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

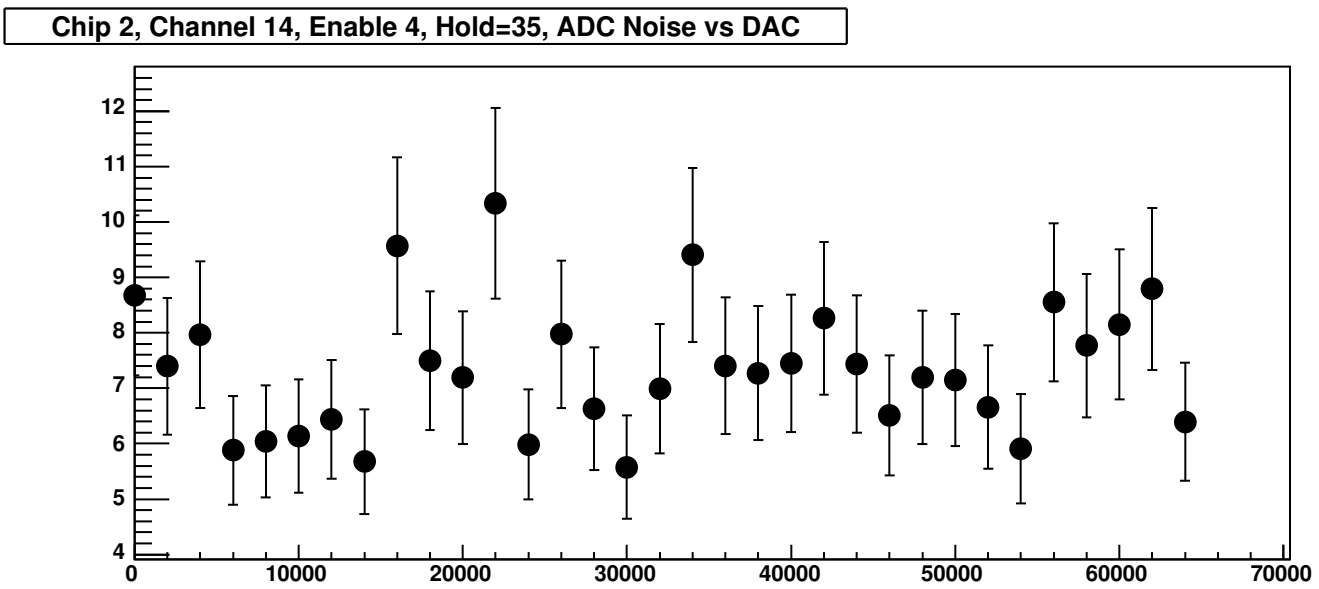

Chip 2, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
-30
0
10000
20000
30000
40000
50000
60000
70000

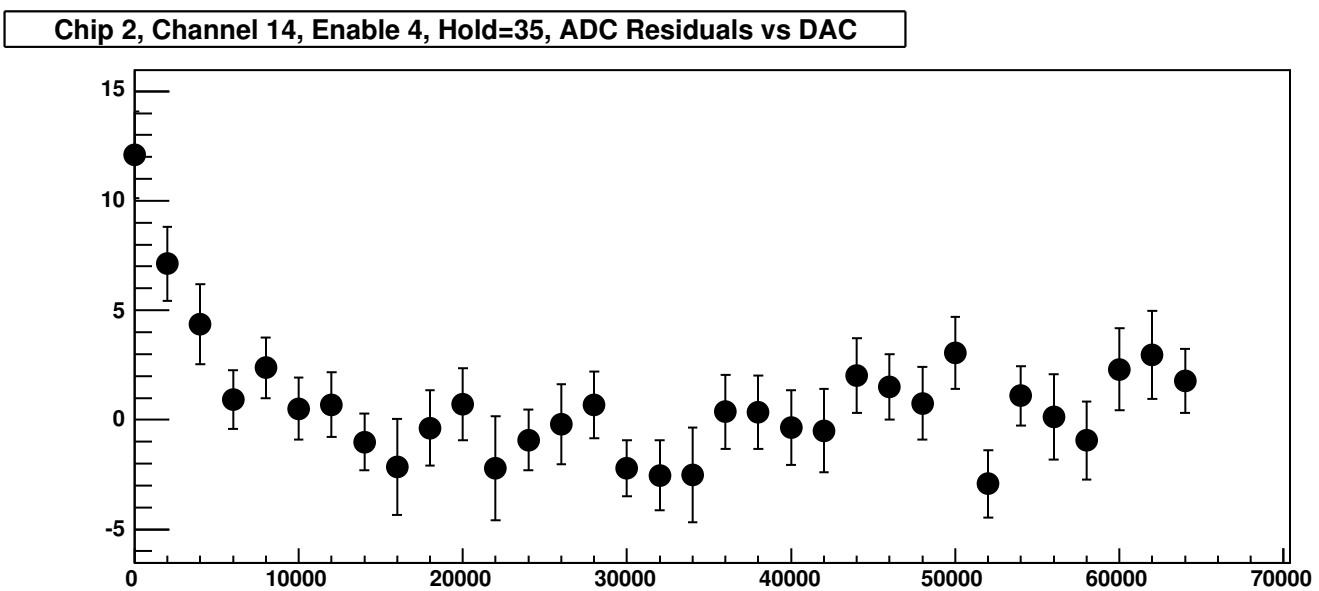

Chip 2, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 36.06 / 23
p0 513.3 ± 0.7005
p1 0.002837 ± 2.184e-05
720
700
680
660
640
620
600
580
560
540
520
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

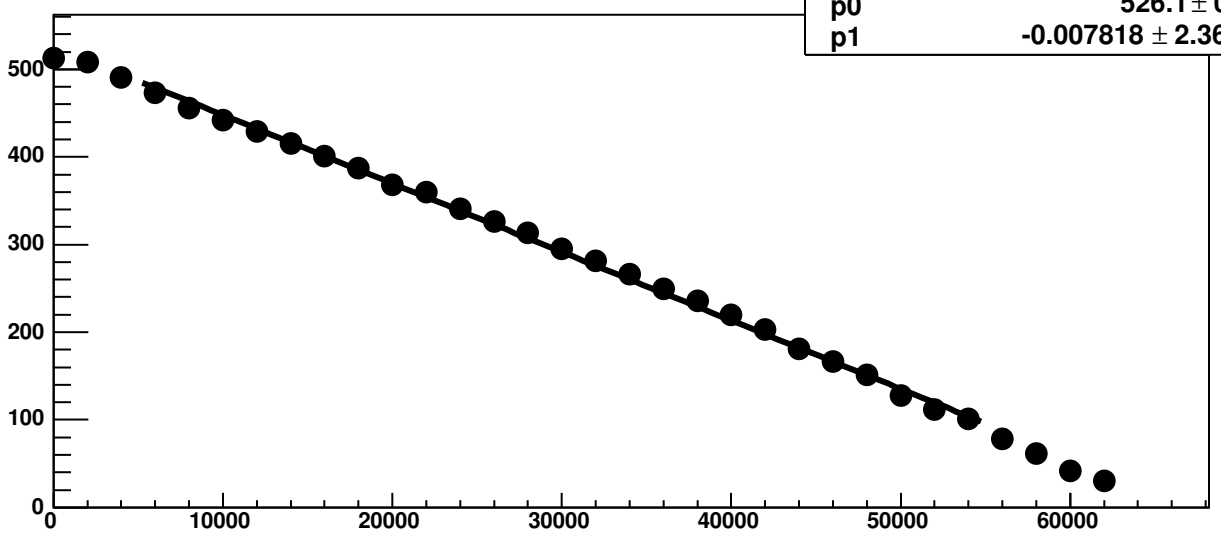

Chip 2, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.45 / 23
p0 511.8 ± 0.685
p1 0.003038 ± 2.08e-05
720
700
680
660
640
620
600
580
560
540
520
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

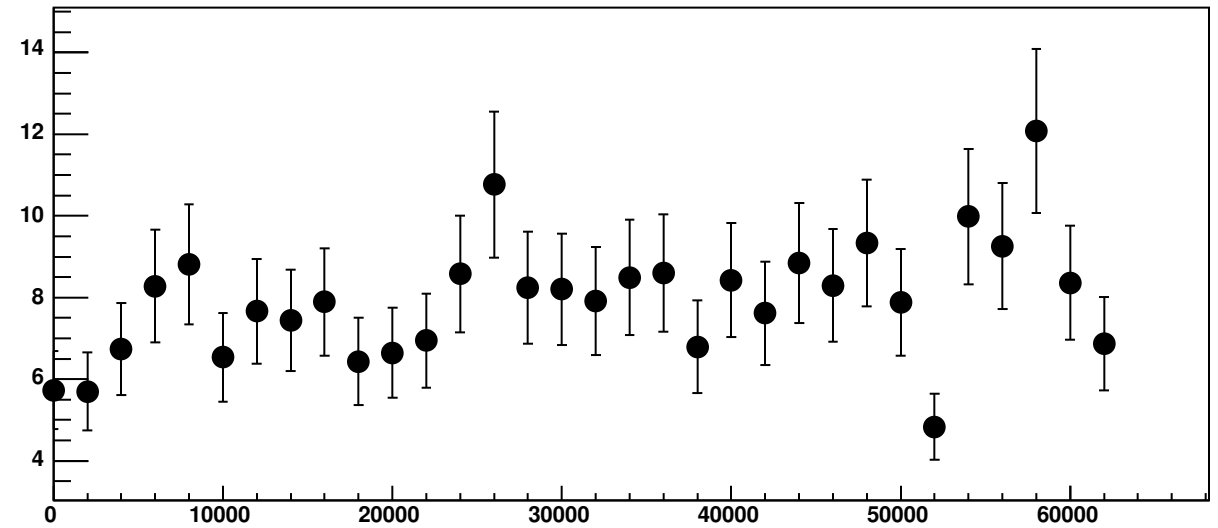

Chip 2, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

Chip 2, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC

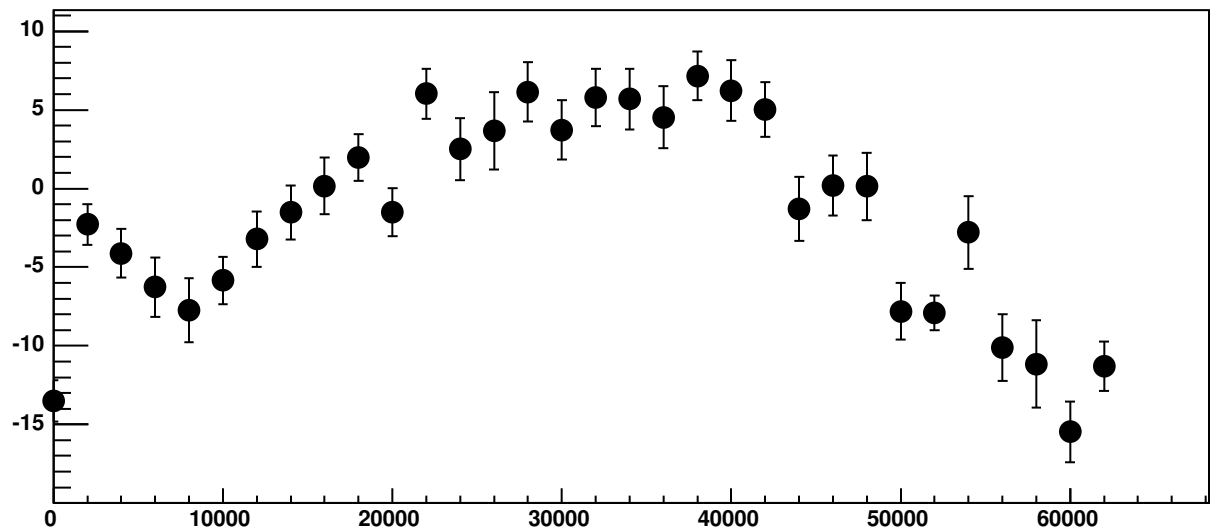
χ^2 / ndf	215.4 / 23
p0	526.1 ± 0.7954
p1	-0.007818 ± 2.366e-05

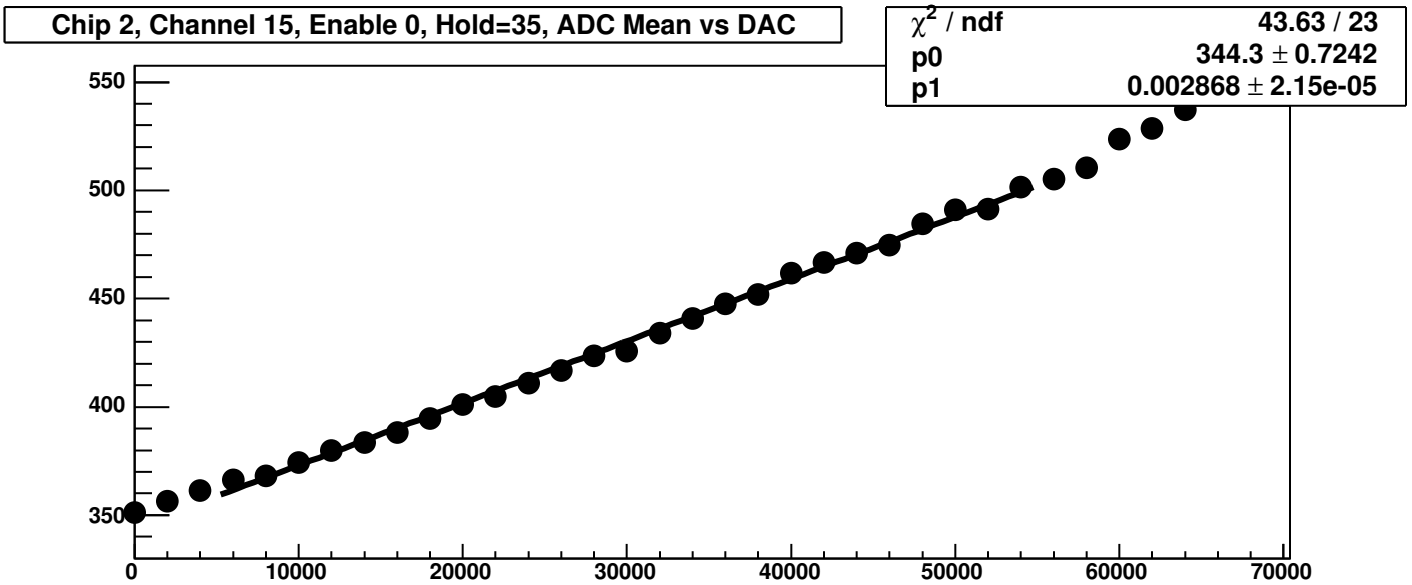


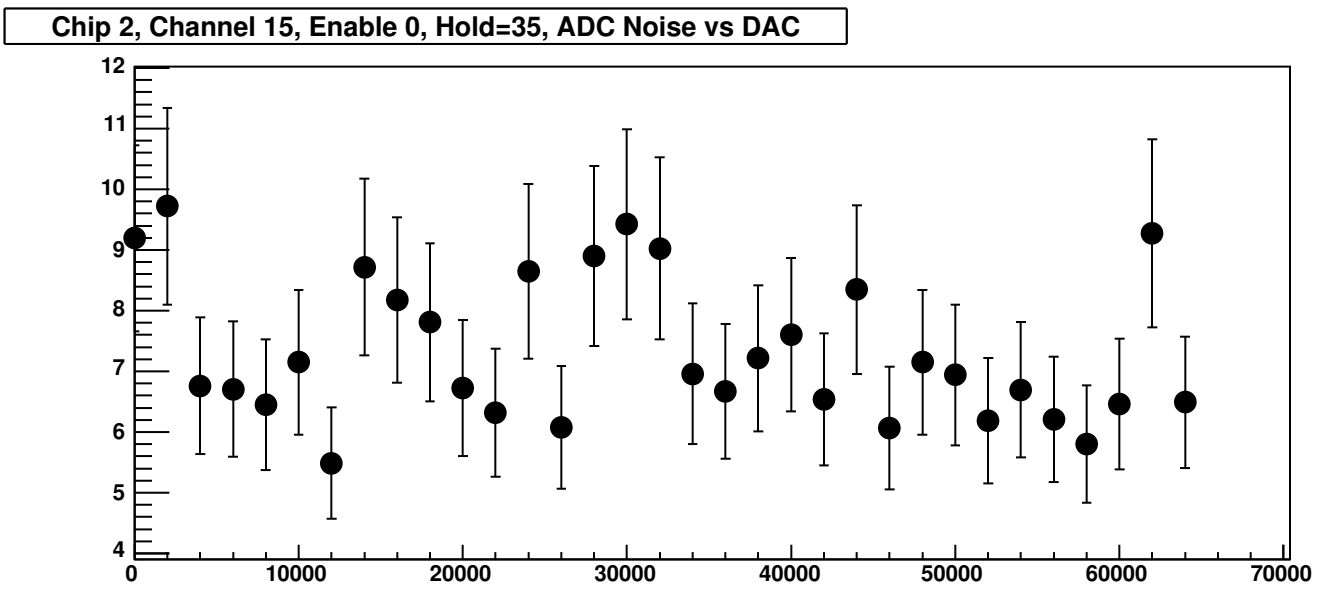
Chip 2, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC

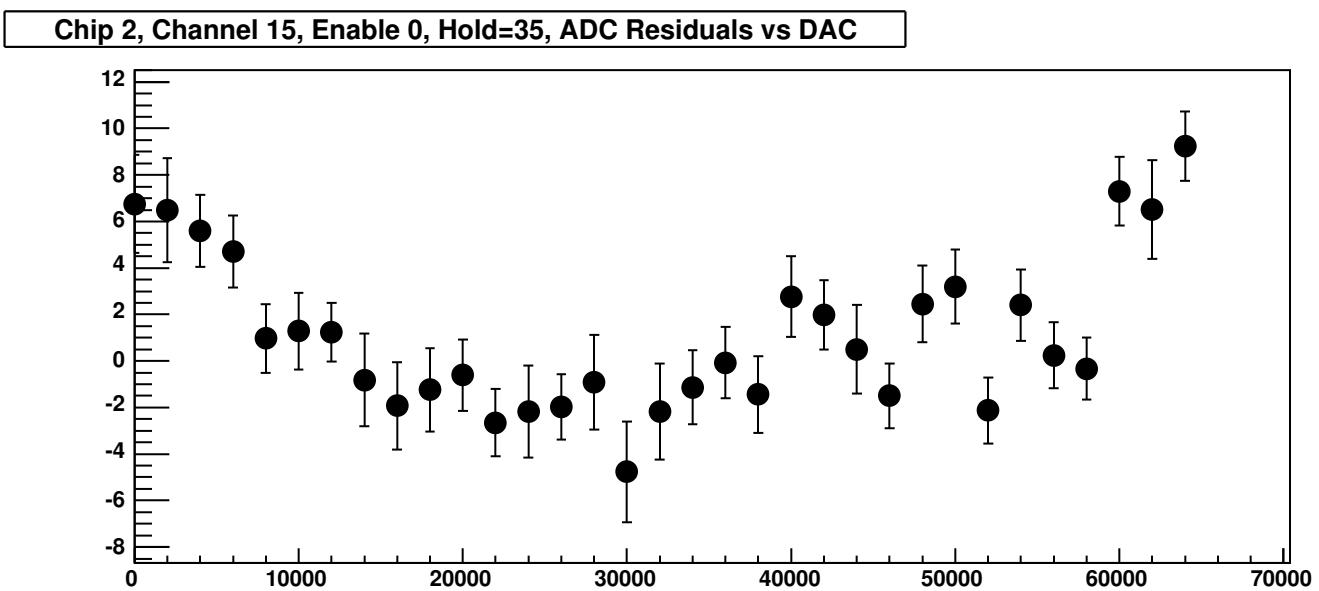


Chip 2, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC

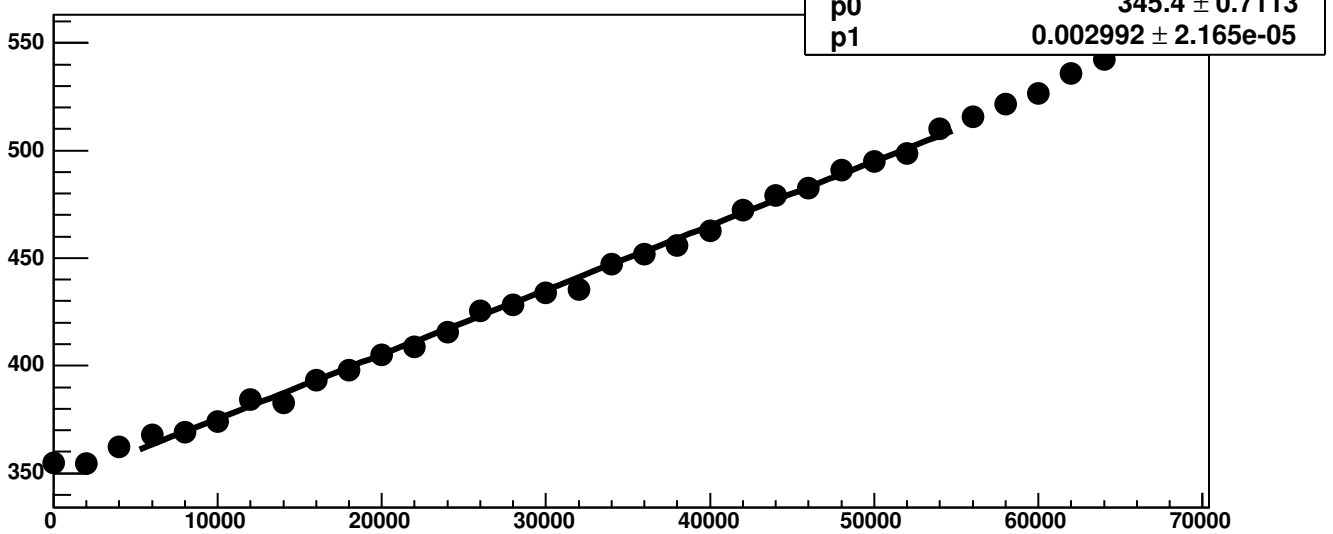



Chip 2, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 43.63 / 23
p0 344.3 ± 0.7242
p1 0.002868 ± 2.15e-05
550
500
450
400
350
0
10000
20000
30000
40000
50000
60000
70000

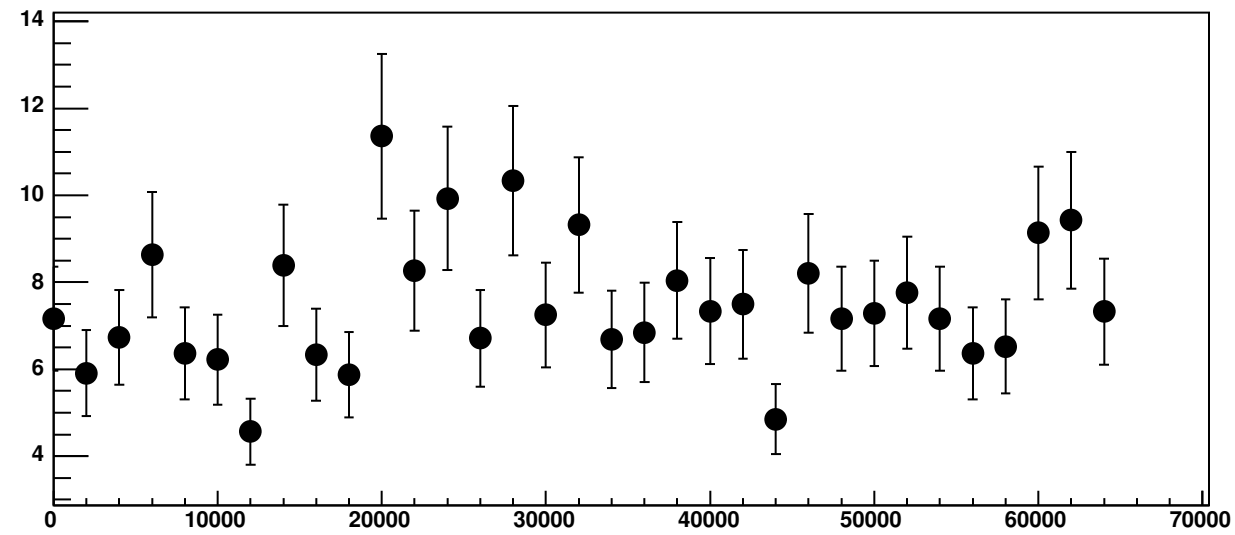

Chip 2, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

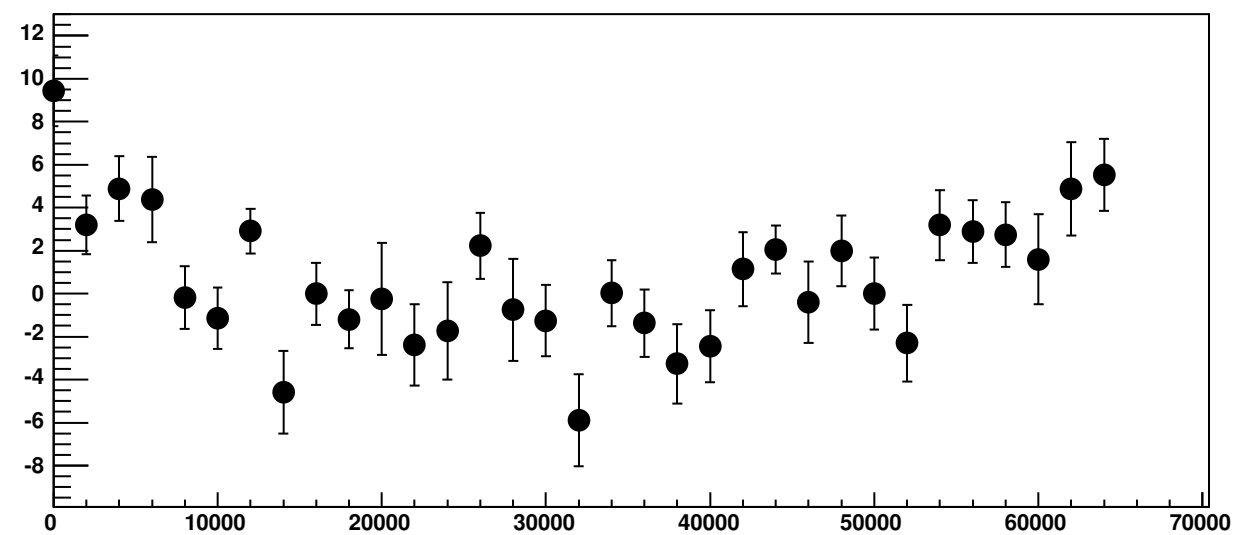
Chip 2, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

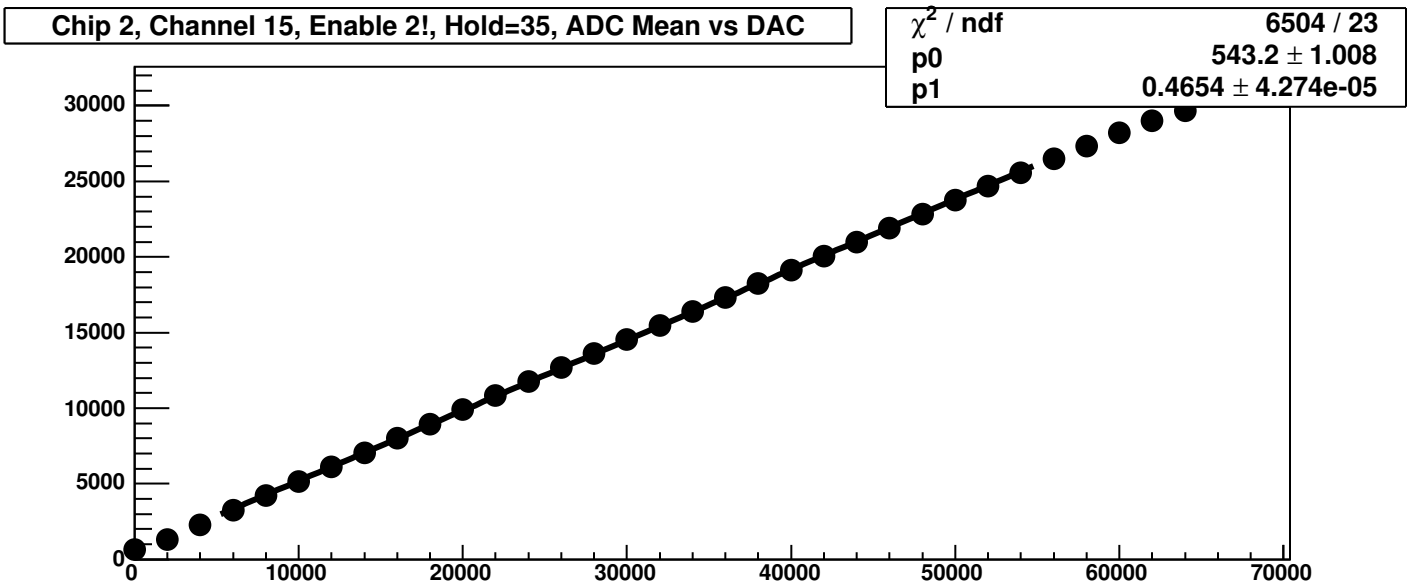


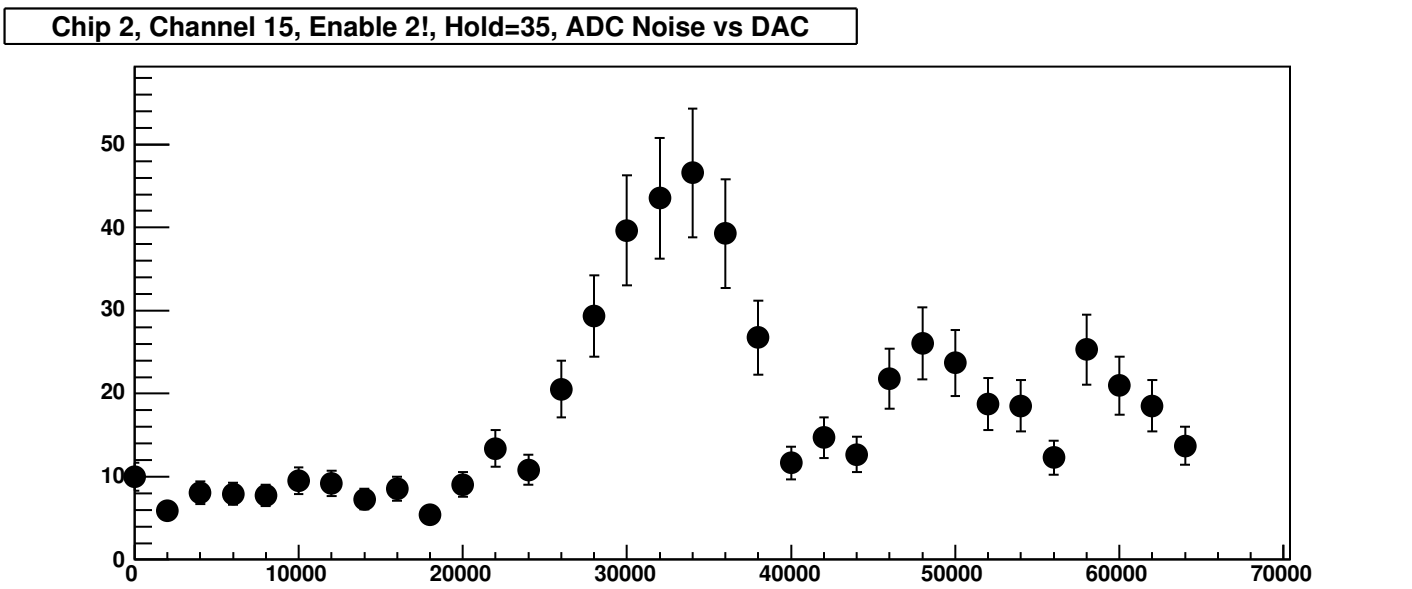
Chip 2, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

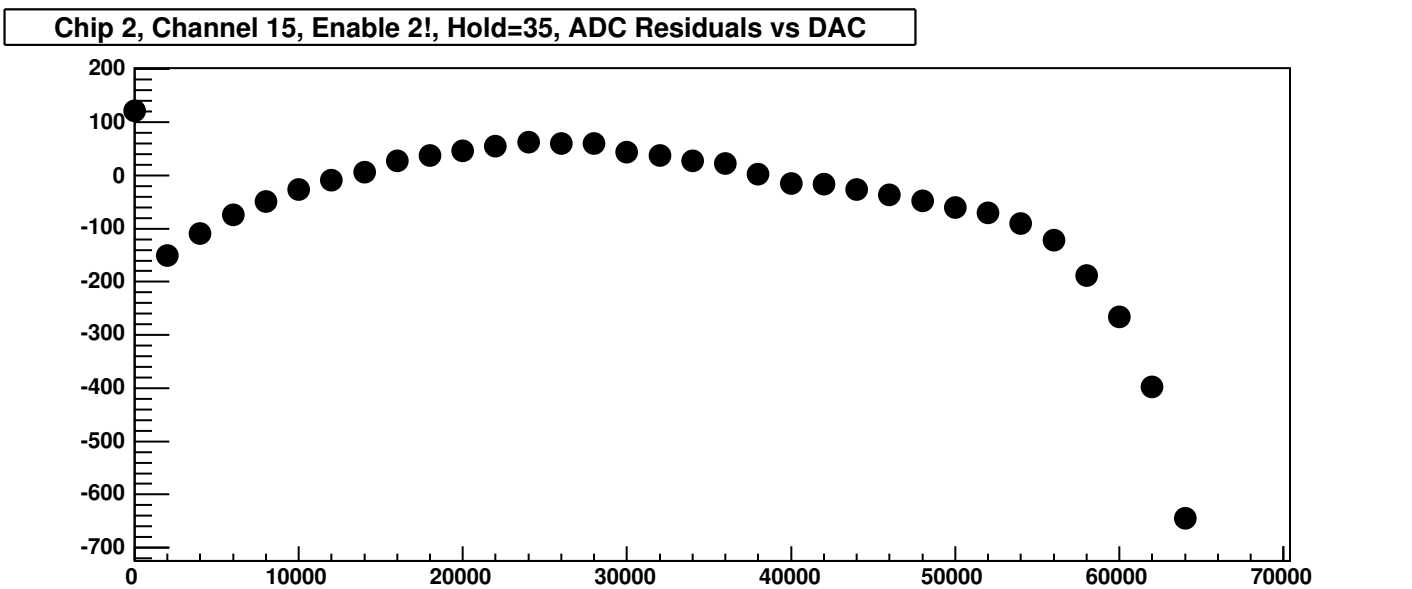


Chip 2, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

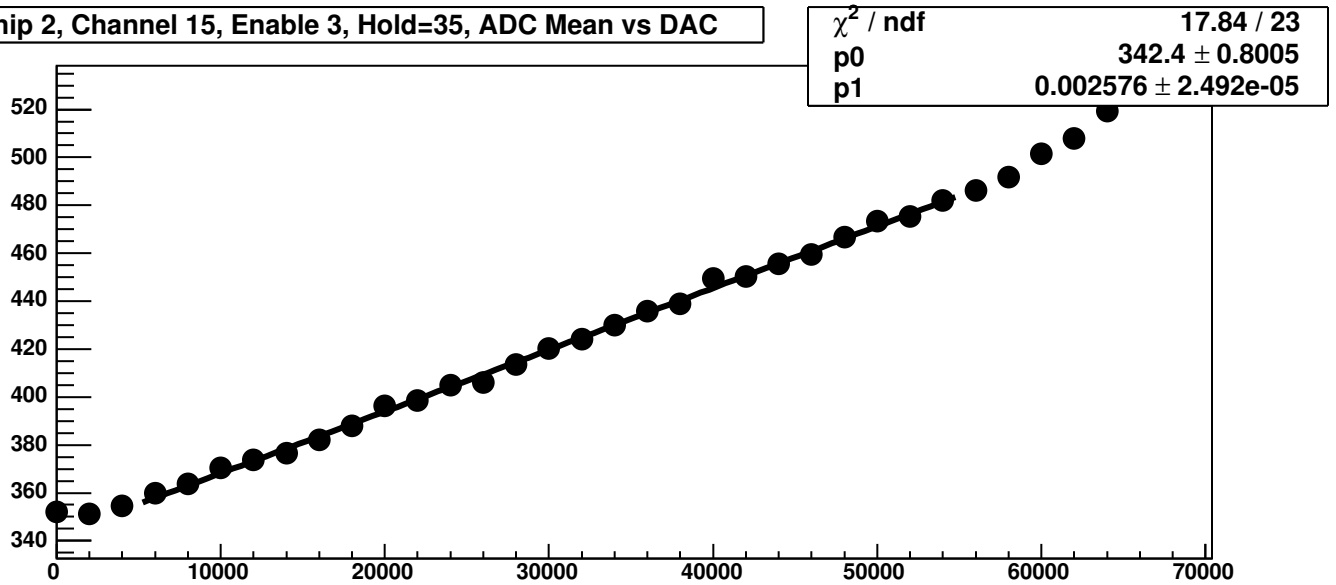



Chip 2, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 6504 / 23
p0 543.2 ± 1.008
p1 0.4654 ± 4.274e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

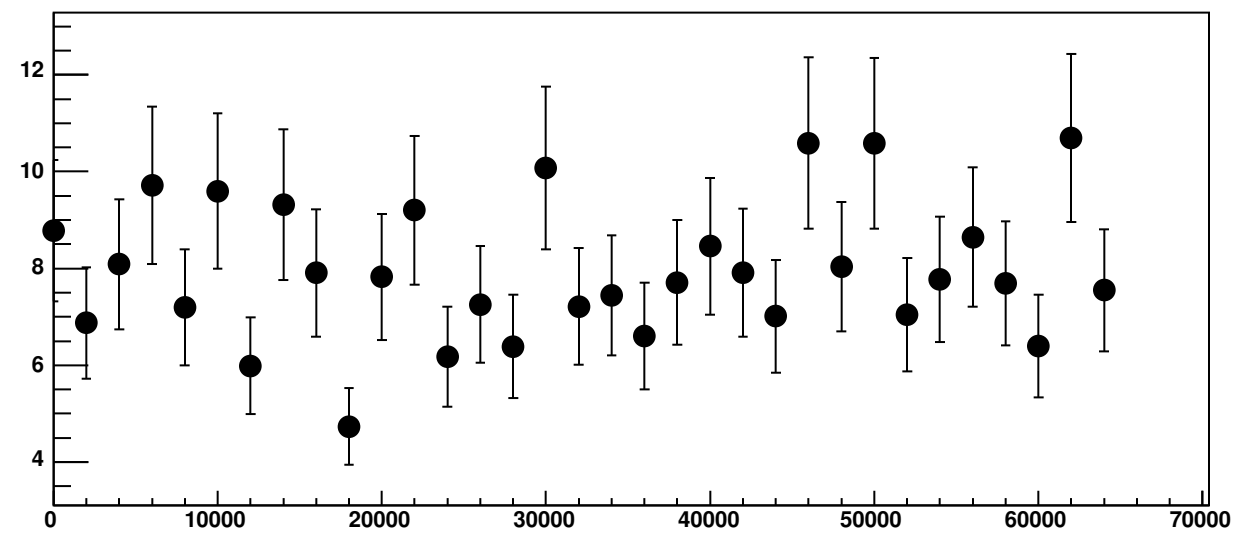

Chip 2, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
200
100
0
-100
-200
-300
-400
-500
-600
-700
0
10000
20000
30000
40000
50000
60000
70000

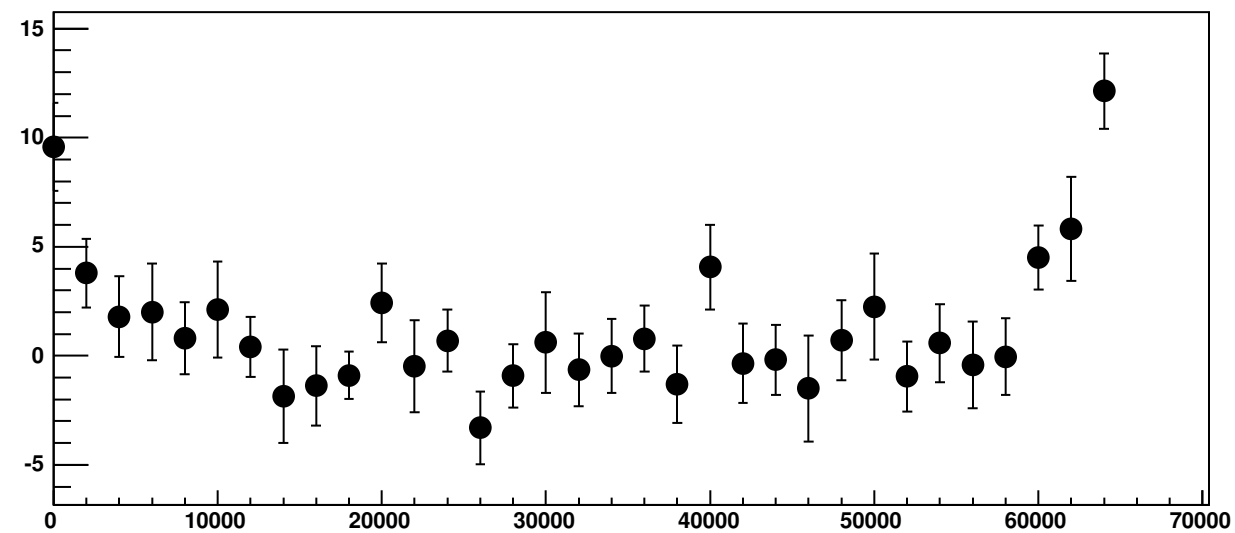
Chip 2, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC

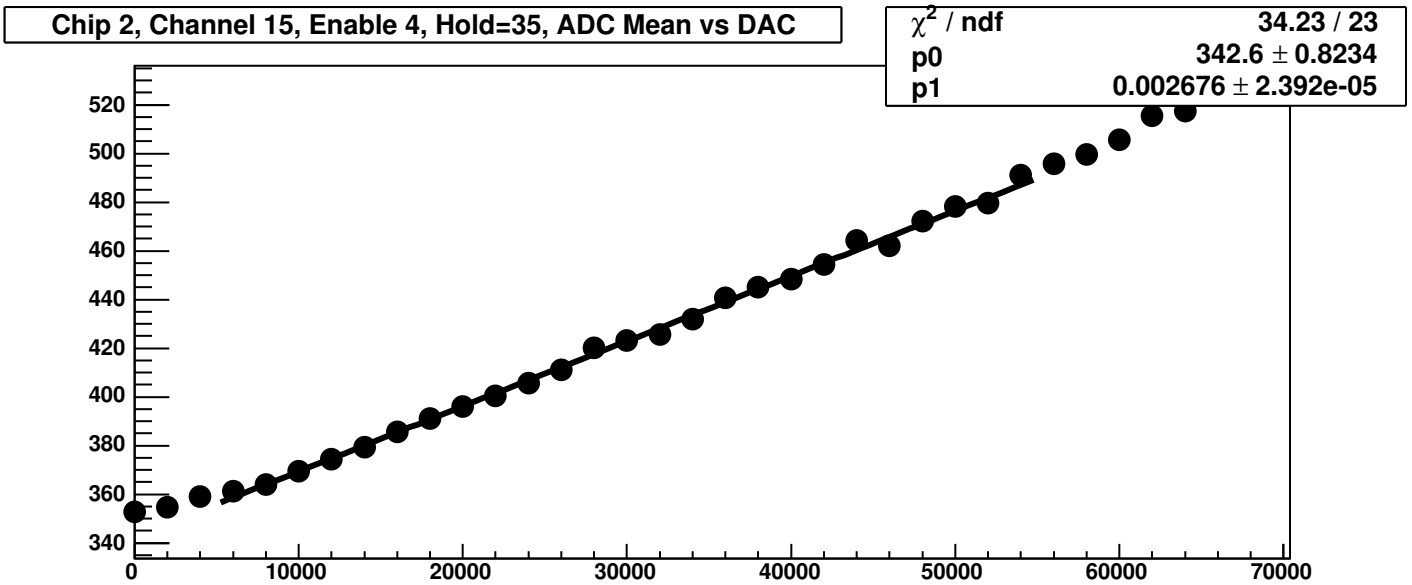


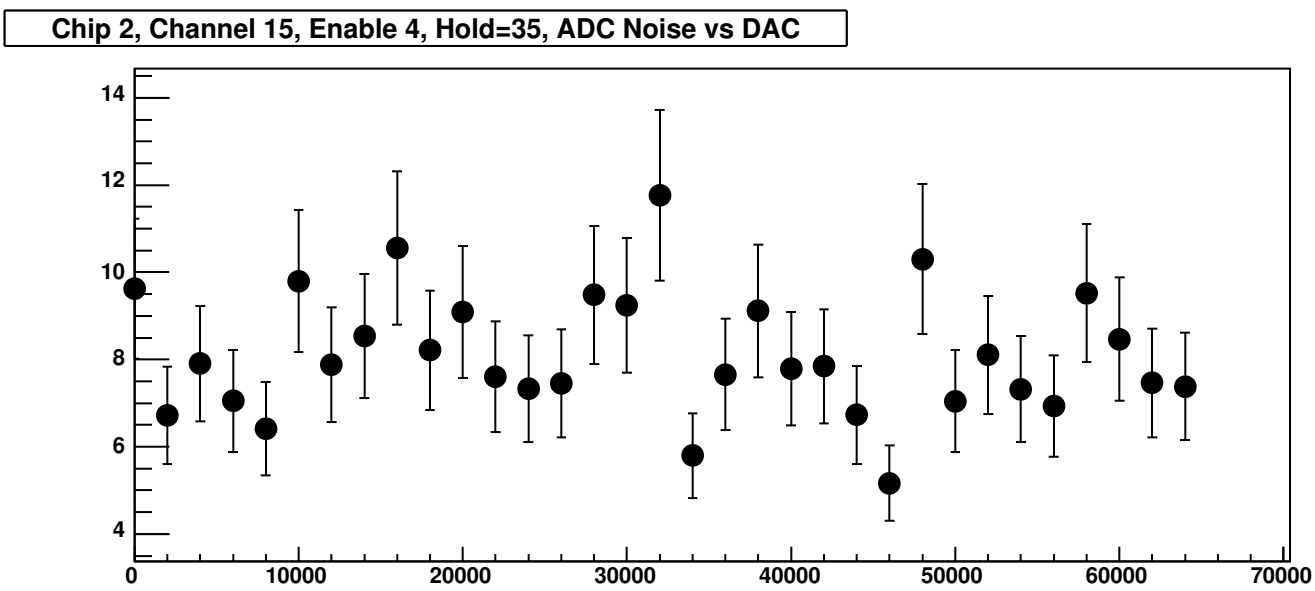
Chip 2, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC

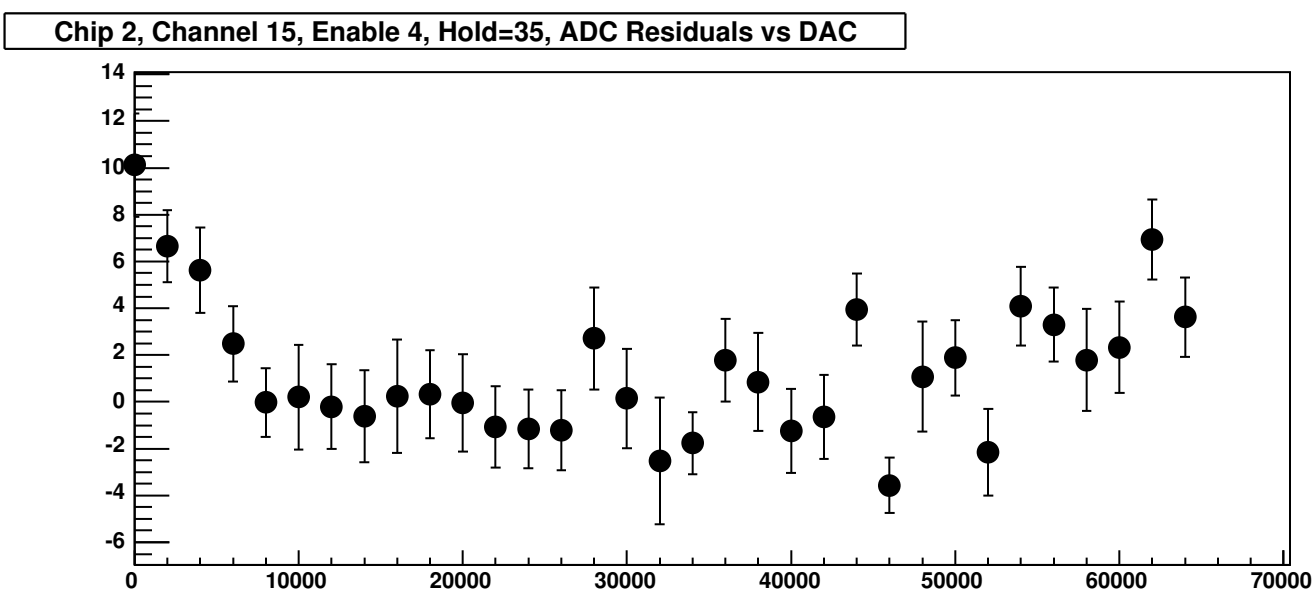


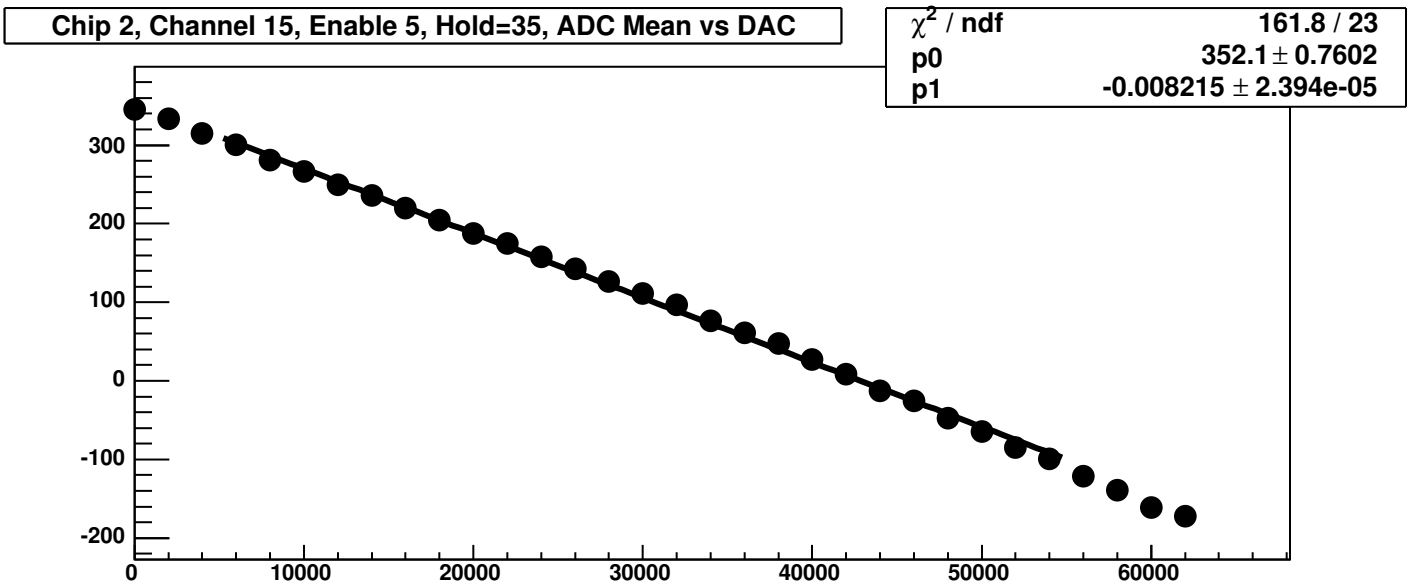
Chip 2, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC

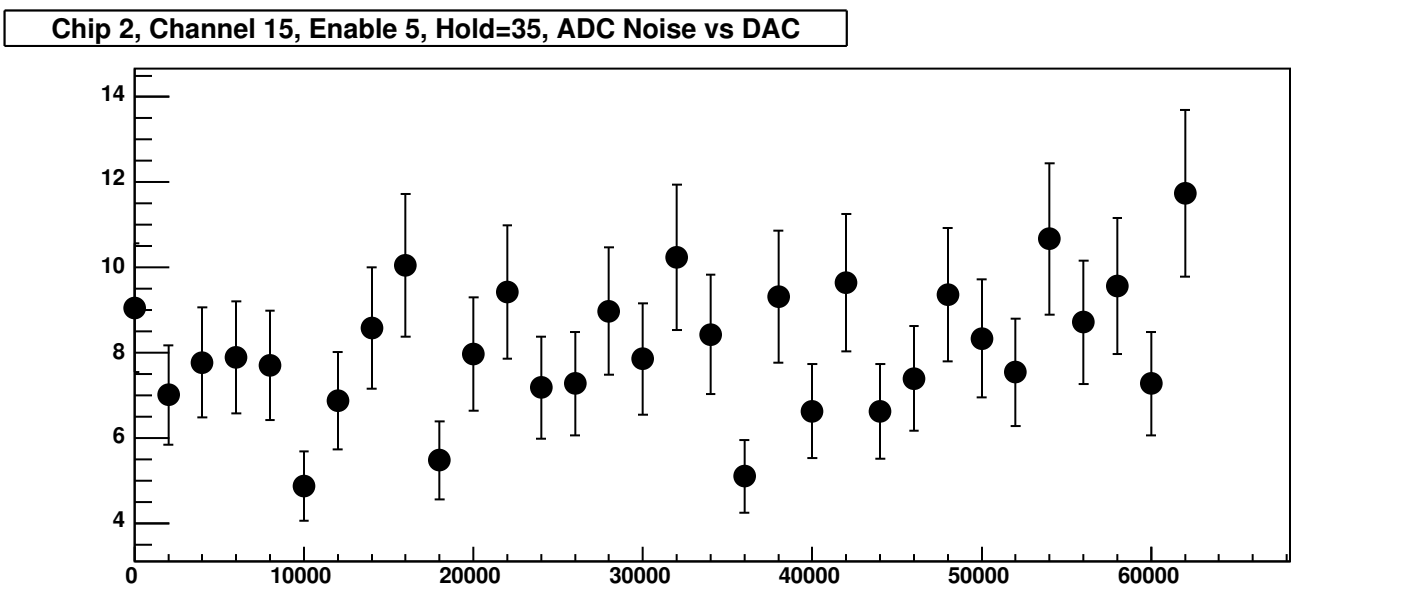


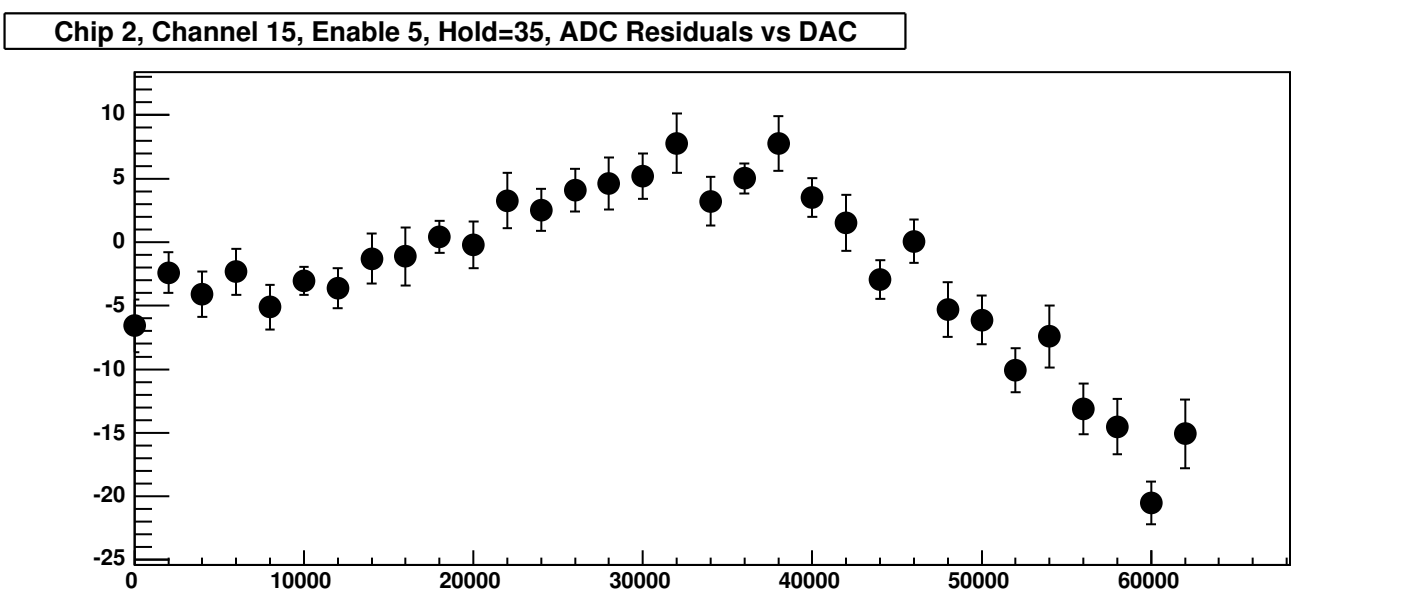

Chip 2, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 34.23 / 23
p0 342.6 ± 0.8234
p1 0.002676 ± 2.392e-05
520
500
480
460
440
420
400
380
360
340
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

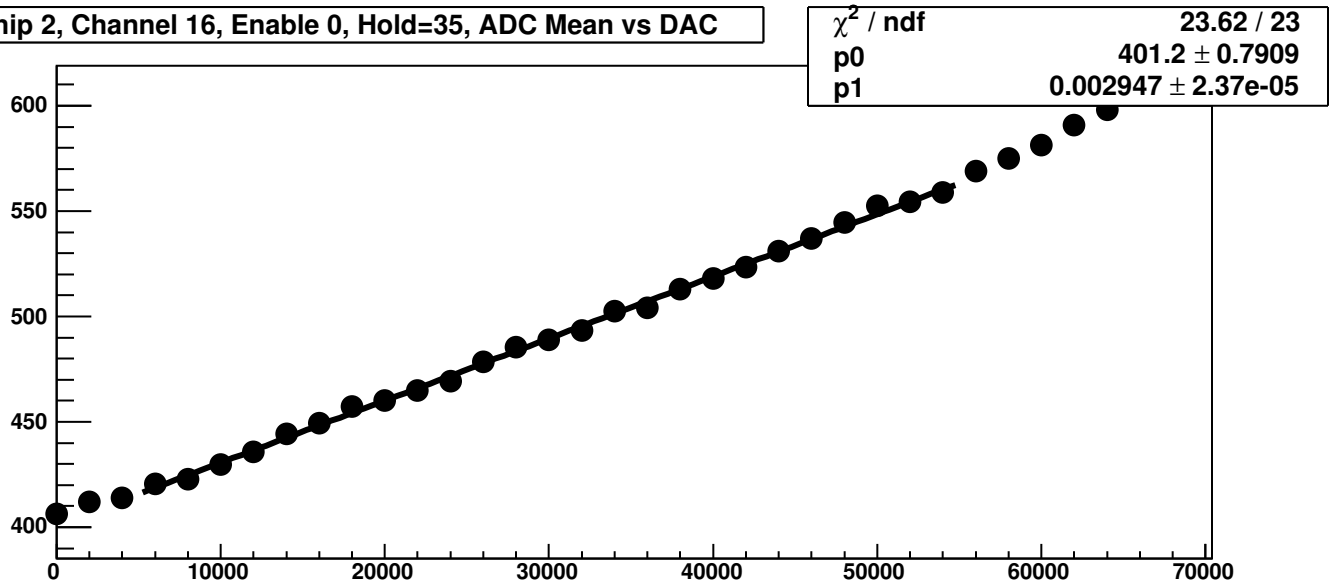

Chip 2, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000


Chip 2, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 161.8 / 23
p0 352.1 ± 0.7602
p1 -0.008215 ± 2.394e-05
300
200
100
0
-100
-200
0
10000
20000
30000
40000
50000
60000

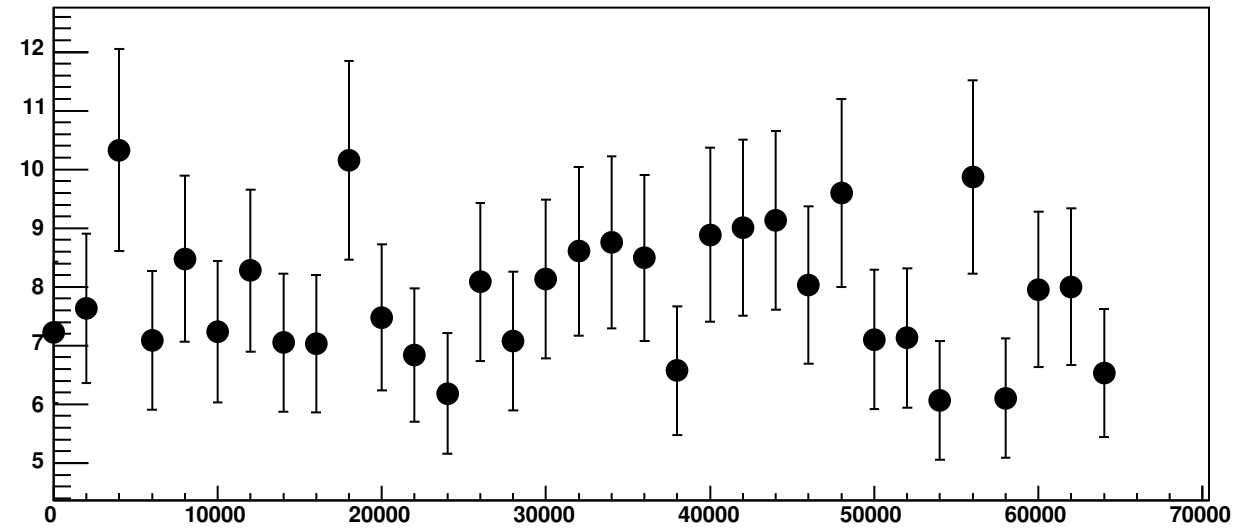

Chip 2, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000


Chip 2, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
10000
20000
30000
40000
50000
60000

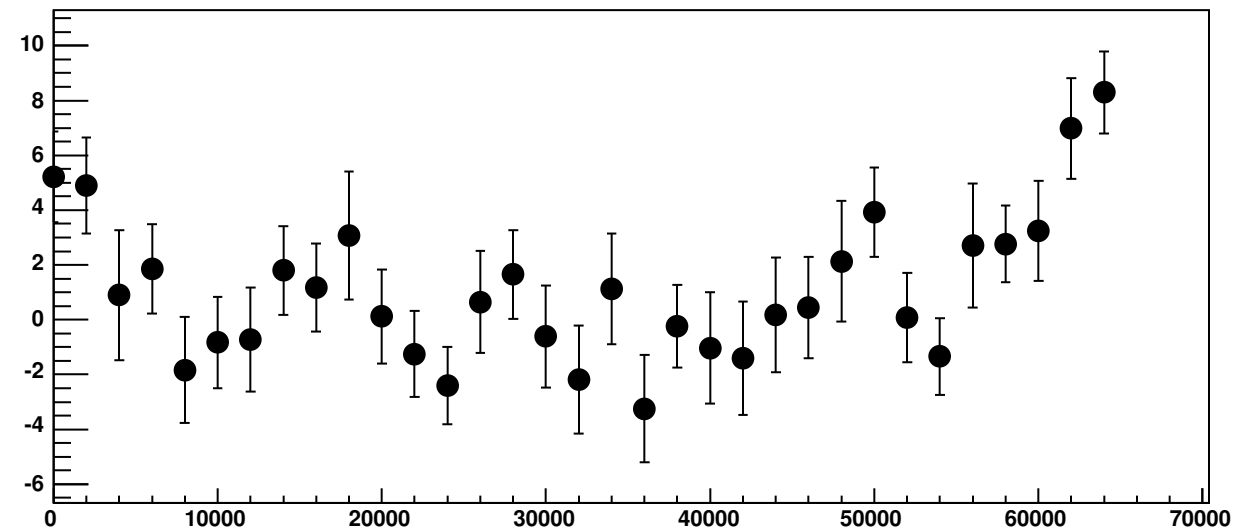
Chip 2, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC



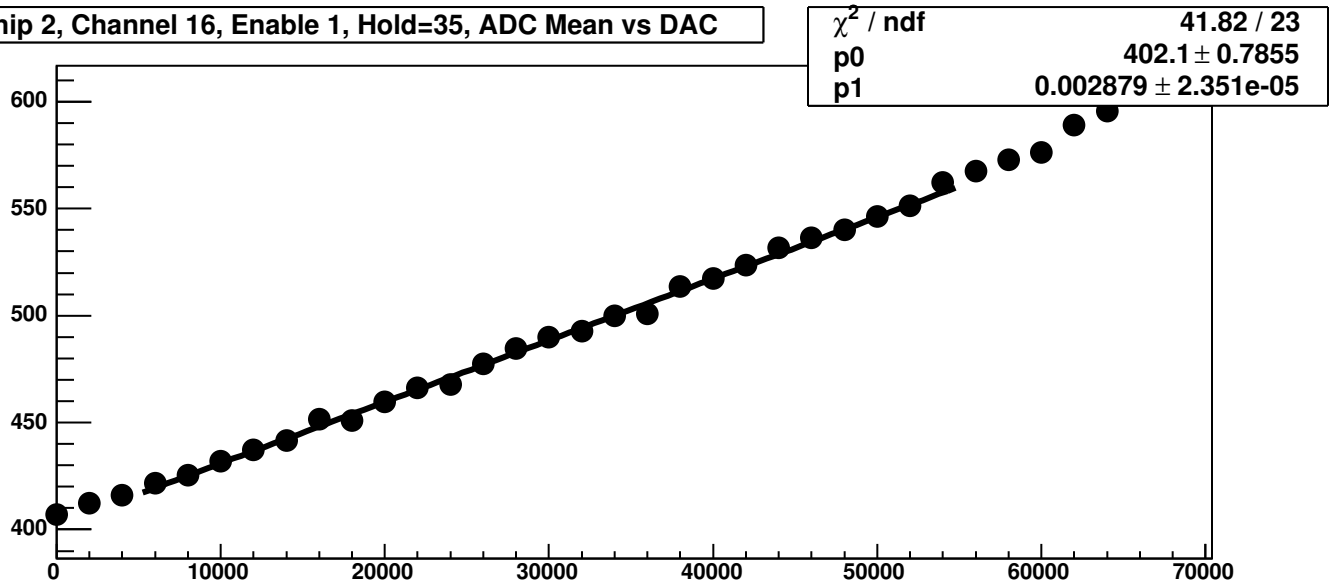
Chip 2, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC



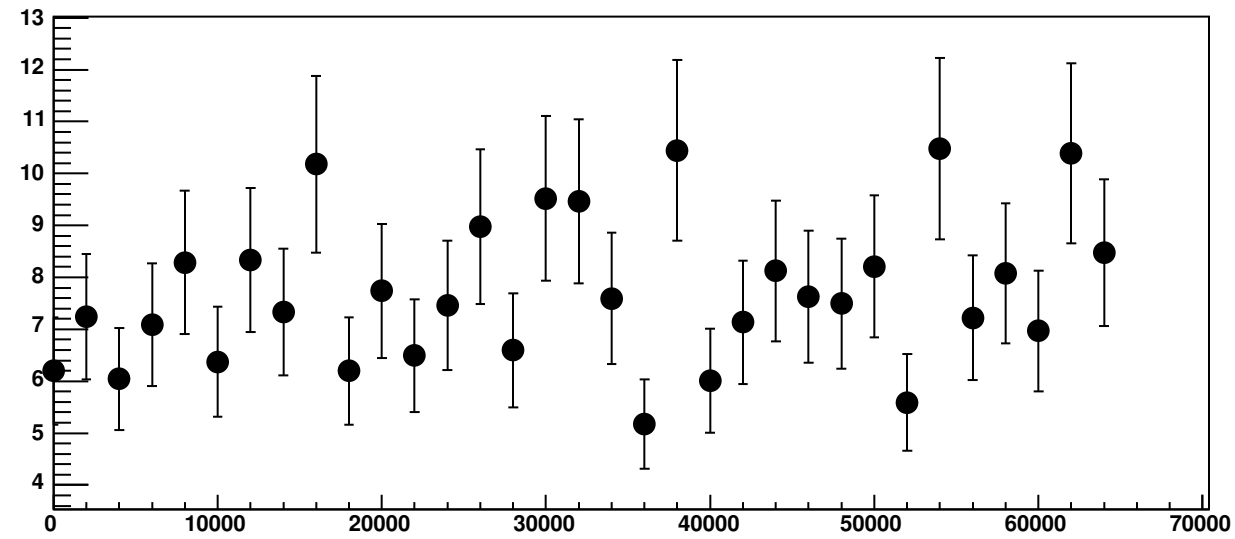
Chip 2, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC



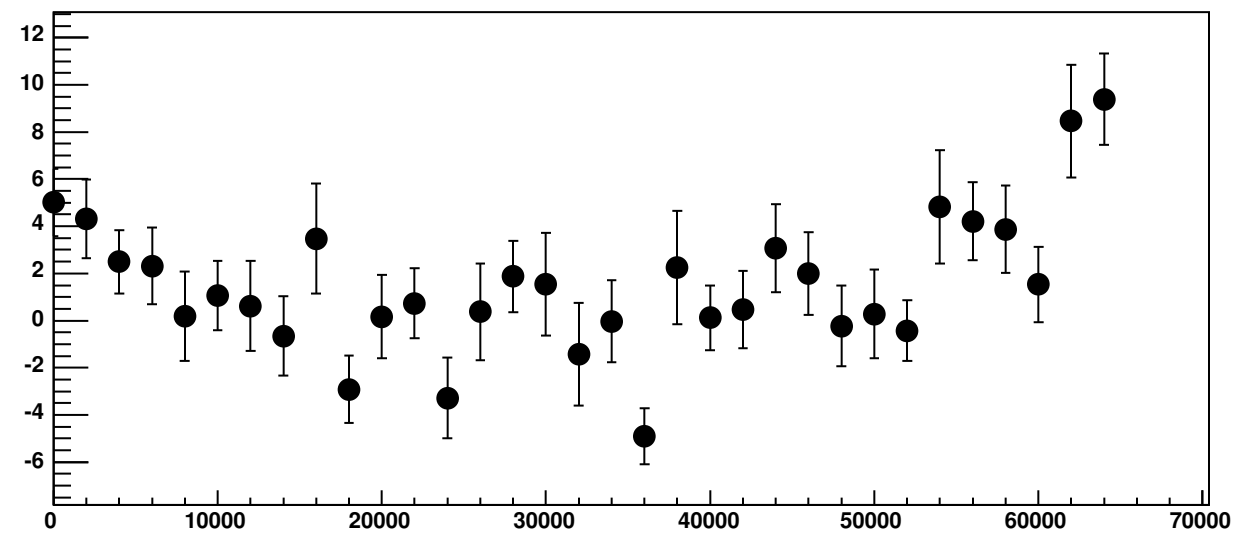
Chip 2, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC



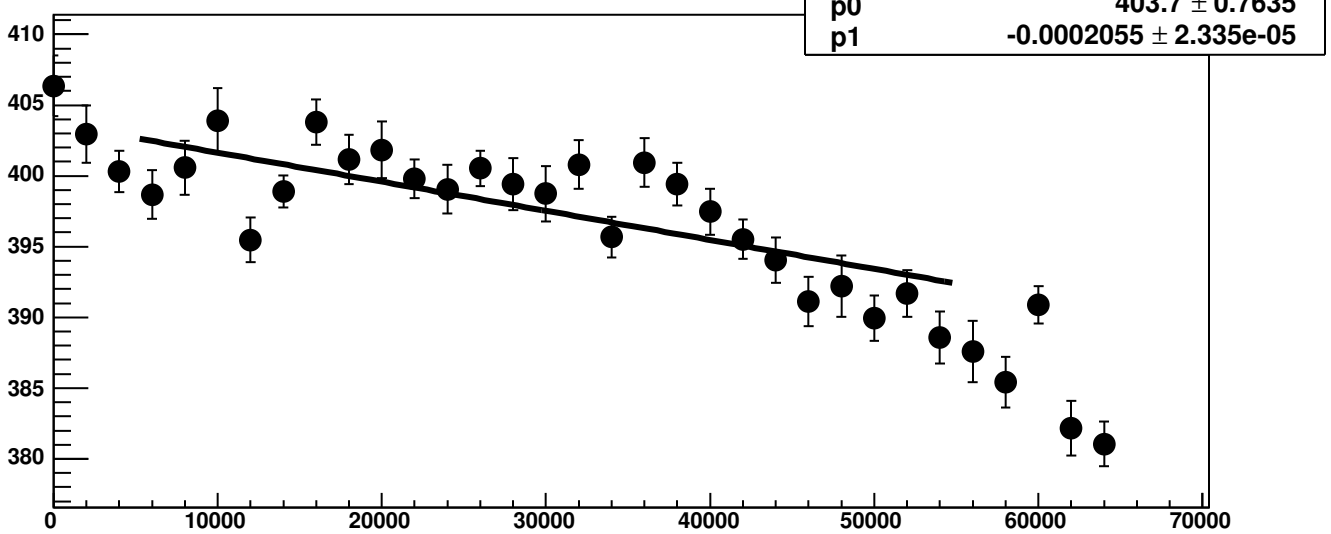
Chip 2, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC



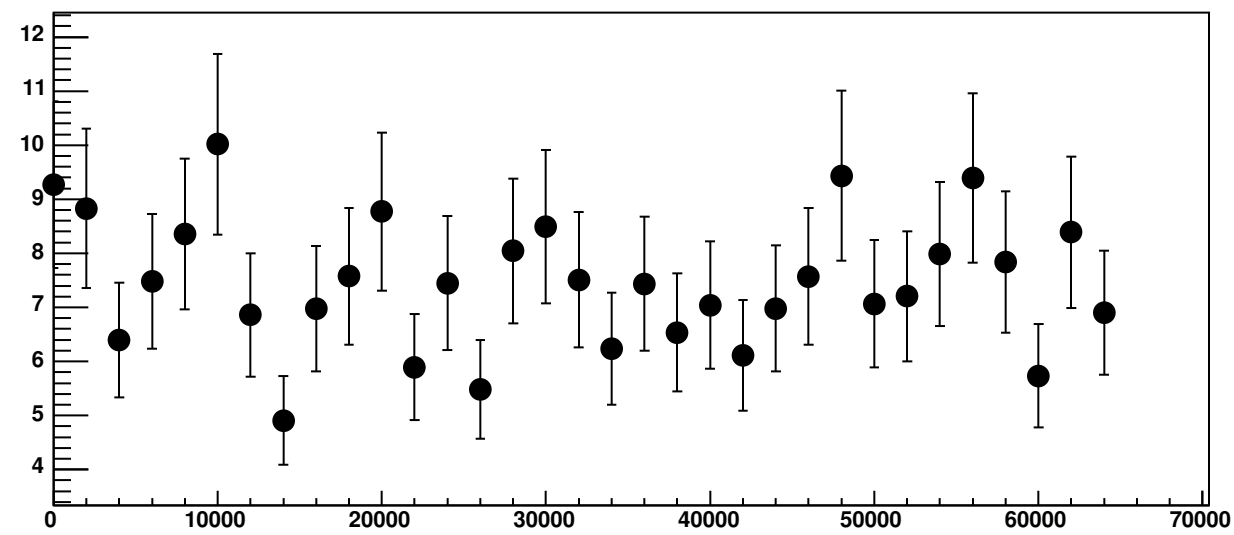
Chip 2, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC



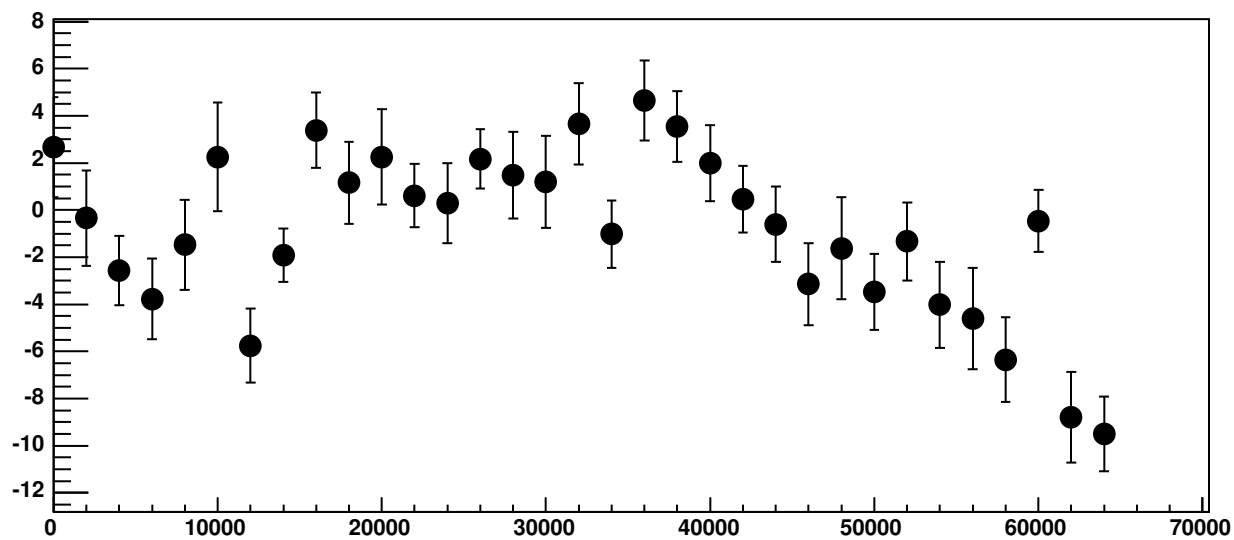
Chip 2, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

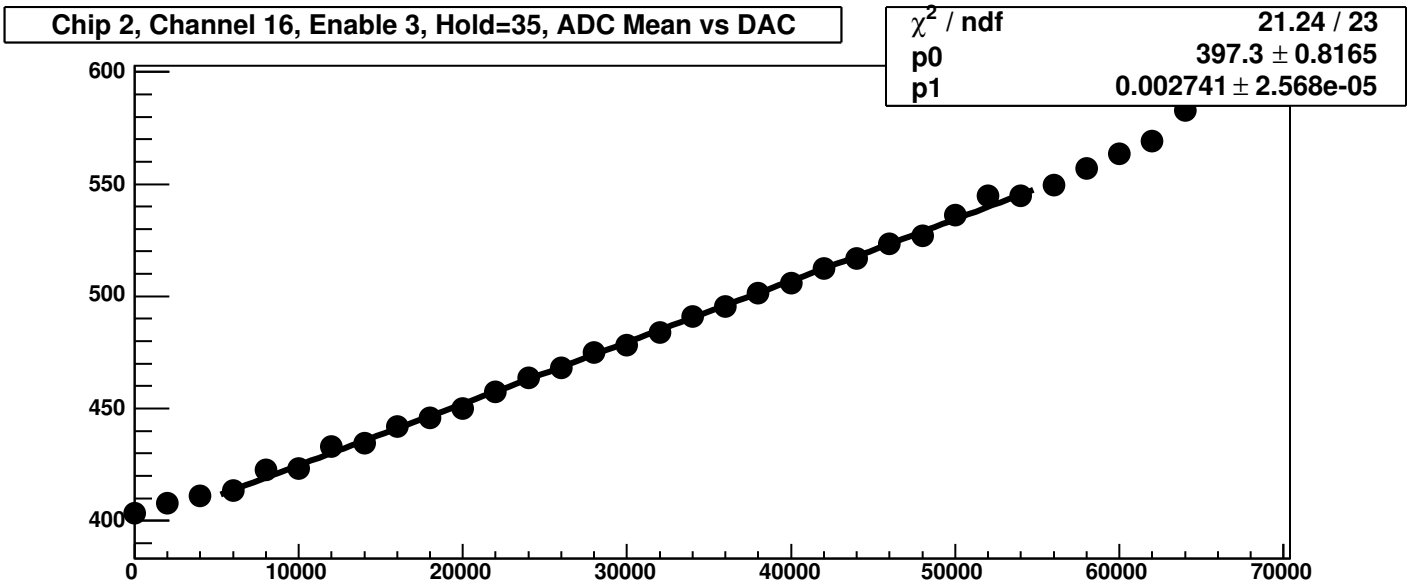


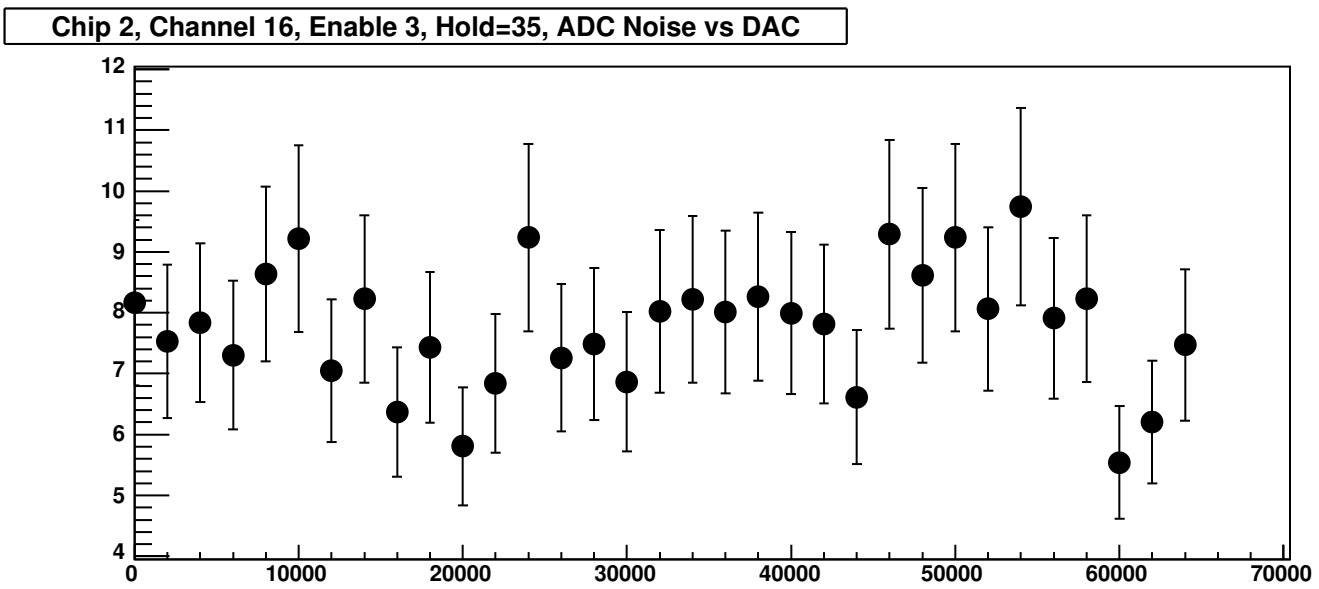
Chip 2, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC

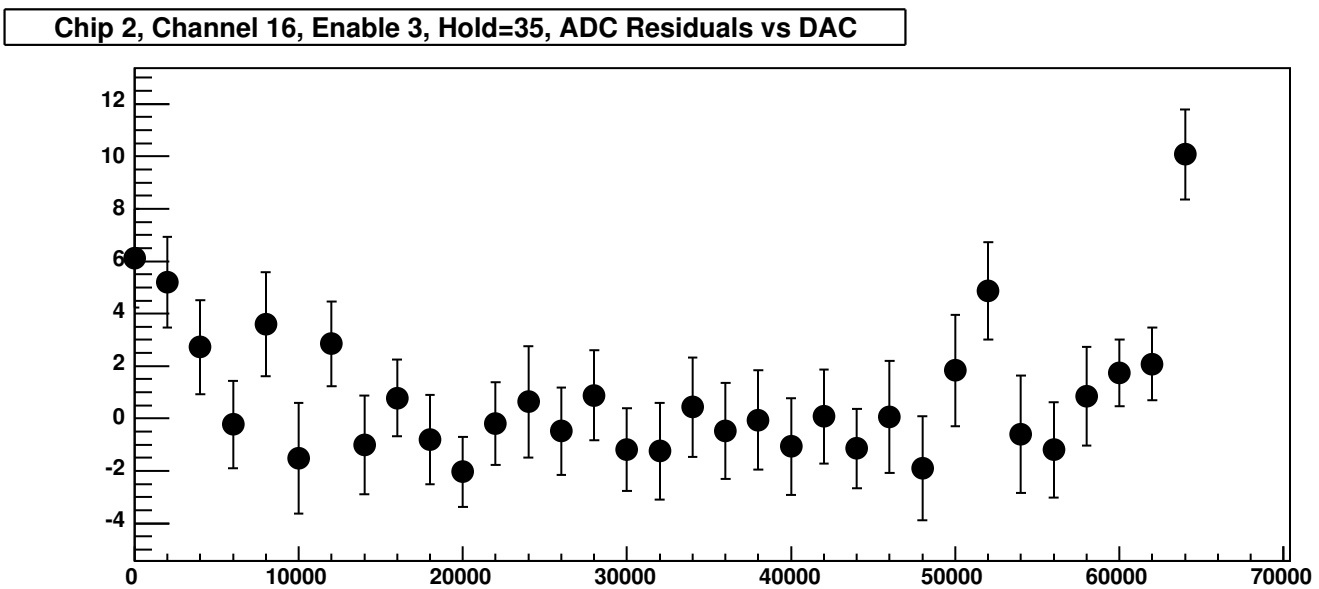


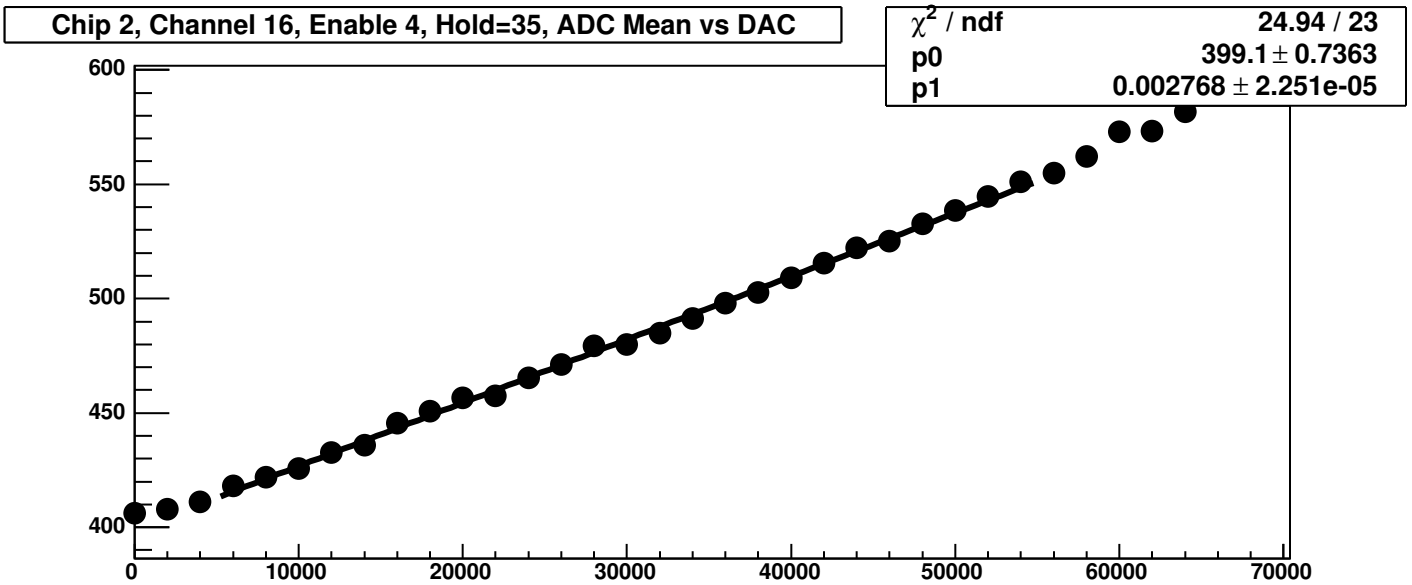
Chip 2, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

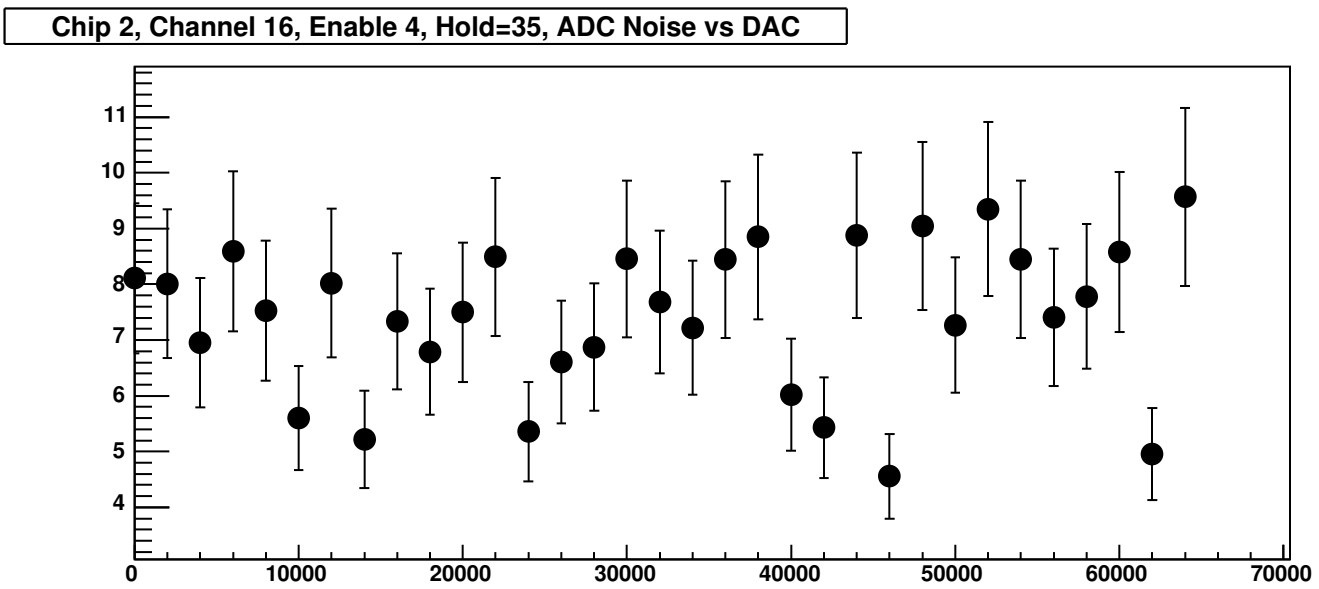


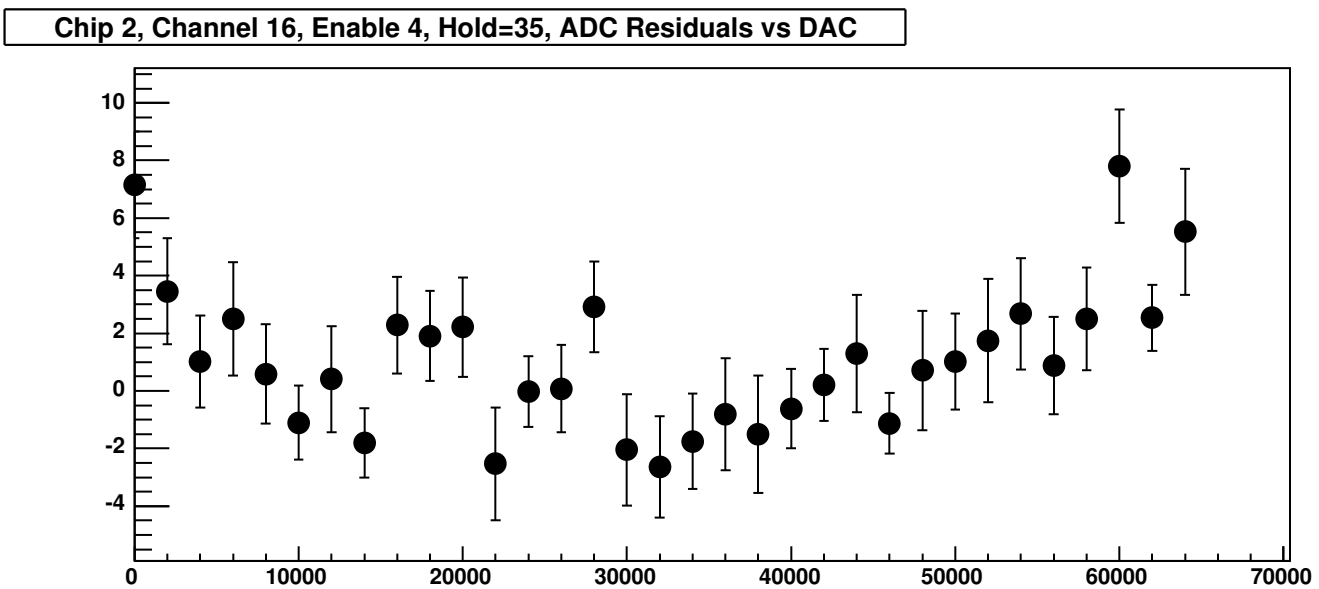

Chip 2, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.24 / 23
p0 397.3 ± 0.8165
p1 0.002741 ± 2.568e-05
600
550
500
450
400
0
10000
20000
30000
40000
50000
60000
70000

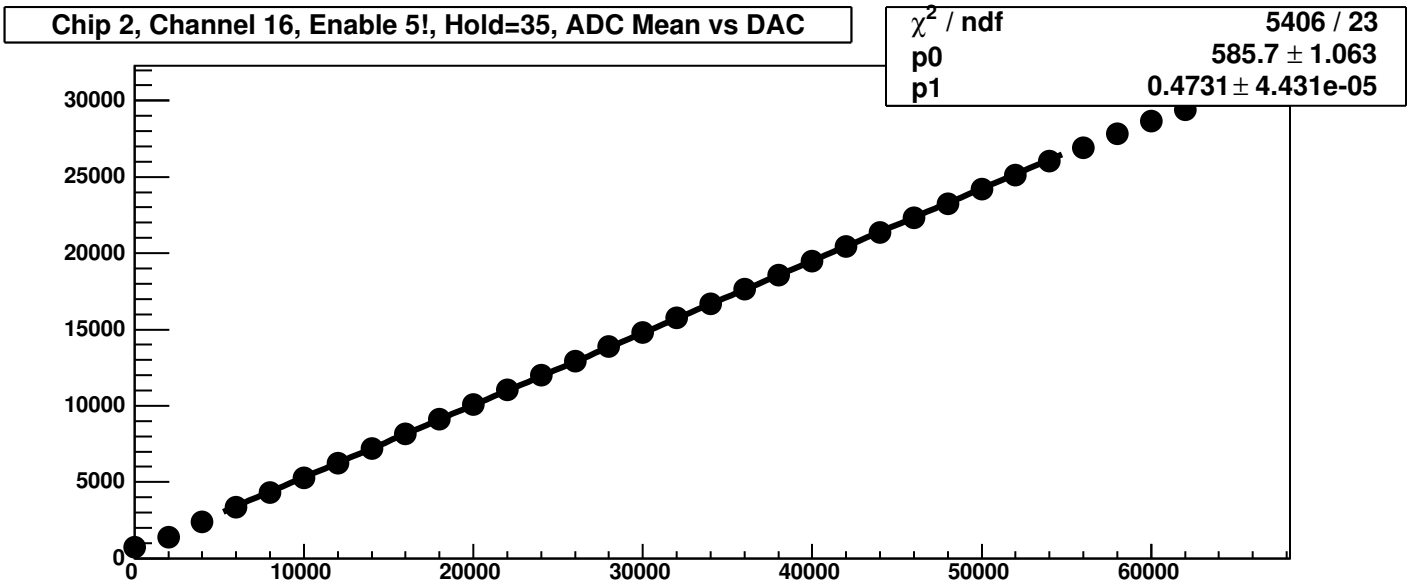

Chip 2, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

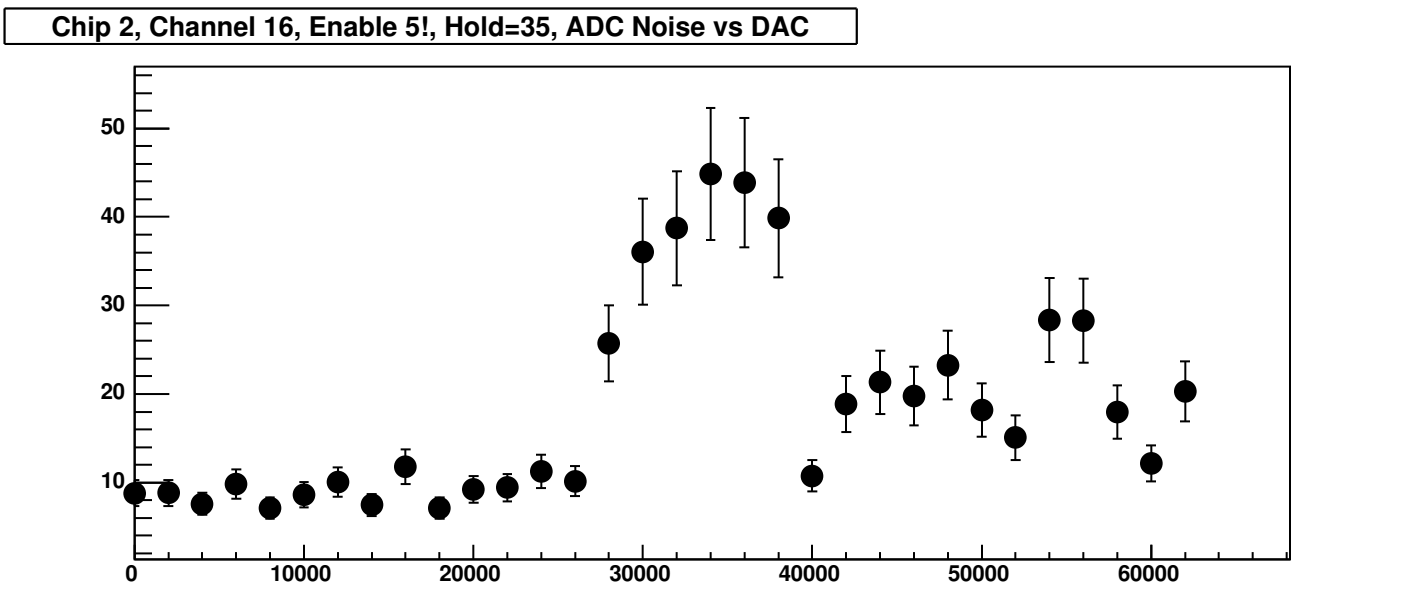

Chip 2, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
0
10000
20000
30000
40000
50000
60000
70000

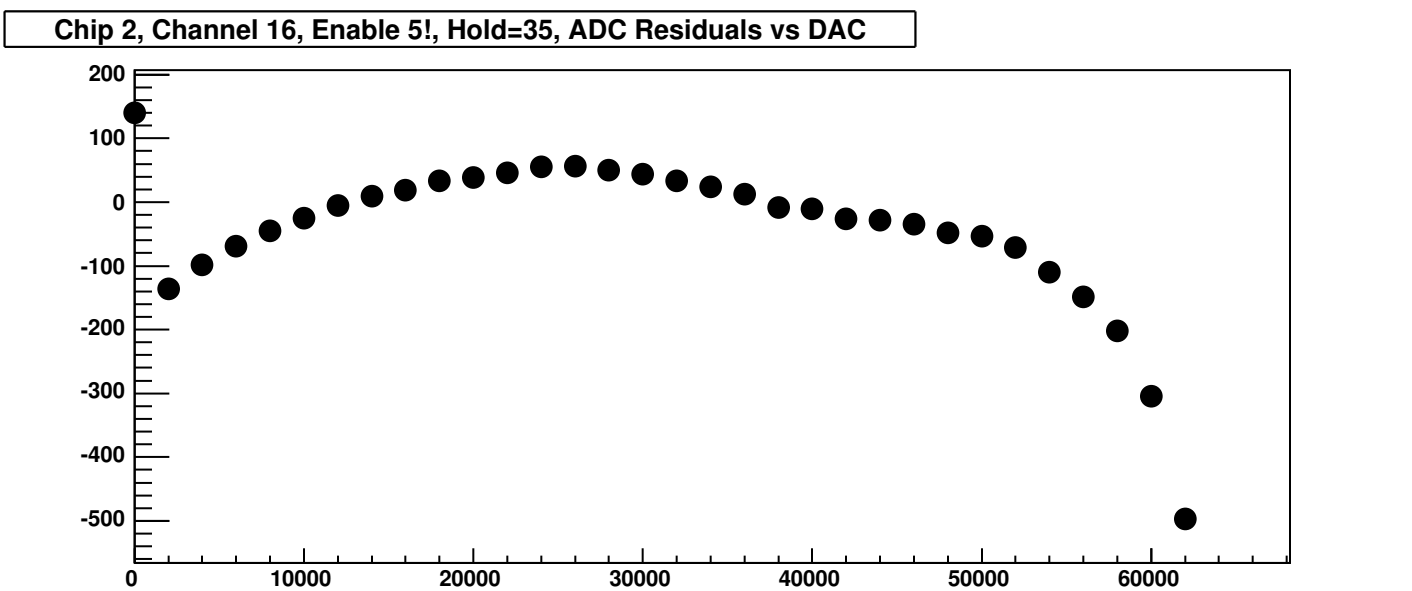

Chip 2, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.94 / 23
p0 399.1 ± 0.7363
p1 0.002768 ± 2.251e-05
600
550
500
450
400
0
10000
20000
30000
40000
50000
60000
70000

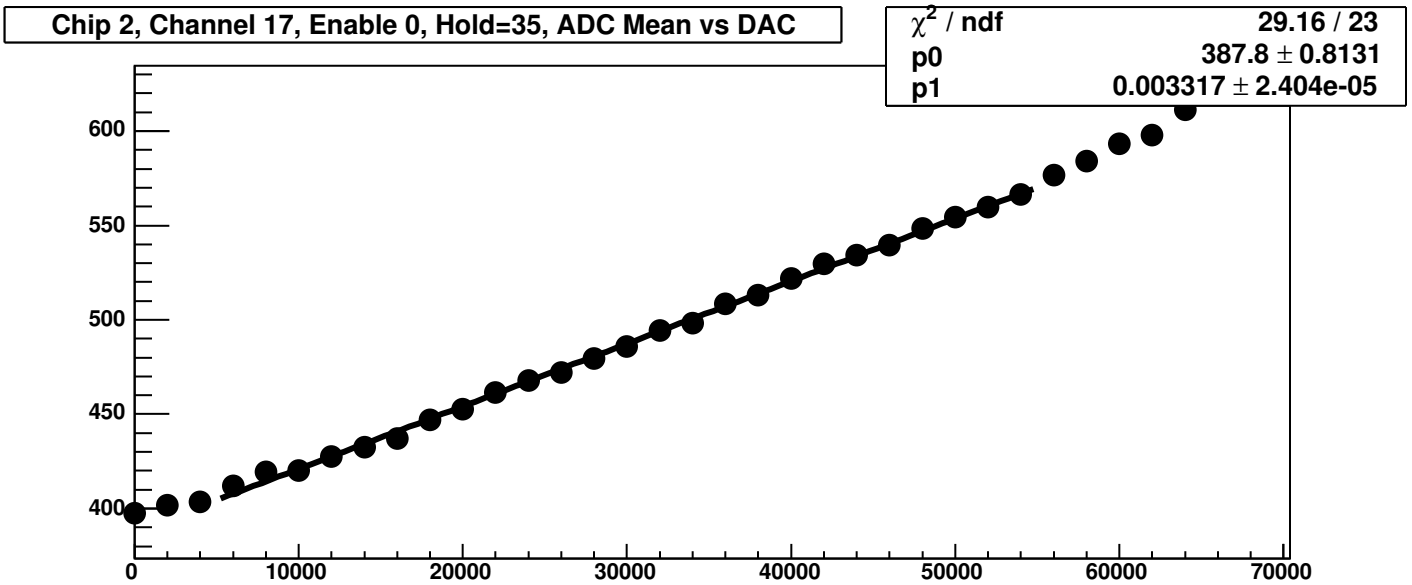

Chip 2, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

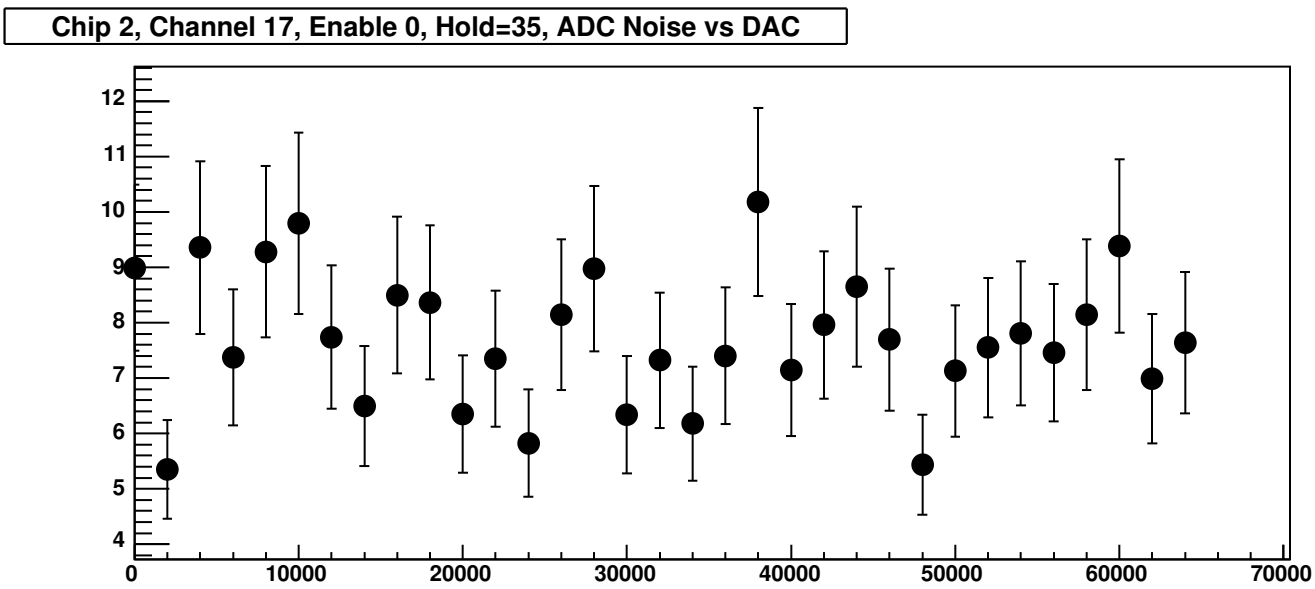

Chip 2, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
0
10000
20000
30000
40000
50000
60000
70000

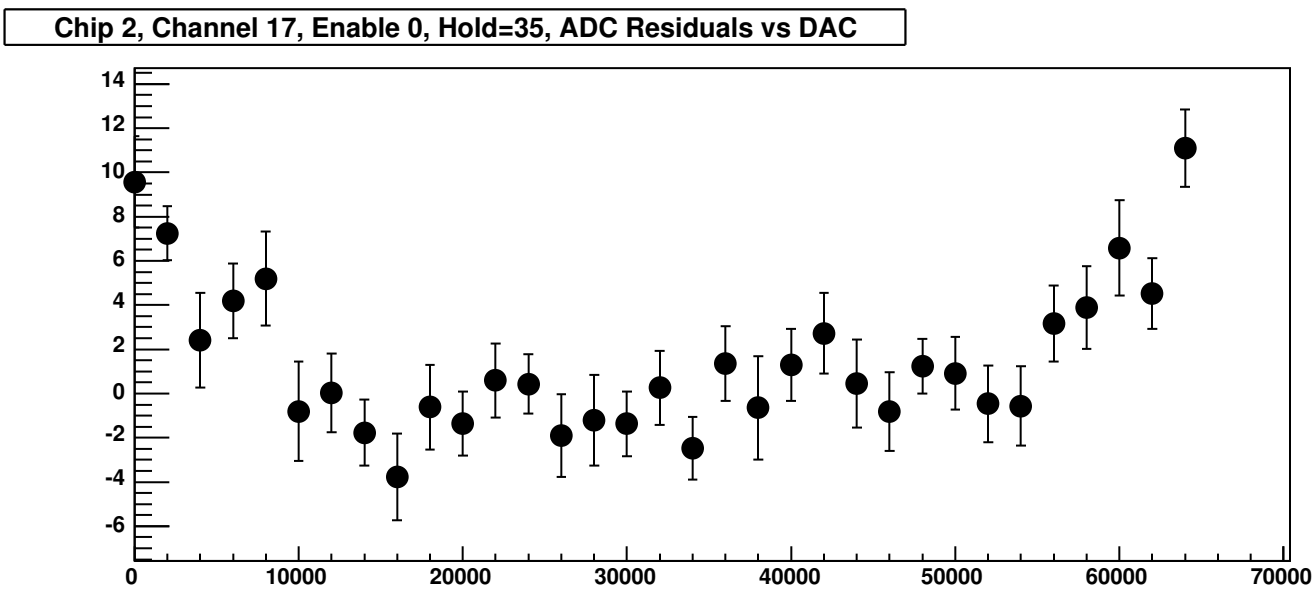

Chip 2, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 5406 / 23
p0 585.7 ± 1.063
p1 0.4731 ± 4.431e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

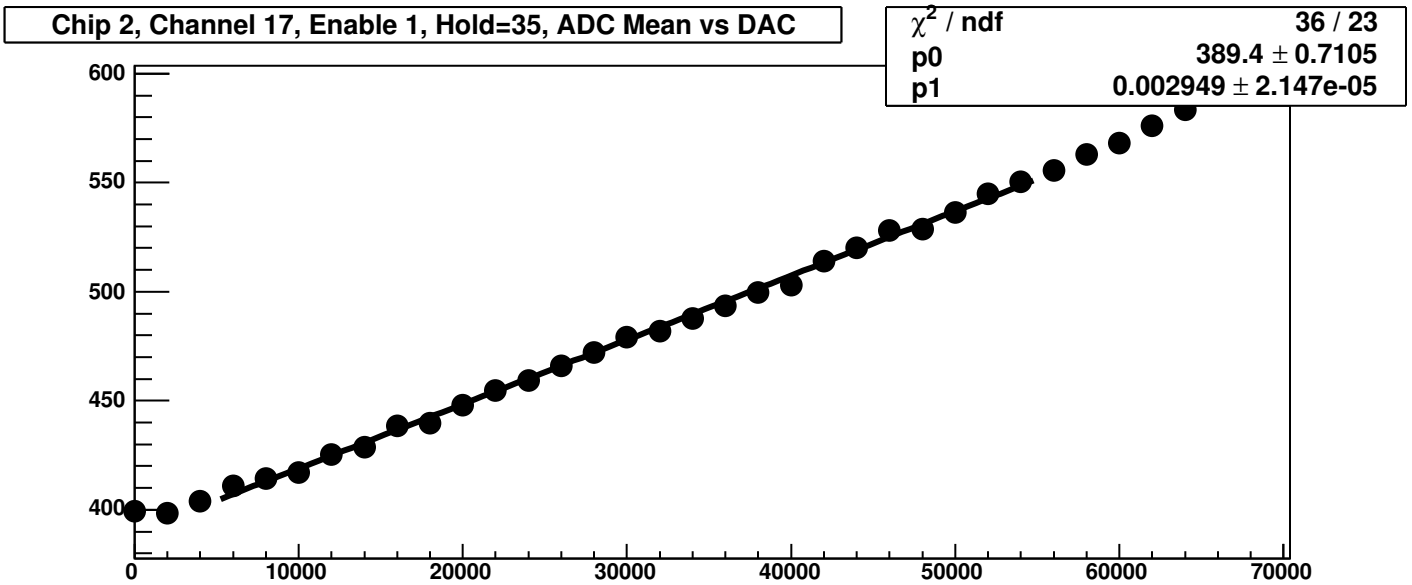

Chip 2, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000

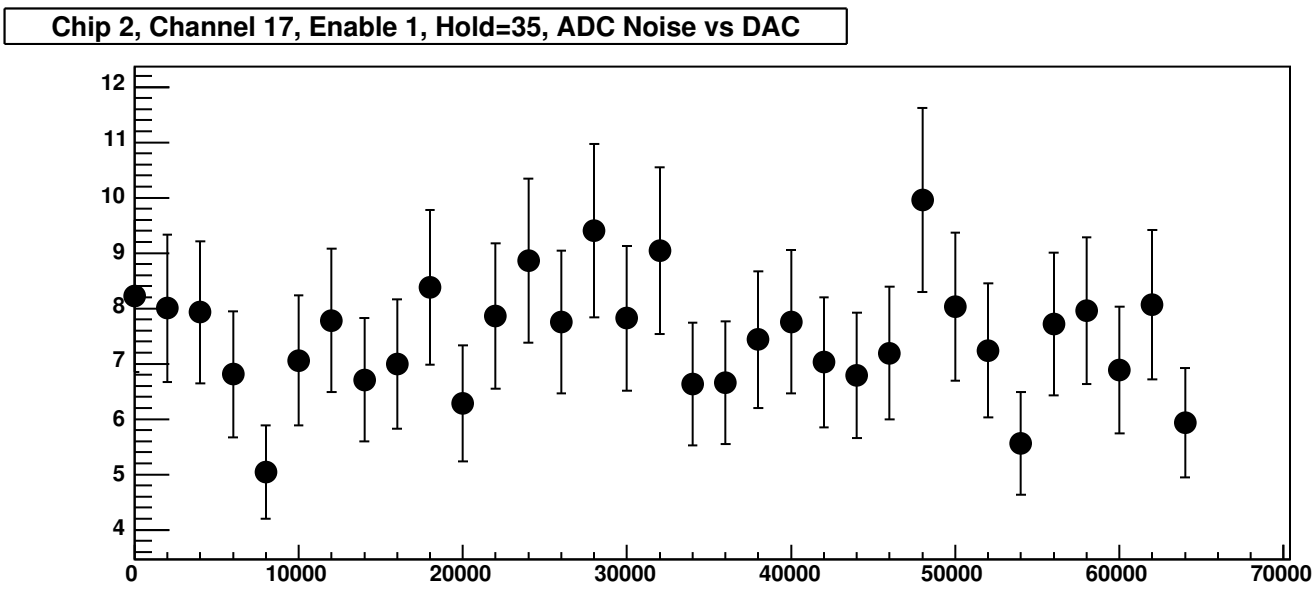

Chip 2, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
200
100
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000

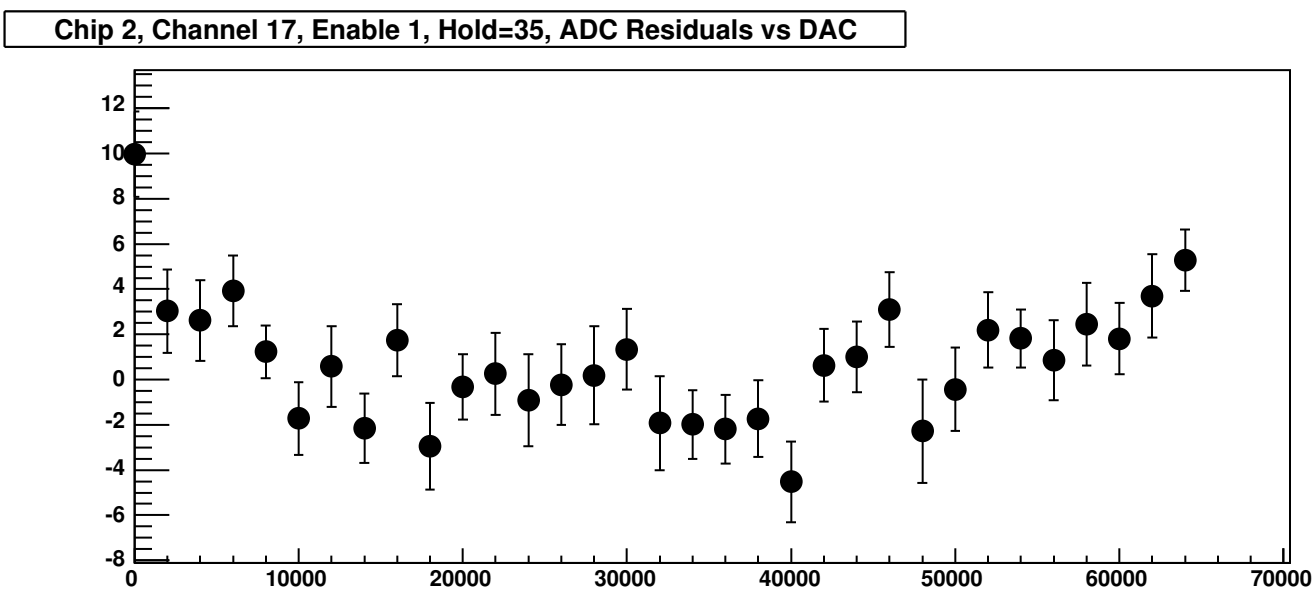

Chip 2, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 29.16 / 23
p0 387.8 ± 0.8131
p1 0.003317 ± 2.404e-05
600
550
500
450
400
0
10000
20000
30000
40000
50000
60000
70000

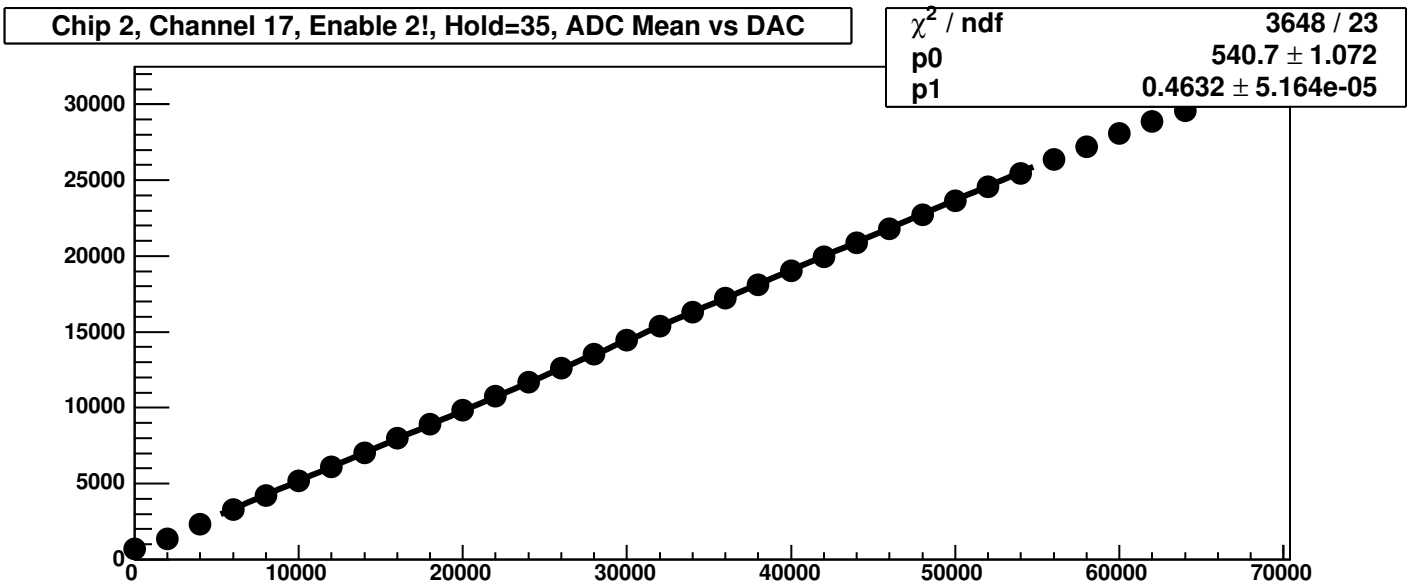

Chip 2, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

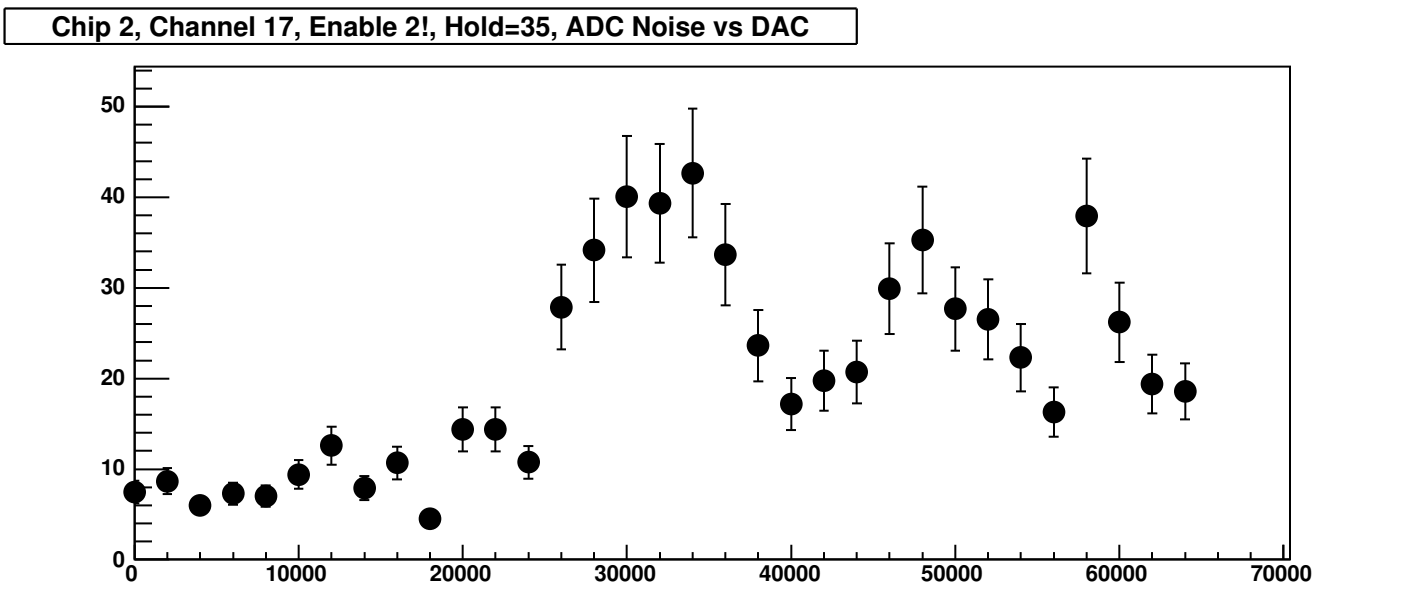

Chip 2, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

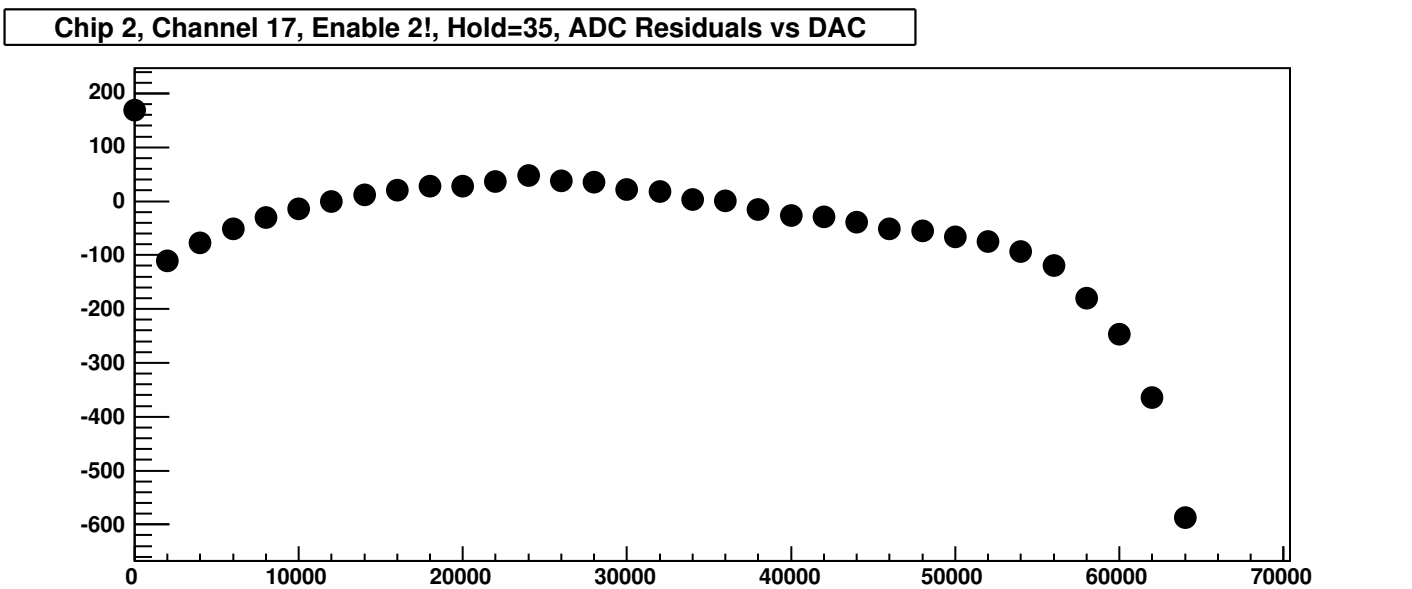

Chip 2, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 36 / 23
p0 389.4 ± 0.7105
p1 0.002949 ± 2.147e-05
600
550
500
450
400
0
10000
20000
30000
40000
50000
60000
70000

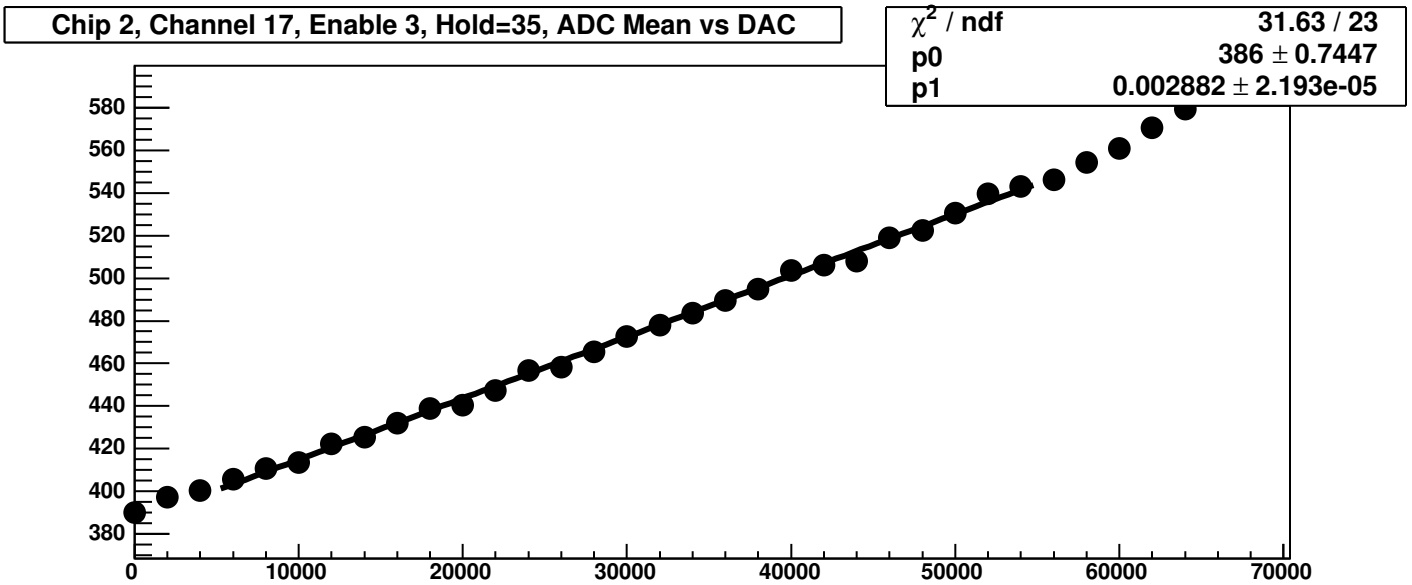

Chip 2, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

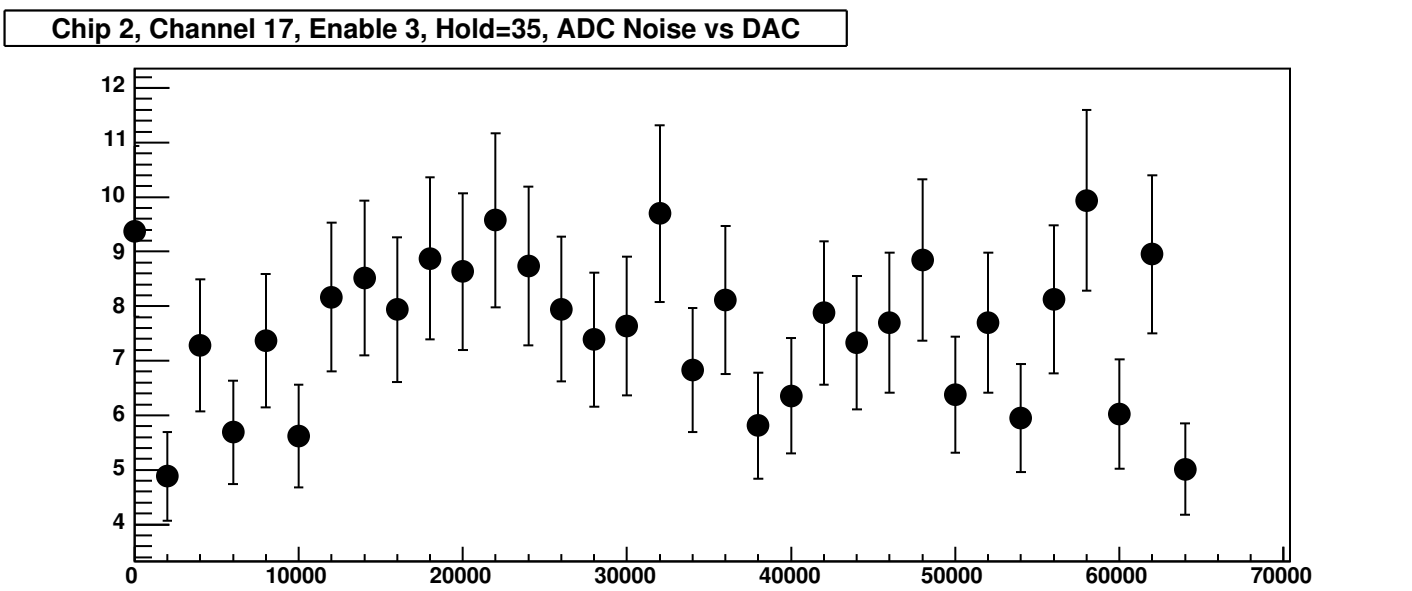

Chip 2, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

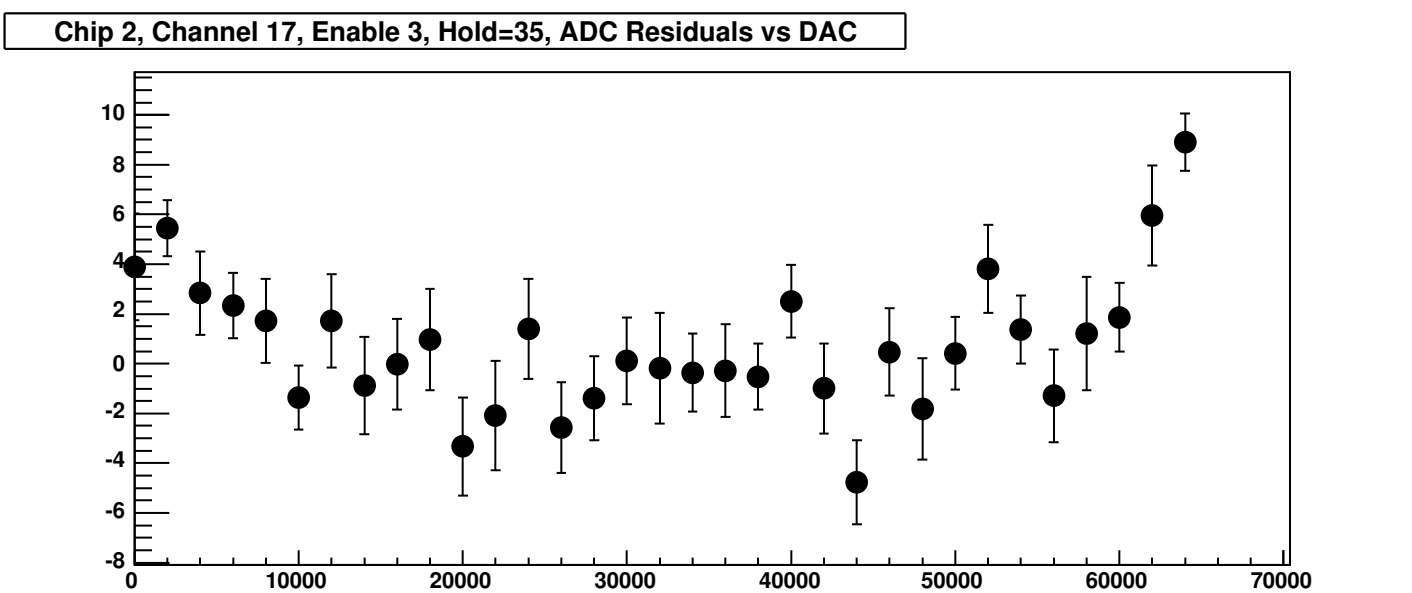

Chip 2, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 3648 / 23
p0 540.7 ± 1.072
p1 0.4632 ± 5.164e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

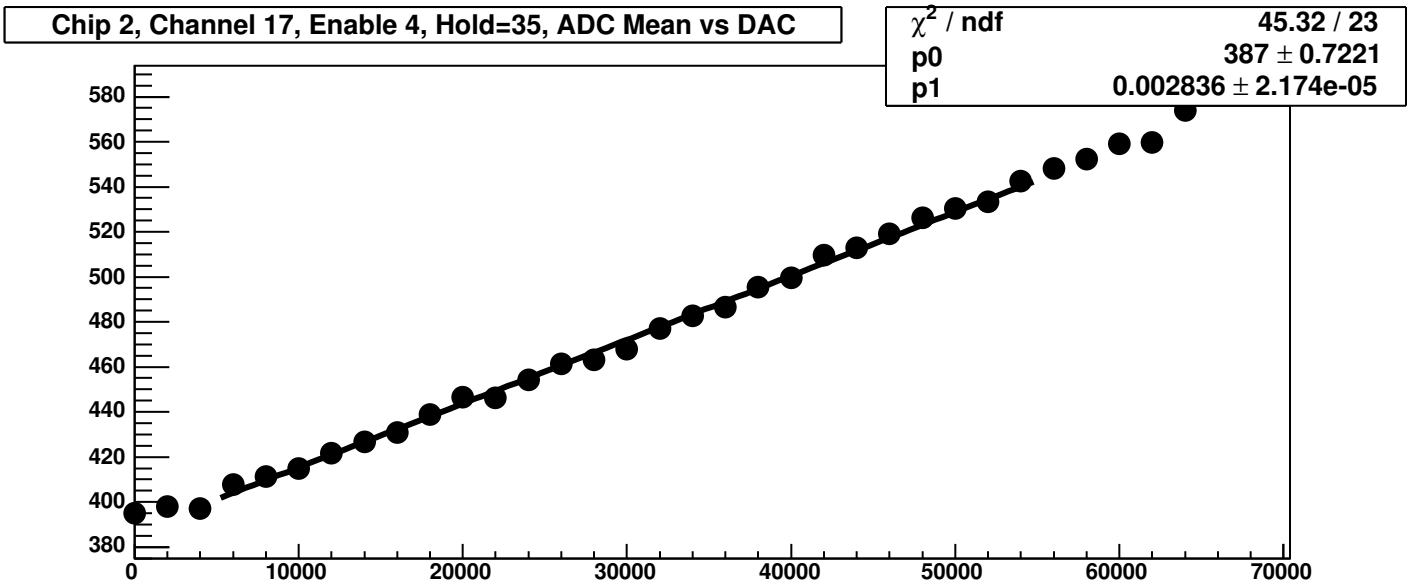

Chip 2, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

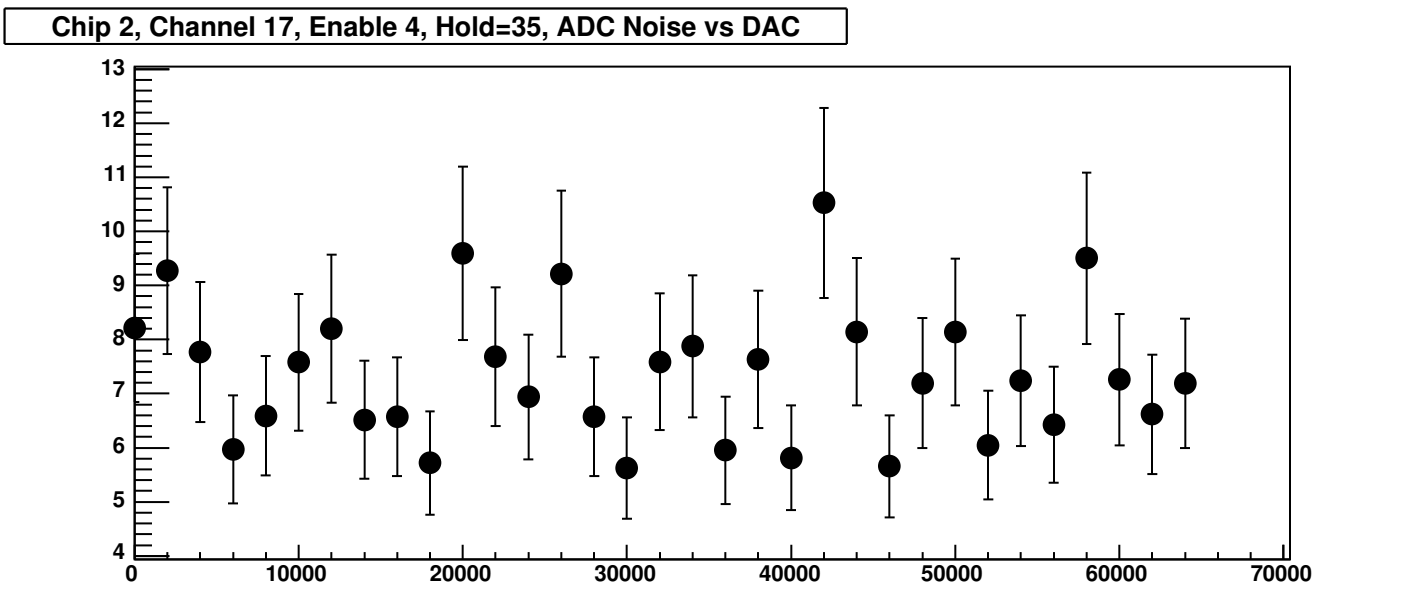

Chip 2, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
200
100
0
-100
-200
-300
-400
-500
-600
0
10000
20000
30000
40000
50000
60000
70000

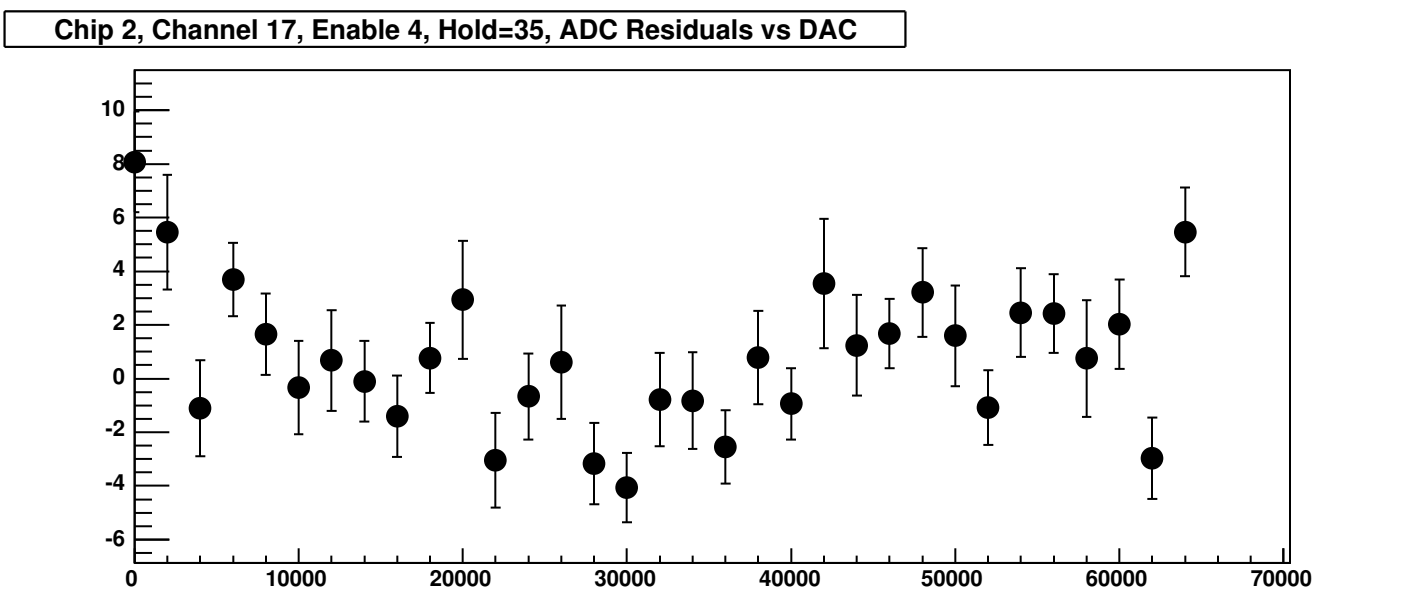

Chip 2, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.63 / 23
p0 386 ± 0.7447
p1 0.002882 ± 2.193e-05
580
560
540
520
500
480
460
440
420
400
380
0
10000
20000
30000
40000
50000
60000
70000

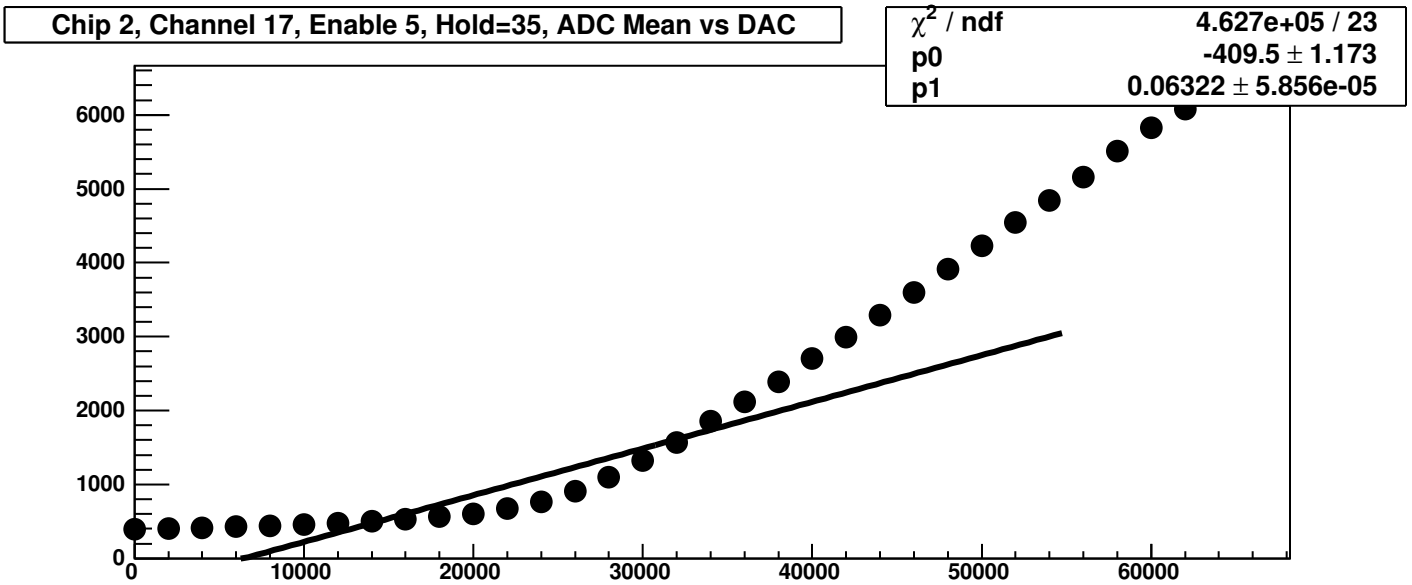

Chip 2, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

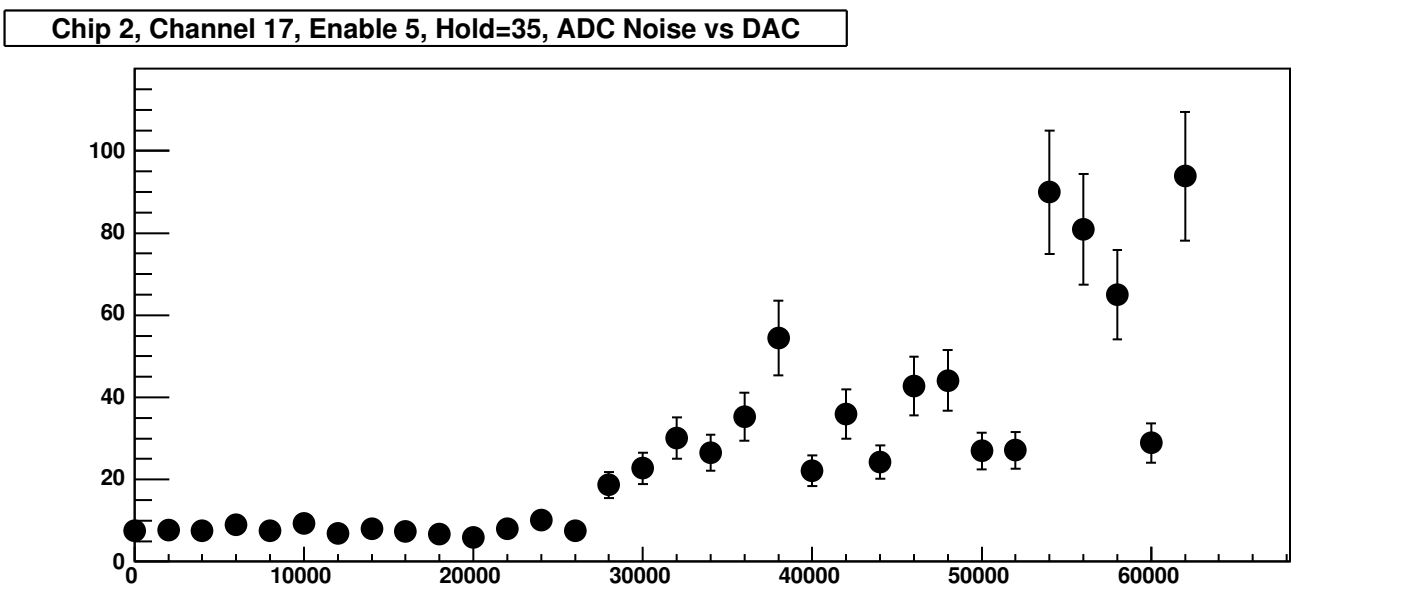

Chip 2, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

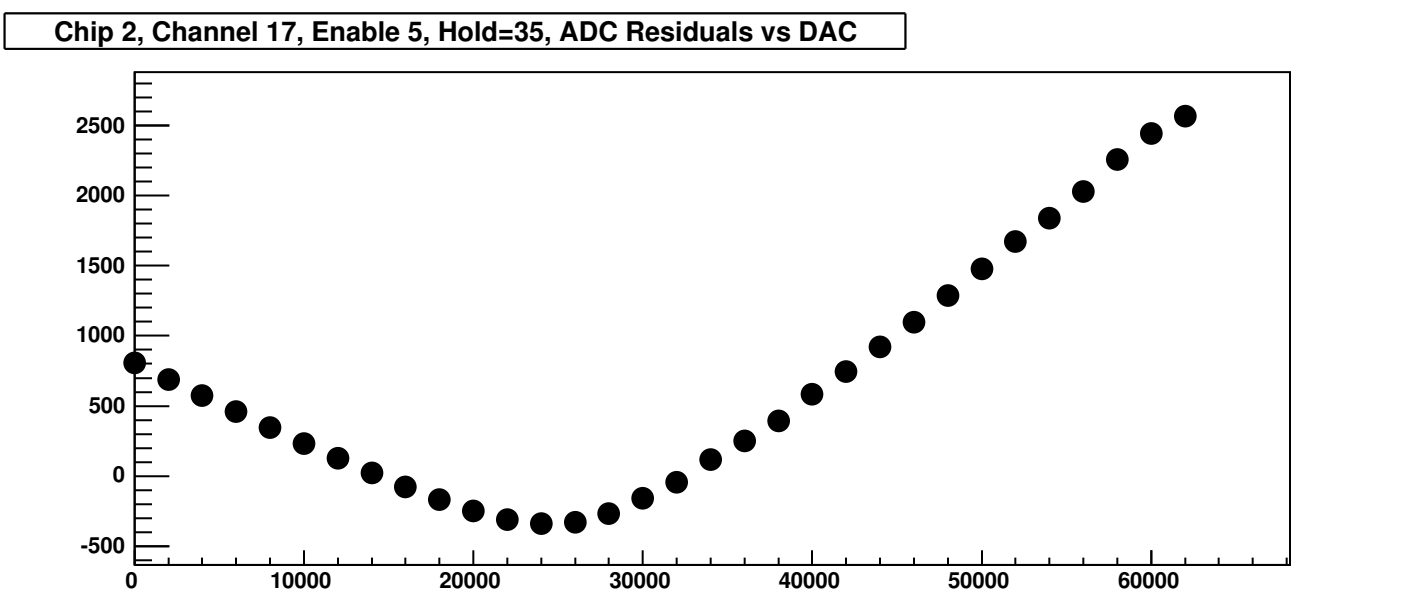

Chip 2, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 45.32 / 23
p0 387 ± 0.7221
p1 0.002836 ± 2.174e-05
580
560
540
520
500
480
460
440
420
400
380
0
10000
20000
30000
40000
50000
60000
70000

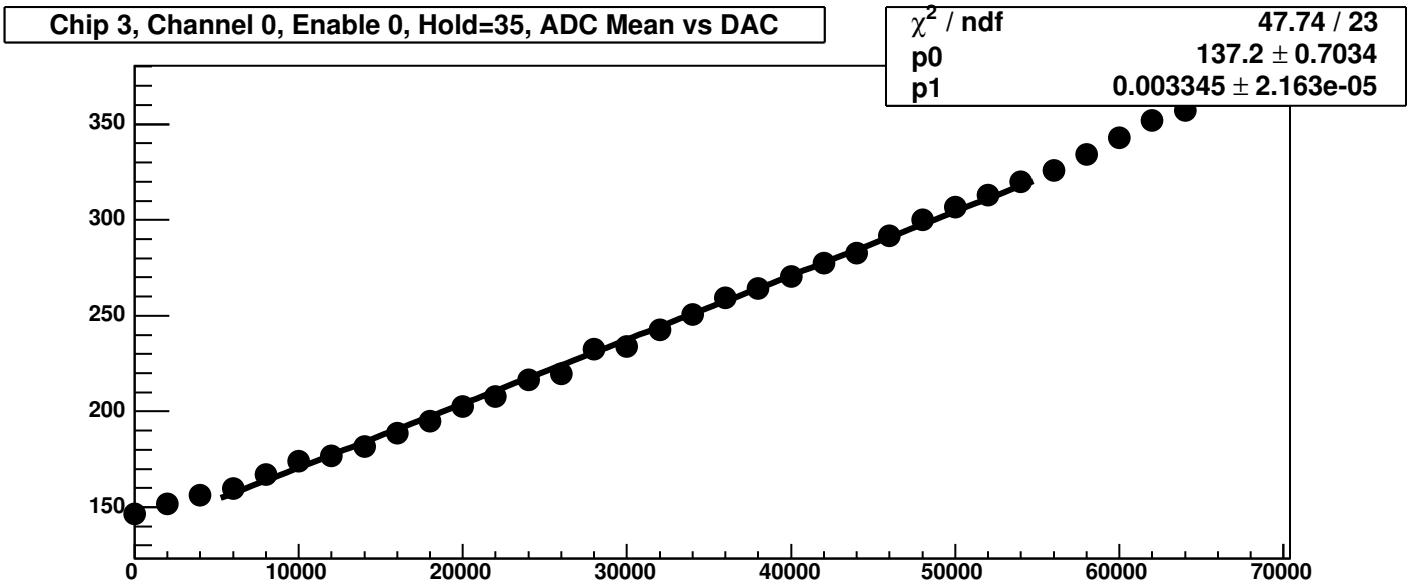

Chip 2, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

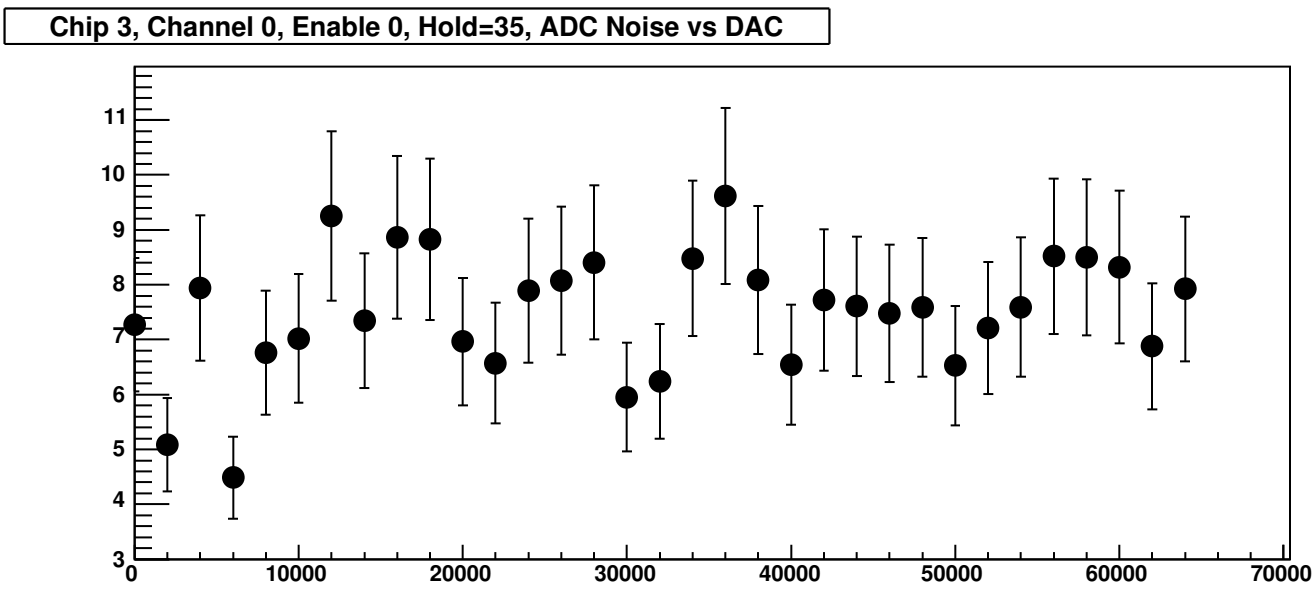

Chip 2, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

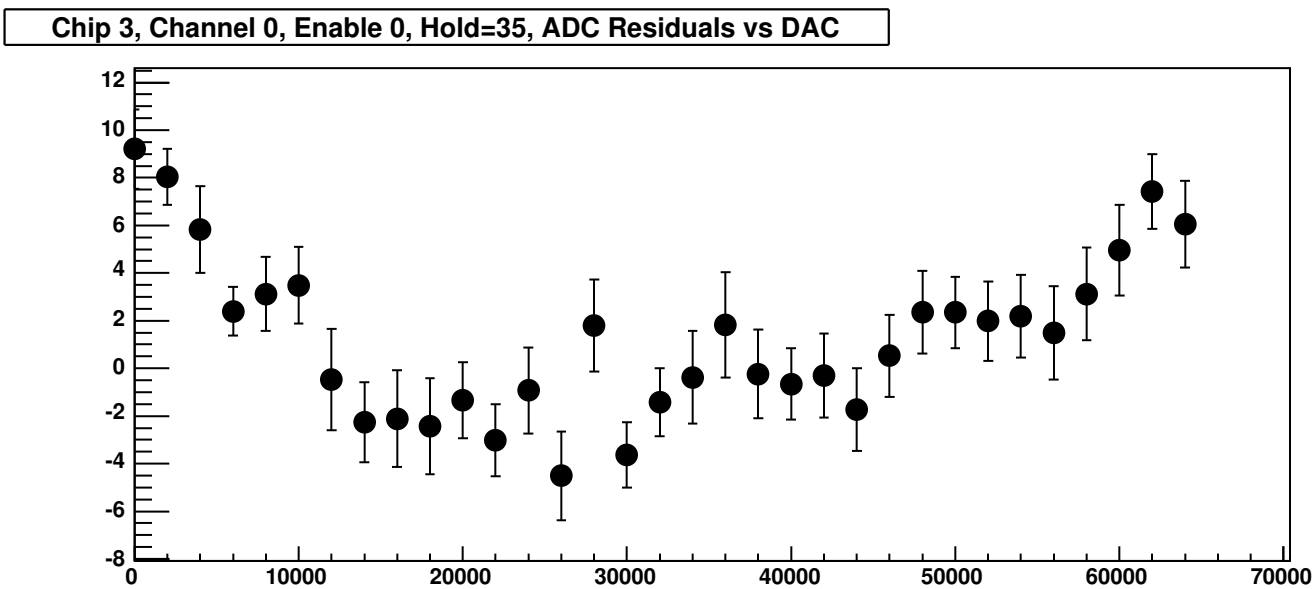

Chip 2, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 4.627e+05 / 23
p0 -409.5 ± 1.173
p1 0.06322 ± 5.856e-05
6000
5000
4000
3000
2000
1000
0
0
10000
20000
30000
40000
50000
60000

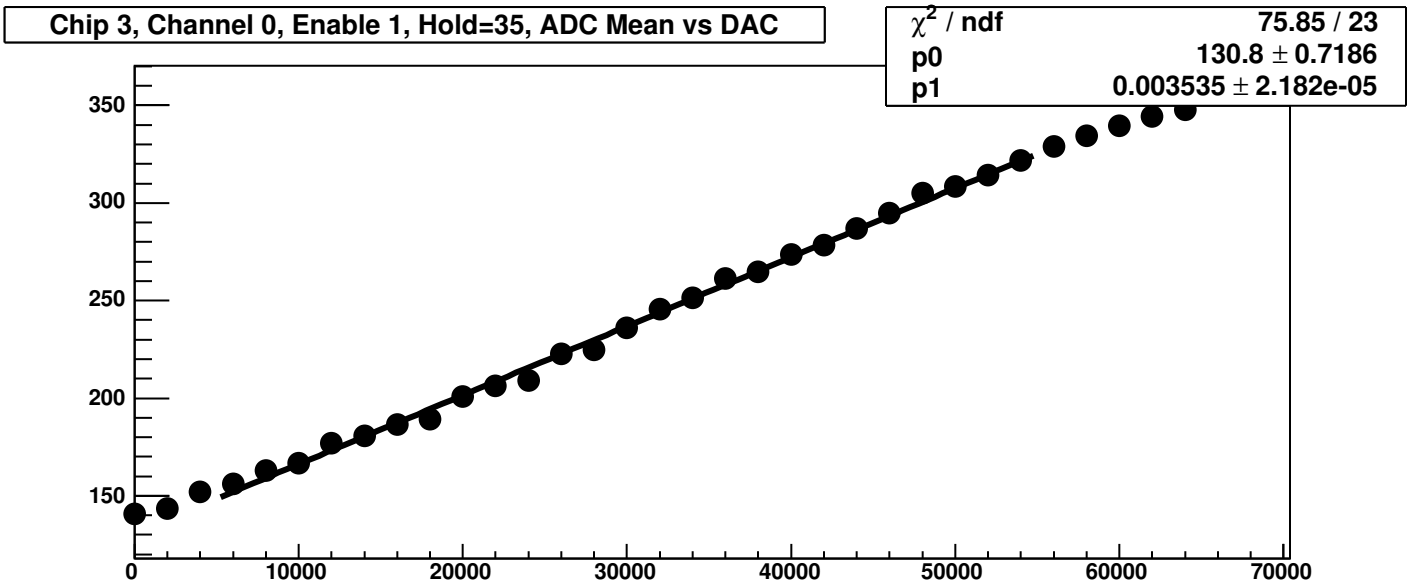

Chip 2, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

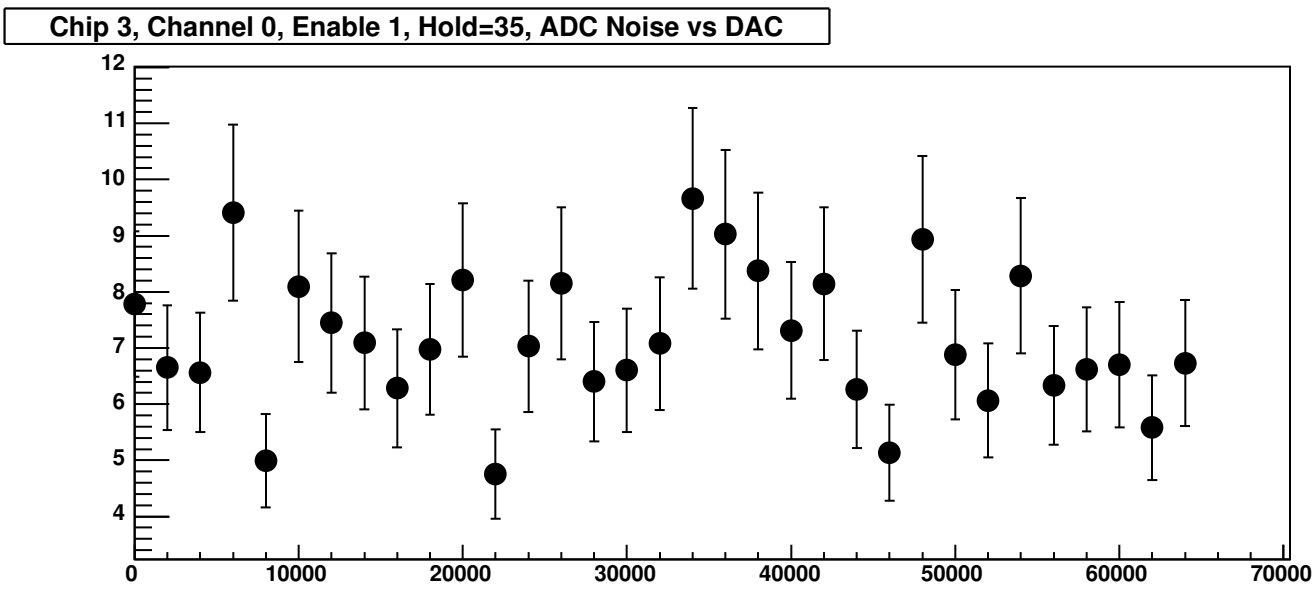

Chip 2, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
2500
2000
1500
1000
500
0
-500
0
10000
20000
30000
40000
50000
60000

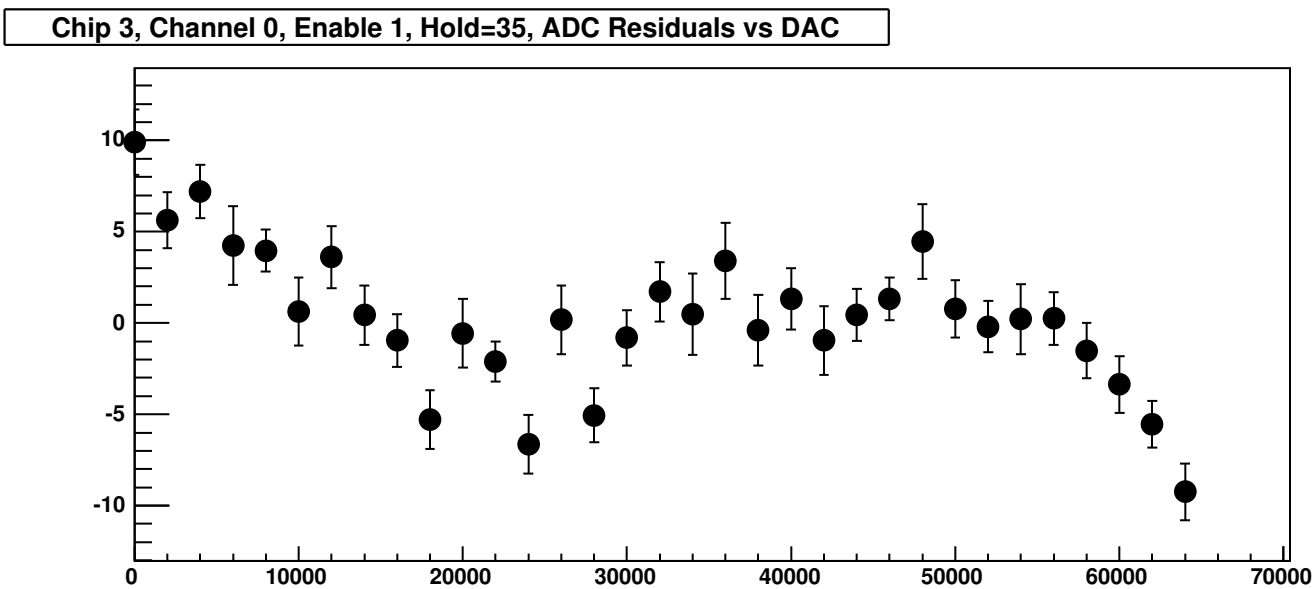

Chip 3, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 47.74 / 23
p0 137.2 ± 0.7034
p1 0.003345 ± 2.163e-05
350
300
250
200
150
0
10000
20000
30000
40000
50000
60000
70000

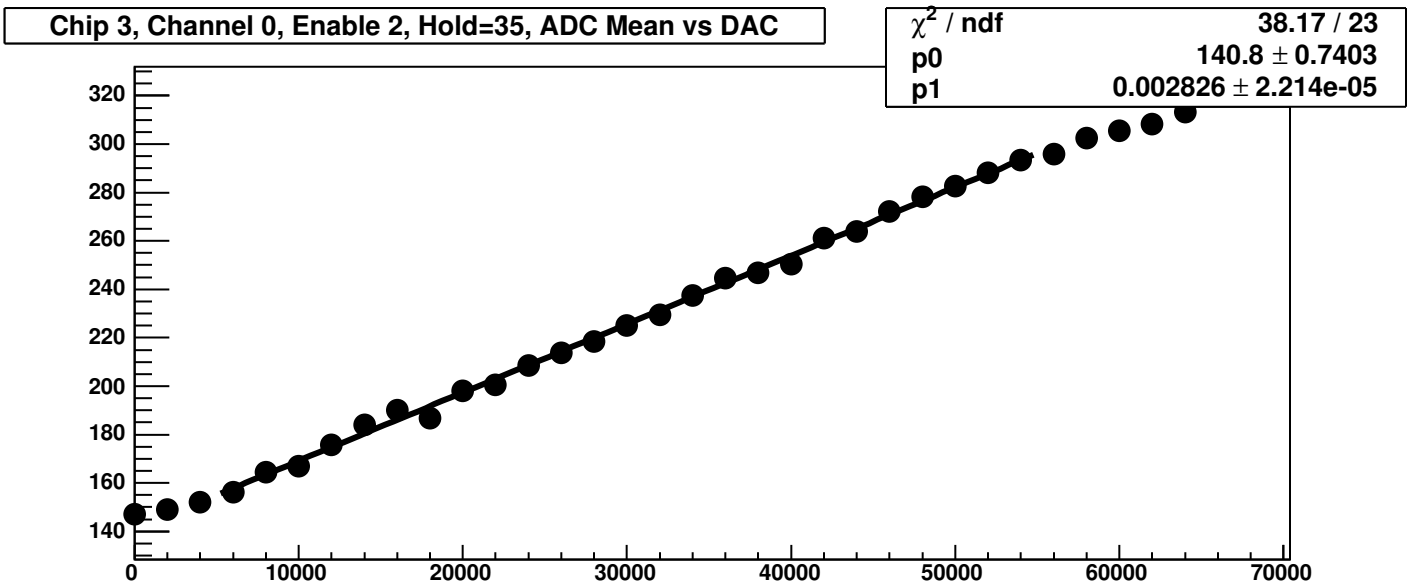

Chip 3, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

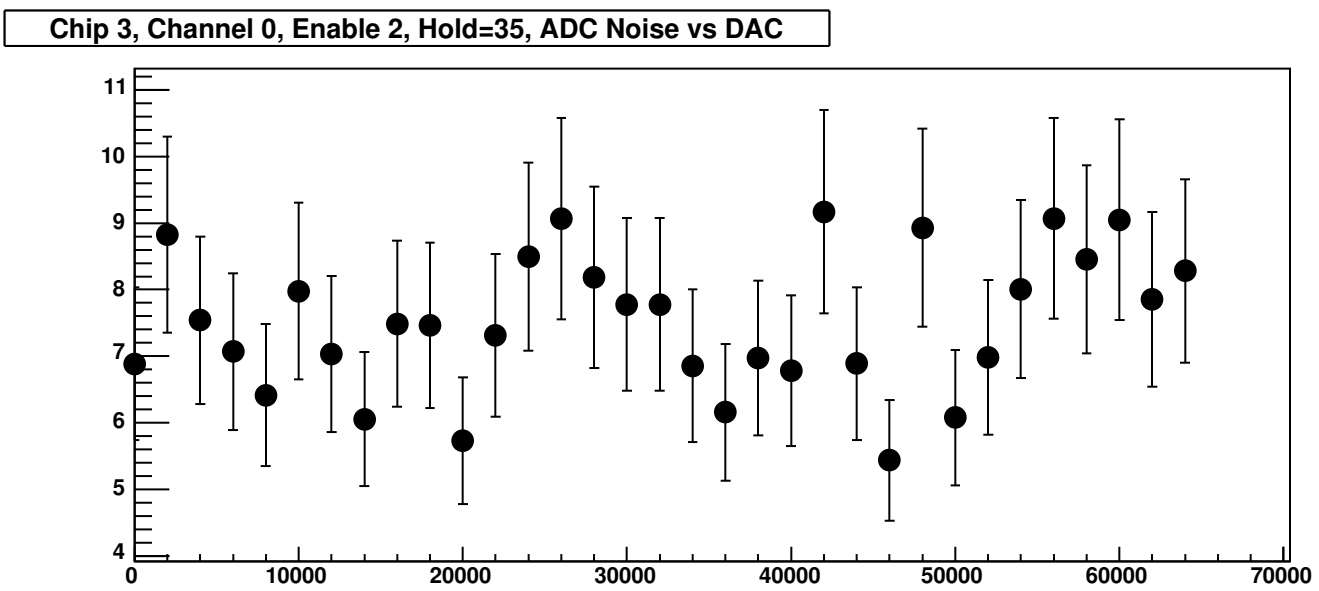

Chip 3, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

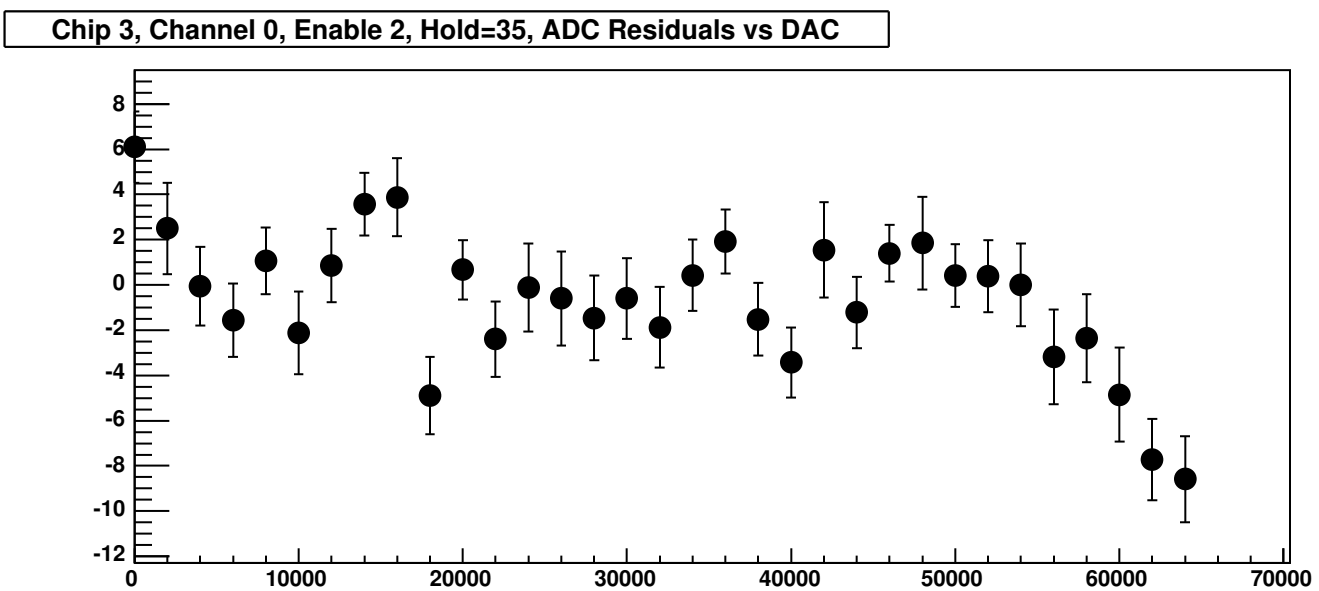

Chip 3, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 75.85 / 23
p0 130.8 ± 0.7186
p1 0.003535 ± 2.182e-05
350
300
250
200
150
0
10000
20000
30000
40000
50000
60000
70000

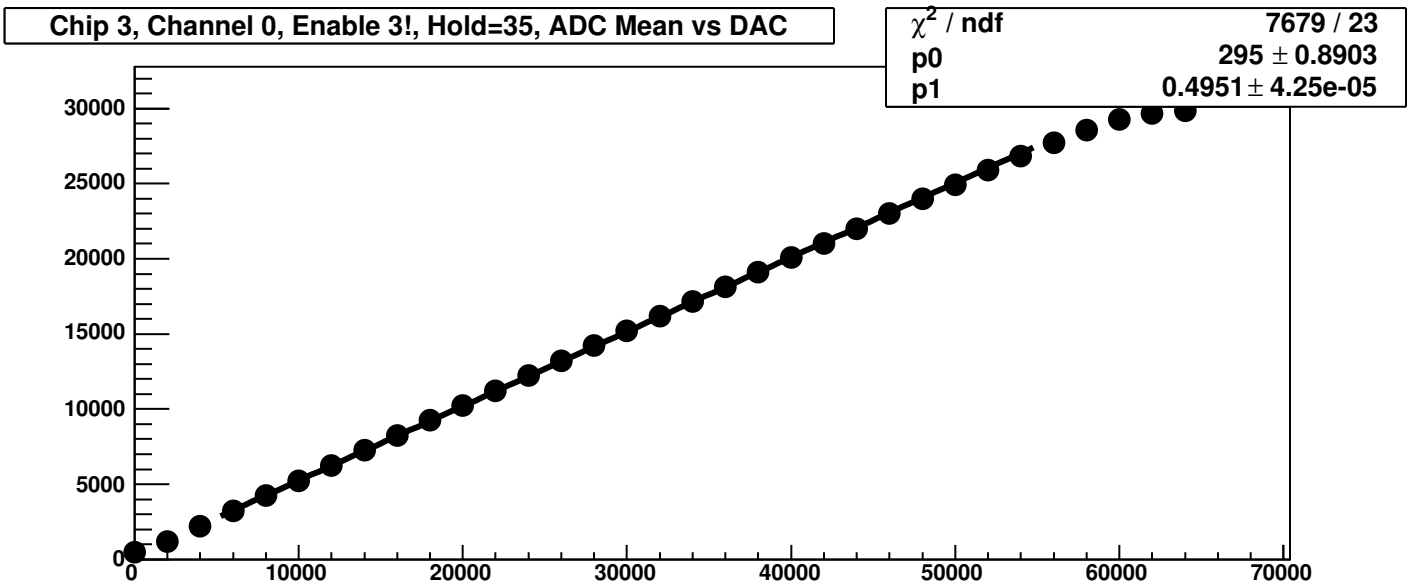

Chip 3, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

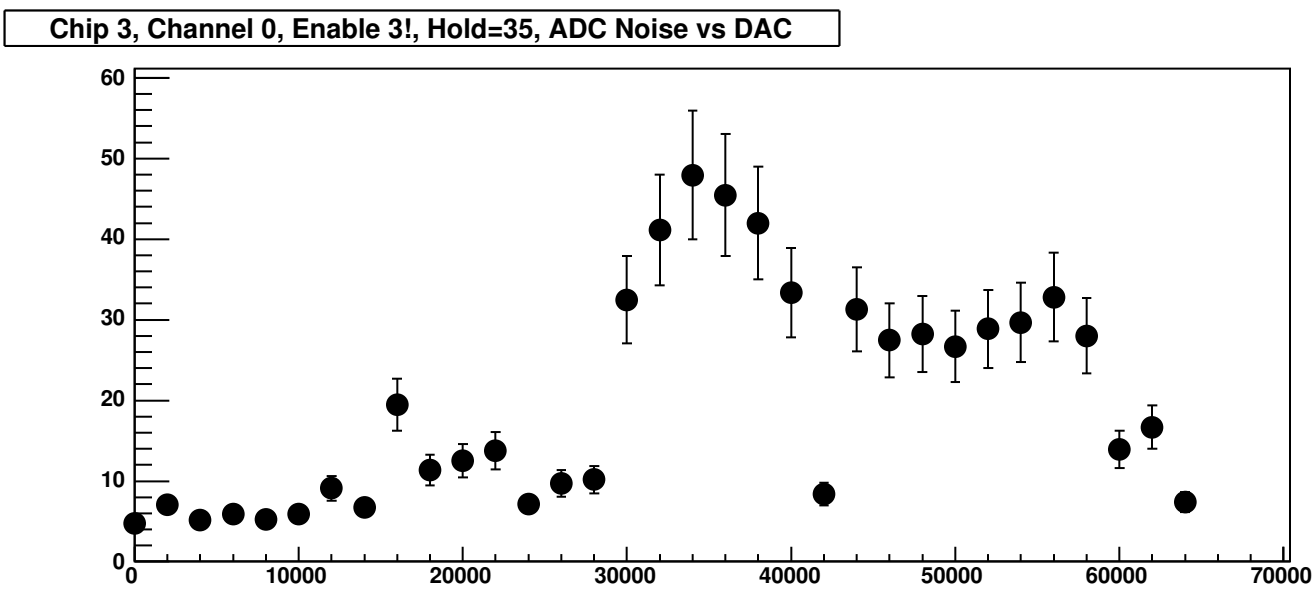

Chip 3, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

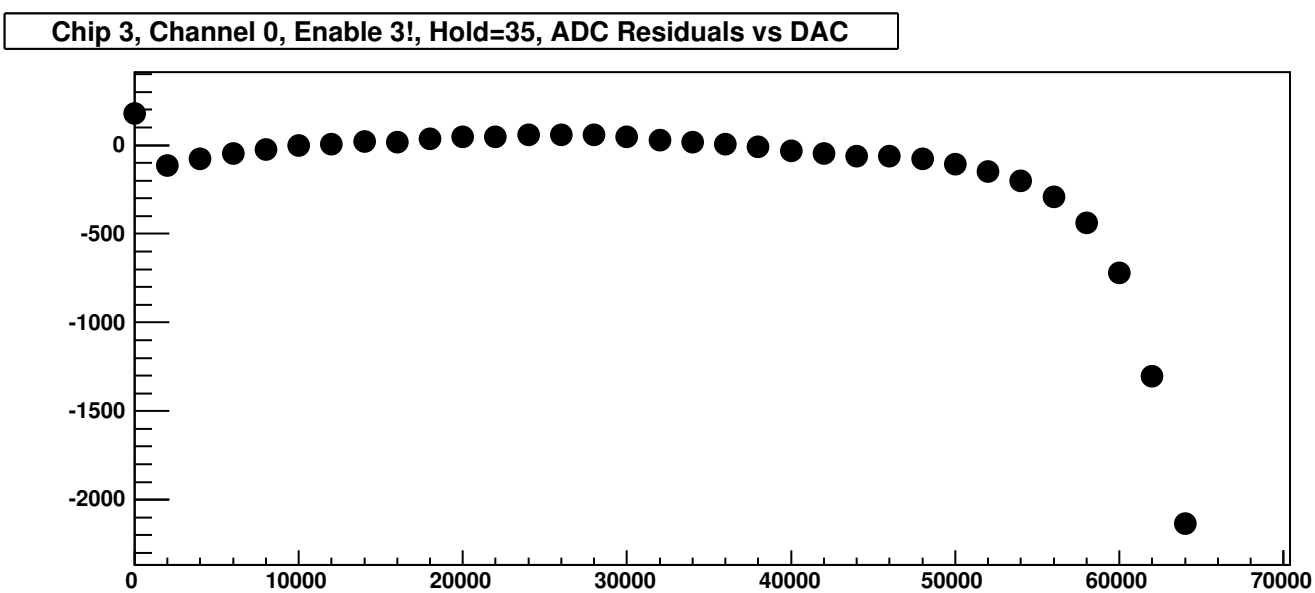

Chip 3, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 38.17 / 23
p0 140.8 ± 0.7403
p1 0.002826 ± 2.214e-05
320
300
280
260
240
220
200
180
160
140
0
10000
20000
30000
40000
50000
60000
70000

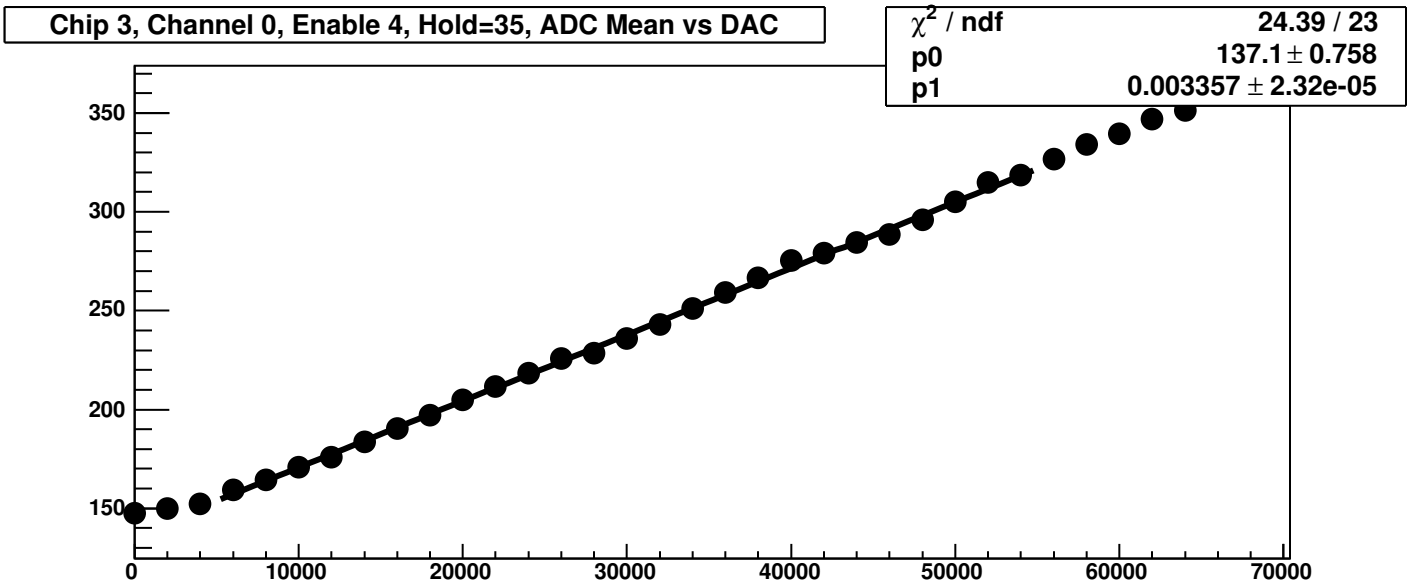

Chip 3, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

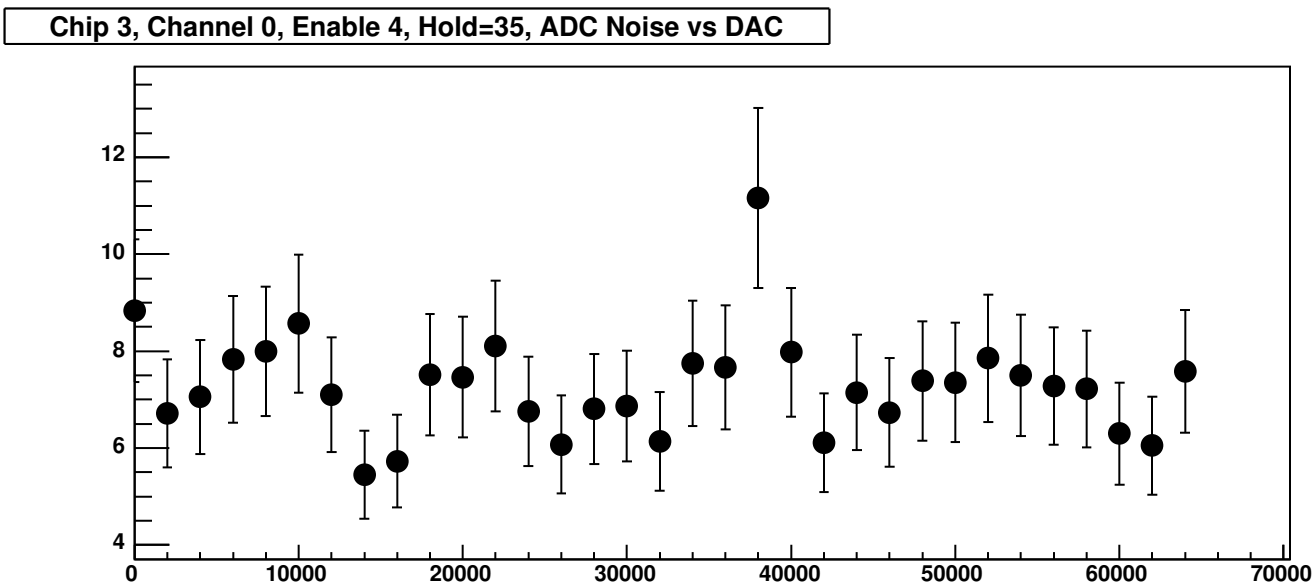

Chip 3, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
0
10000
20000
30000
40000
50000
60000
70000

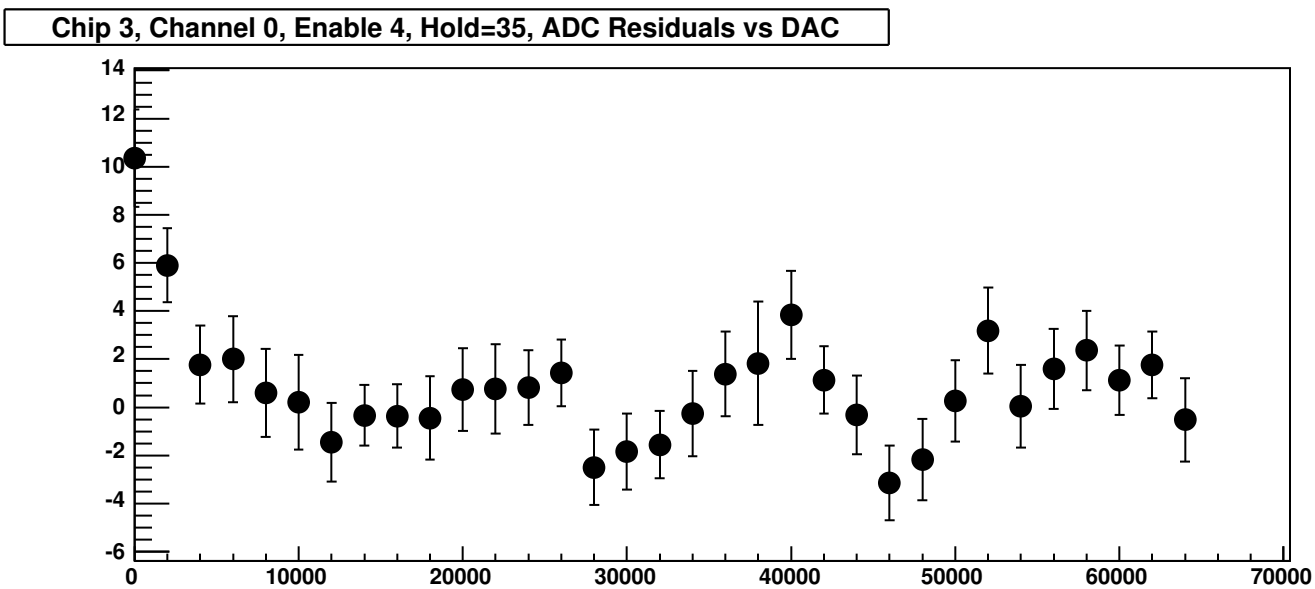

Chip 3, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 7679 / 23
p0 295 ± 0.8903
p1 0.4951 ± 4.25e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

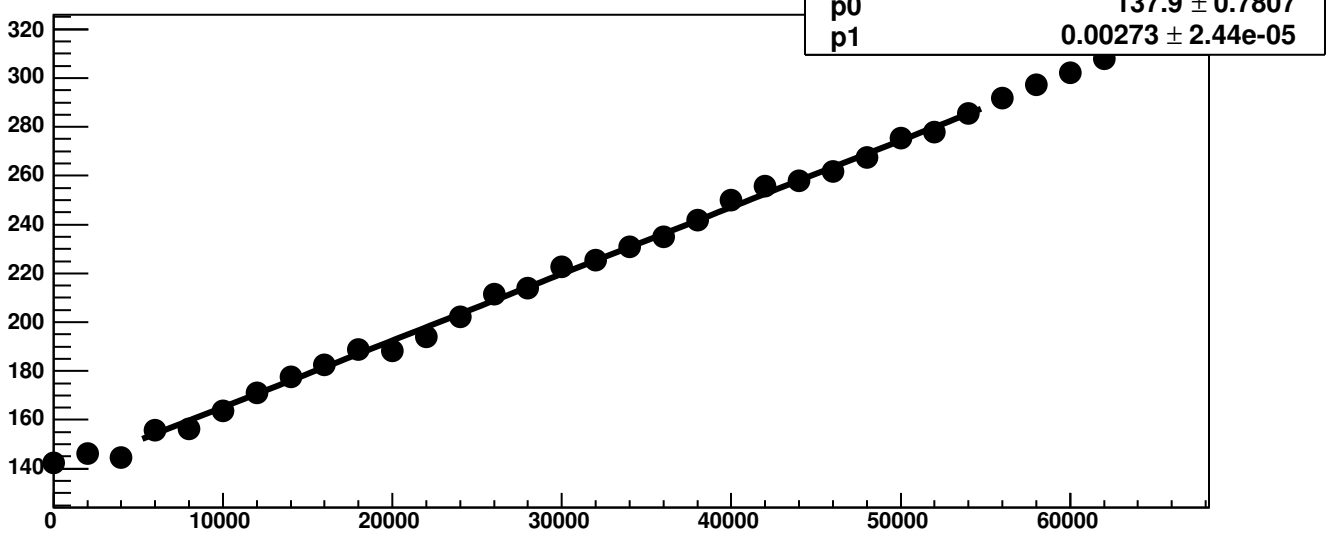

Chip 3, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.39 / 23
p0 137.1 ± 0.758
p1 0.003357 ± 2.32e-05
350
300
250
200
150
0
10000
20000
30000
40000
50000
60000
70000

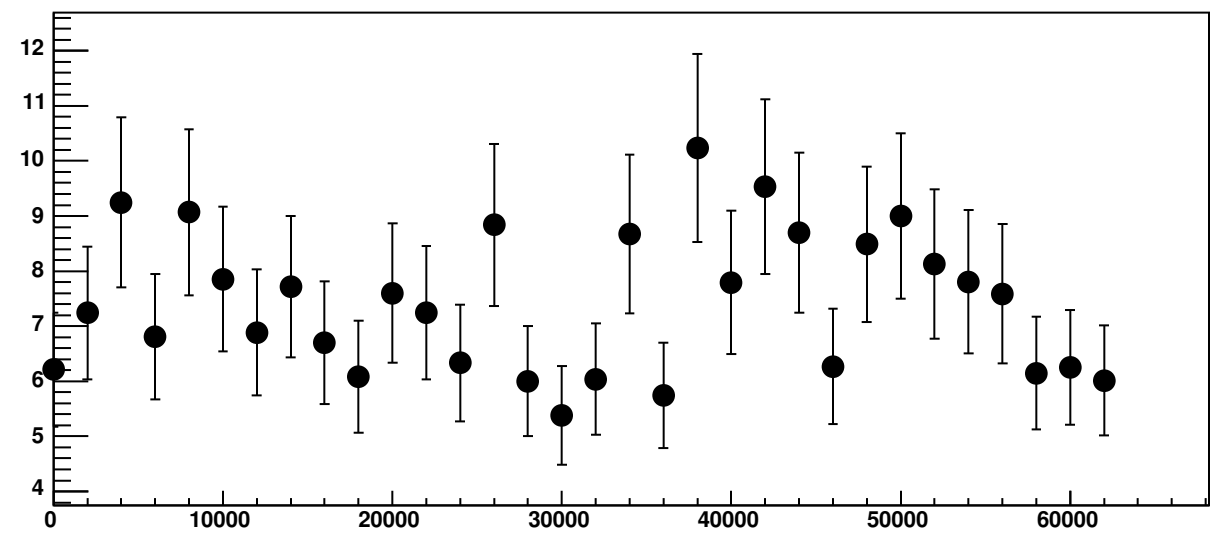

Chip 3, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

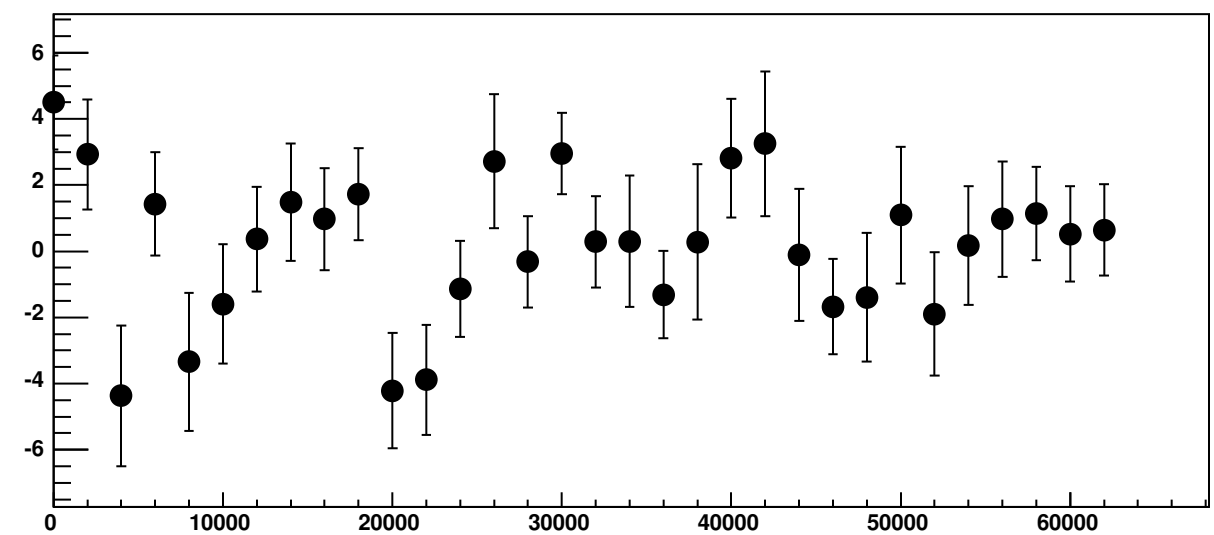
Chip 3, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC

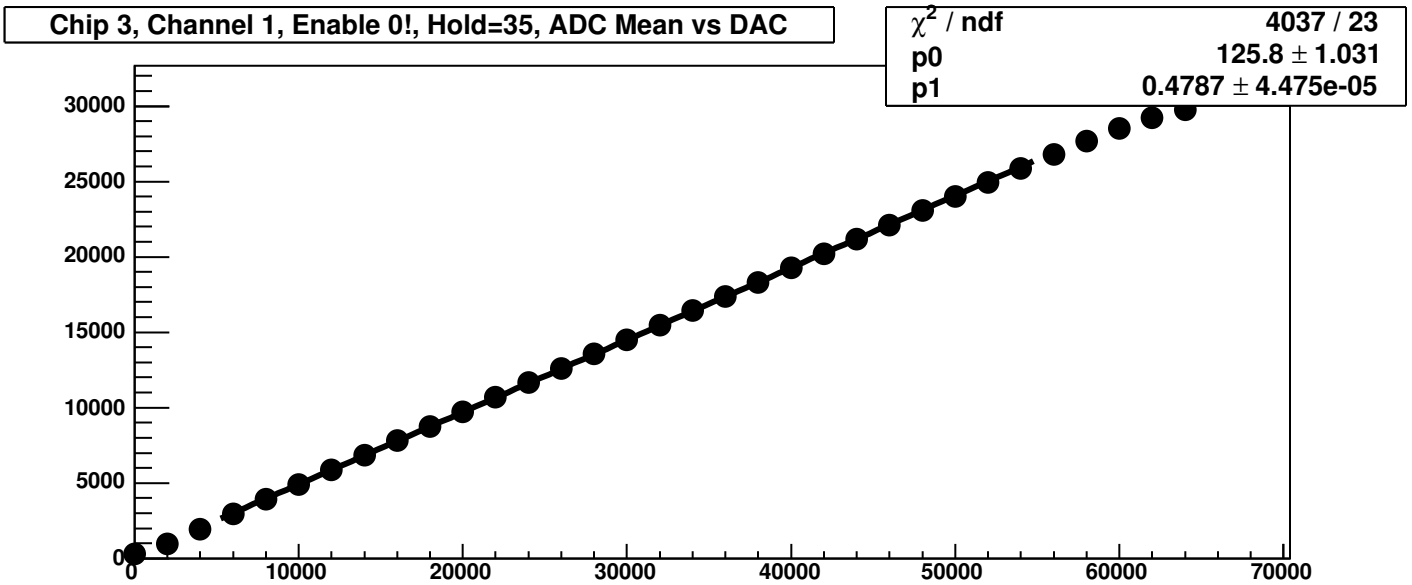


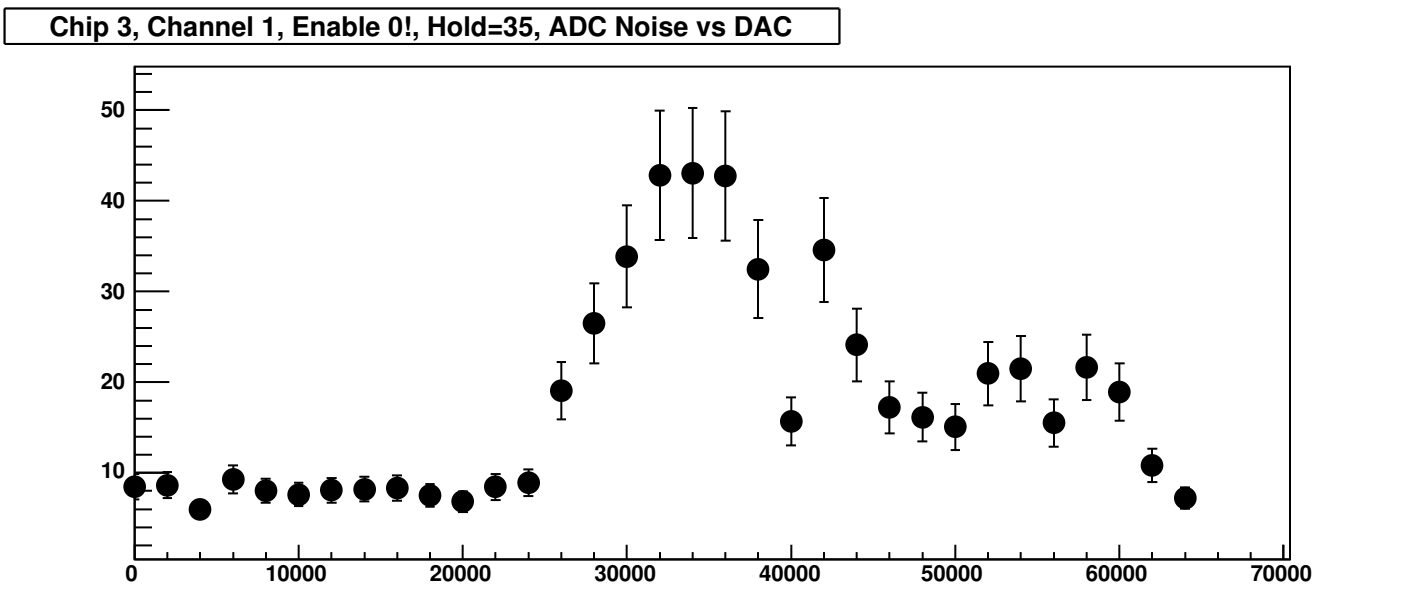
Chip 3, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC

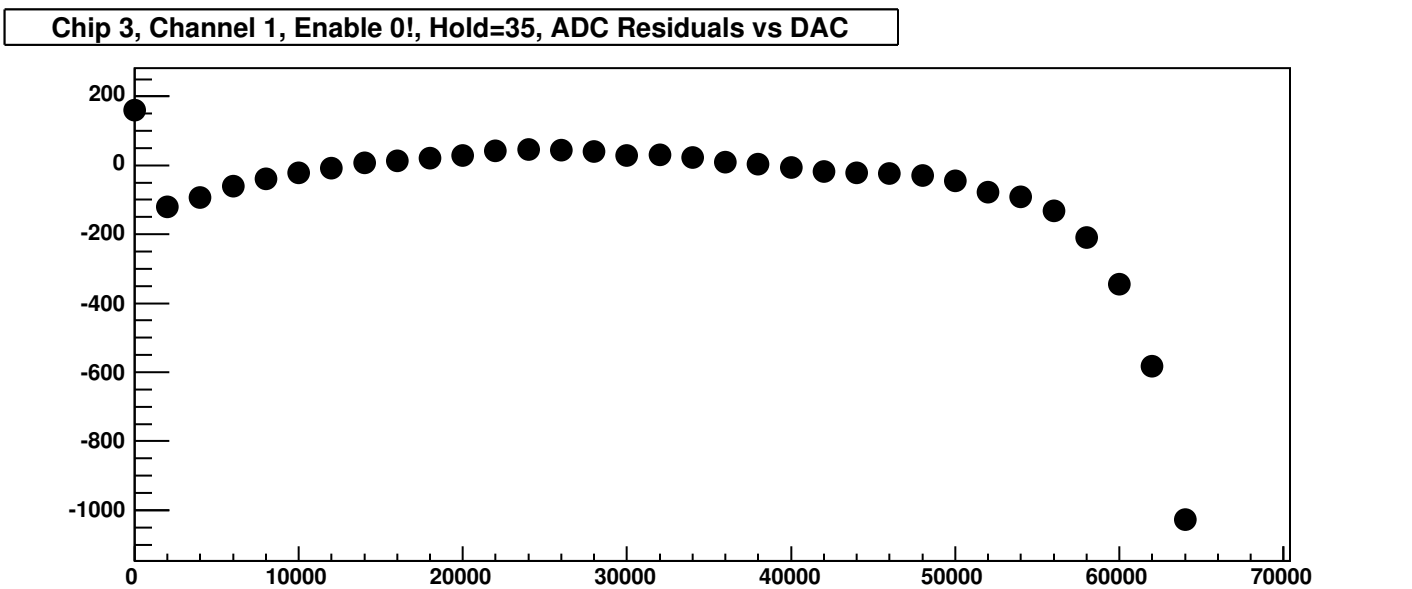


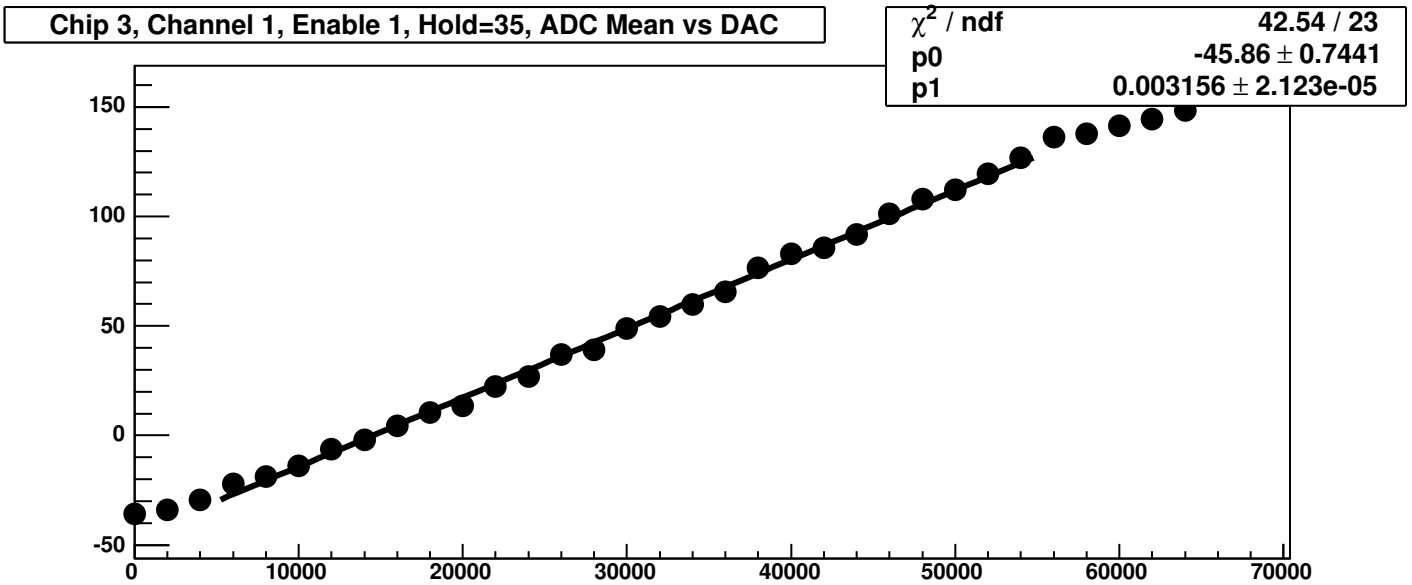
Chip 3, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC

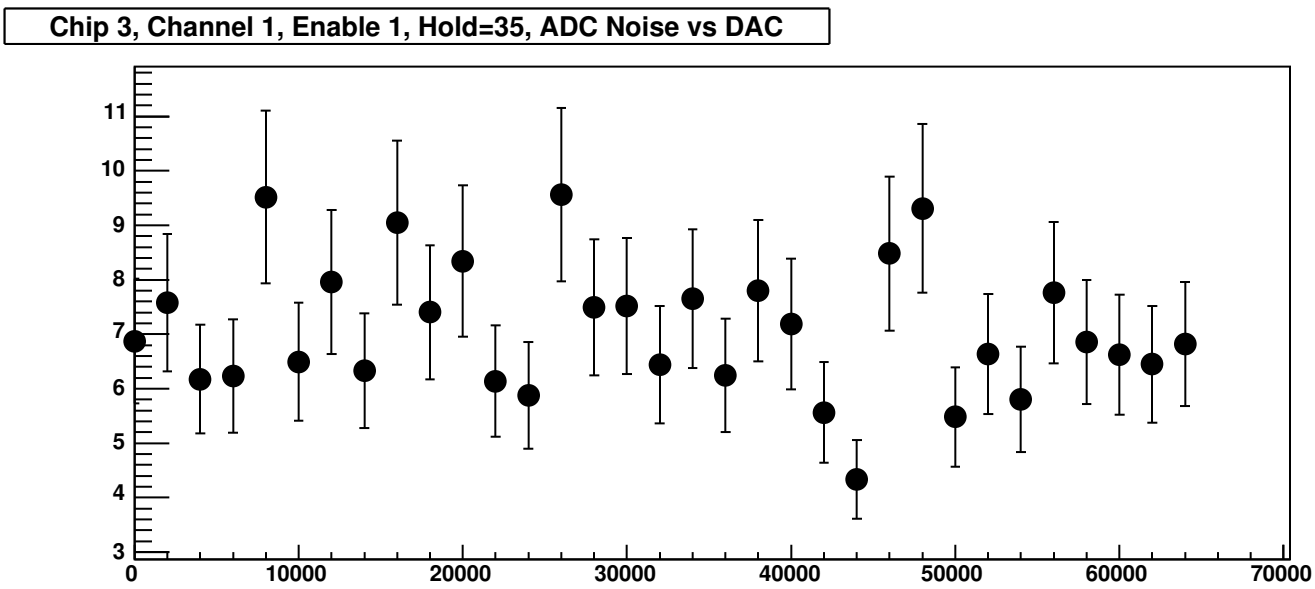


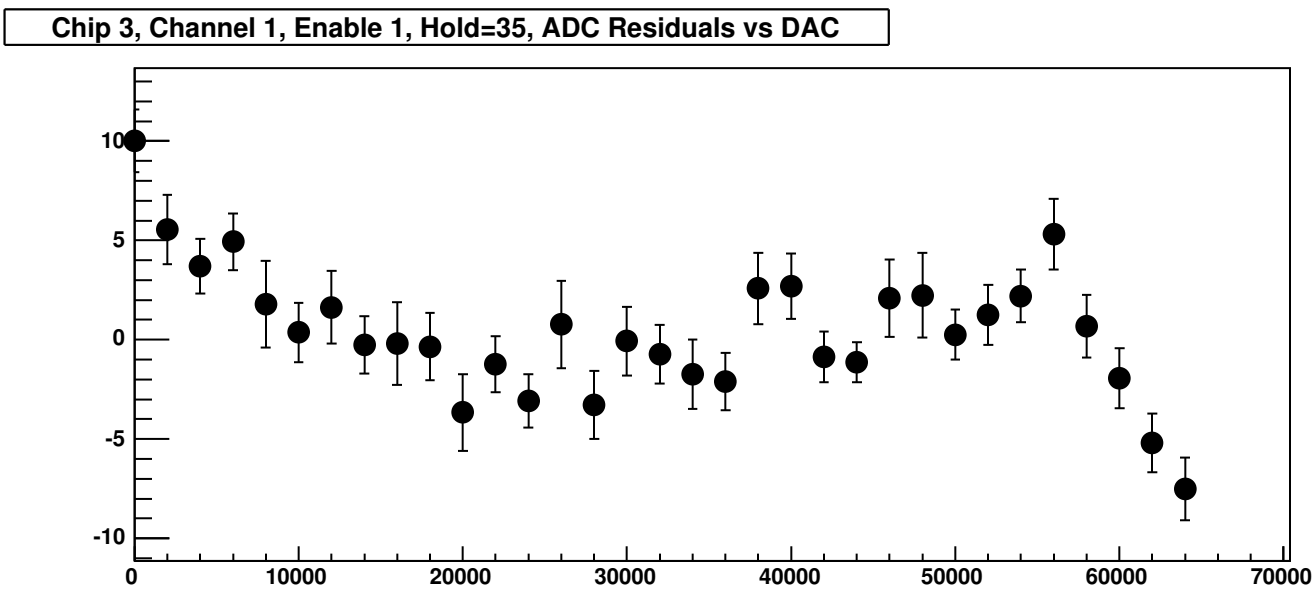

Chip 3, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 4037 / 23
p0 125.8 ± 1.031
p1 0.4787 ± 4.475e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

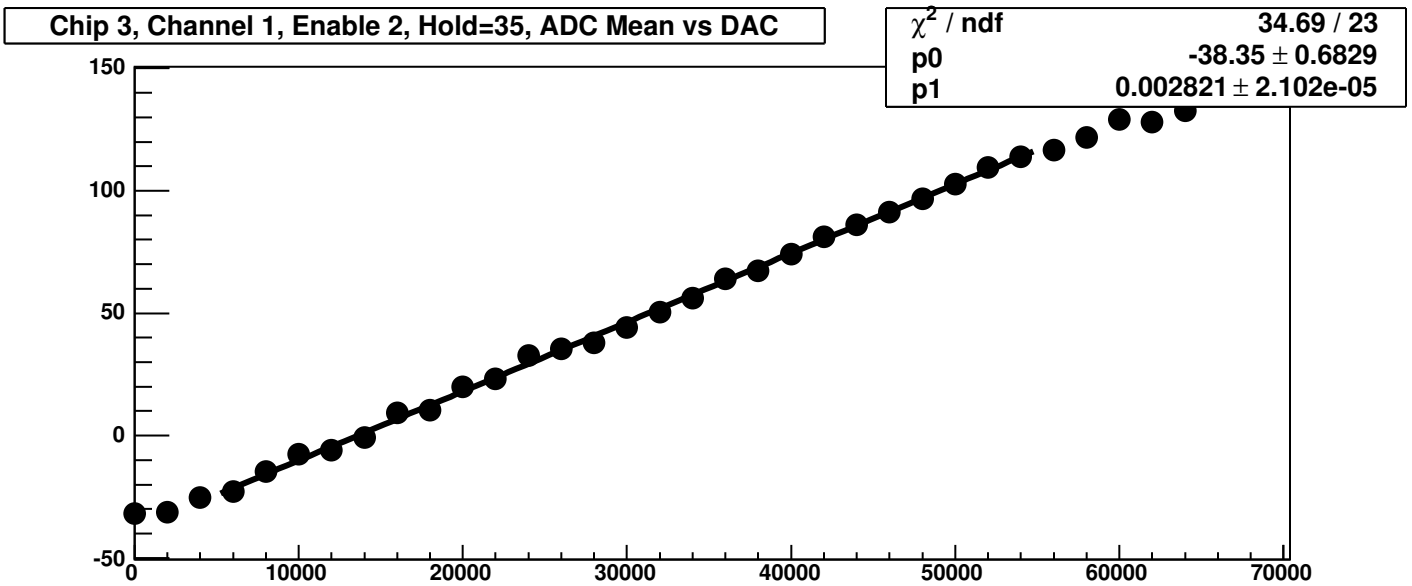

Chip 3, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000

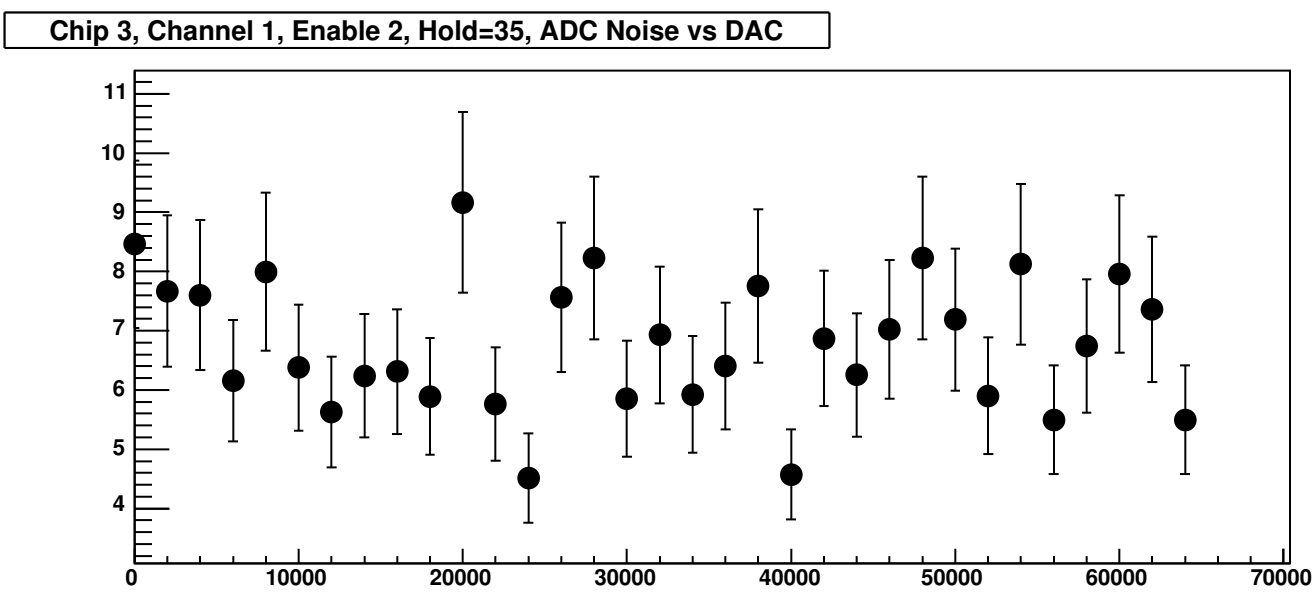

Chip 3, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
10000
20000
30000
40000
50000
60000
70000

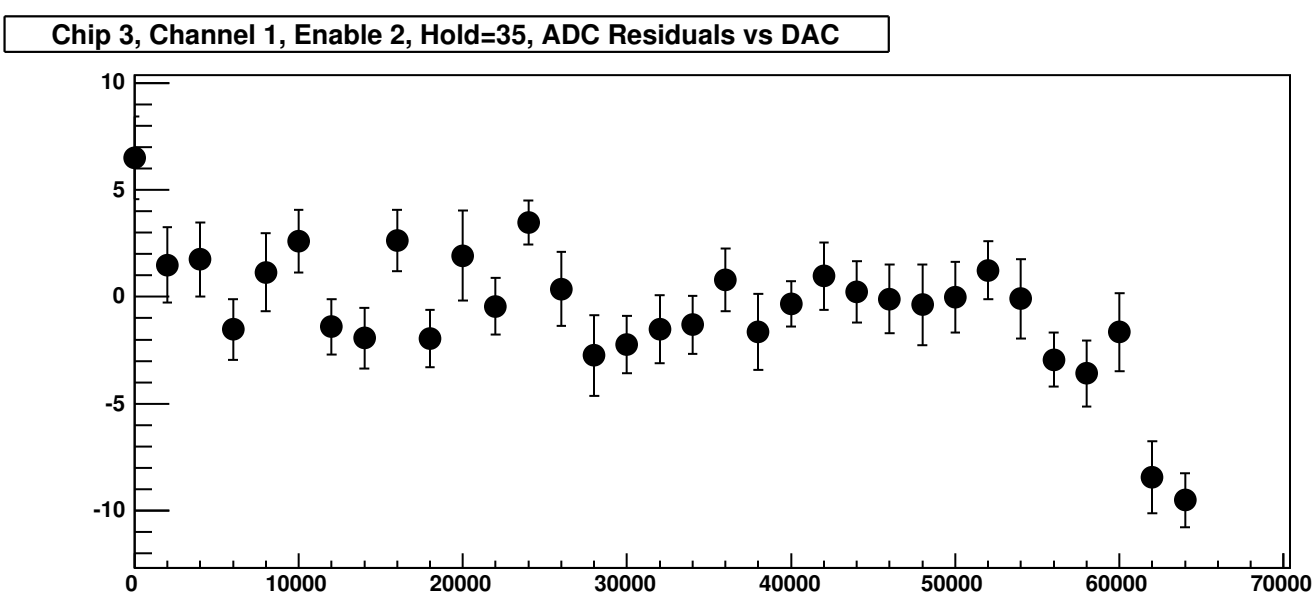

Chip 3, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 42.54 / 23
p0 -45.86 ± 0.7441
p1 0.003156 ± 2.123e-05
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000

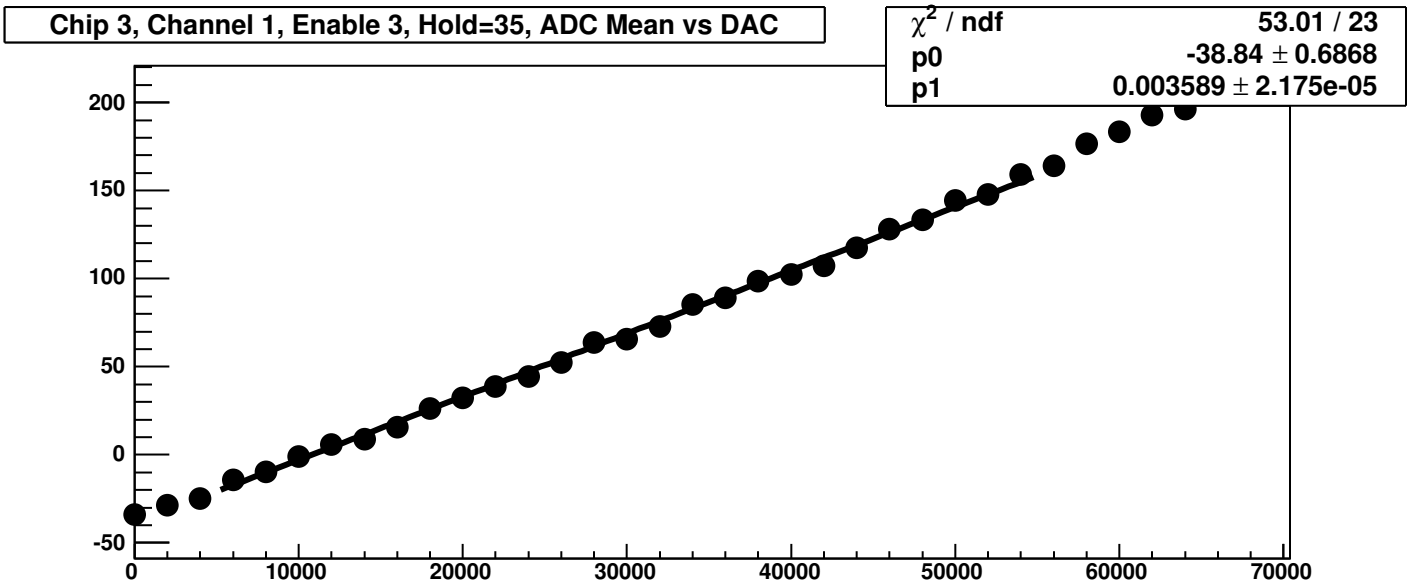

Chip 3, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

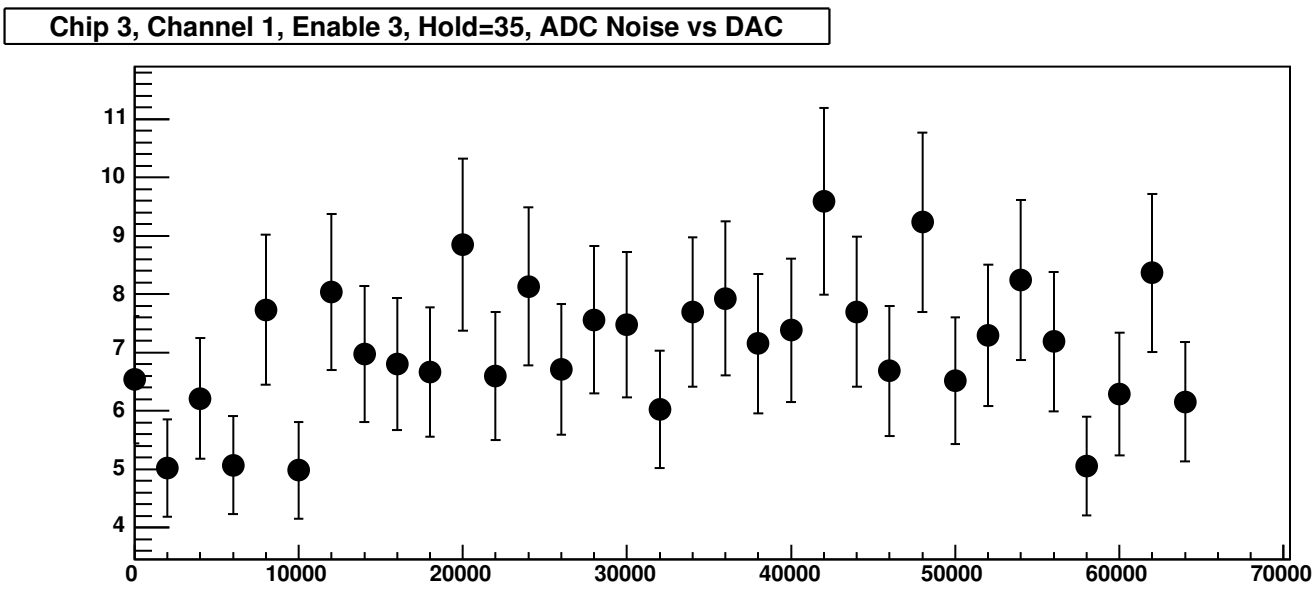

Chip 3, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

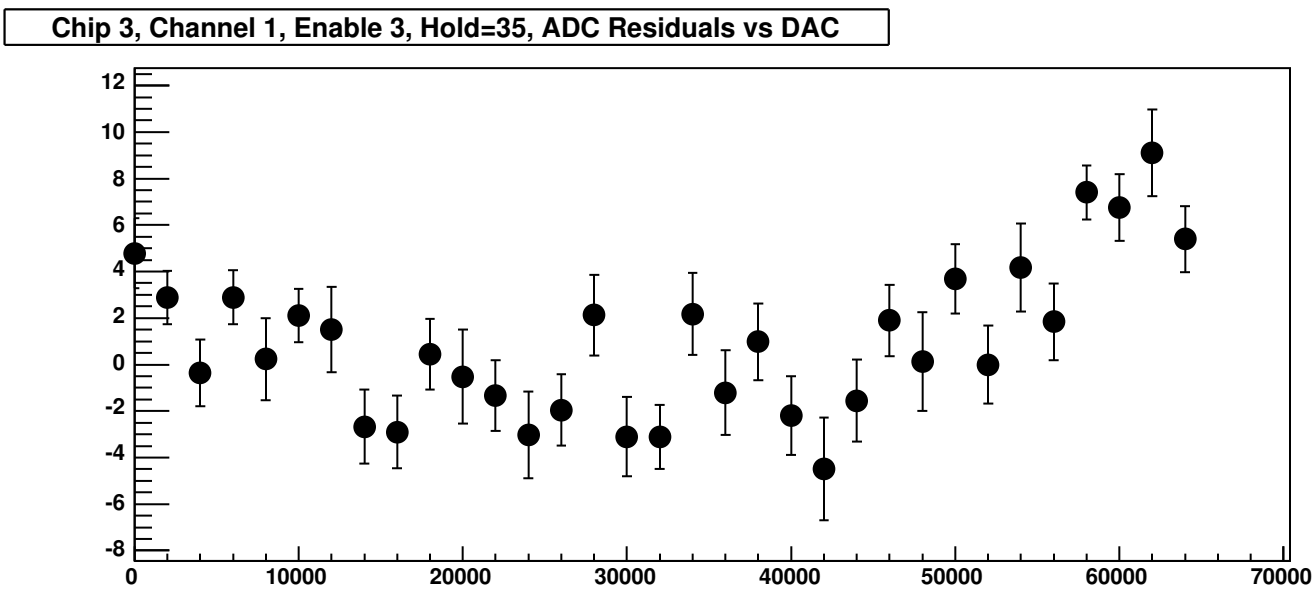

Chip 3, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 34.69 / 23
p0 -38.35 ± 0.6829
p1 0.002821 ± 2.102e-05
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000

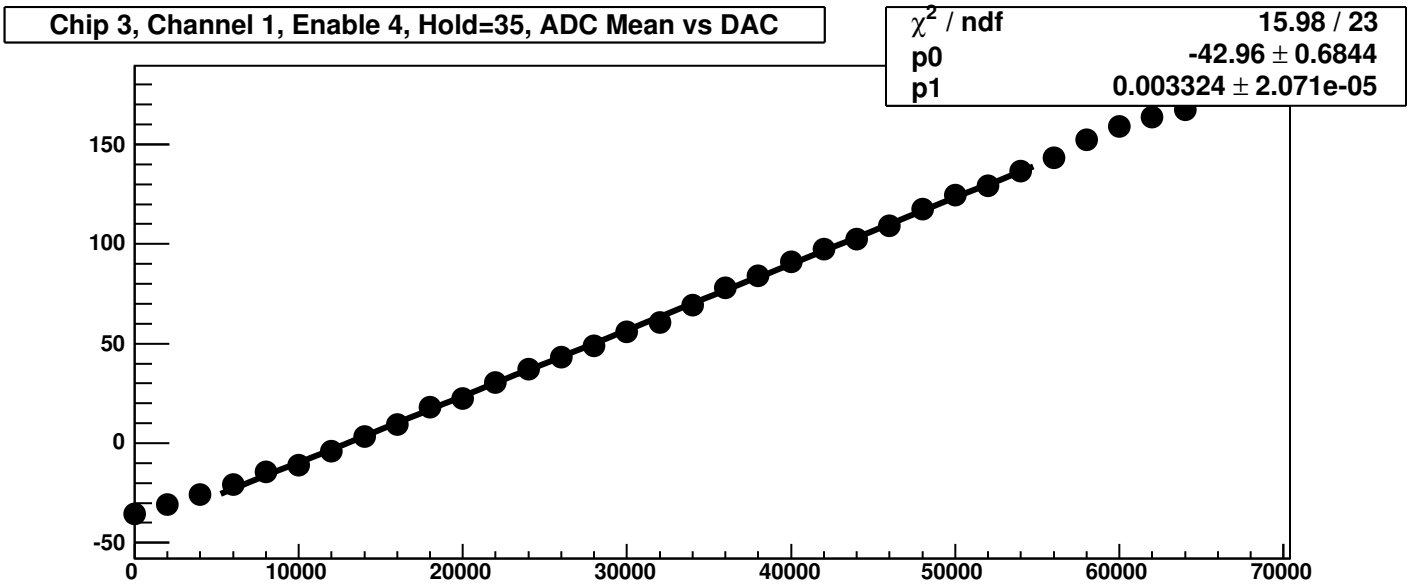

Chip 3, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

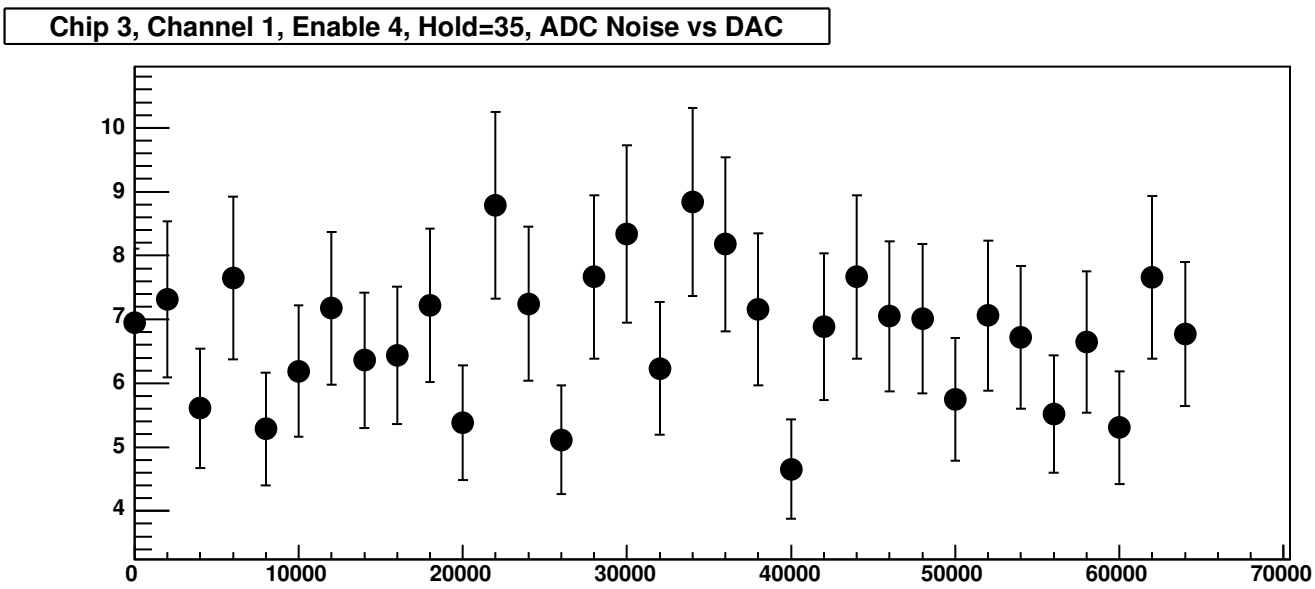

Chip 3, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

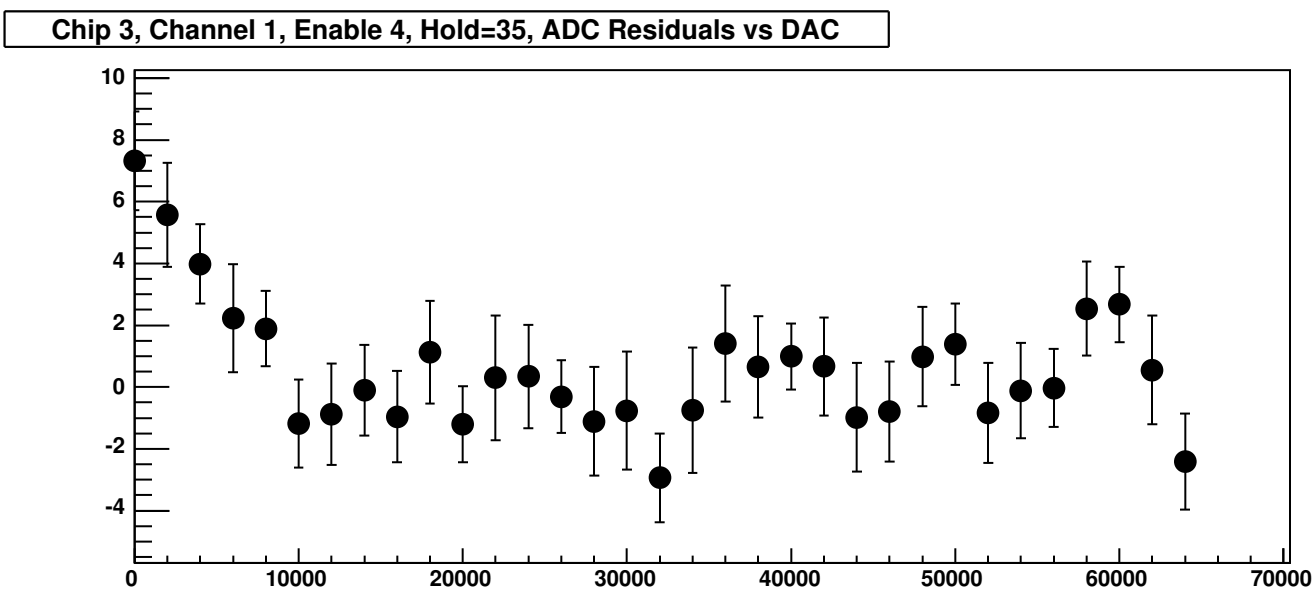

Chip 3, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 53.01 / 23
p0 -38.84 ± 0.6868
p1 0.003589 ± 2.175e-05
200
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000

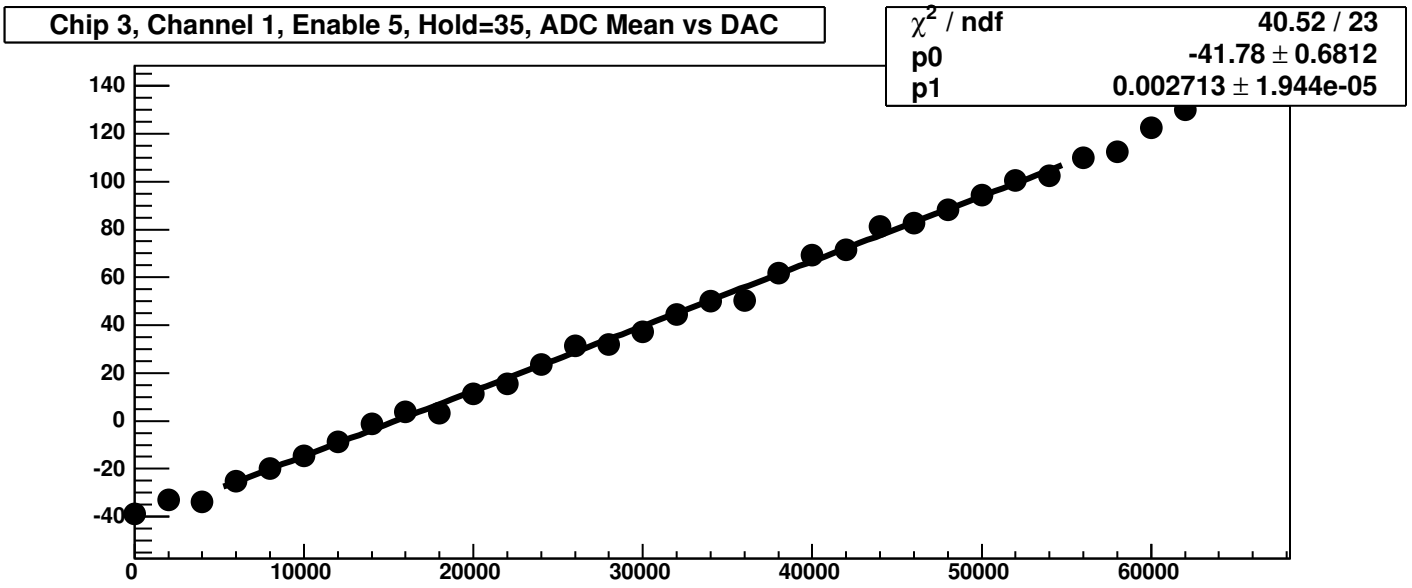

Chip 3, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

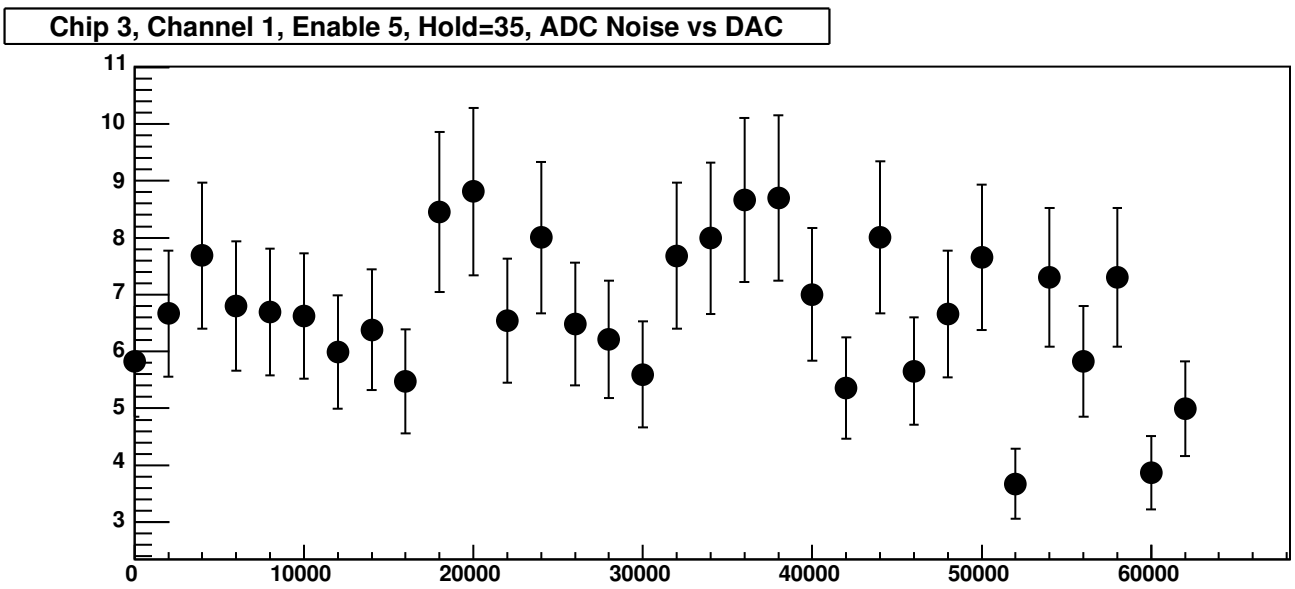

Chip 3, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

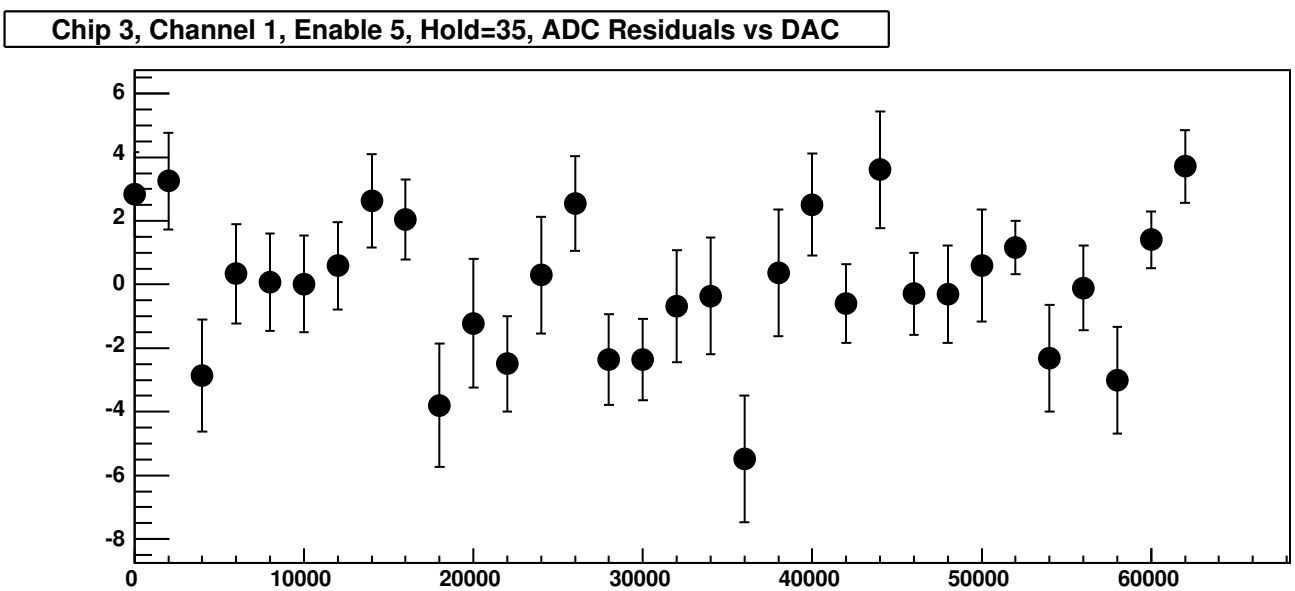

Chip 3, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 15.98 / 23
p0 -42.96 ± 0.6844
p1 0.003324 ± 2.071e-05
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000

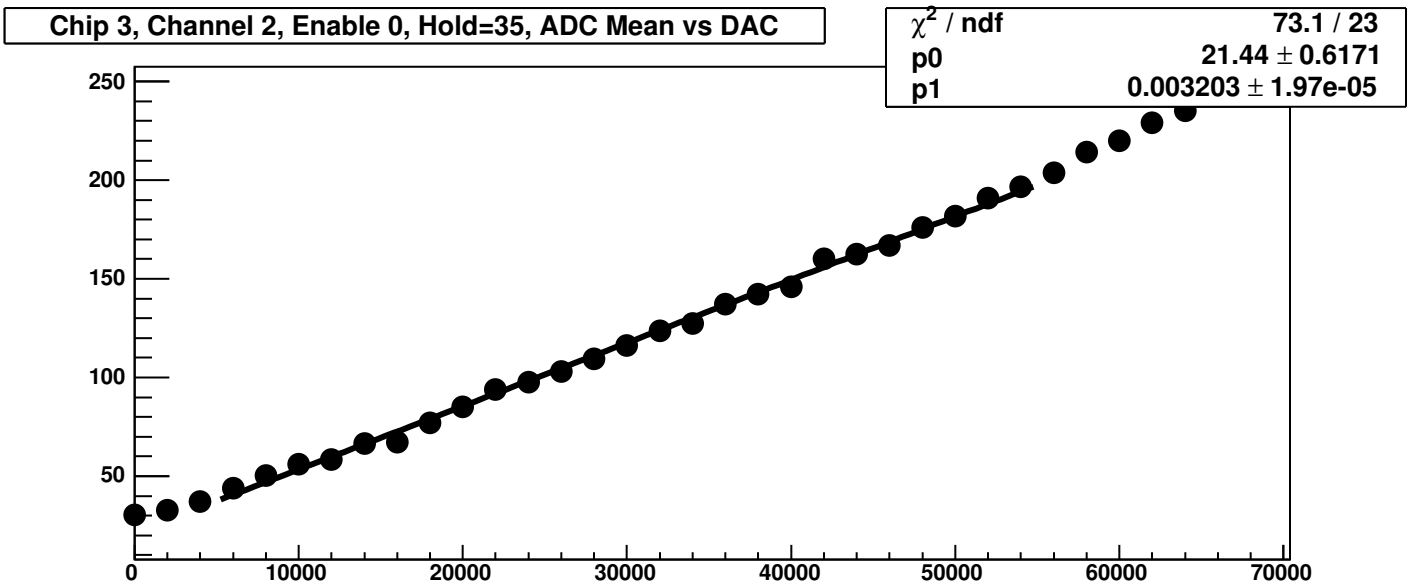

Chip 3, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

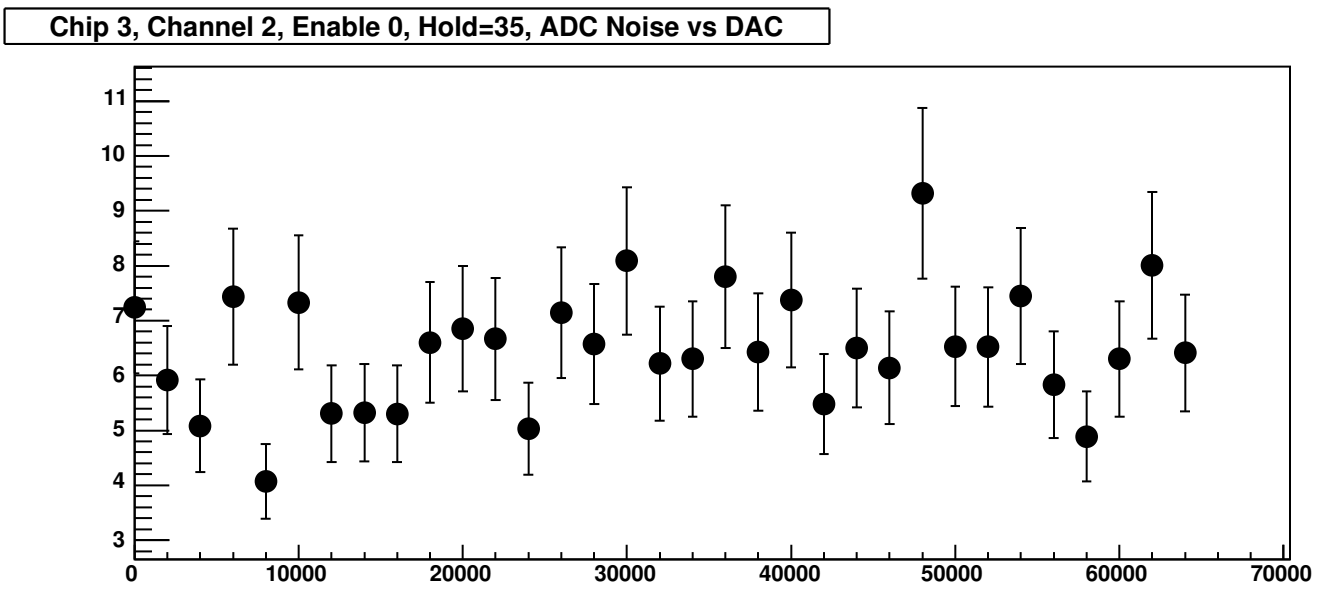

Chip 3, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
0
10000
20000
30000
40000
50000
60000
70000

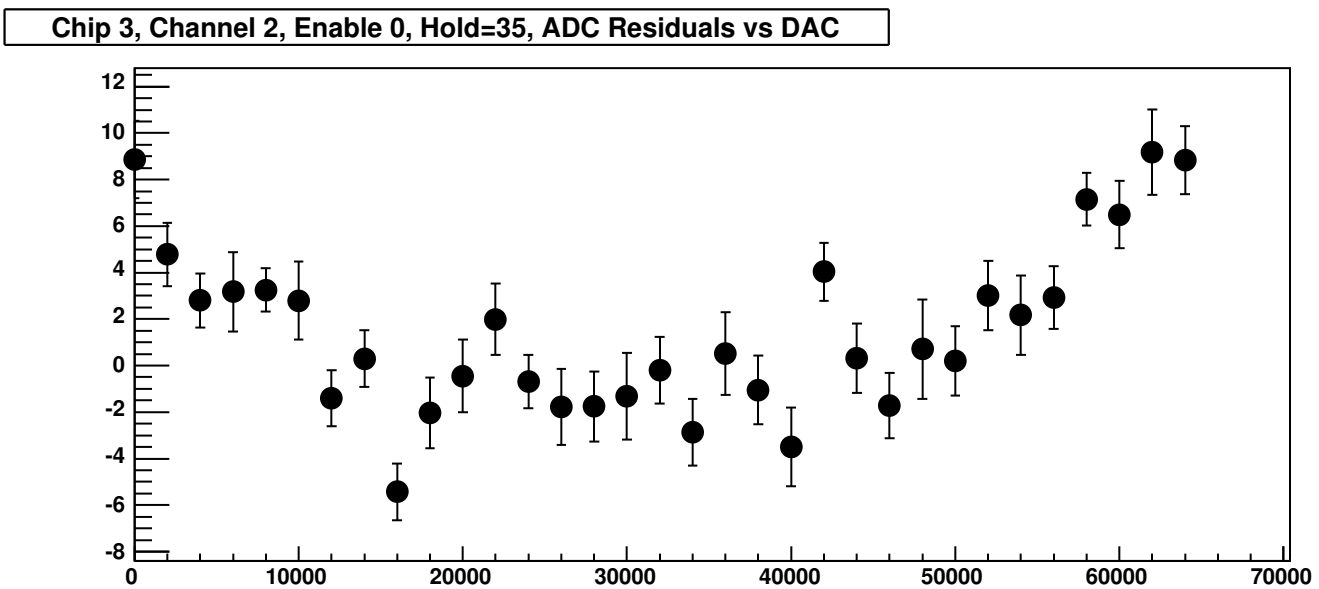

Chip 3, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 40.52 / 23
p0 -41.78 ± 0.6812
p1 0.002713 ± 1.944e-05
140
120
100
80
60
40
20
0
-20
-40
0
10000
20000
30000
40000
50000
60000

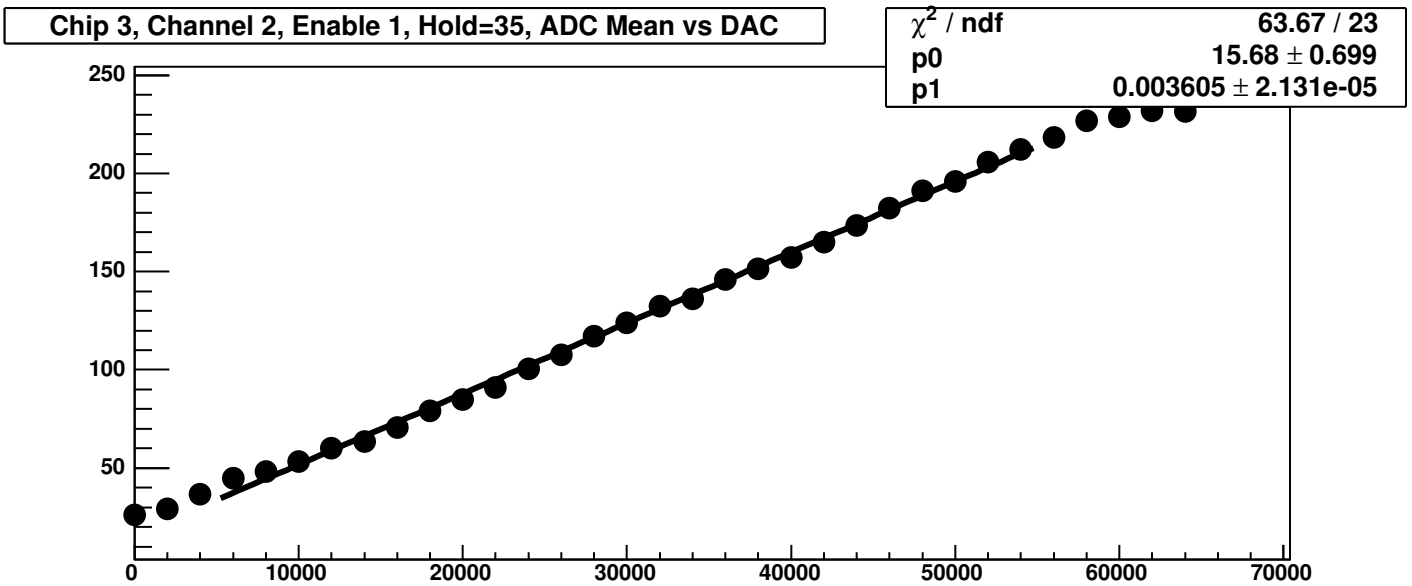

Chip 3, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000

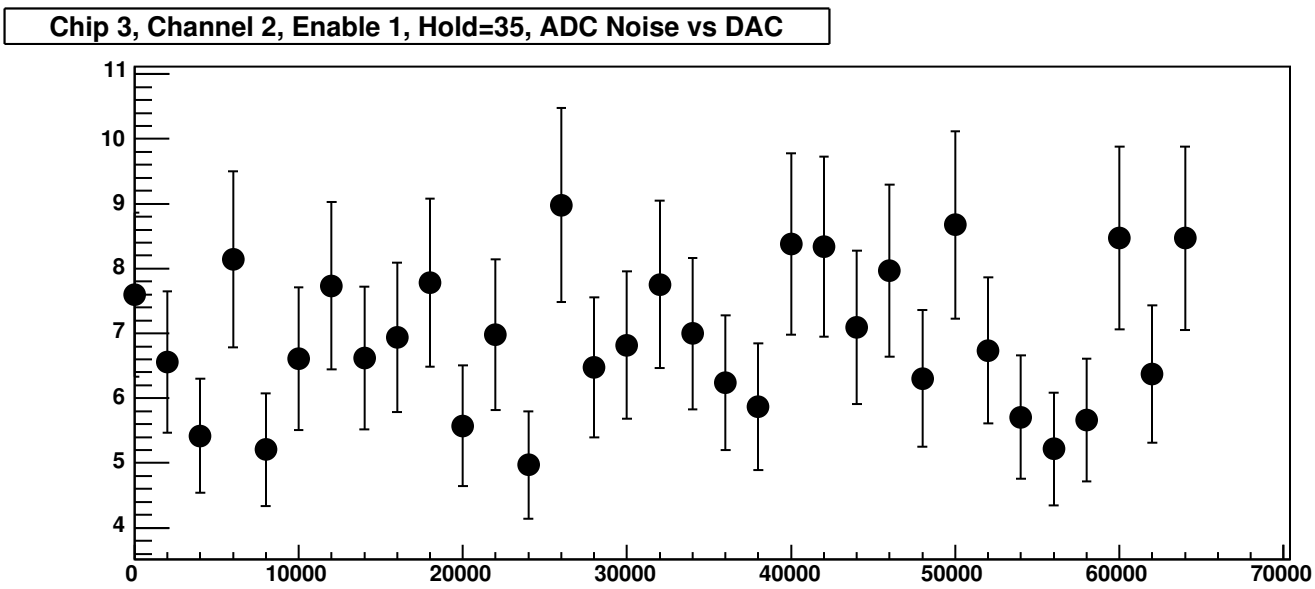

Chip 3, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000

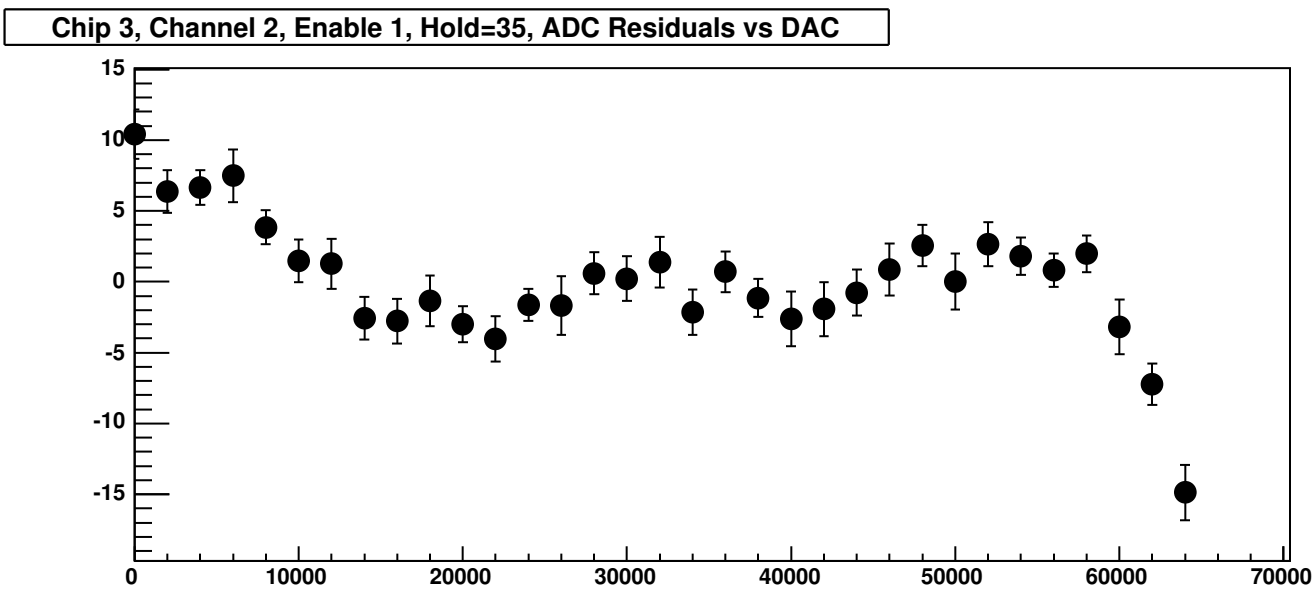

Chip 3, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 73.1 / 23
p0 21.44 ± 0.6171
p1 0.003203 ± 1.97e-05
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

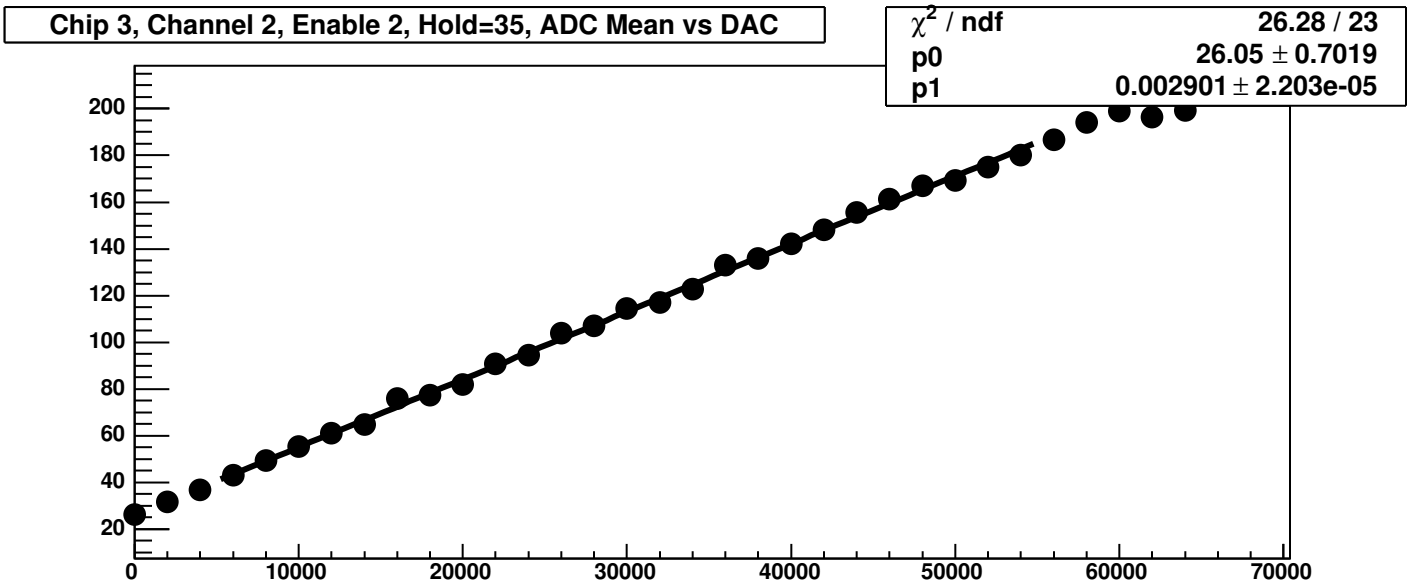

Chip 3, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

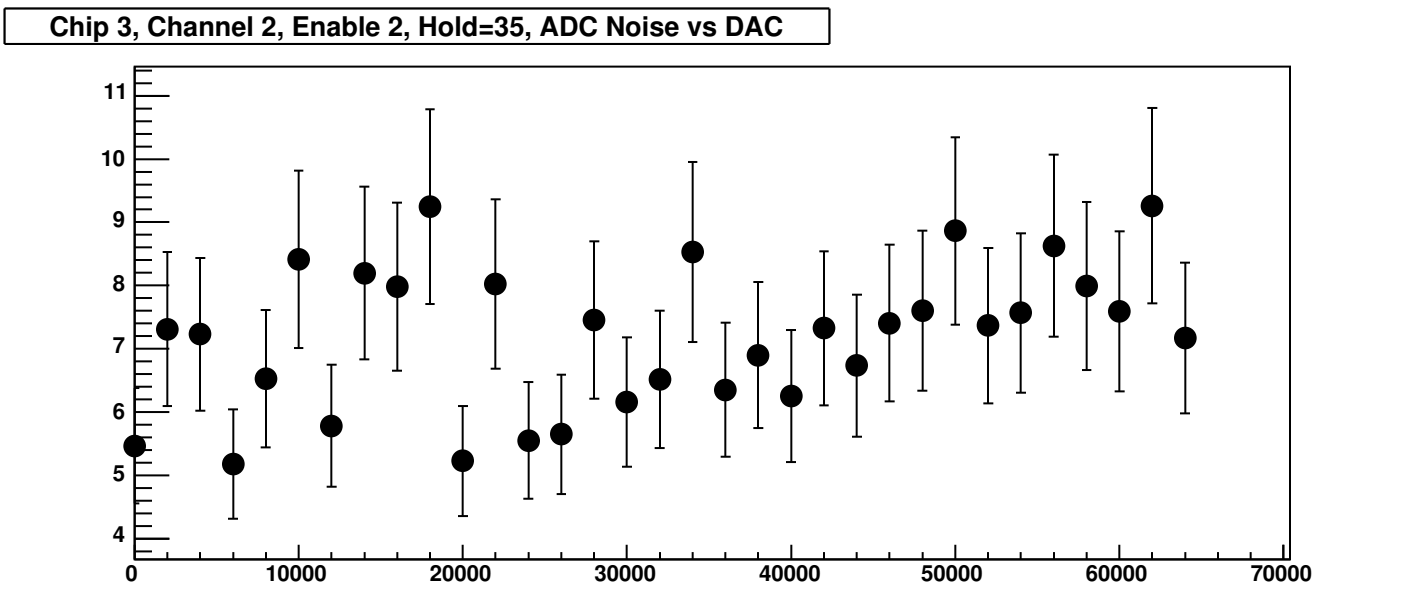

Chip 3, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

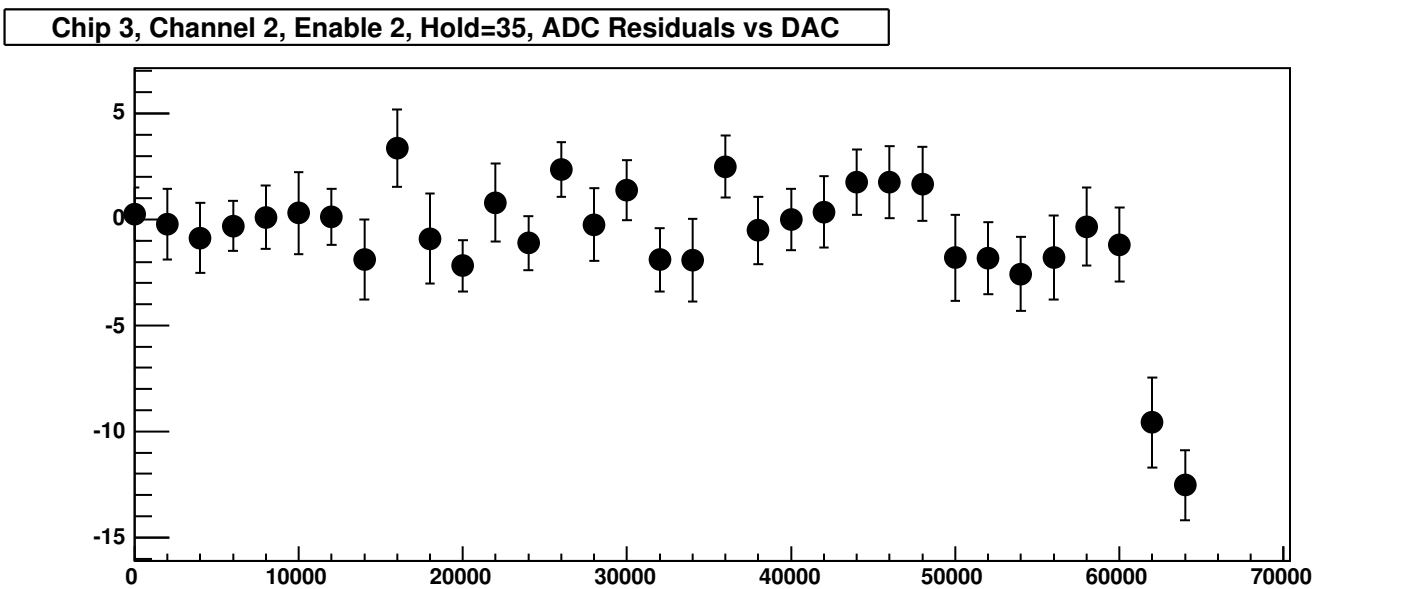

Chip 3, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 63.67 / 23
p0 15.68 ± 0.699
p1 0.003605 ± 2.131e-05
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

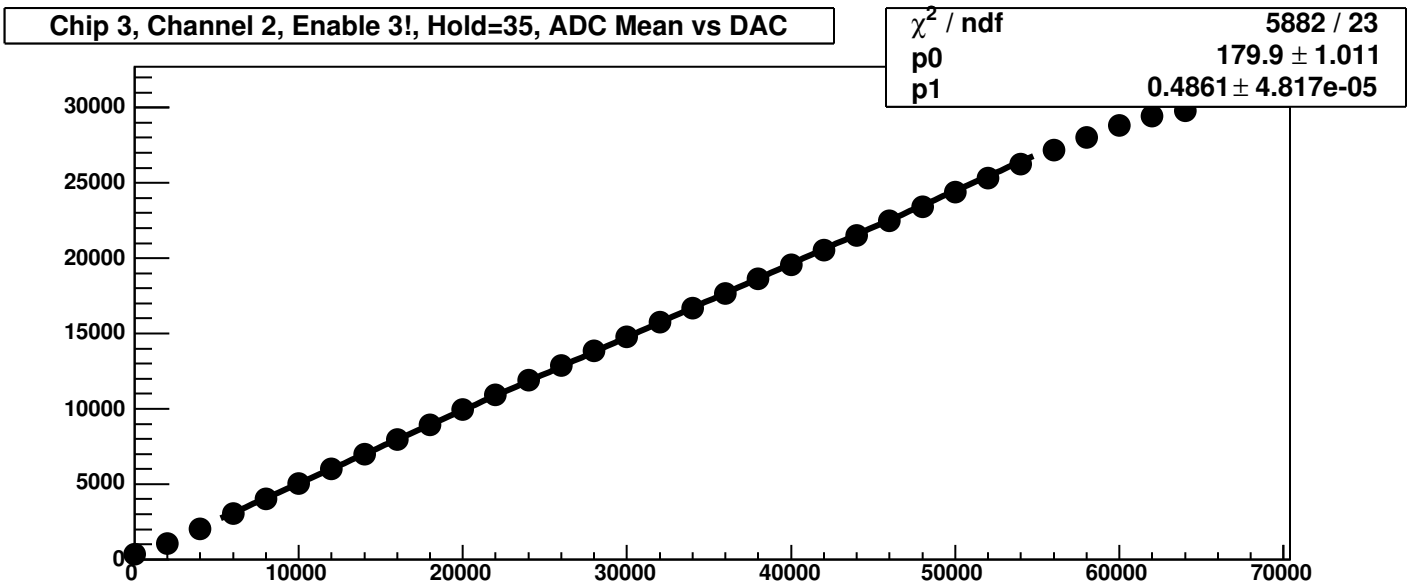

Chip 3, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

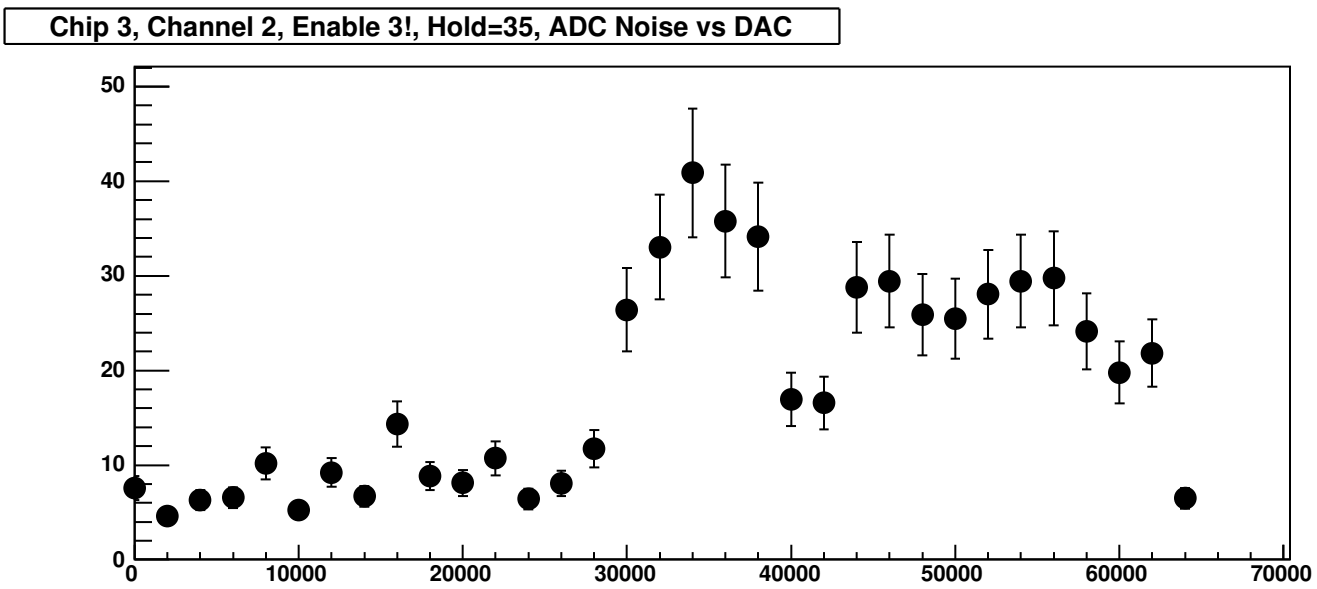

Chip 3, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

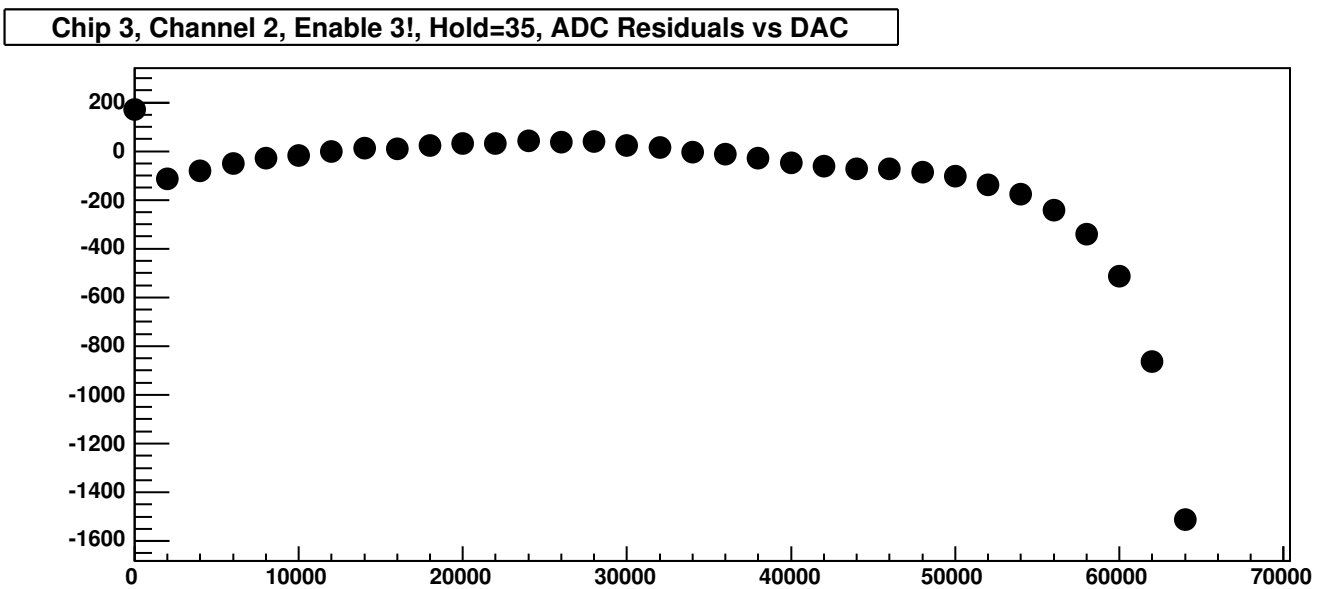

Chip 3, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 26.28 / 23
p0 26.05 ± 0.7019
p1 0.002901 ± 2.203e-05
200
180
160
140
120
100
80
60
40
20
0
10000
20000
30000
40000
50000
60000
70000

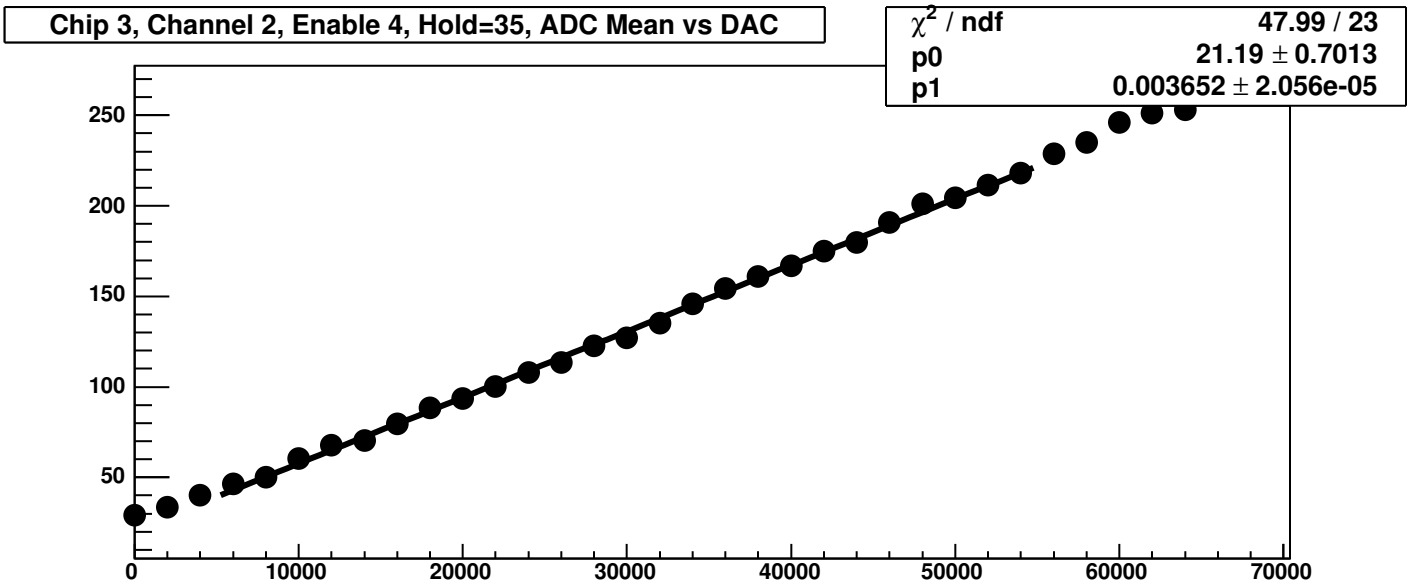

Chip 3, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

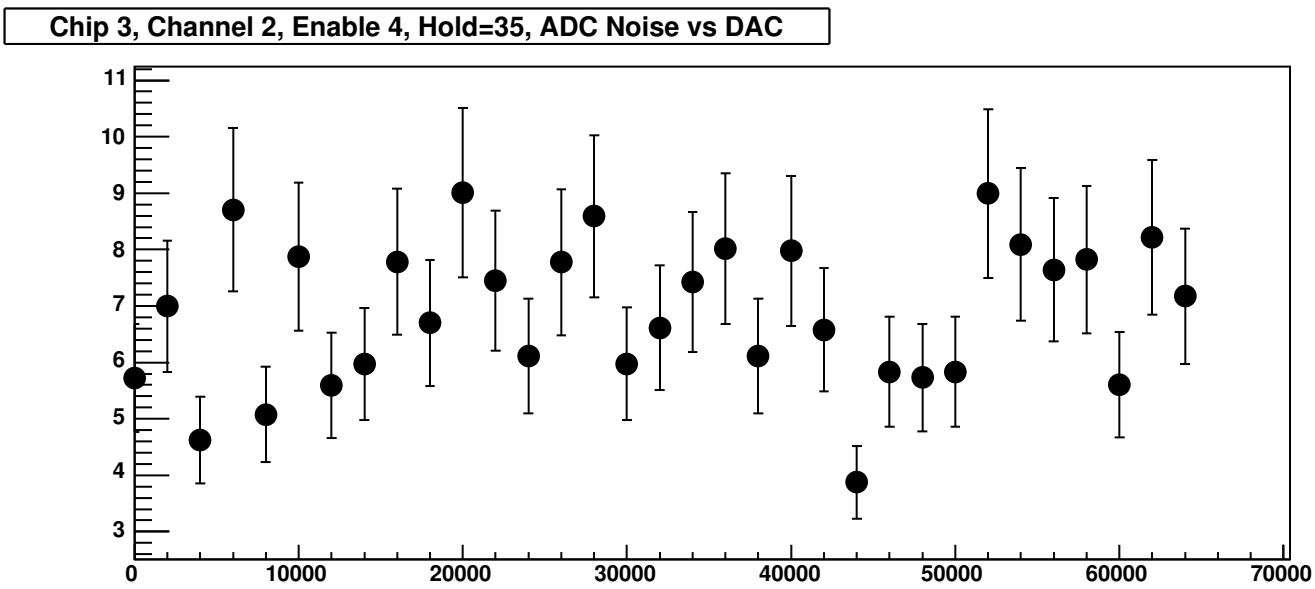

Chip 3, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

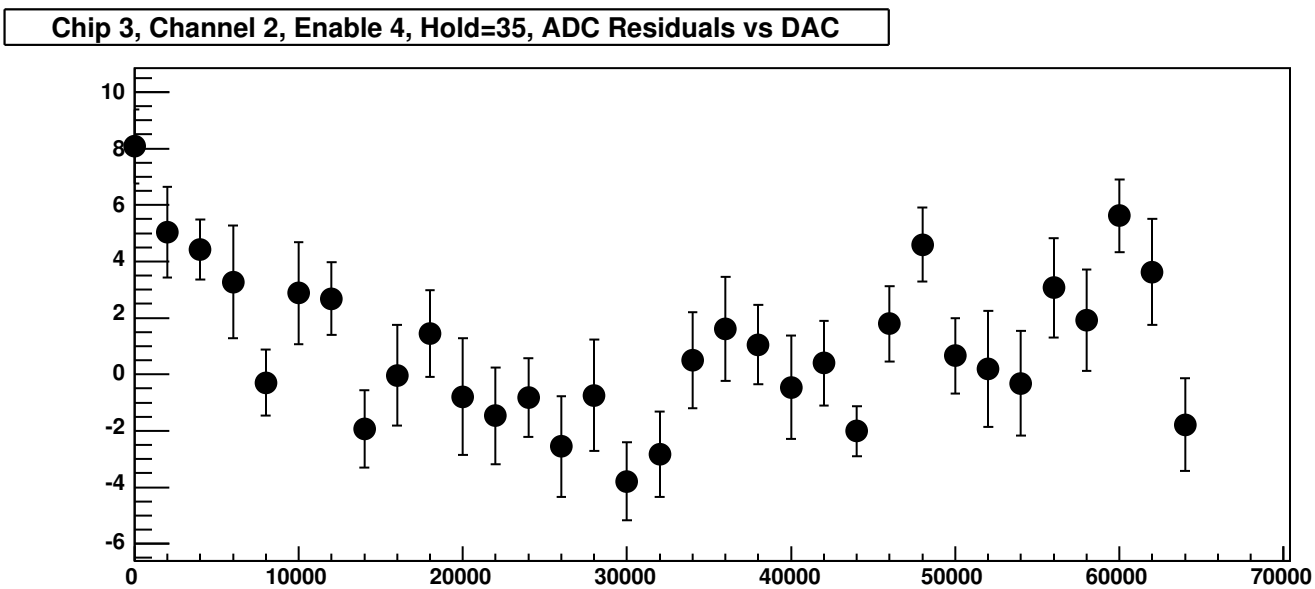

Chip 3, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 5882 / 23
p0 179.9 ± 1.011
p1 0.4861 ± 4.817e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

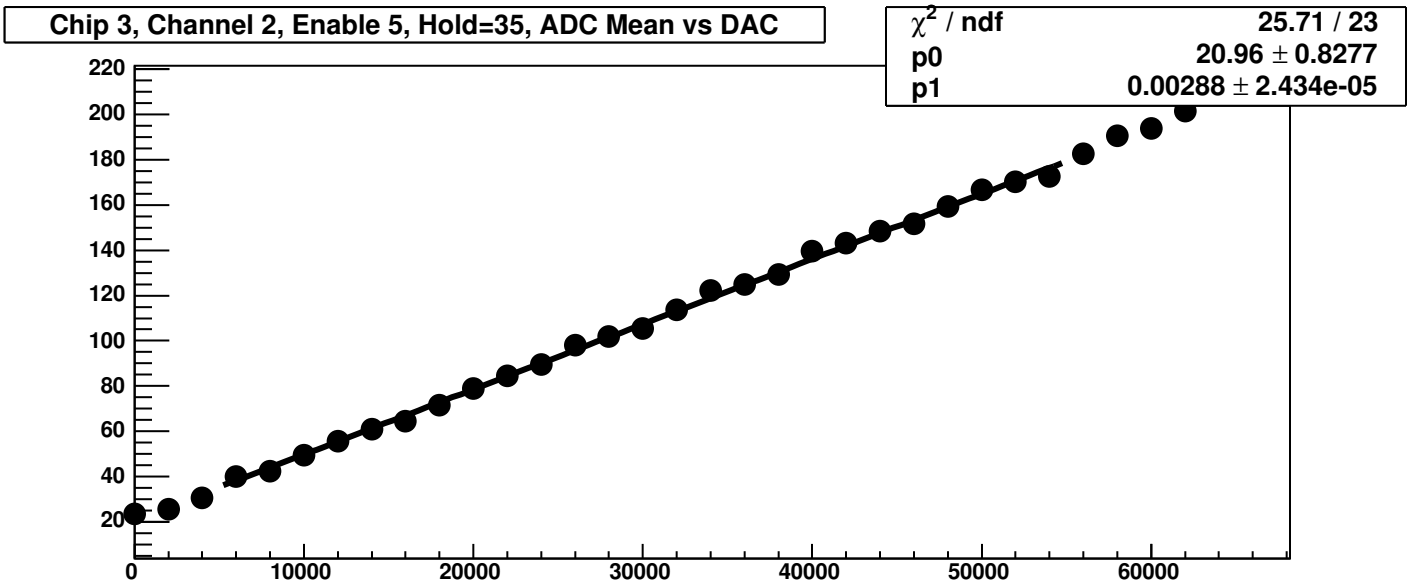

Chip 3, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

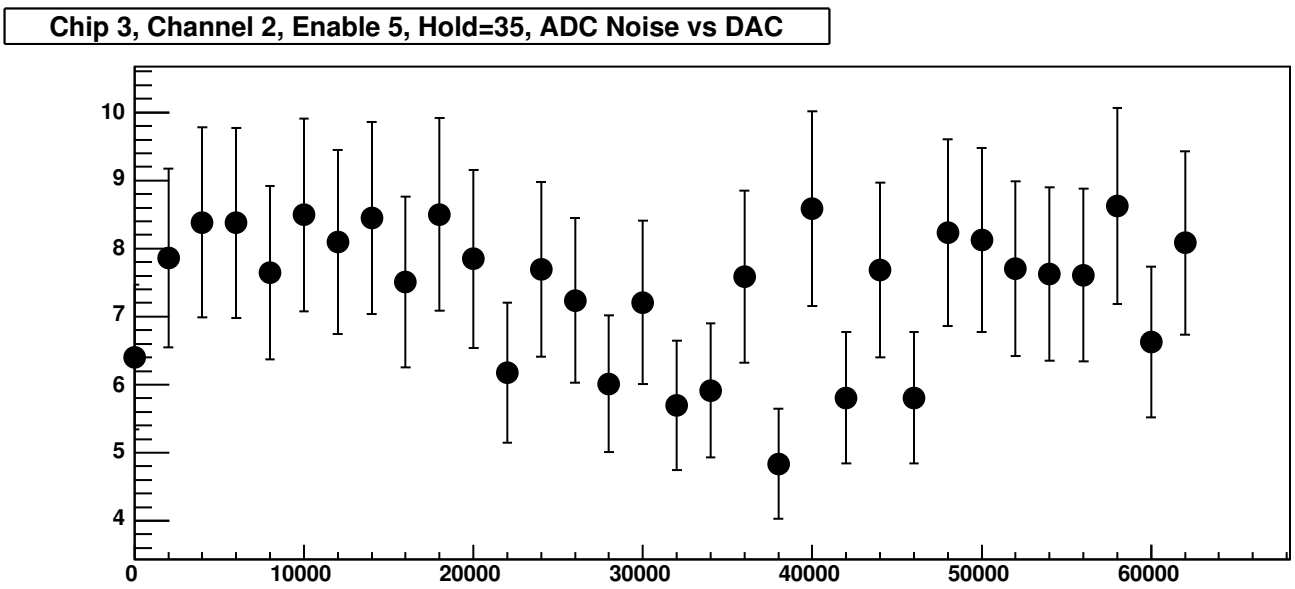

Chip 3, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
0
10000
20000
30000
40000
50000
60000
70000

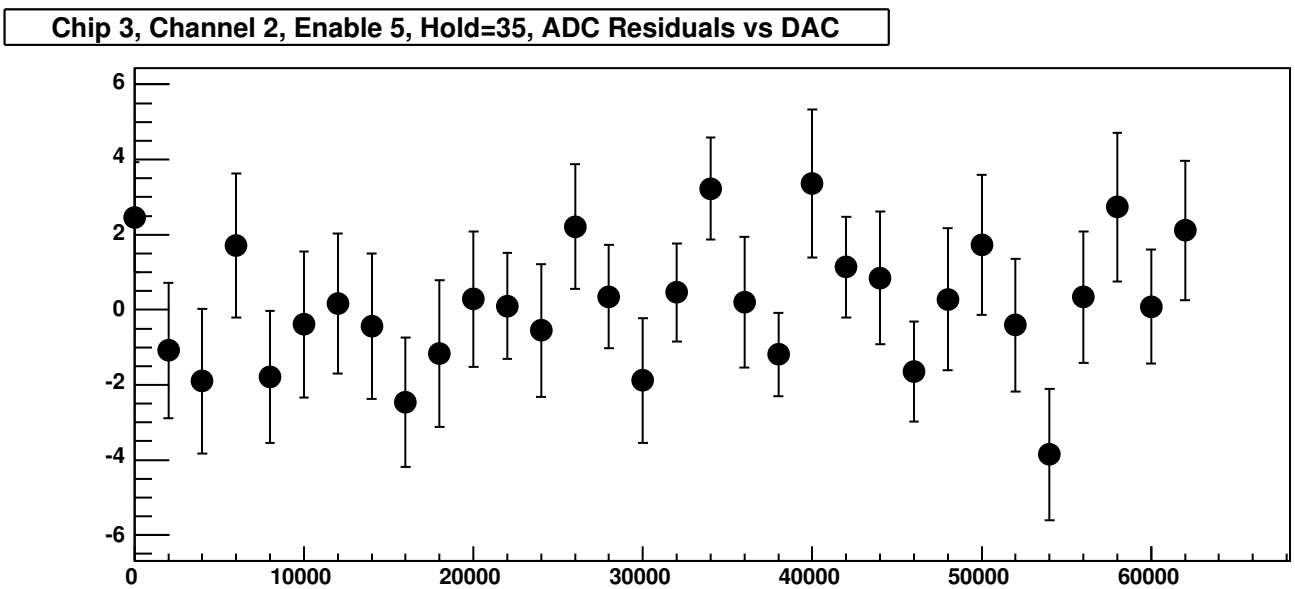

Chip 3, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 47.99 / 23
p0 21.19 ± 0.7013
p1 0.003652 ± 2.056e-05
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

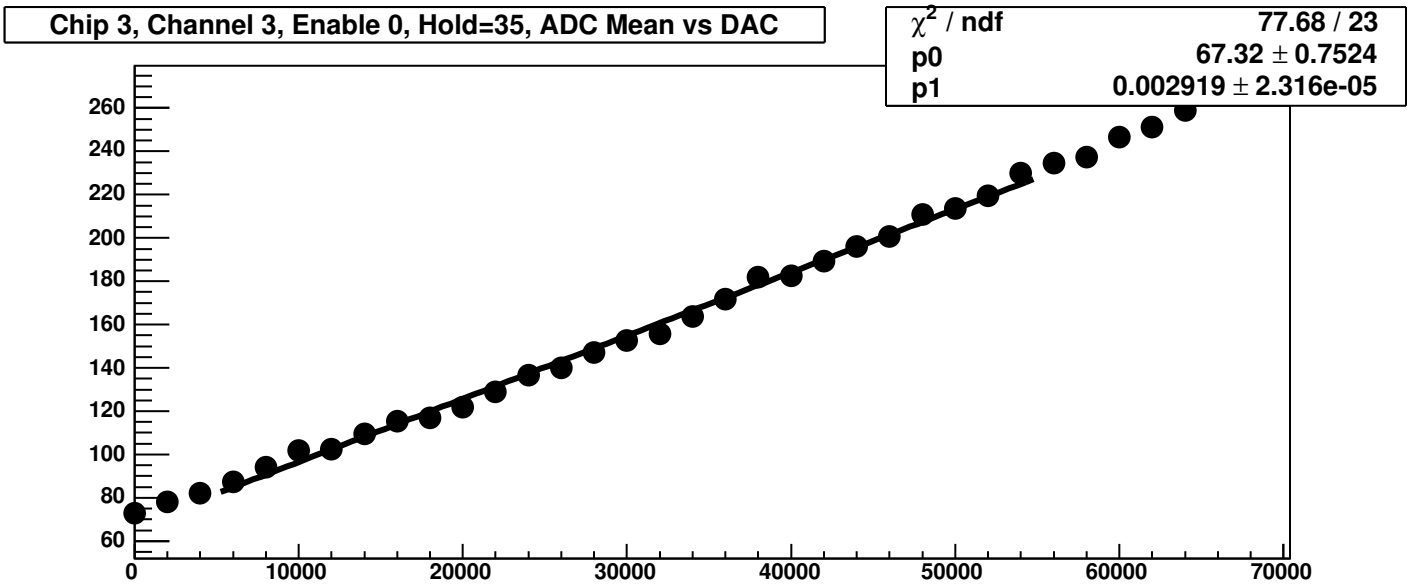

Chip 3, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

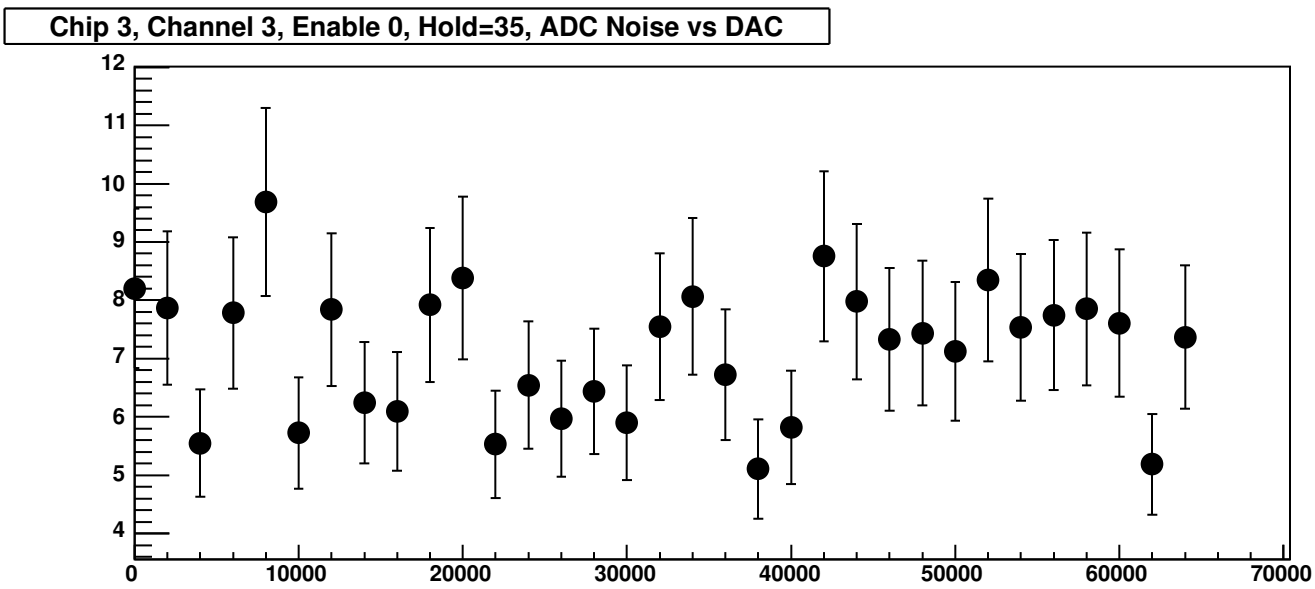

Chip 3, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

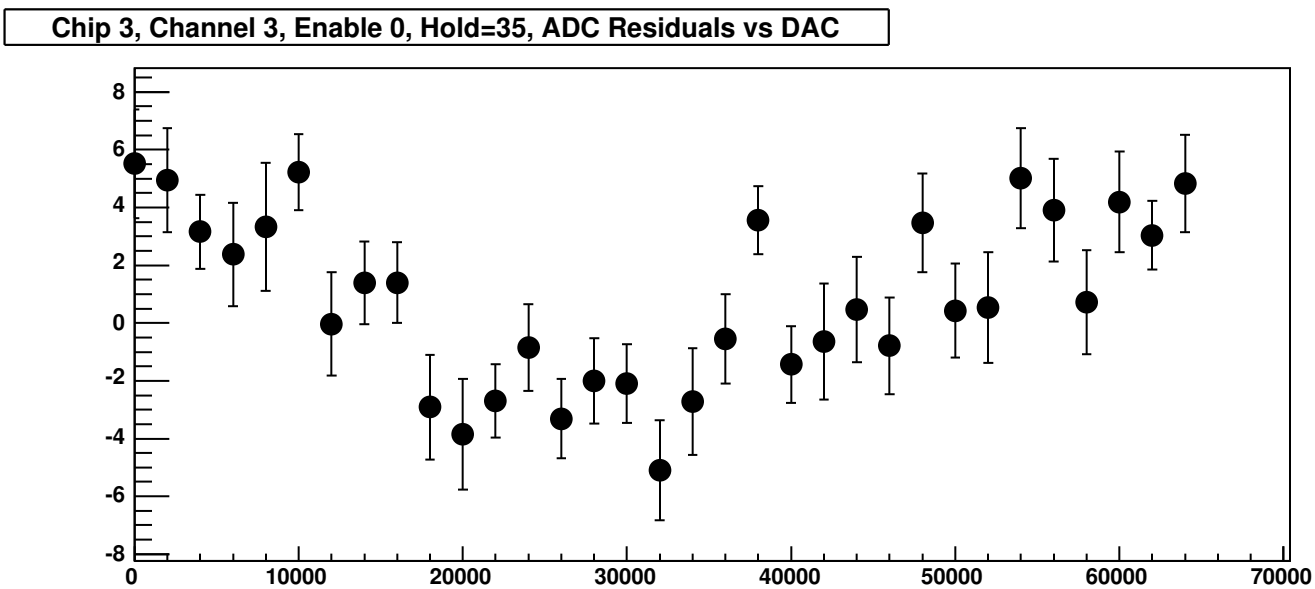

Chip 3, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 25.71 / 23
p0 20.96 ± 0.8277
p1 0.00288 ± 2.434e-05
220
200
180
160
140
120
100
80
60
40
20
0
10000
20000
30000
40000
50000
60000

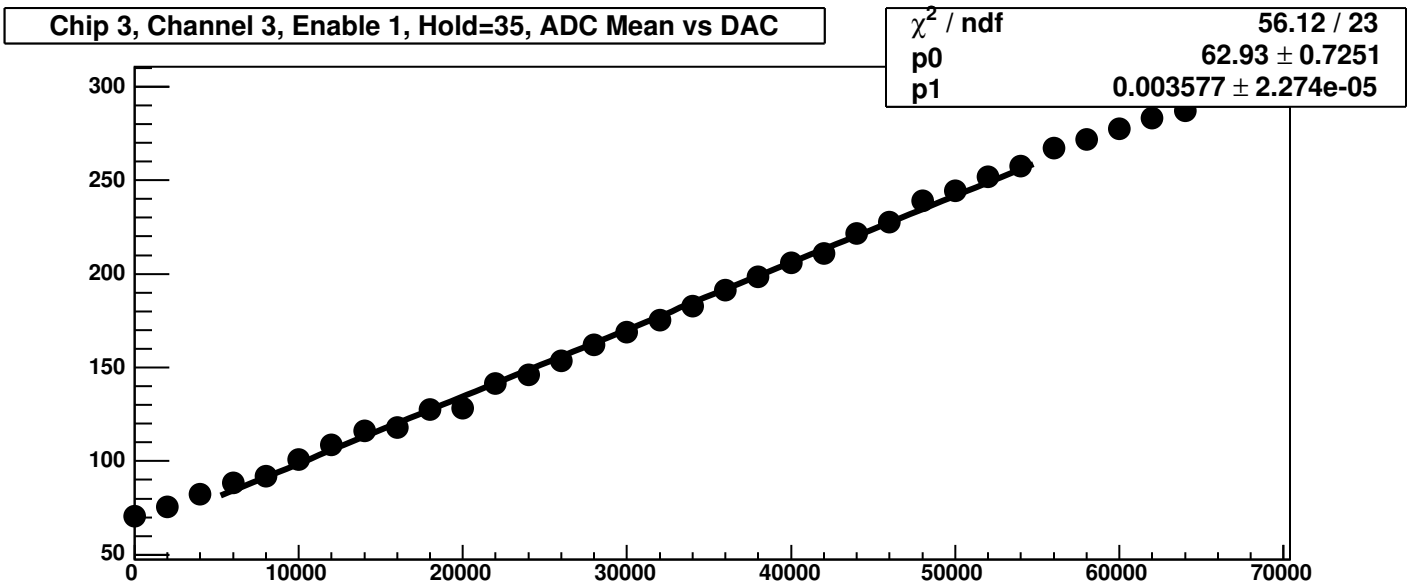

Chip 3, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

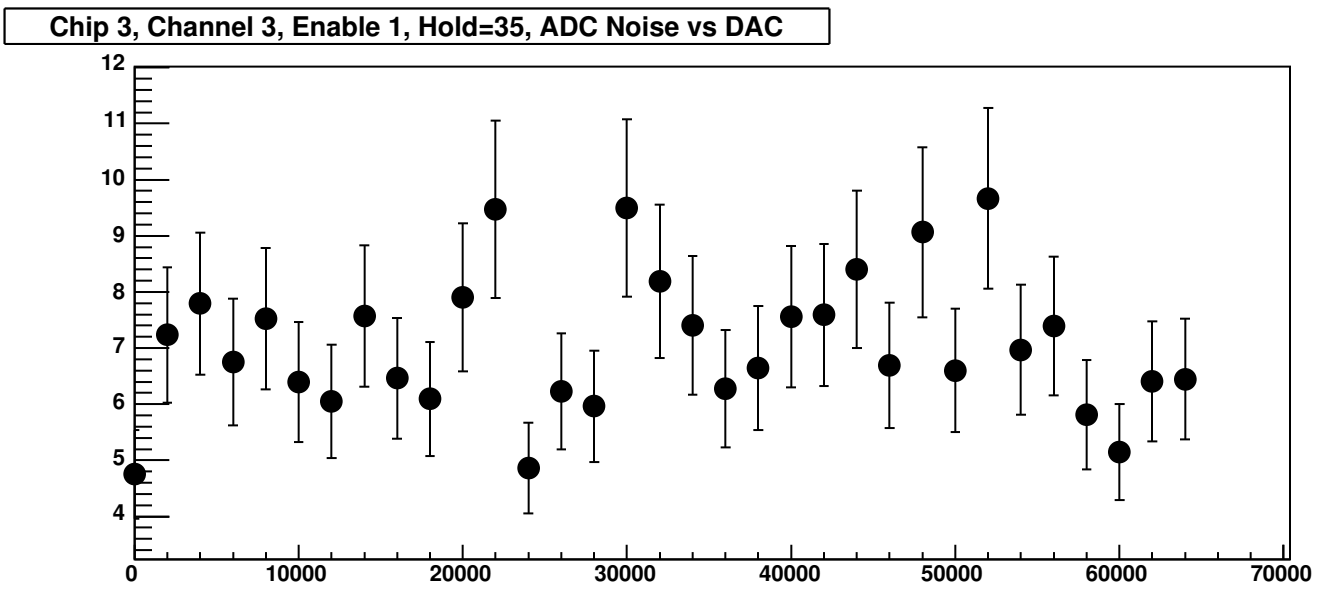

Chip 3, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000

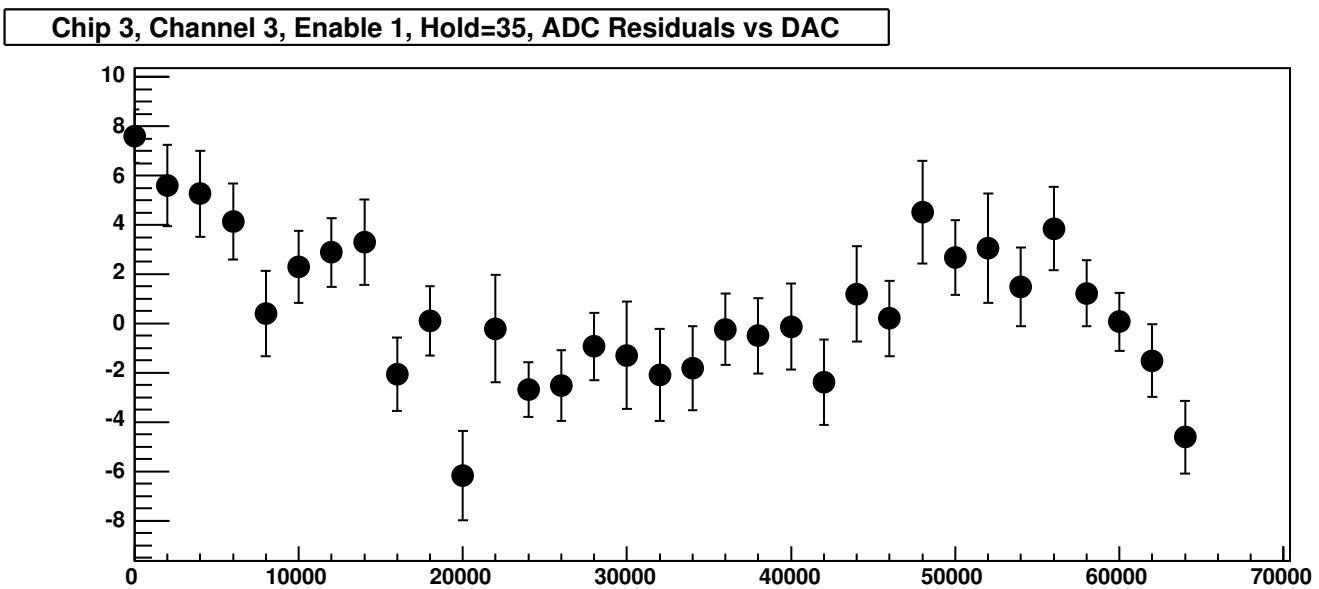

Chip 3, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 77.68 / 23
p0 67.32 ± 0.7524
p1 0.002919 ± 2.316e-05
260
240
220
200
180
160
140
120
100
80
60
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

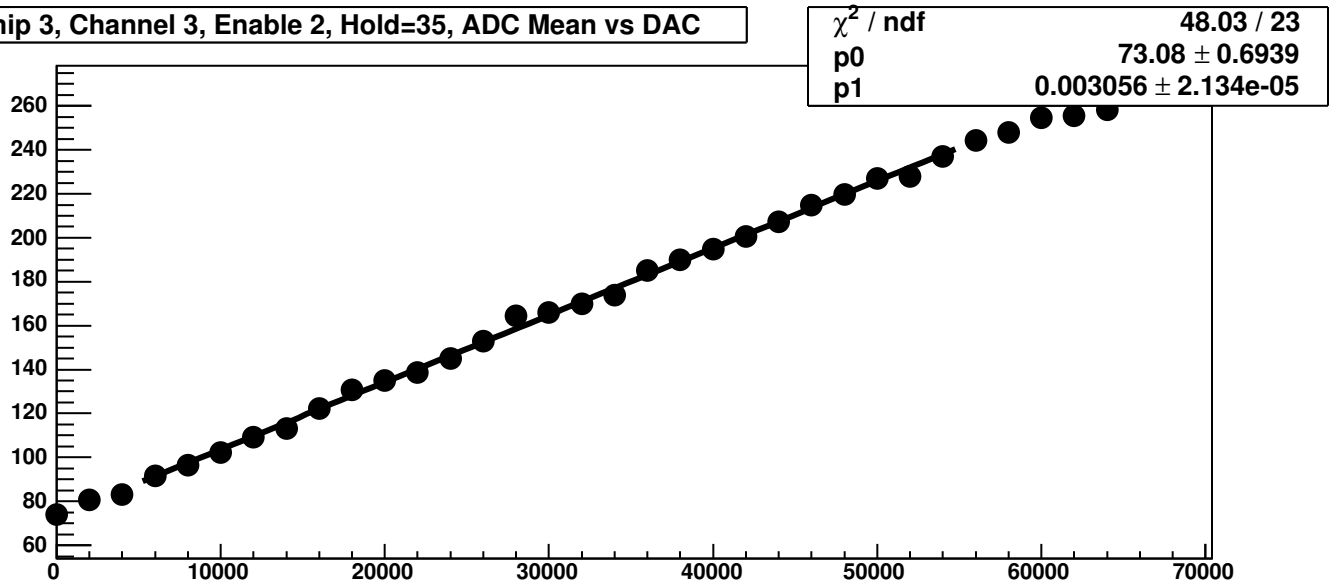

Chip 3, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 56.12 / 23
p0 62.93 ± 0.7251
p1 0.003577 ± 2.274e-05
300
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

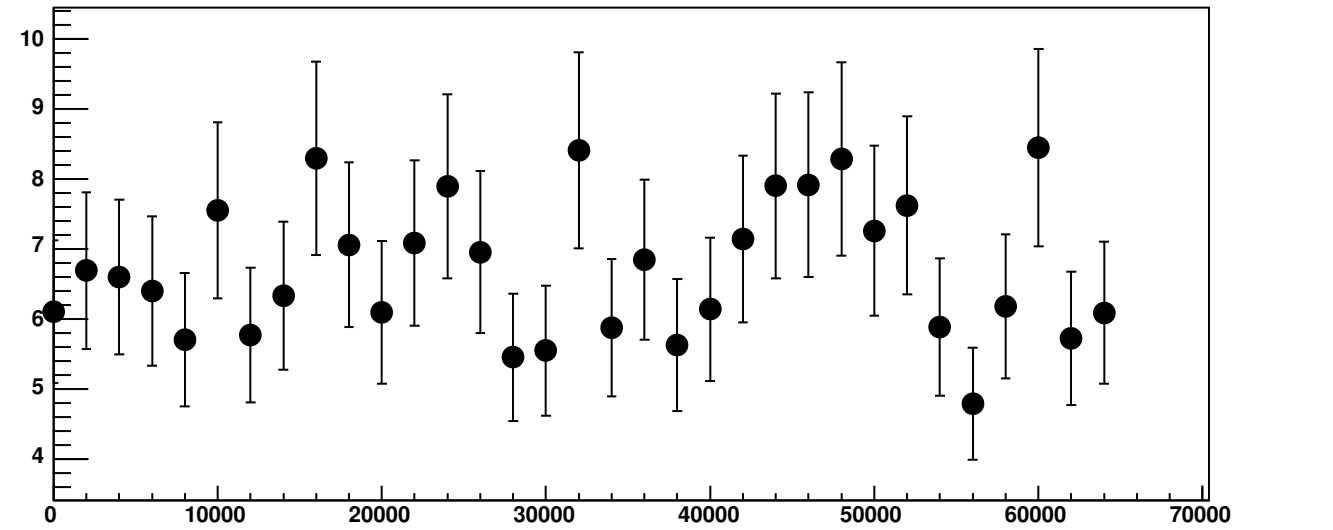

Chip 3, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

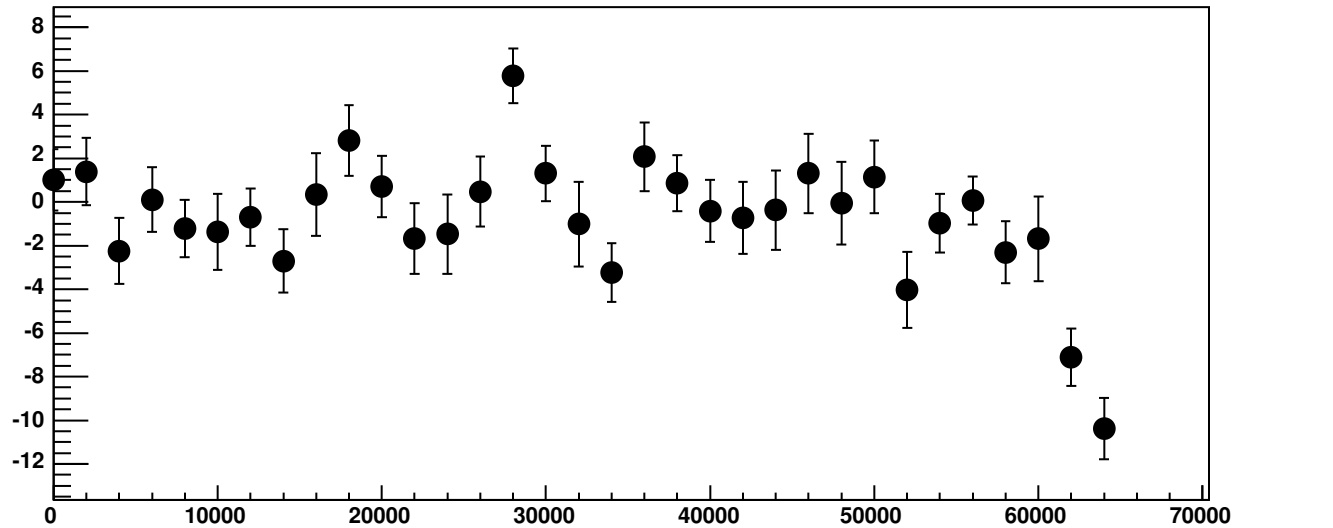
Chip 3, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC

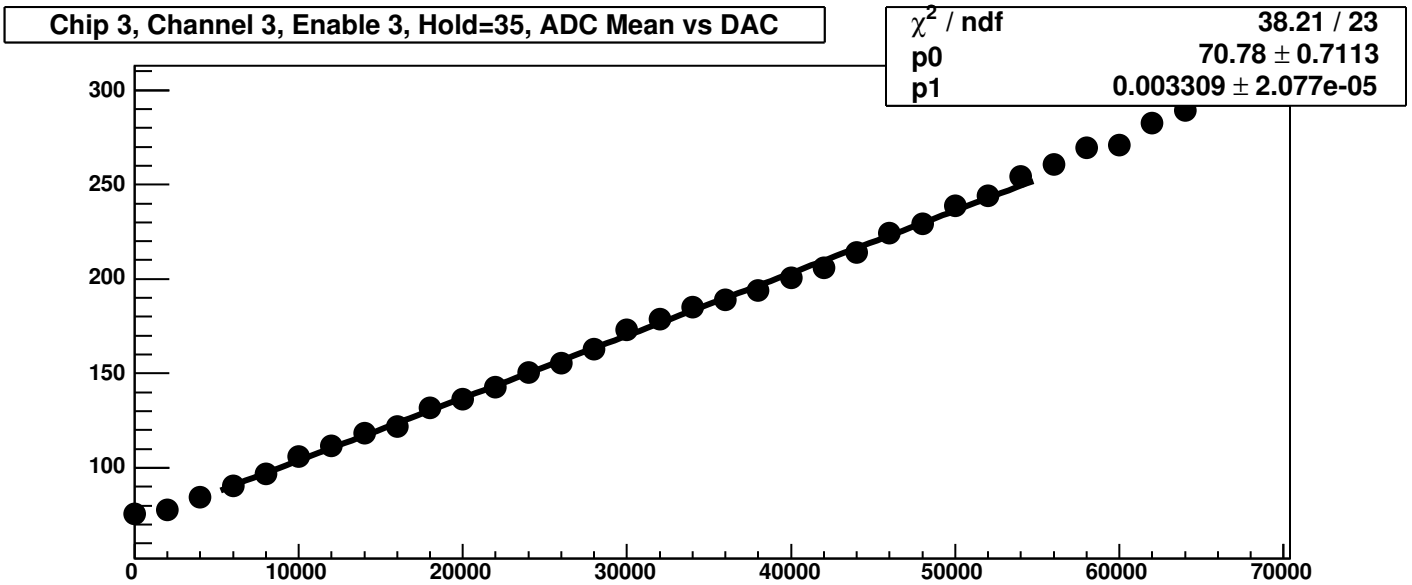


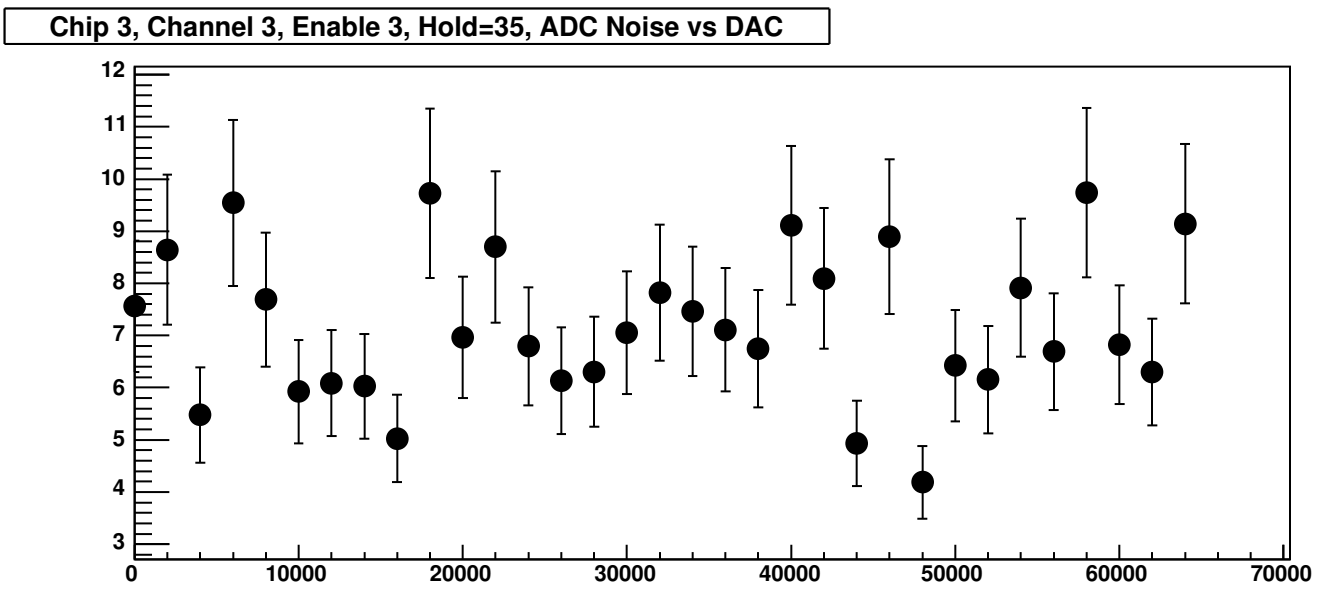
Chip 3, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC

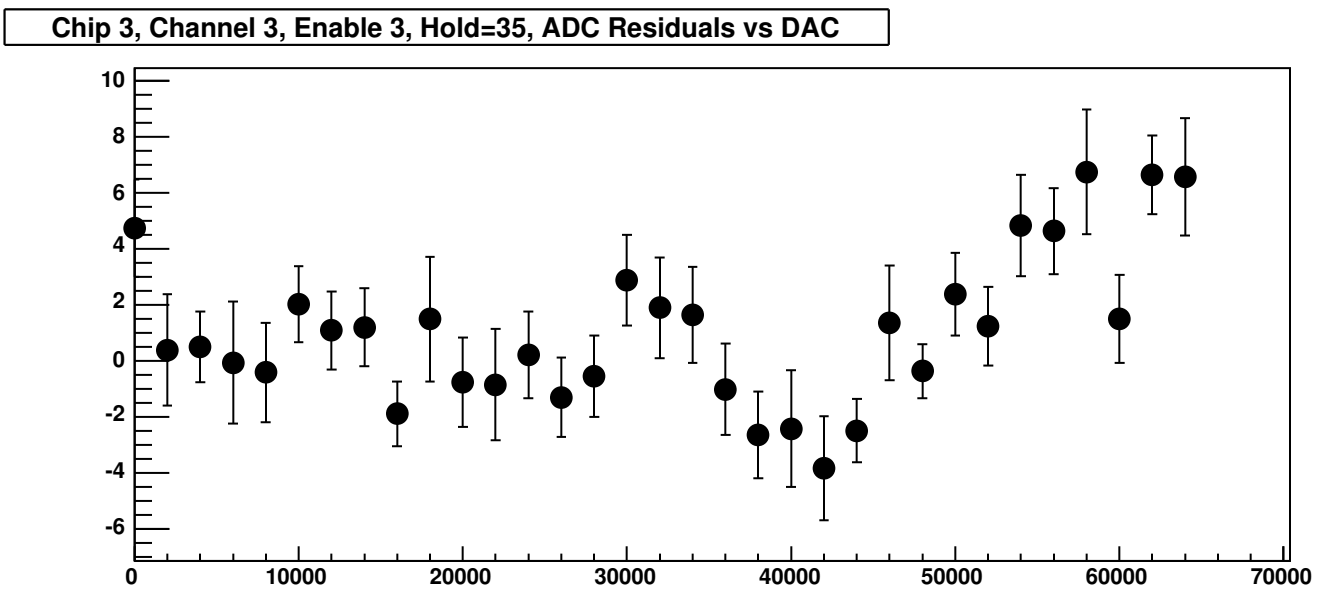


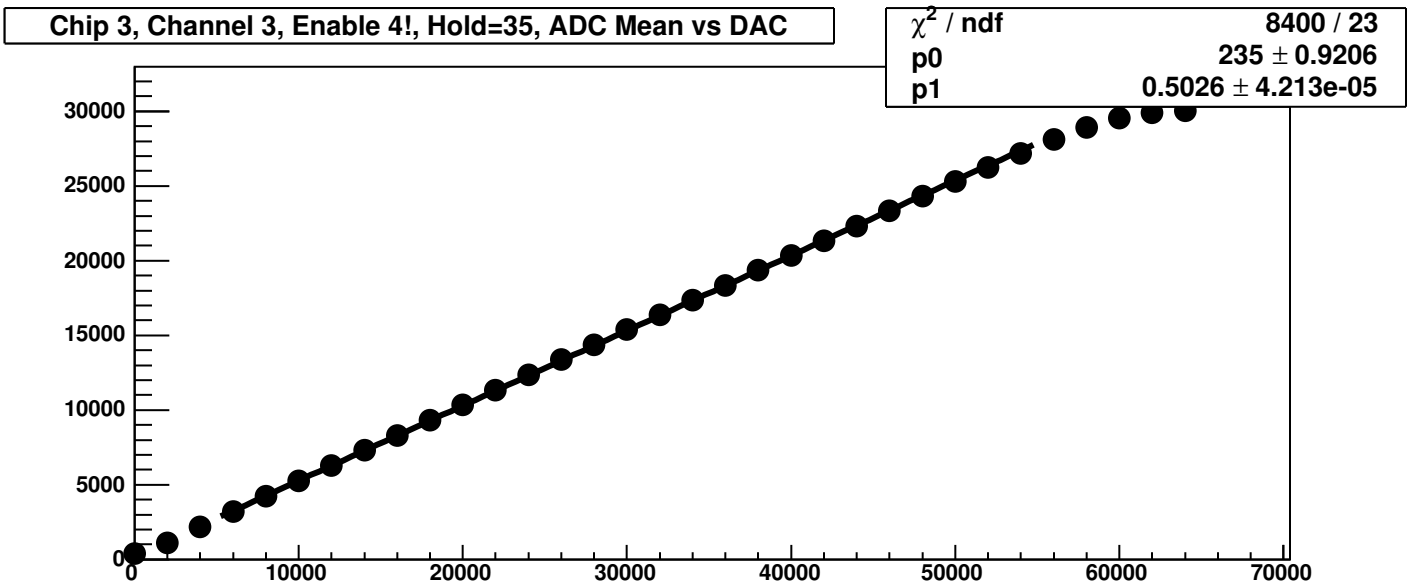
Chip 3, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC

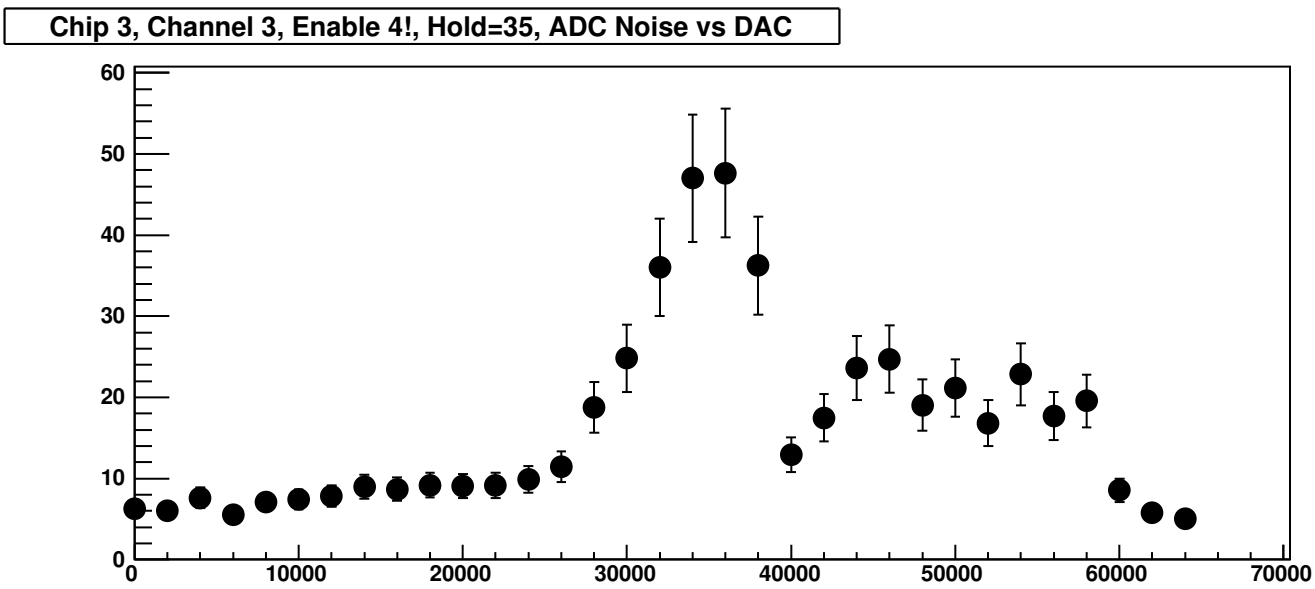


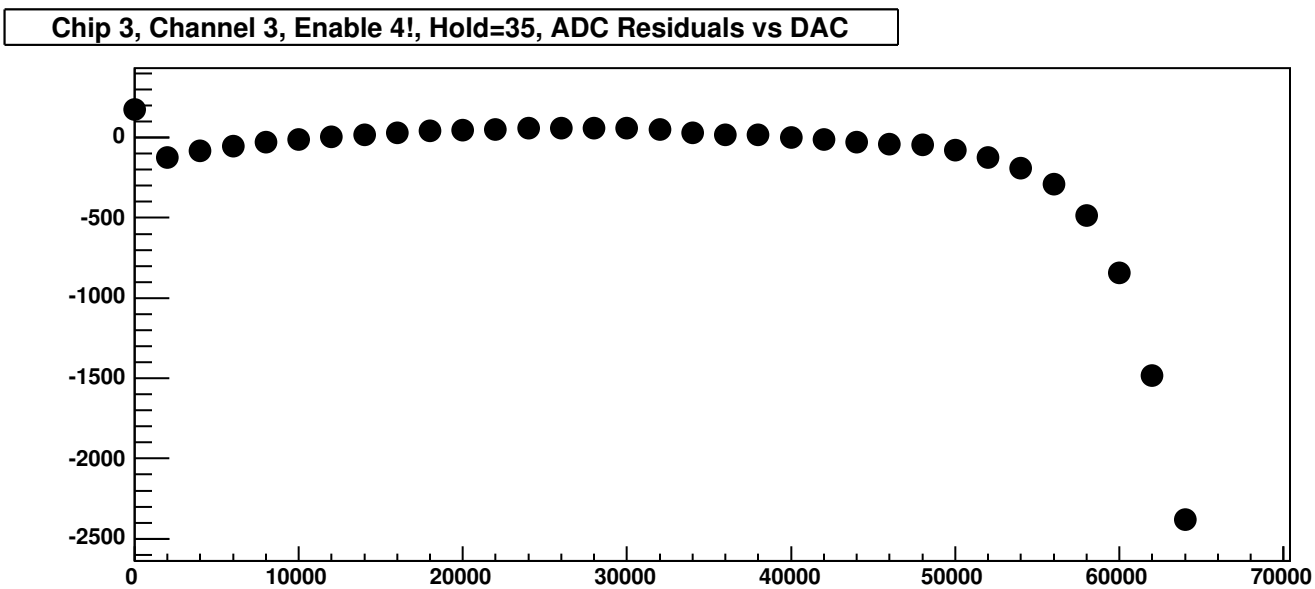

Chip 3, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.21 / 23
p0 70.78 ± 0.7113
p1 0.003309 ± 2.077e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

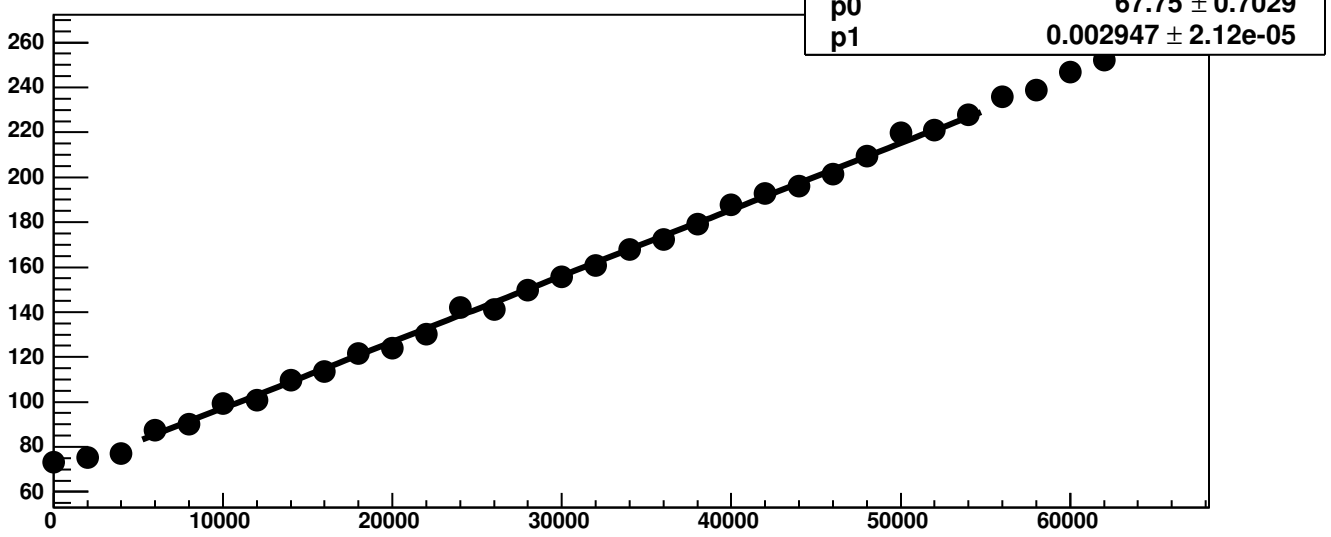

Chip 3, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 8400 / 23
p0 235 ± 0.9206
p1 0.5026 ± 4.213e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

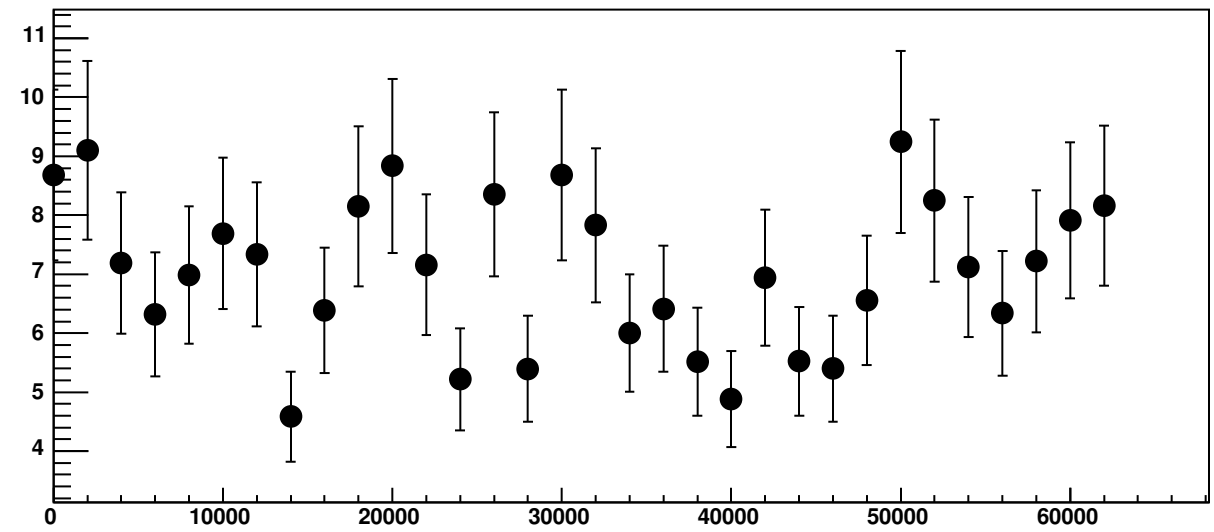

Chip 3, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
0
10000
20000
30000
40000
50000
60000
70000

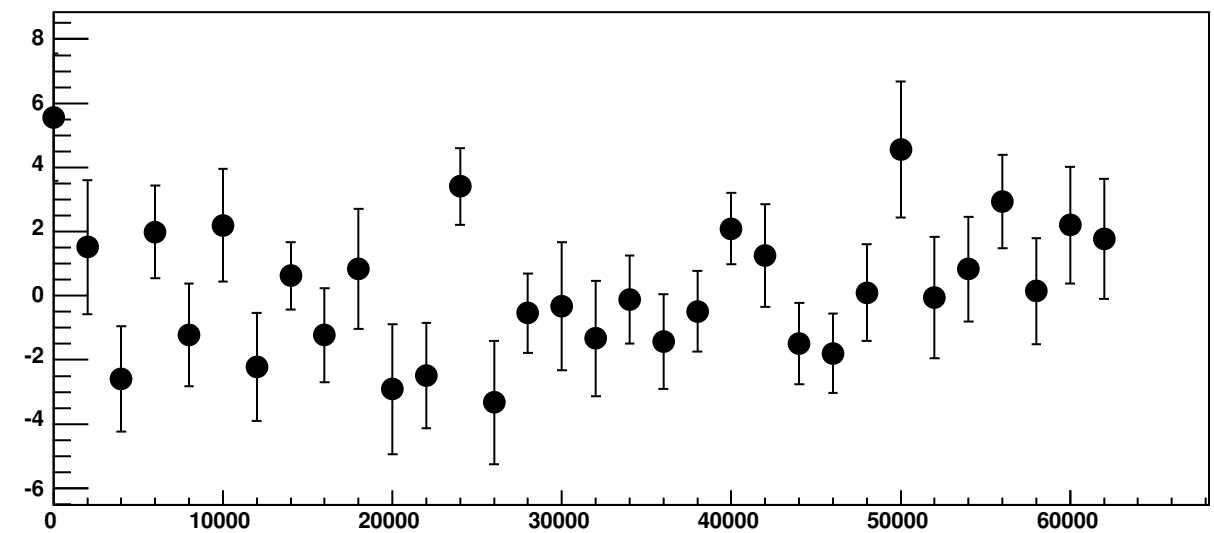
Chip 3, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC

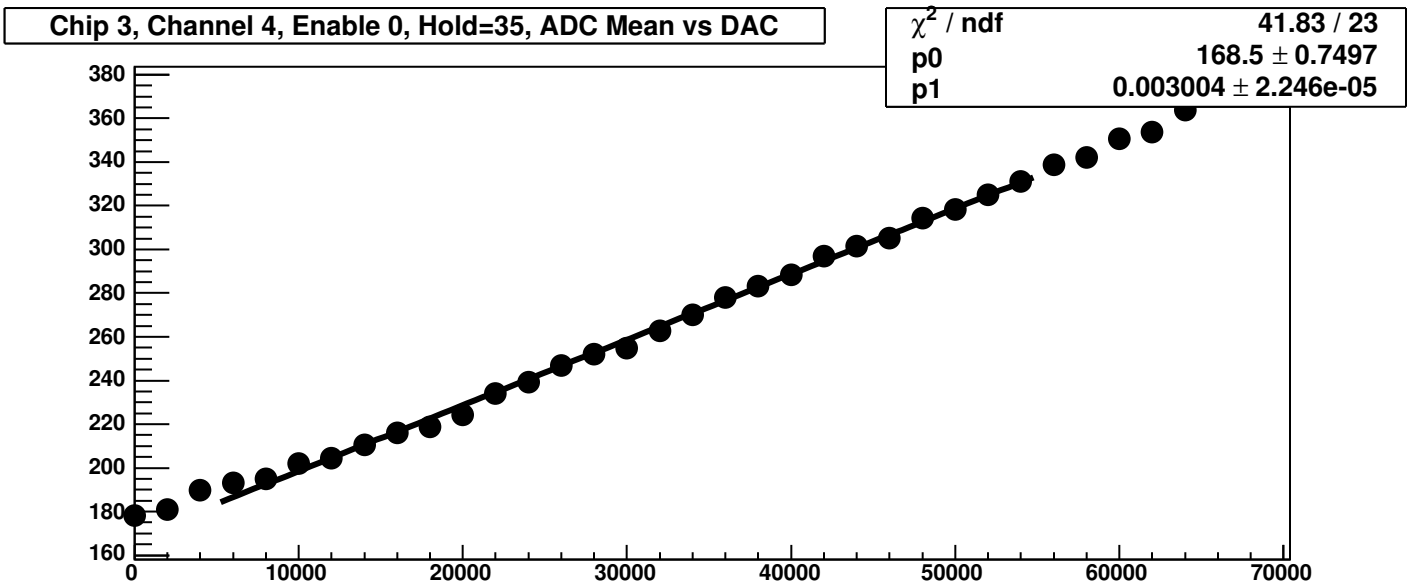


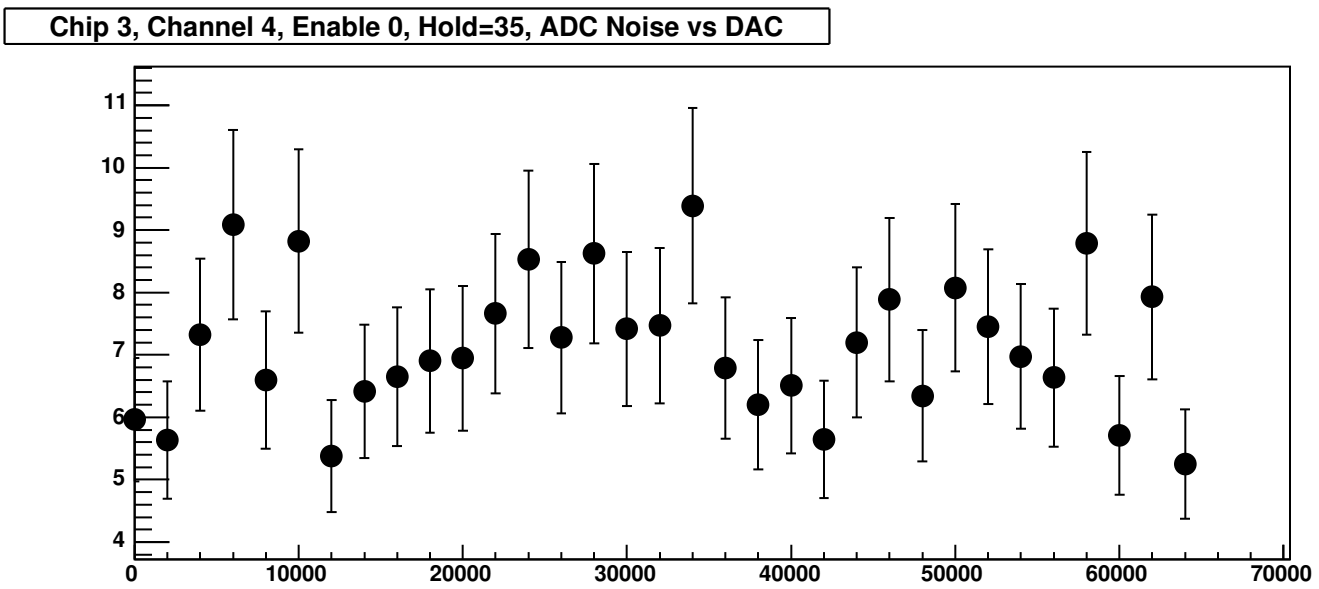
Chip 3, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC

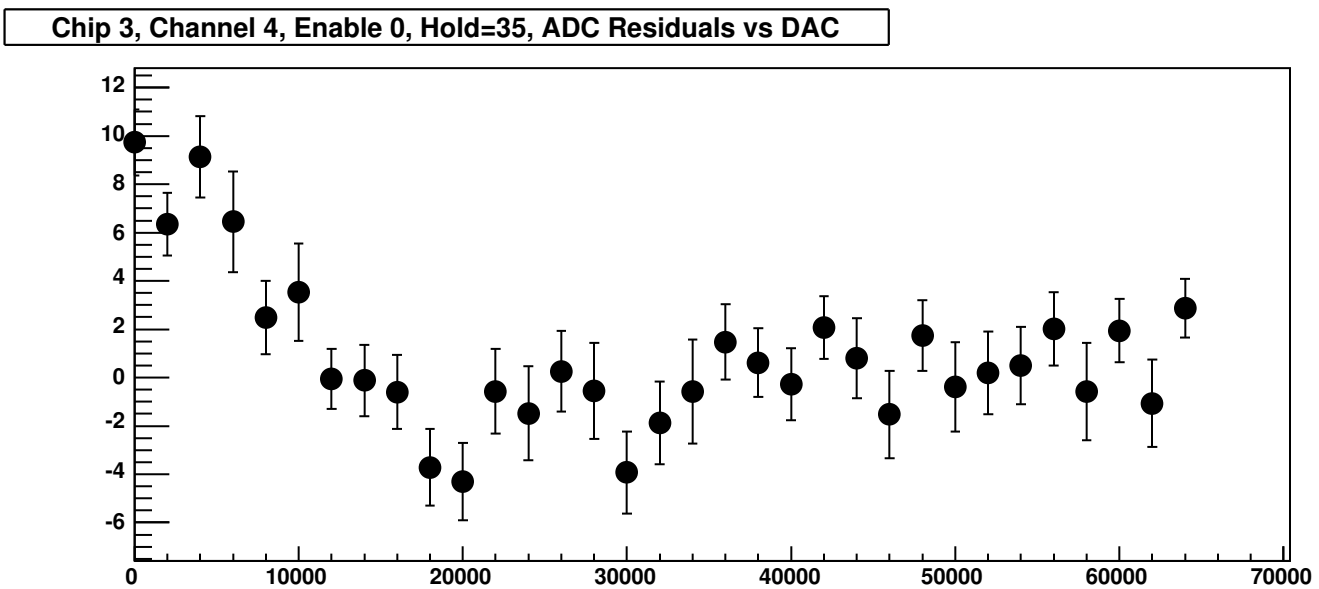


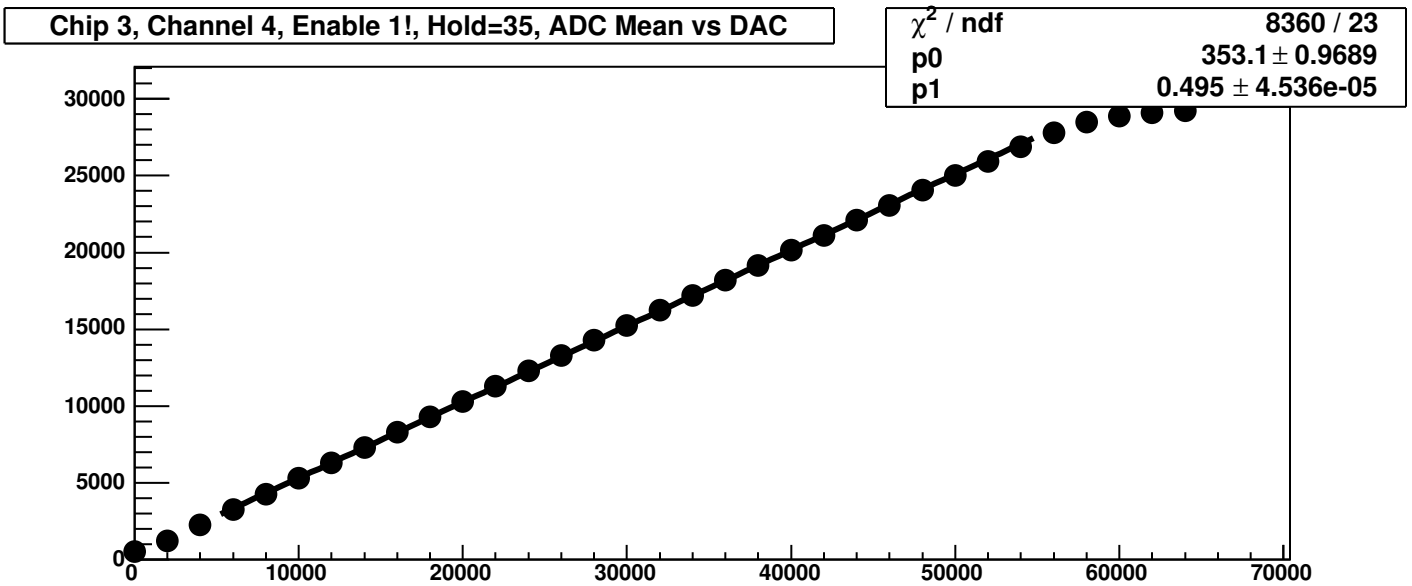
Chip 3, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC

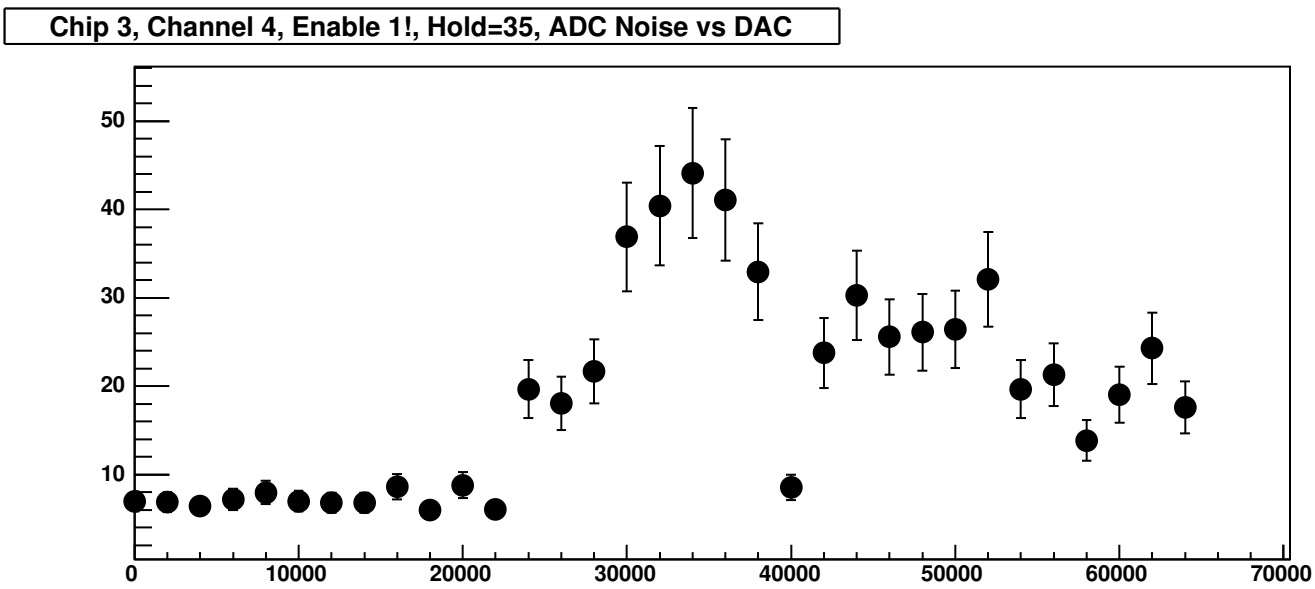


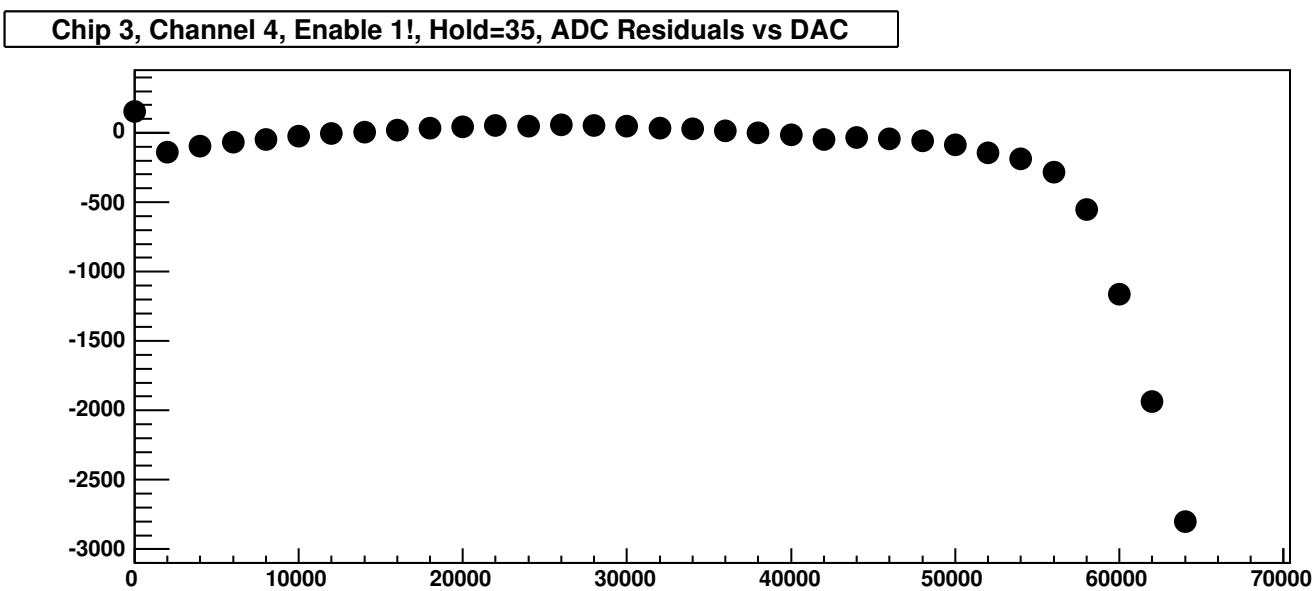

Chip 3, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 41.83 / 23
p0 168.5 ± 0.7497
p1 0.003004 ± 2.246e-05
380
360
340
320
300
280
260
240
220
200
180
160
0
10000
20000
30000
40000
50000
60000
70000

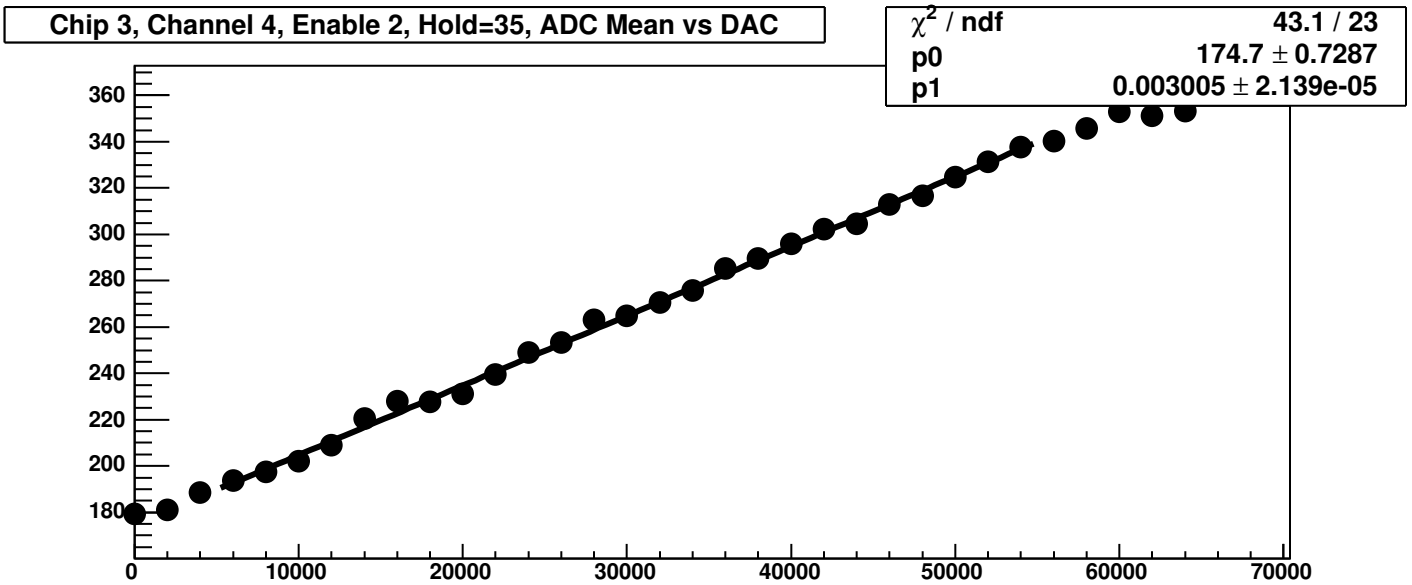

Chip 3, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

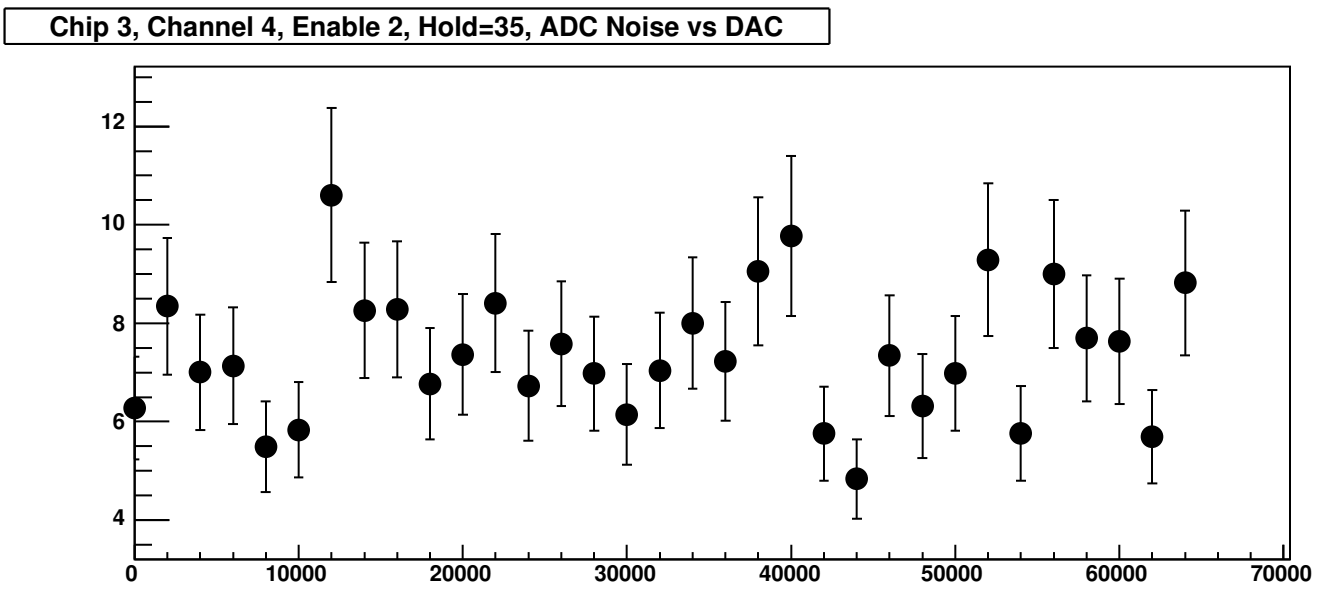

Chip 3, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

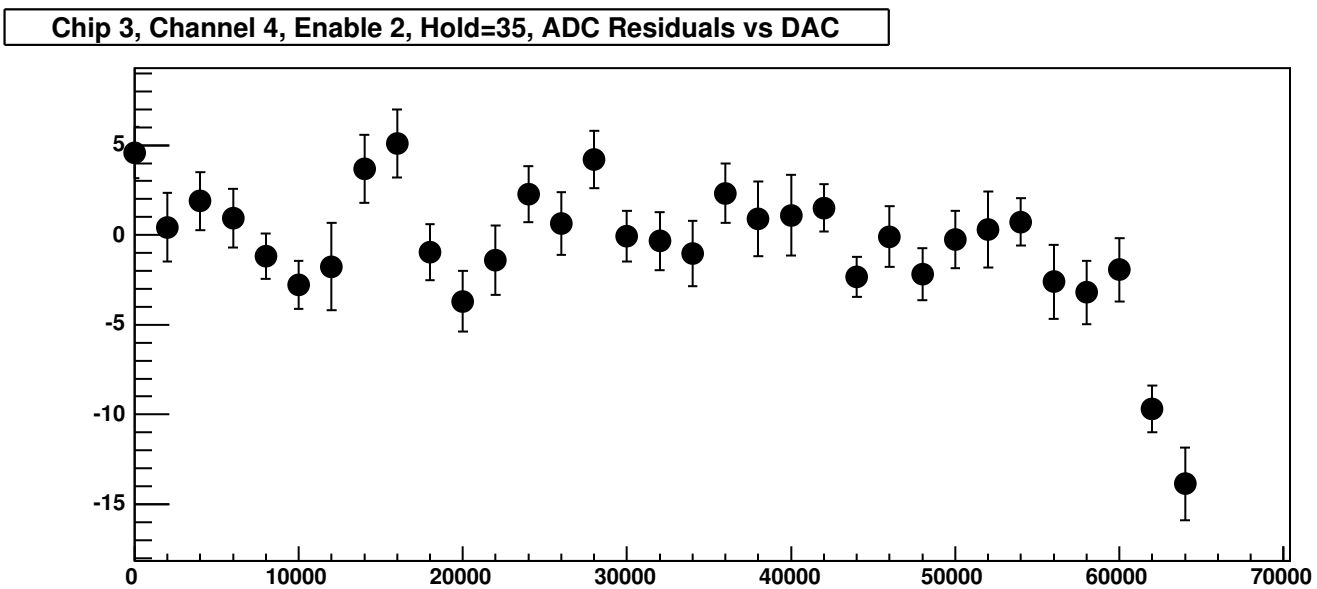

Chip 3, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 8360 / 23
p0 353.1 ± 0.9689
p1 0.495 ± 4.536e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

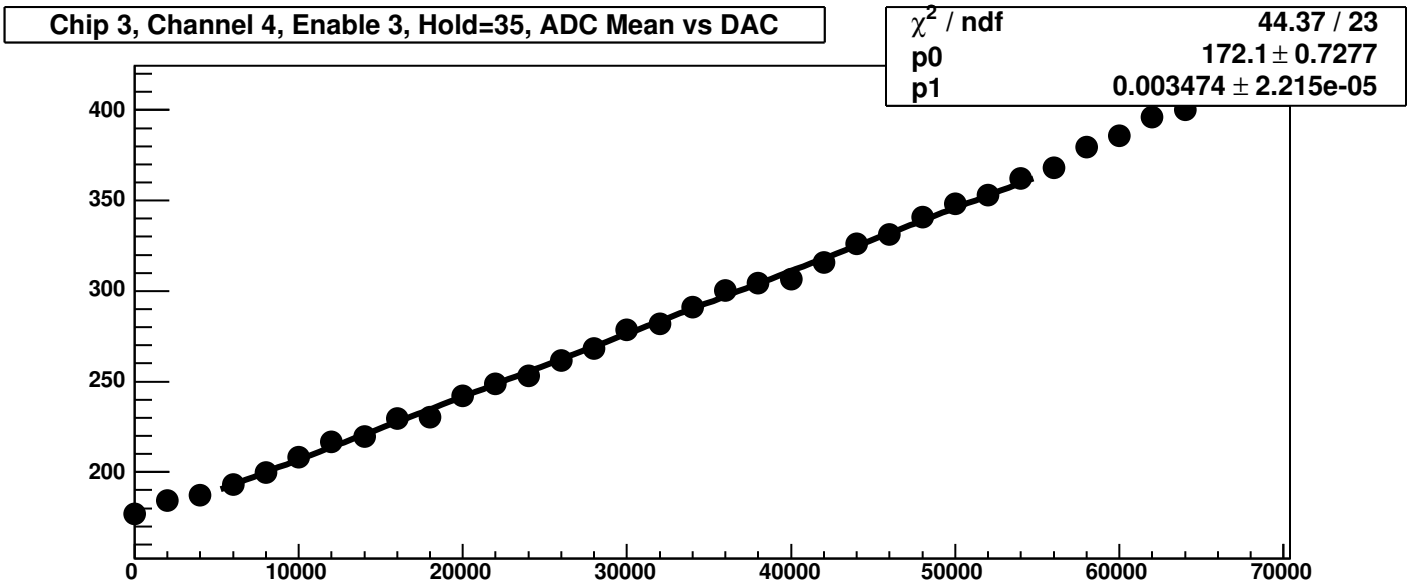

Chip 3, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000

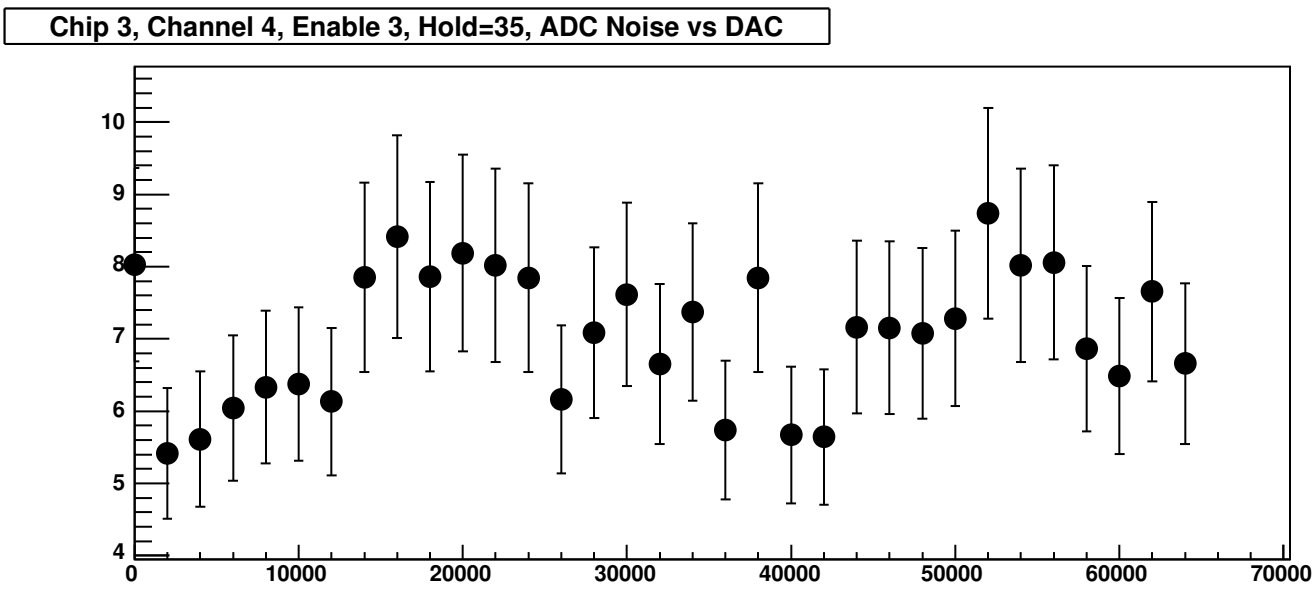

Chip 3, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
0
10000
20000
30000
40000
50000
60000
70000

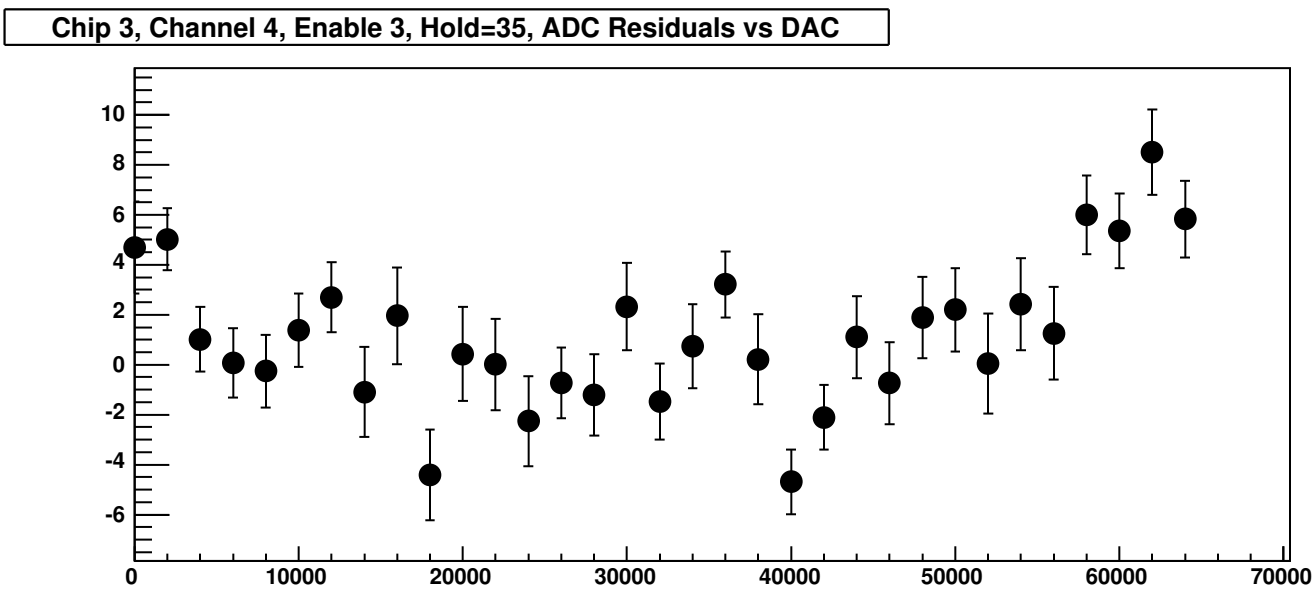

Chip 3, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ^2 / ndf 43.1 / 23
p0 174.7 ± 0.7287
p1 0.003005 ± 2.139e-05
360
340
320
300
280
260
240
220
200
180
0
10000
20000
30000
40000
50000
60000
70000

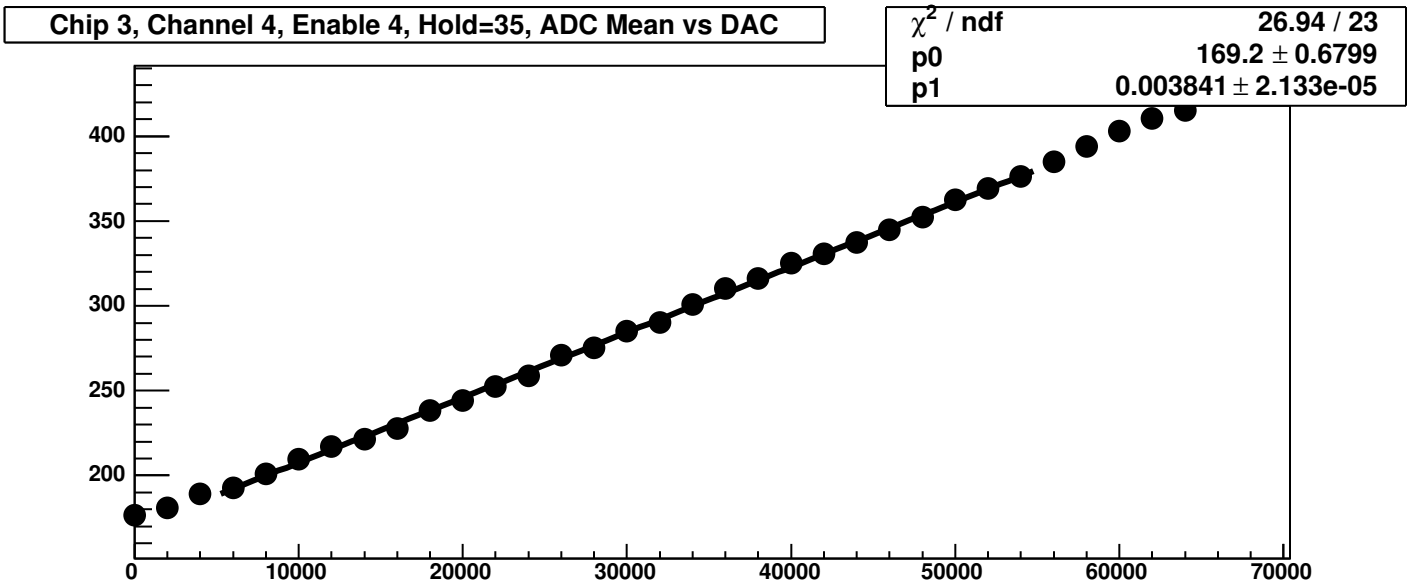

Chip 3, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

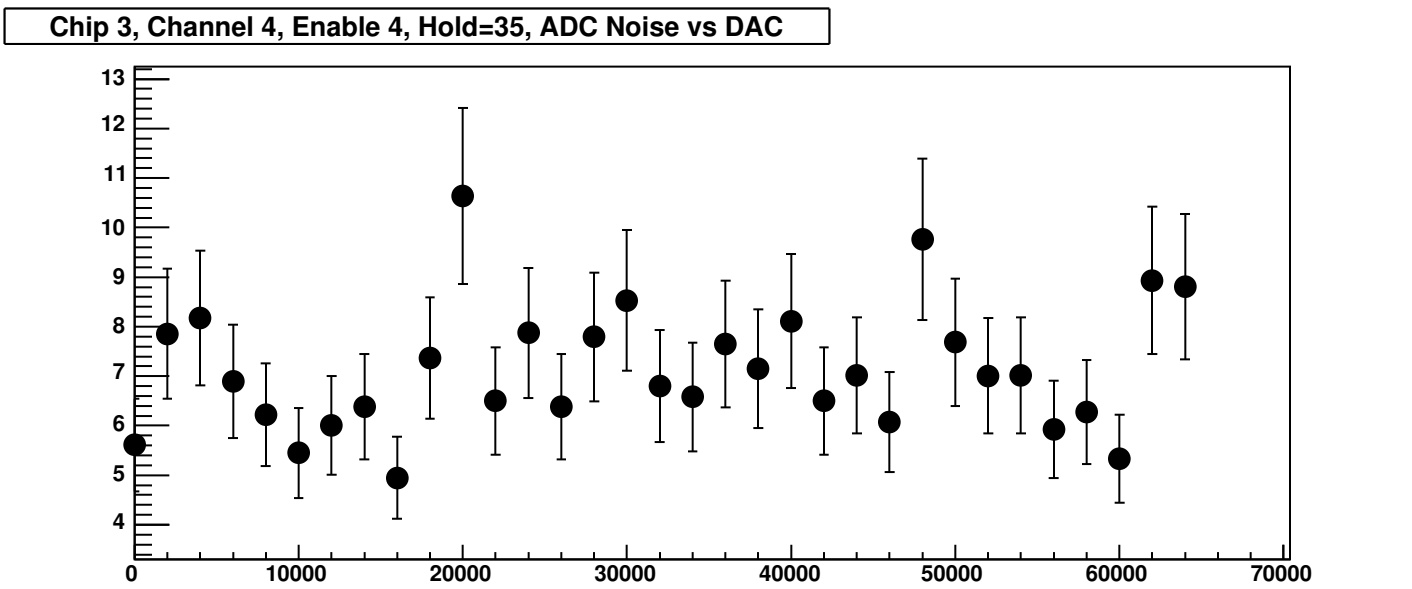

Chip 3, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

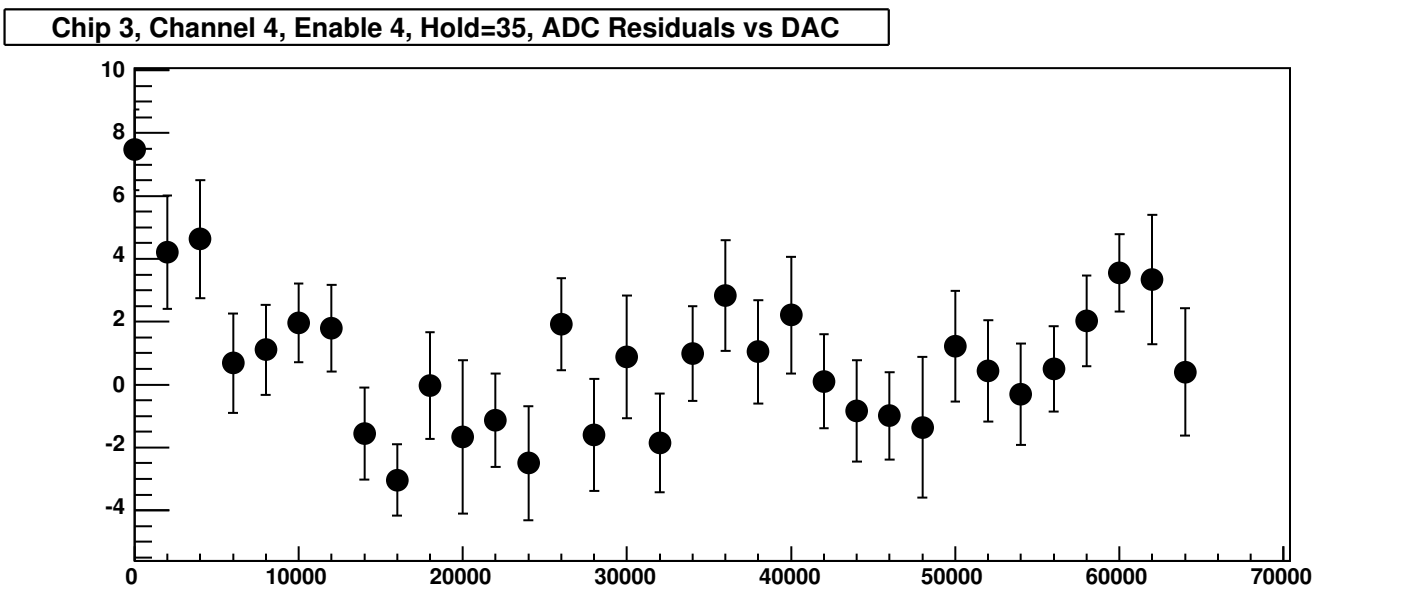

Chip 3, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 44.37 / 23
p0 172.1 ± 0.7277
p1 0.003474 ± 2.215e-05
400
350
300
250
200
0
10000
20000
30000
40000
50000
60000
70000

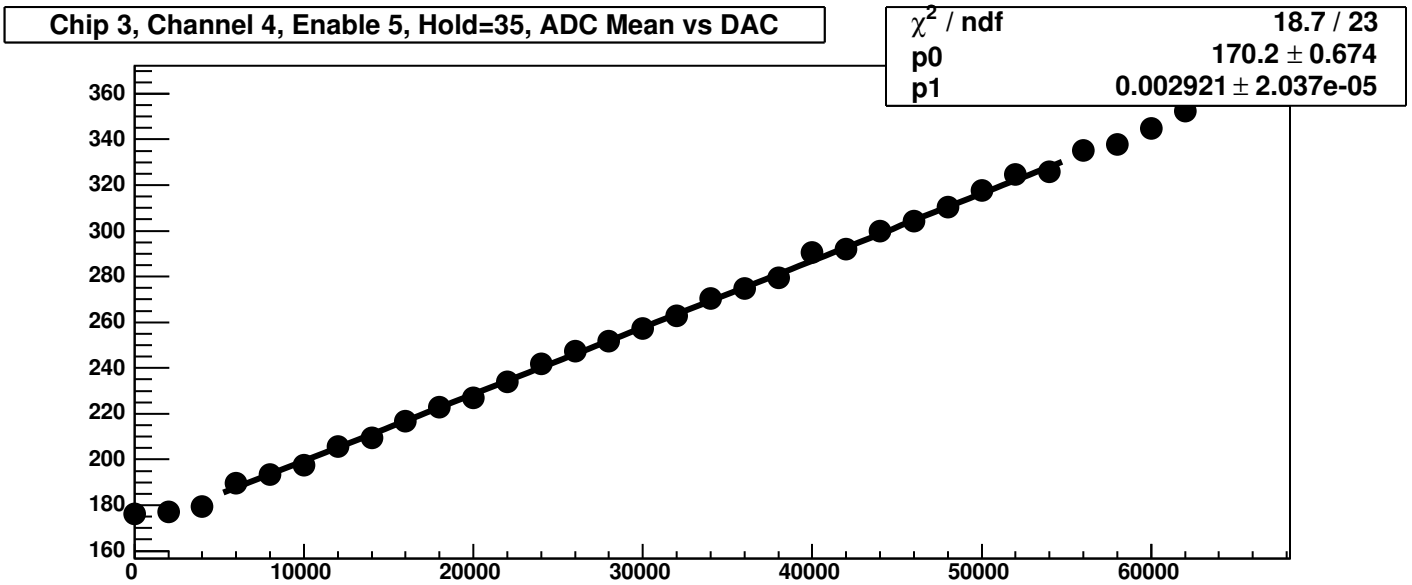

Chip 3, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

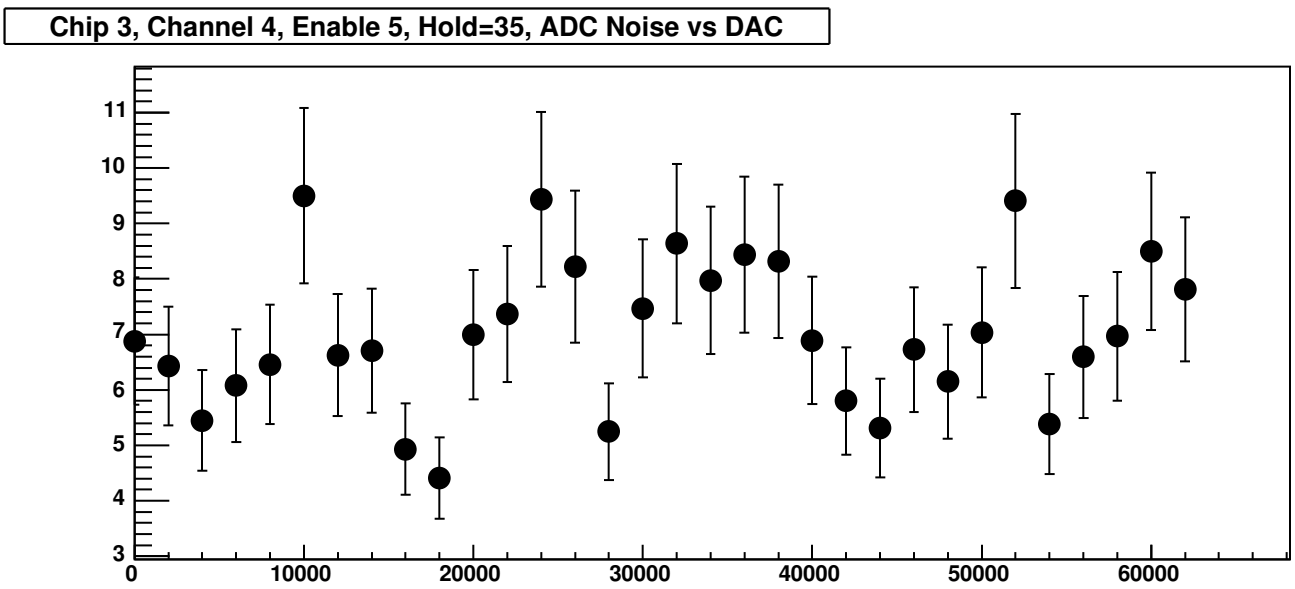

Chip 3, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

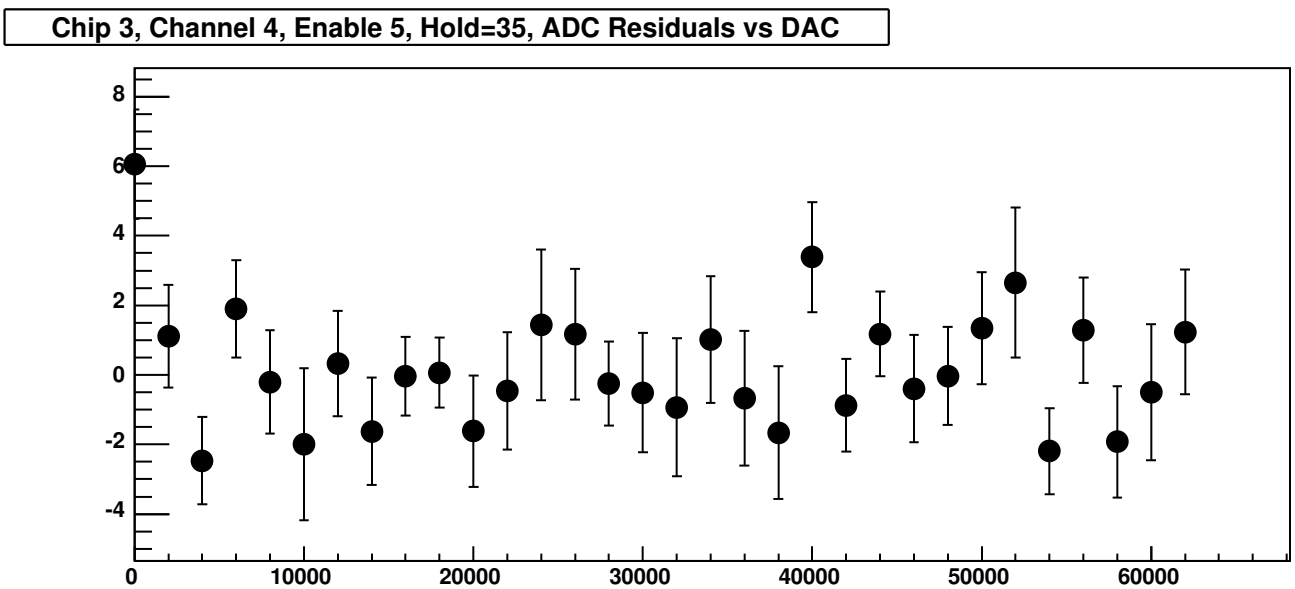

Chip 3, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.94 / 23
p0 169.2 ± 0.6799
p1 0.003841 ± 2.133e-05
400
350
300
250
200
0
10000
20000
30000
40000
50000
60000
70000

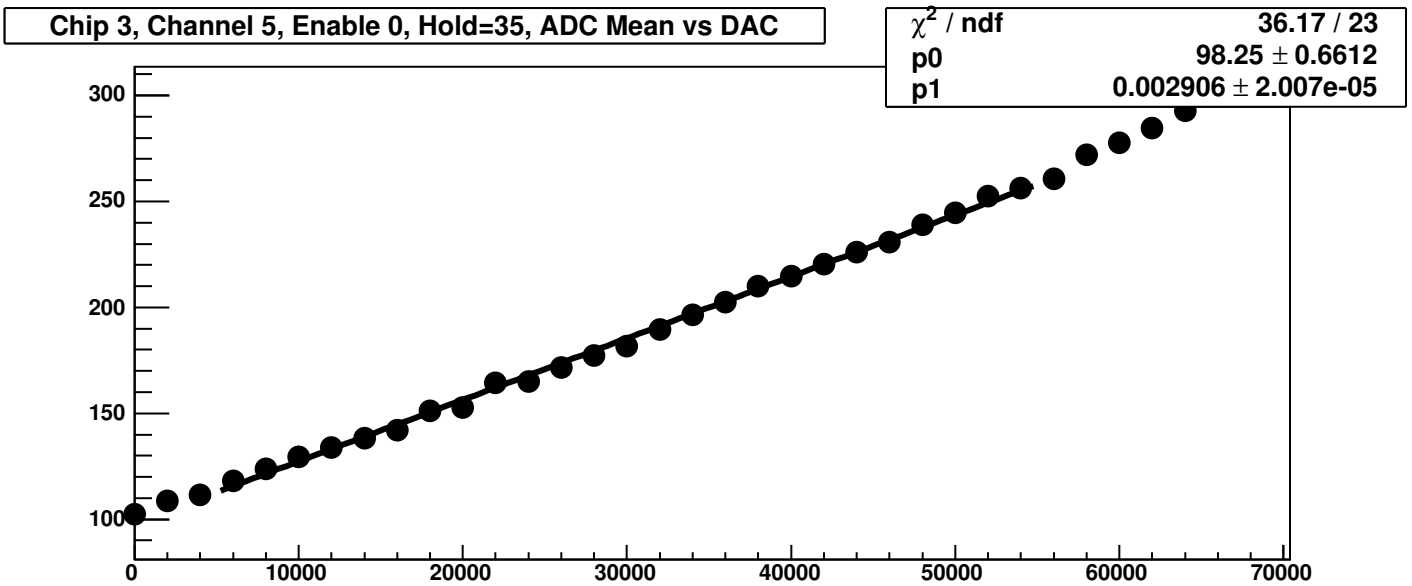

Chip 3, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

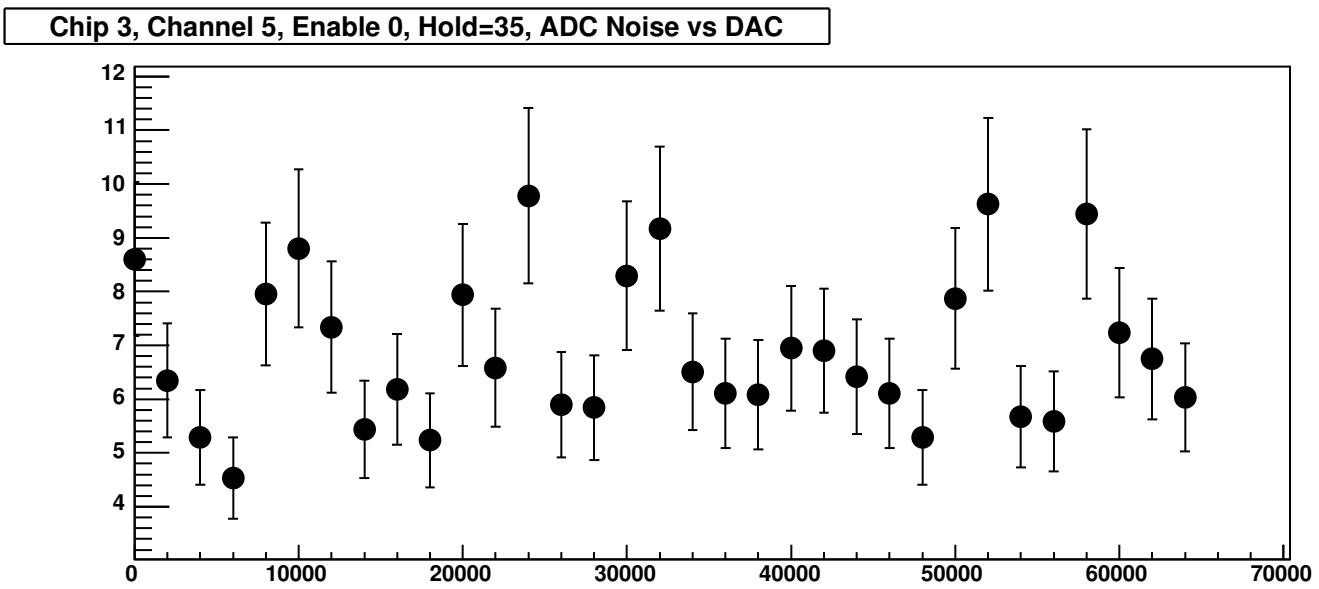

Chip 3, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
0
10000
20000
30000
40000
50000
60000
70000

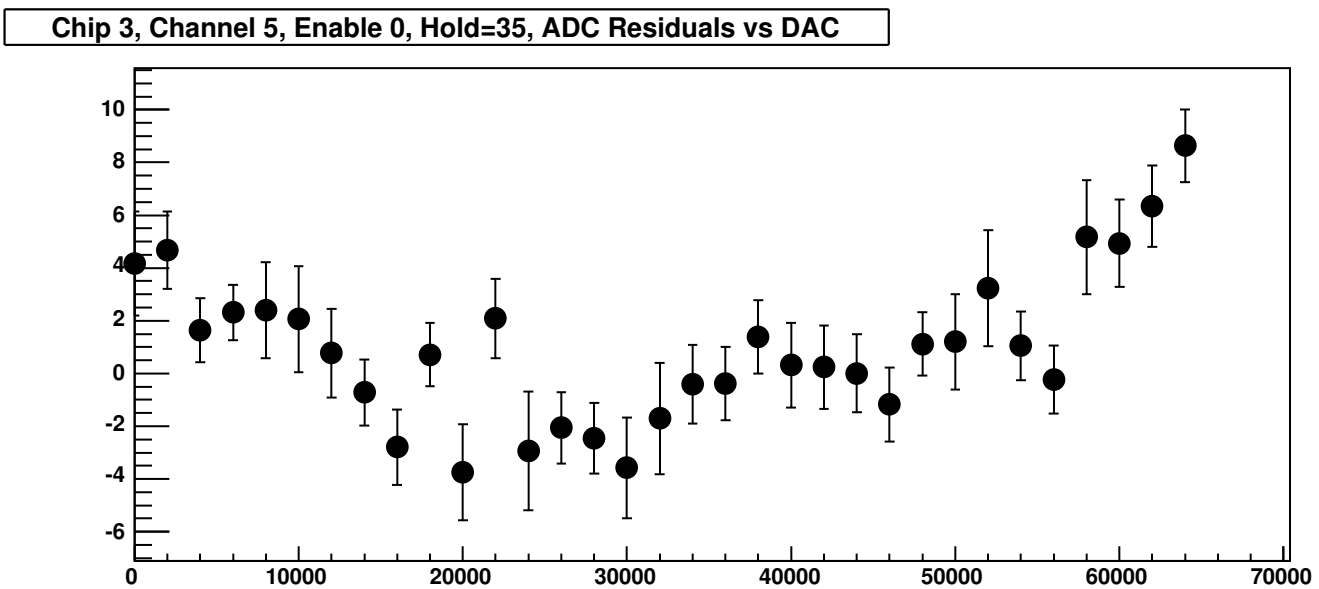

Chip 3, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 18.7 / 23
p0 170.2 ± 0.674
p1 0.002921 ± 2.037e-05
360
340
320
300
280
260
240
220
200
180
160
0
10000
20000
30000
40000
50000
60000

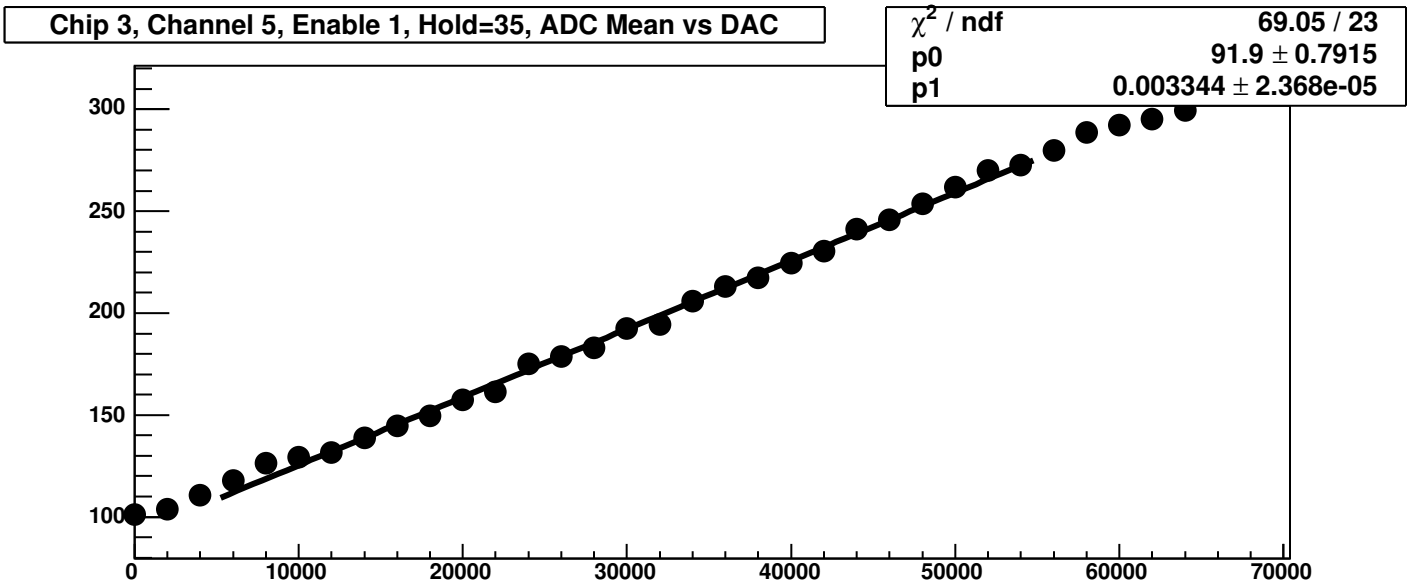

Chip 3, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000

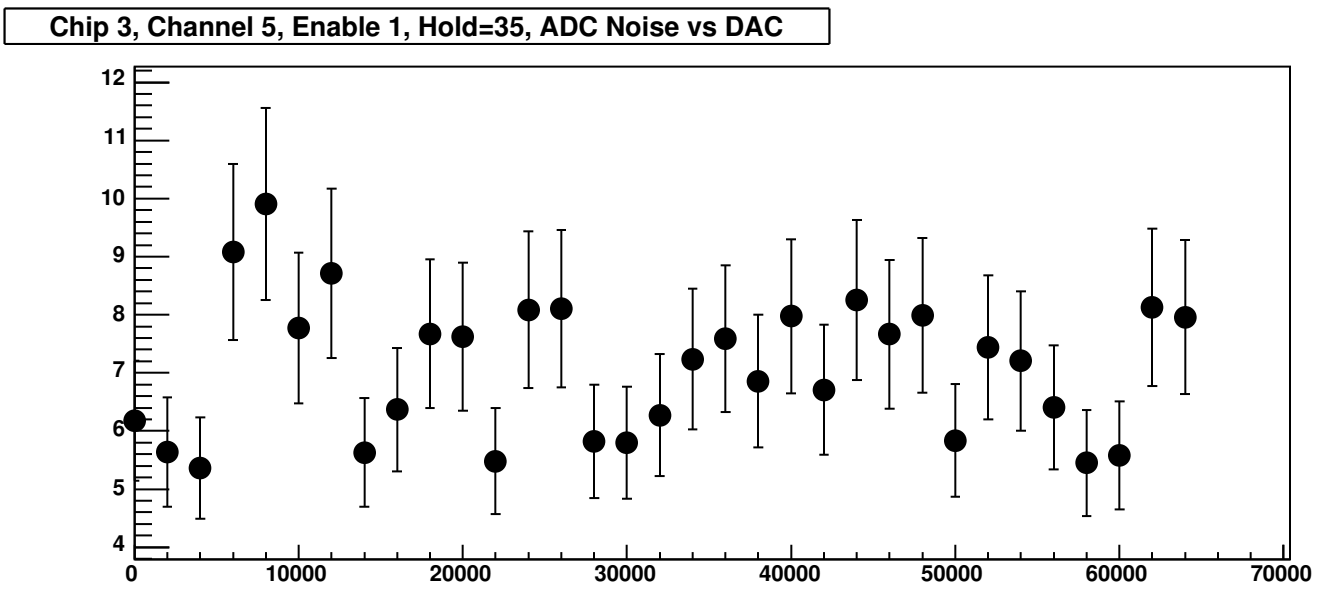

Chip 3, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
0
10000
20000
30000
40000
50000
60000

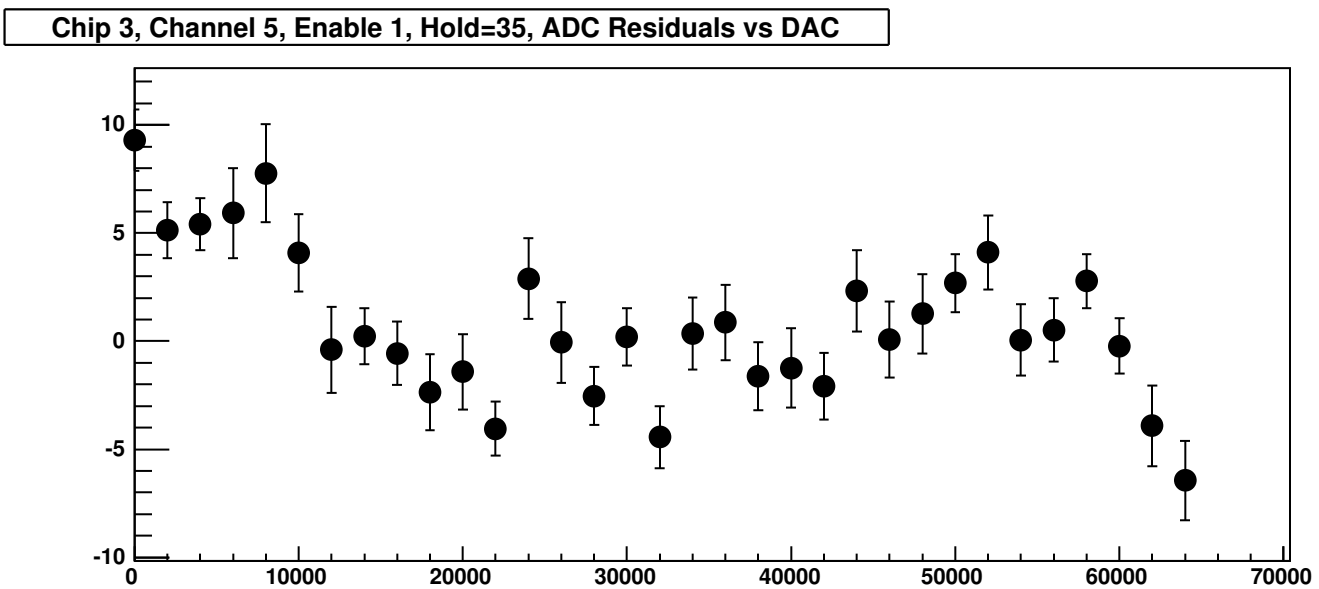

Chip 3, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 36.17 / 23
p0 98.25 ± 0.6612
p1 0.002906 ± 2.007e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

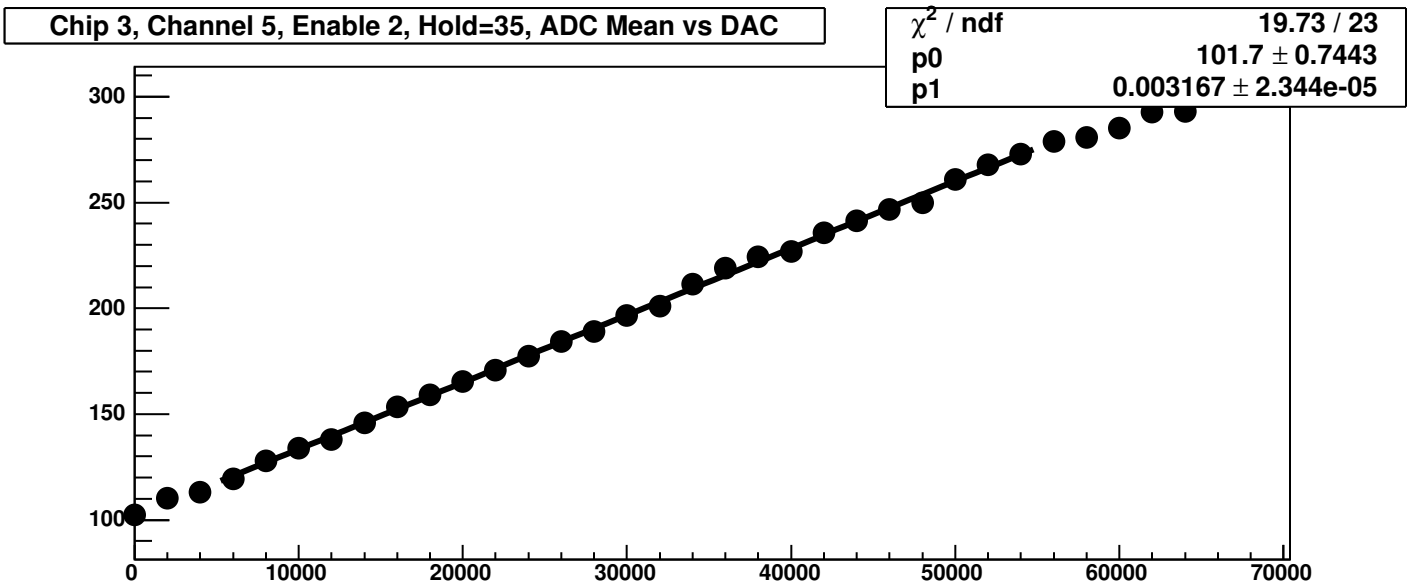

Chip 3, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

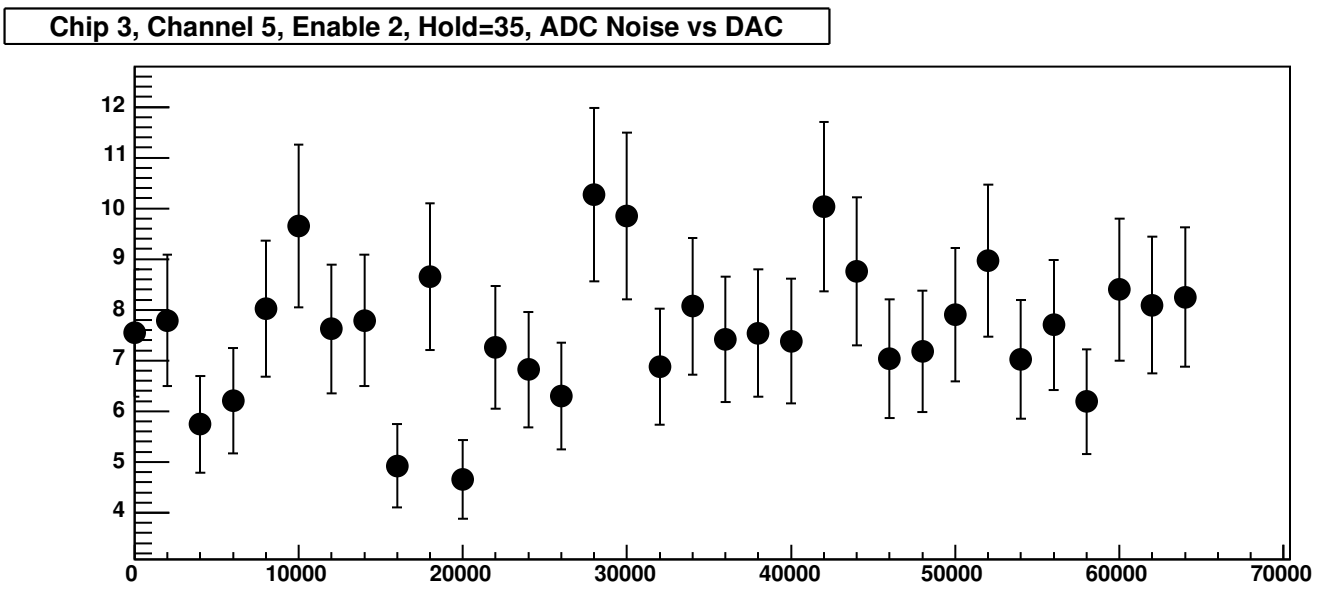

Chip 3, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

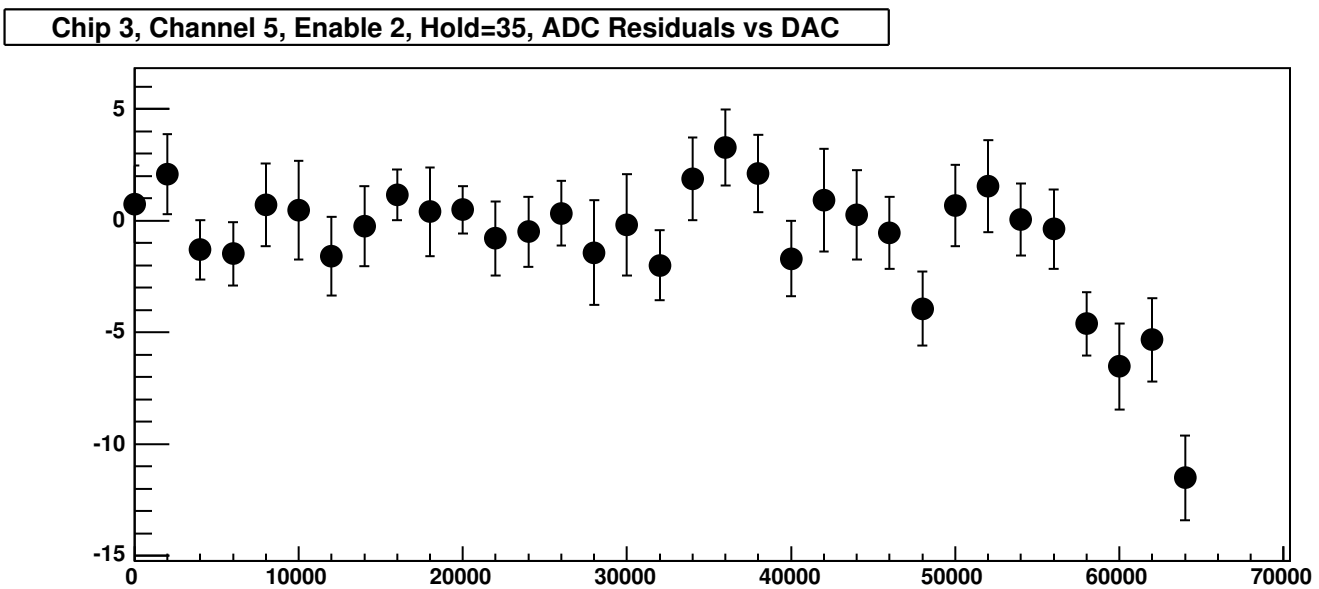

Chip 3, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 69.05 / 23
p0 91.9 ± 0.7915
p1 0.003344 ± 2.368e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

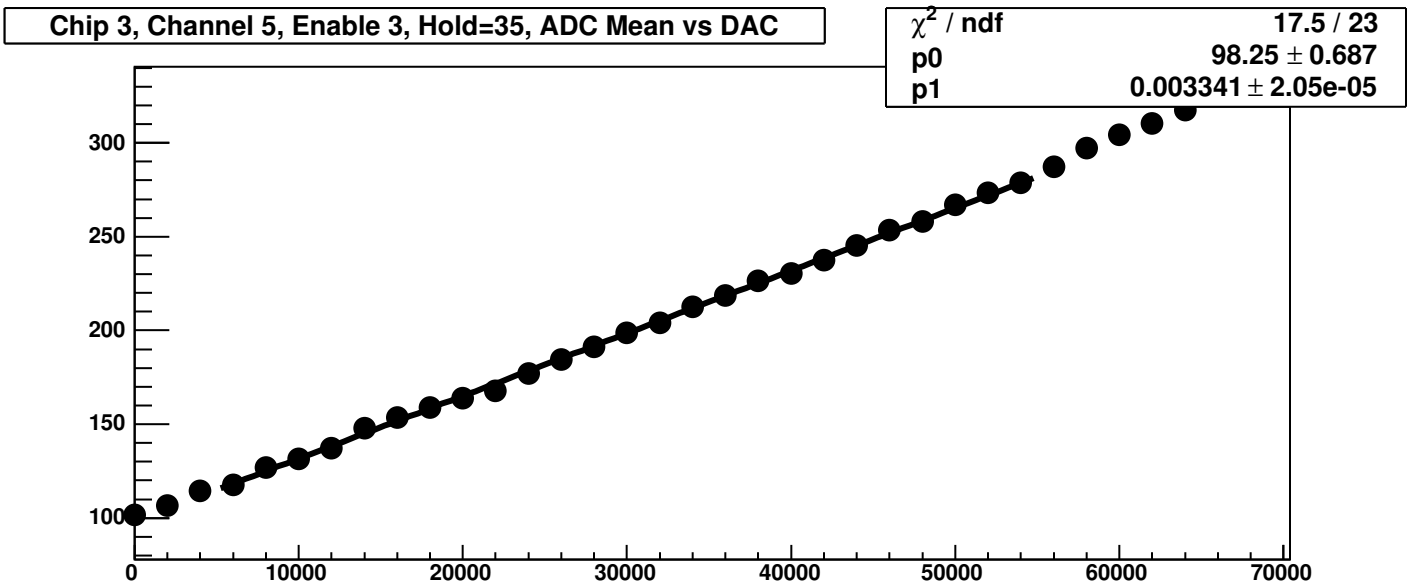

Chip 3, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

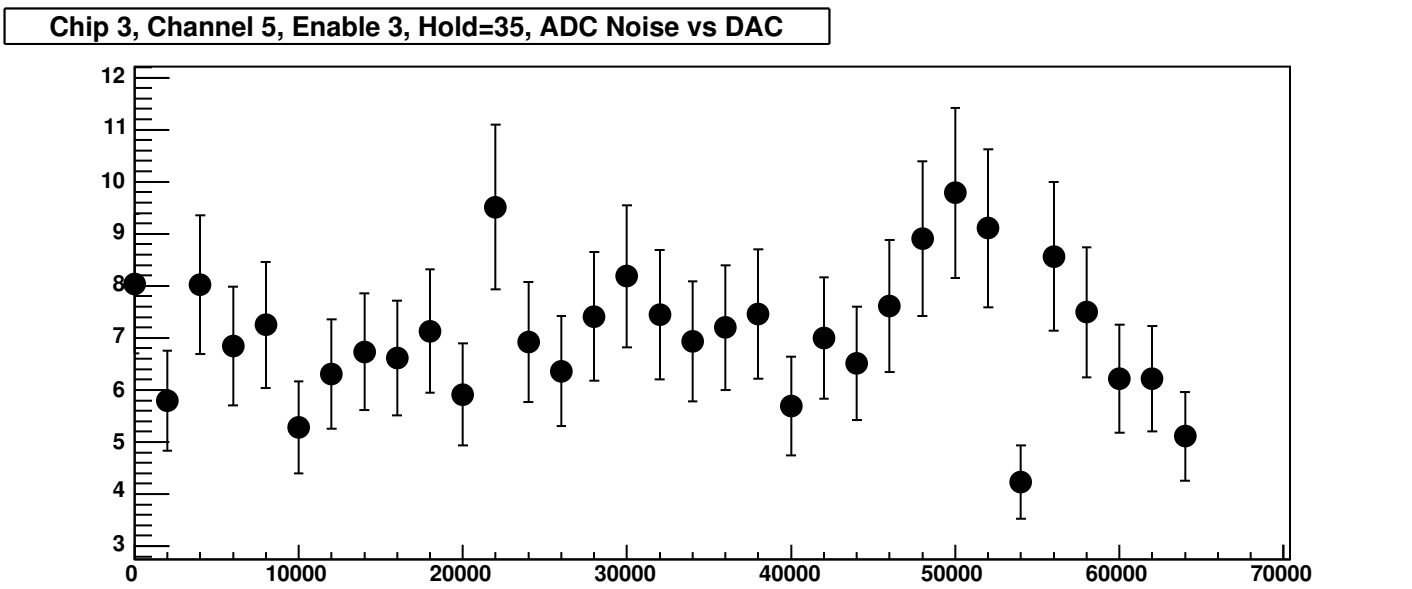

Chip 3, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

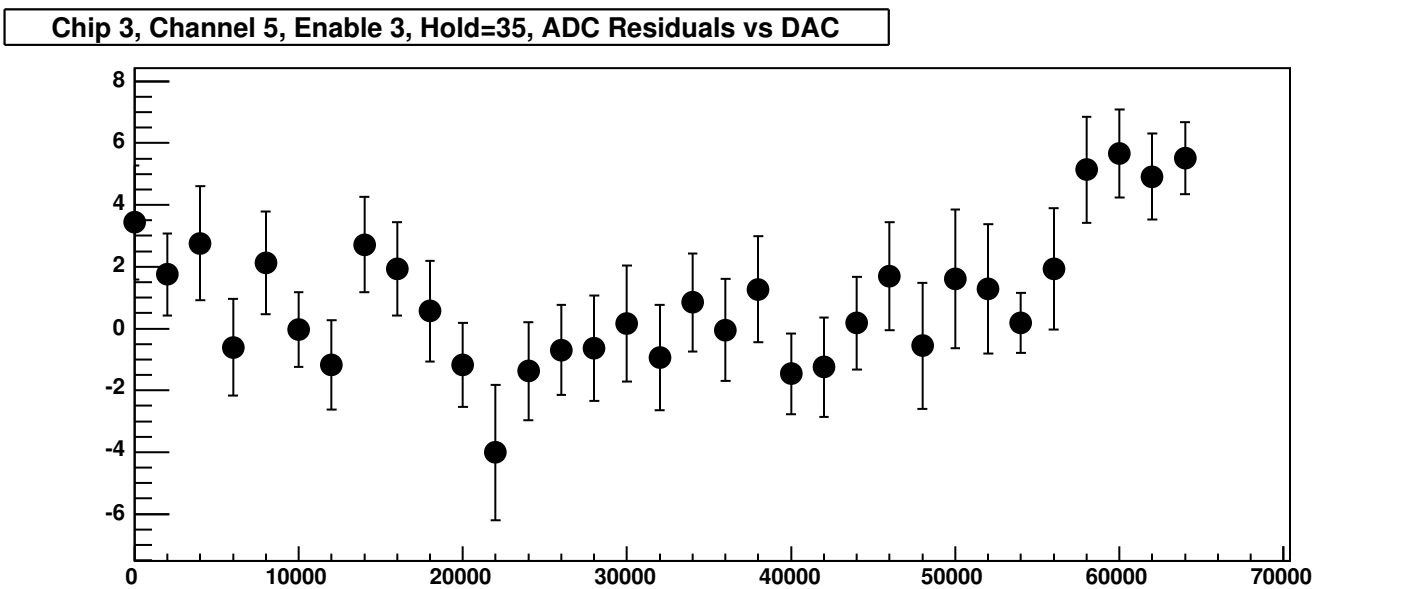

Chip 3, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 19.73 / 23
p0 101.7 ± 0.7443
p1 0.003167 ± 2.344e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

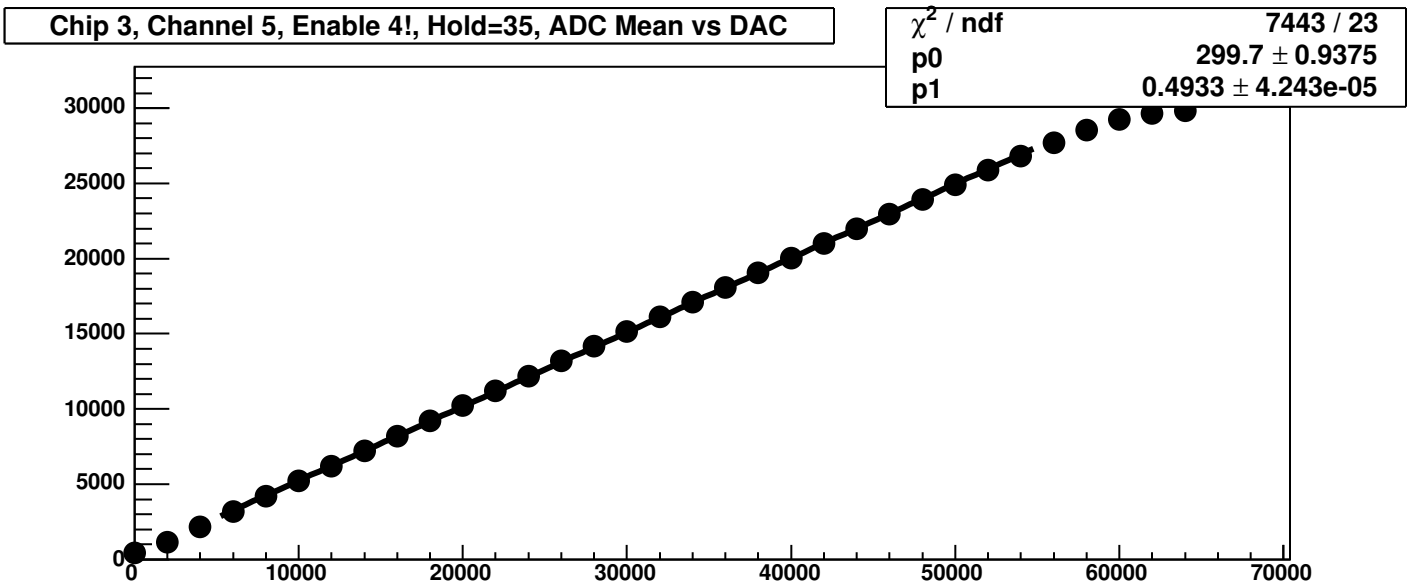

Chip 3, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

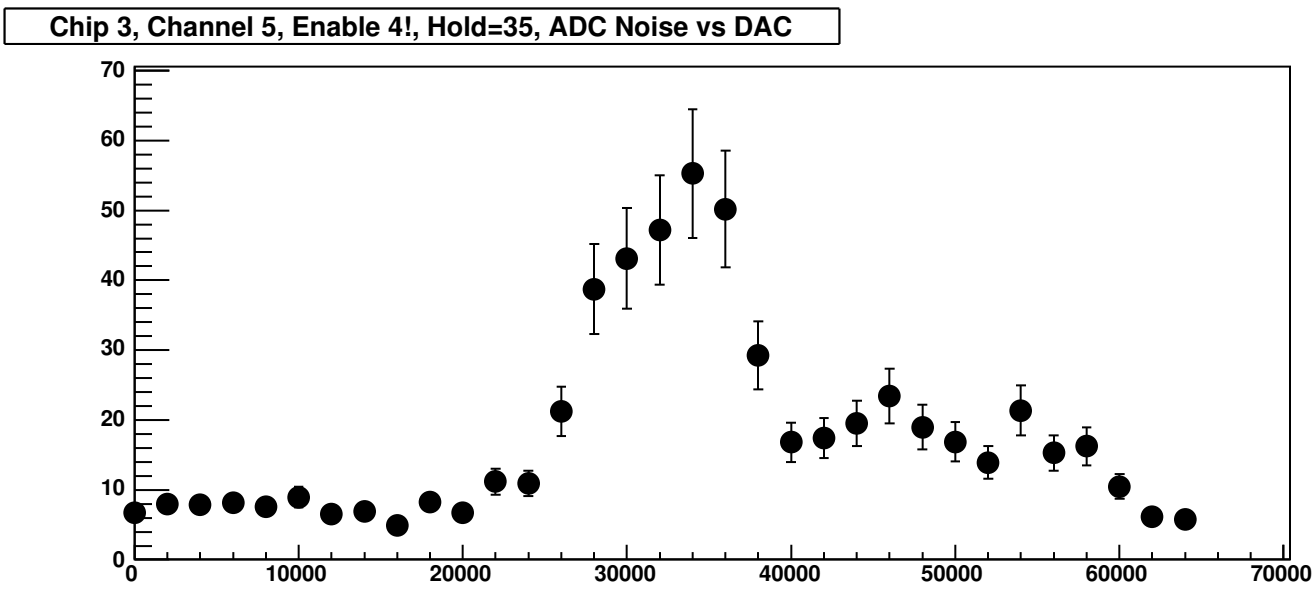

Chip 3, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

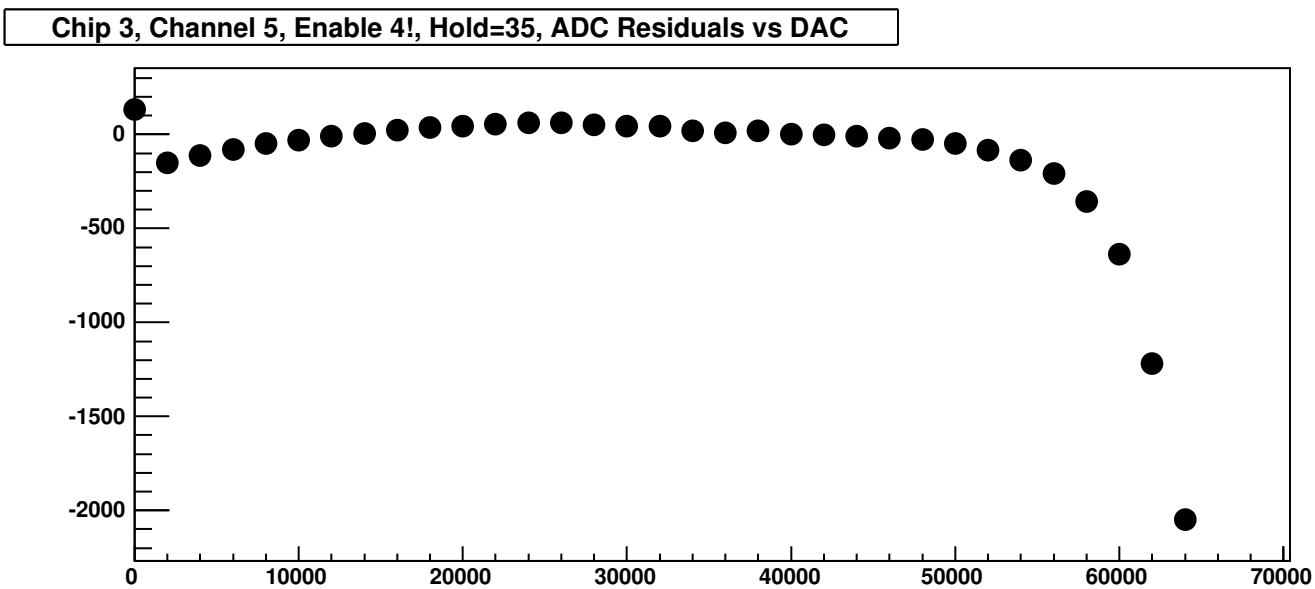

Chip 3, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 17.5 / 23
p0 98.25 ± 0.687
p1 0.003341 ± 2.05e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

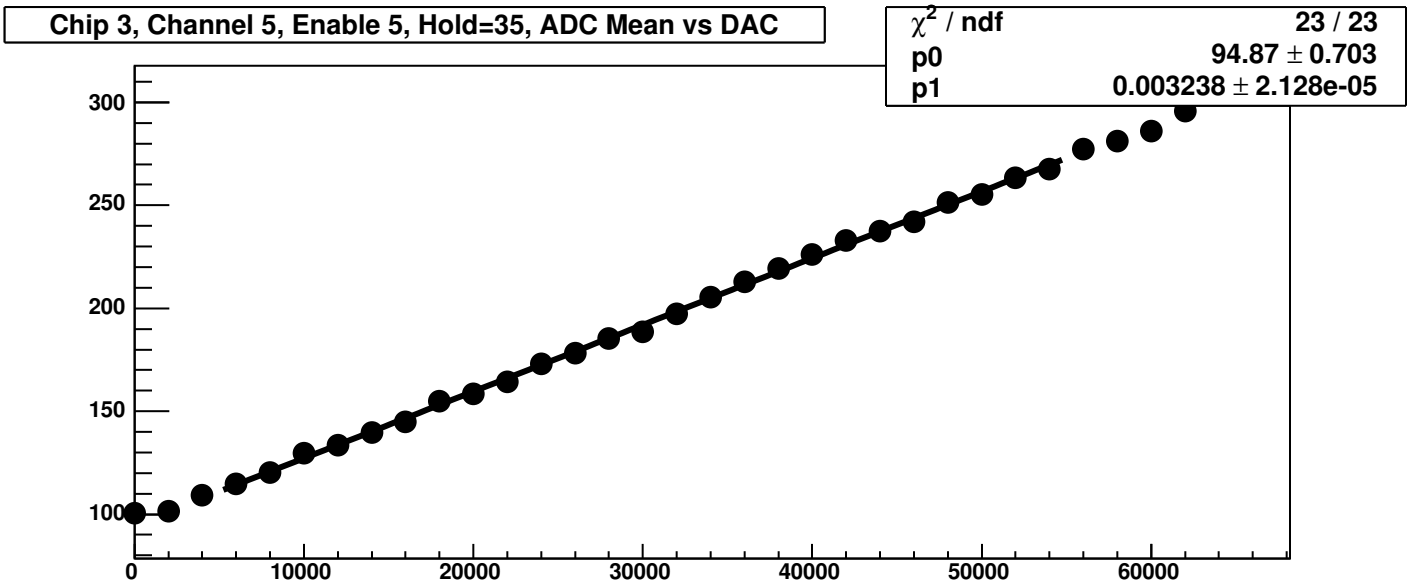

Chip 3, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

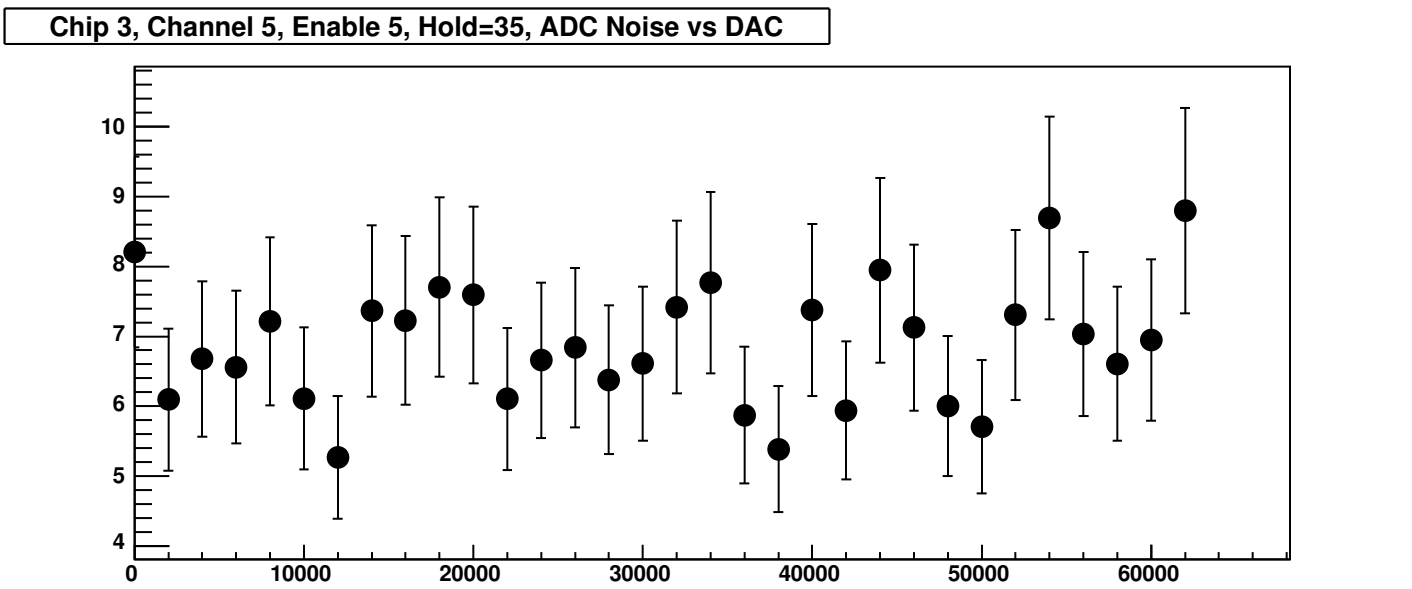

Chip 3, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

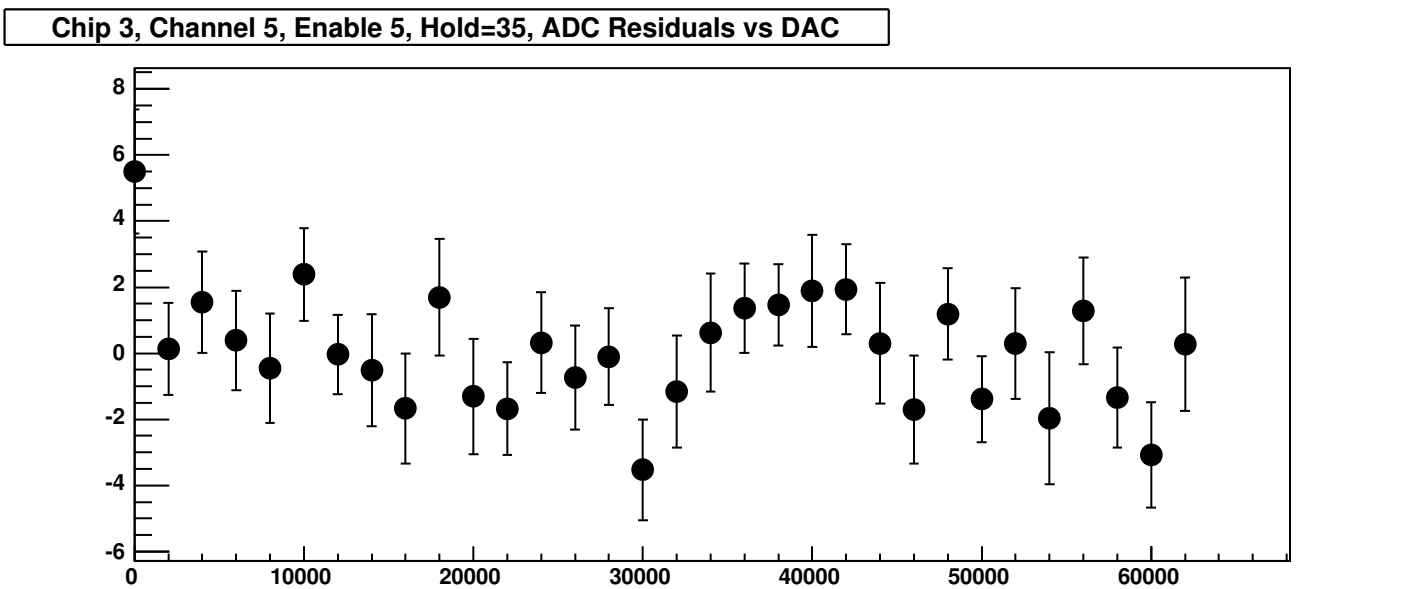

Chip 3, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 7443 / 23
p0 299.7 ± 0.9375
p1 0.4933 ± 4.243e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

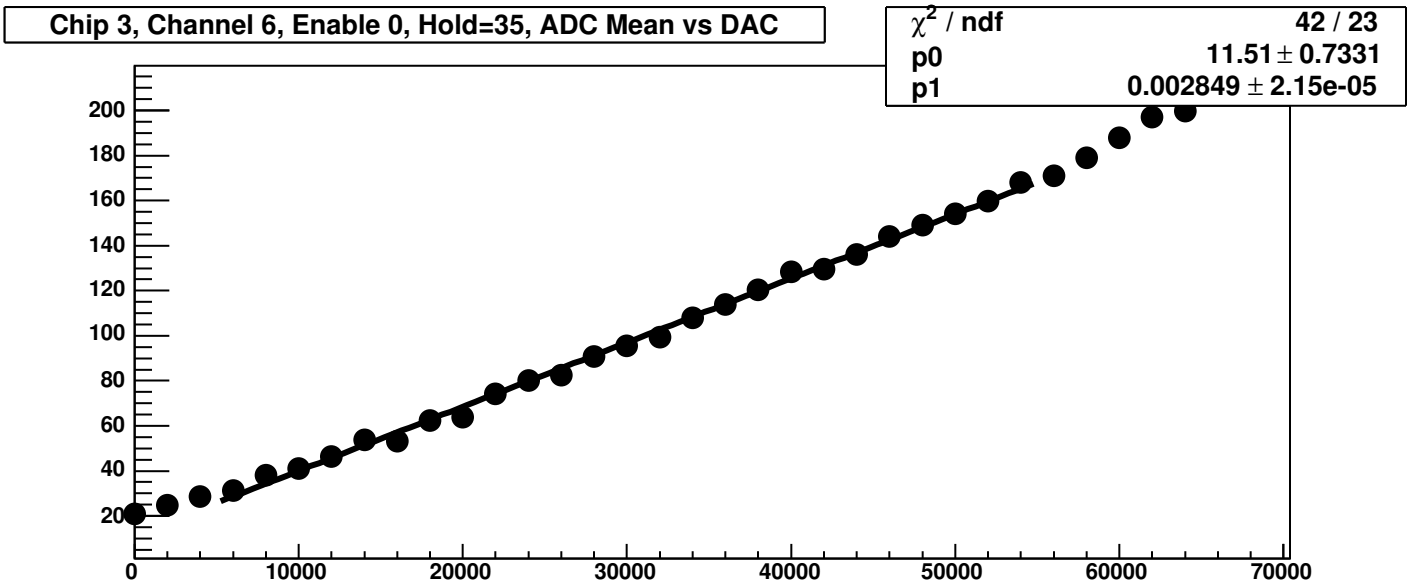

Chip 3, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

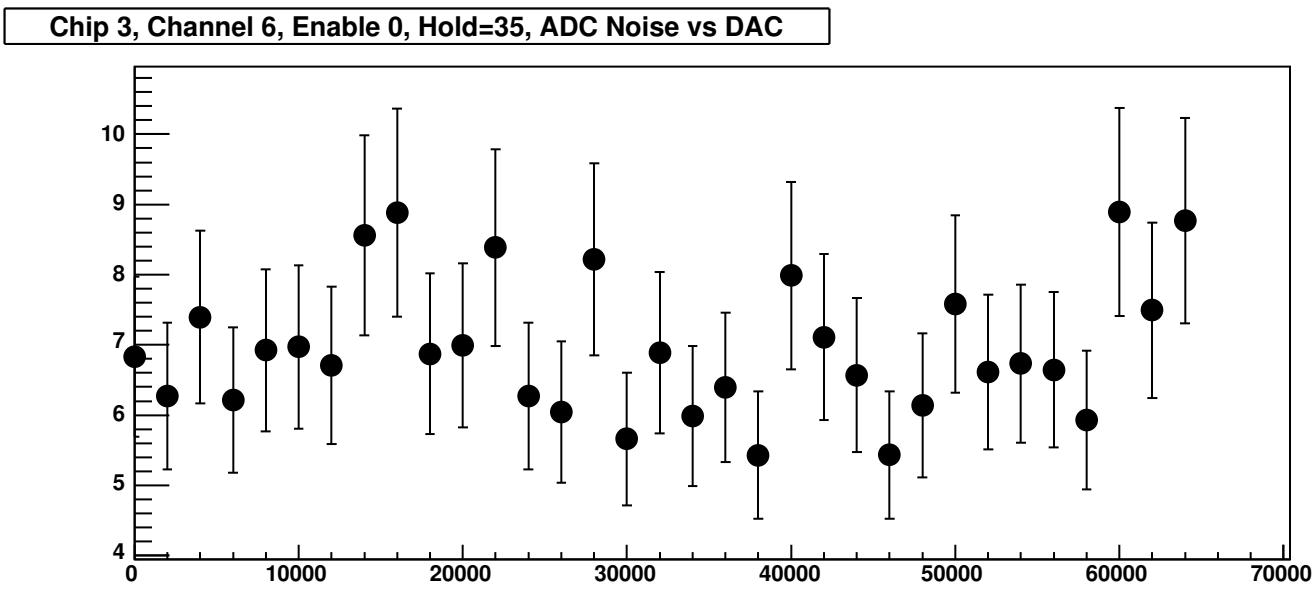

Chip 3, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000

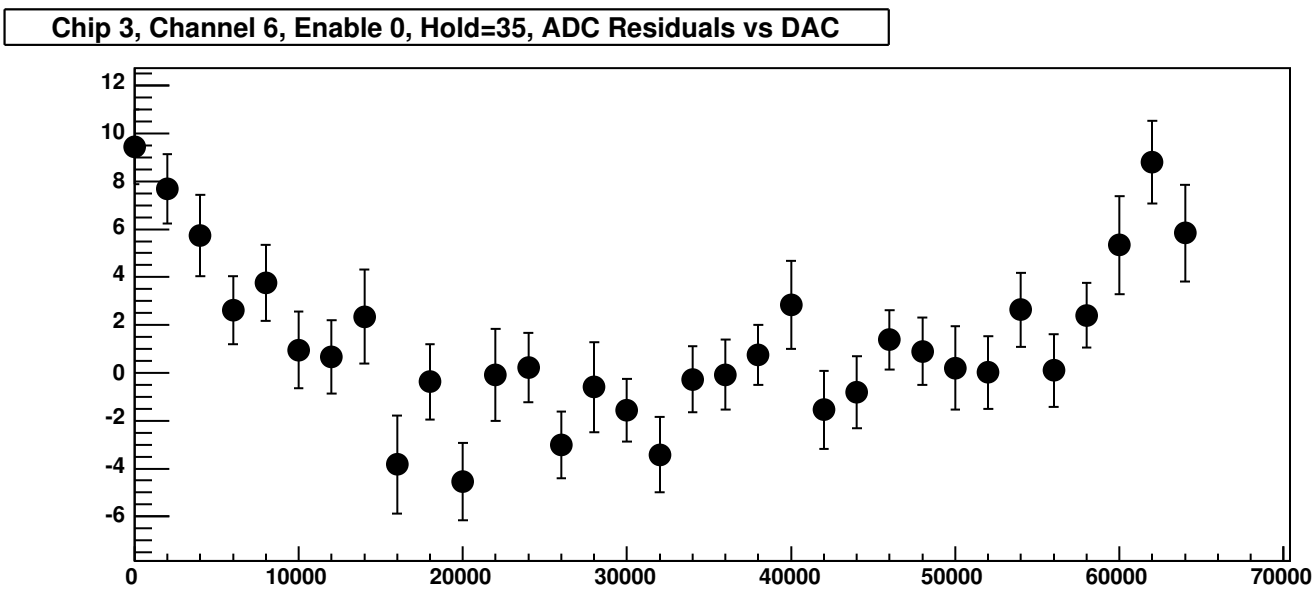

Chip 3, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 23 / 23
p0 94.87 ± 0.703
p1 0.003238 ± 2.128e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000

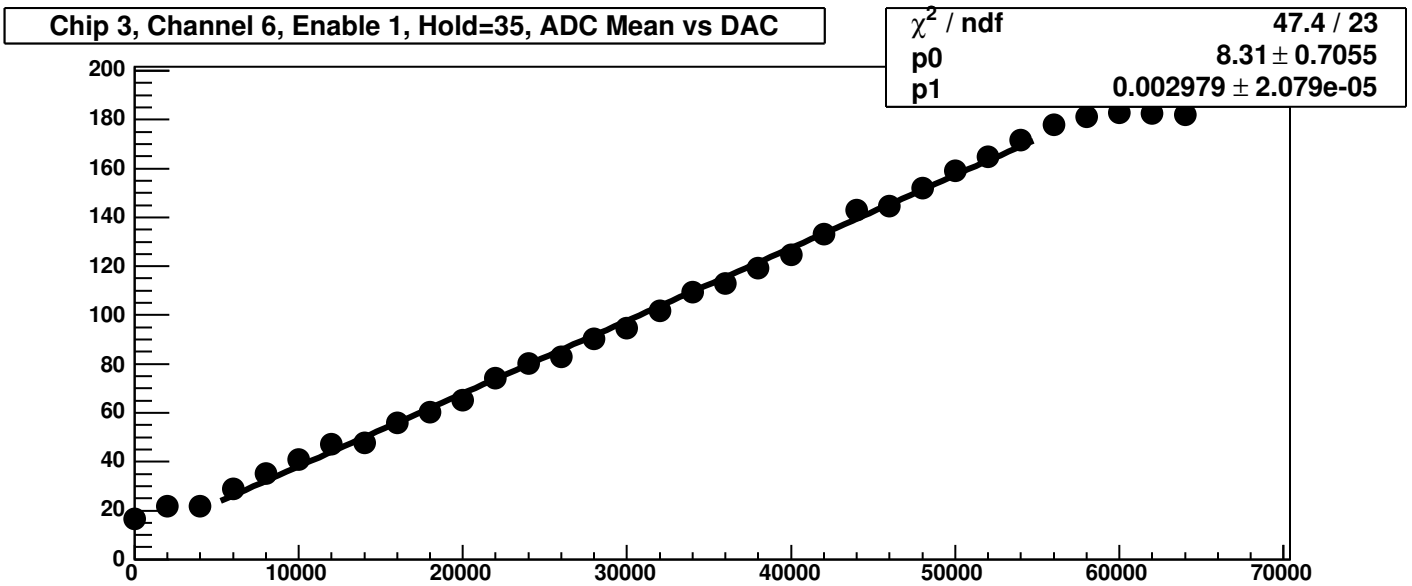

Chip 3, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

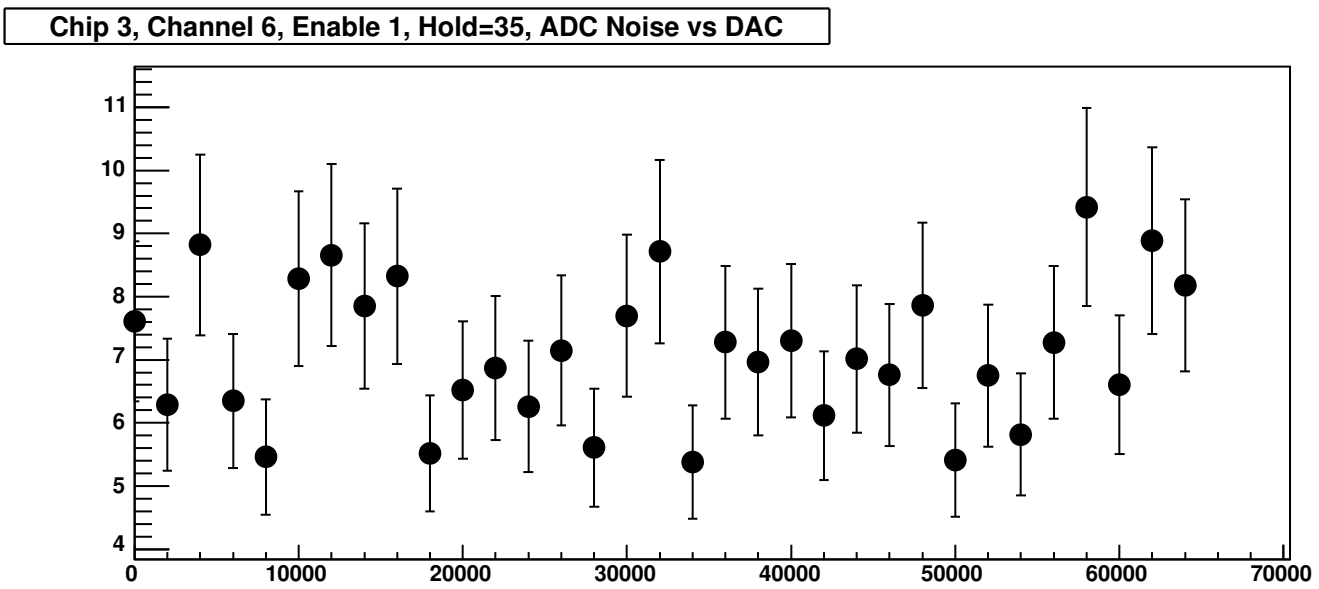

Chip 3, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000

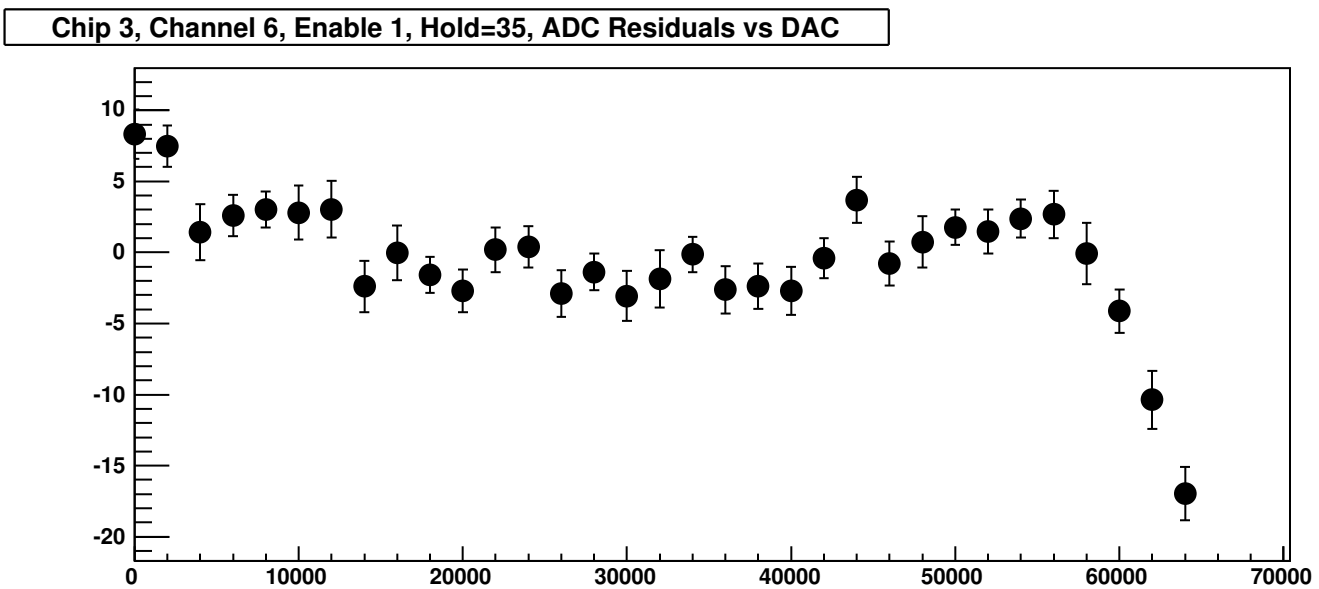

Chip 3, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 42 / 23
p0 11.51 ± 0.7331
p1 0.002849 ± 2.15e-05
200
180
160
140
120
100
80
60
40
20
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

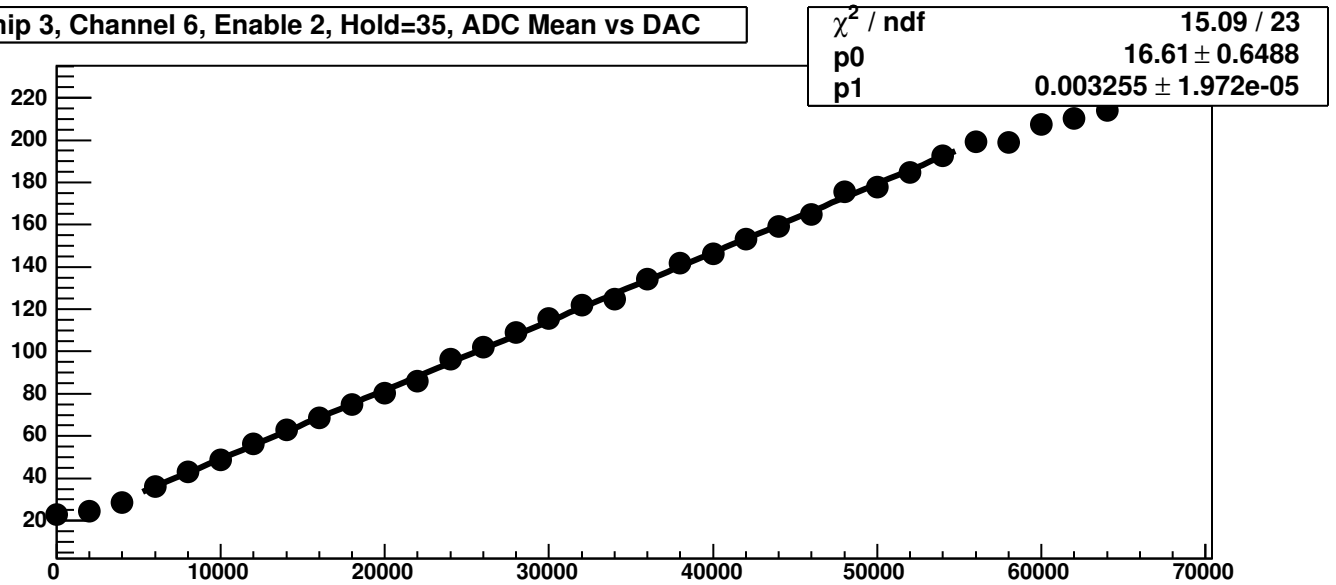

Chip 3, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 47.4 / 23
p0 8.31 ± 0.7055
p1 0.002979 ± 2.079e-05
200
180
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000
70000

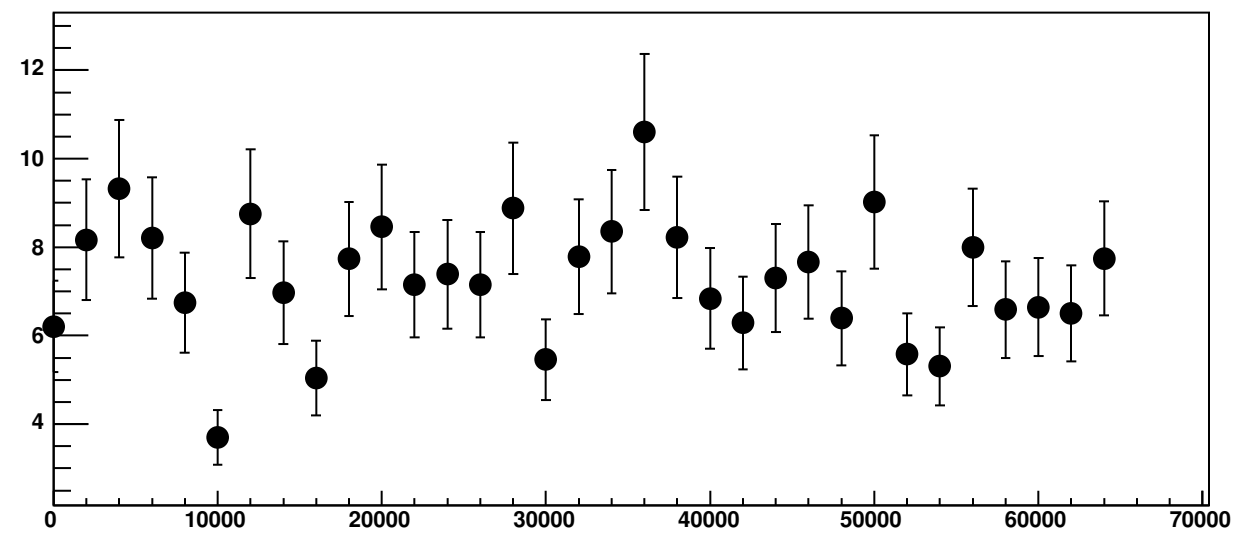

Chip 3, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
0
10000
20000
30000
40000
50000
60000
70000

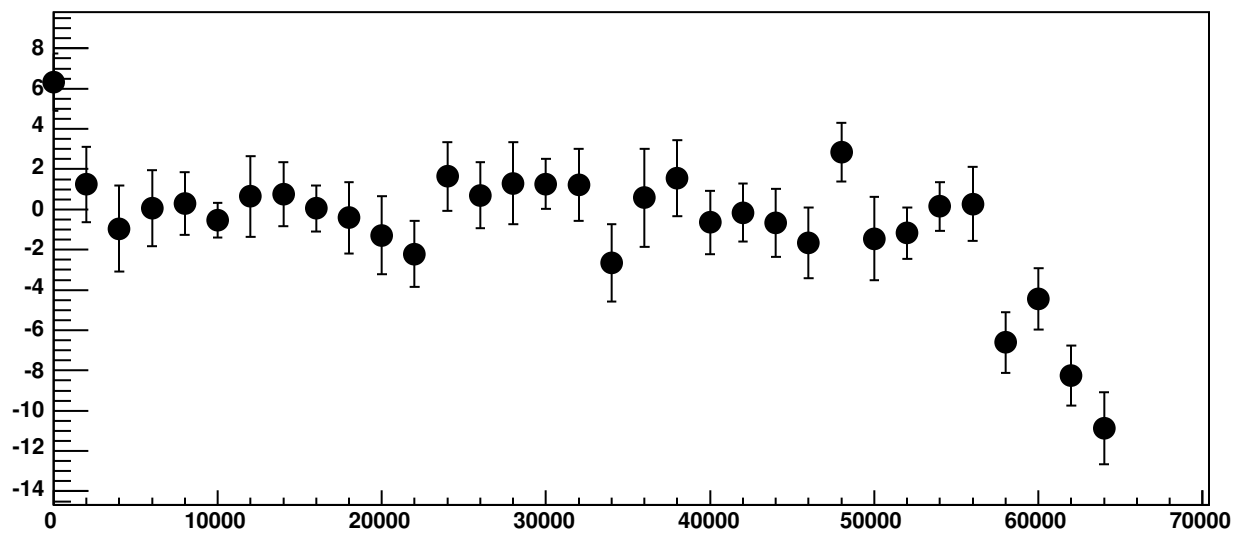
Chip 3, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC

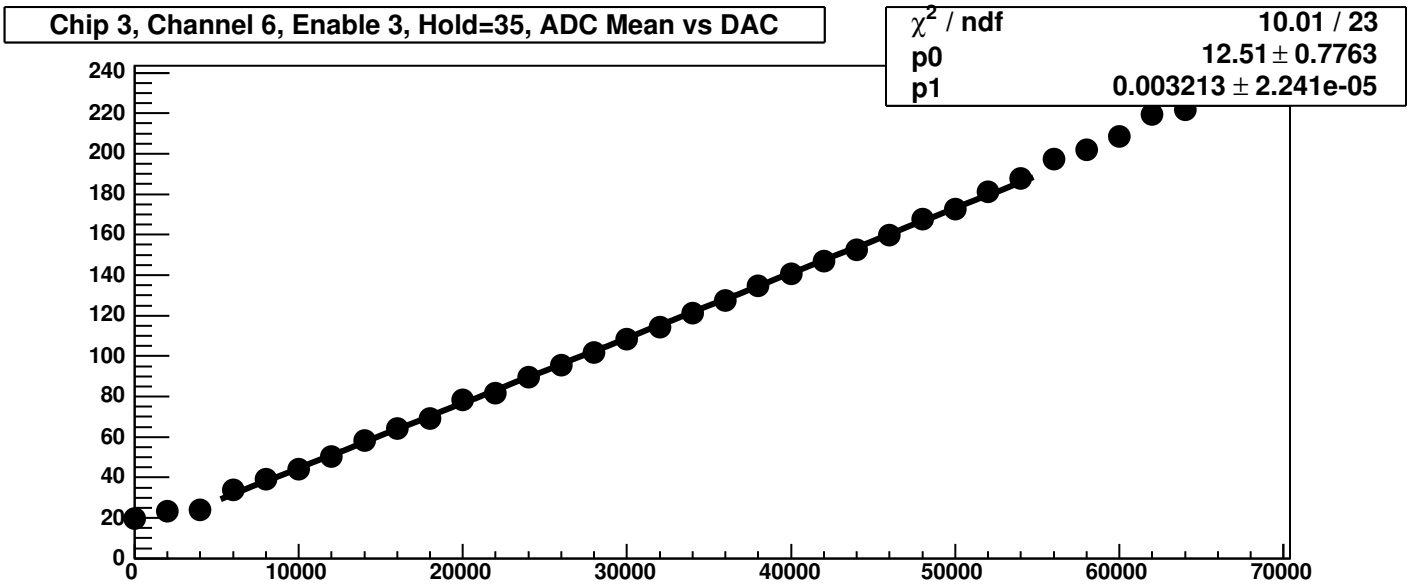


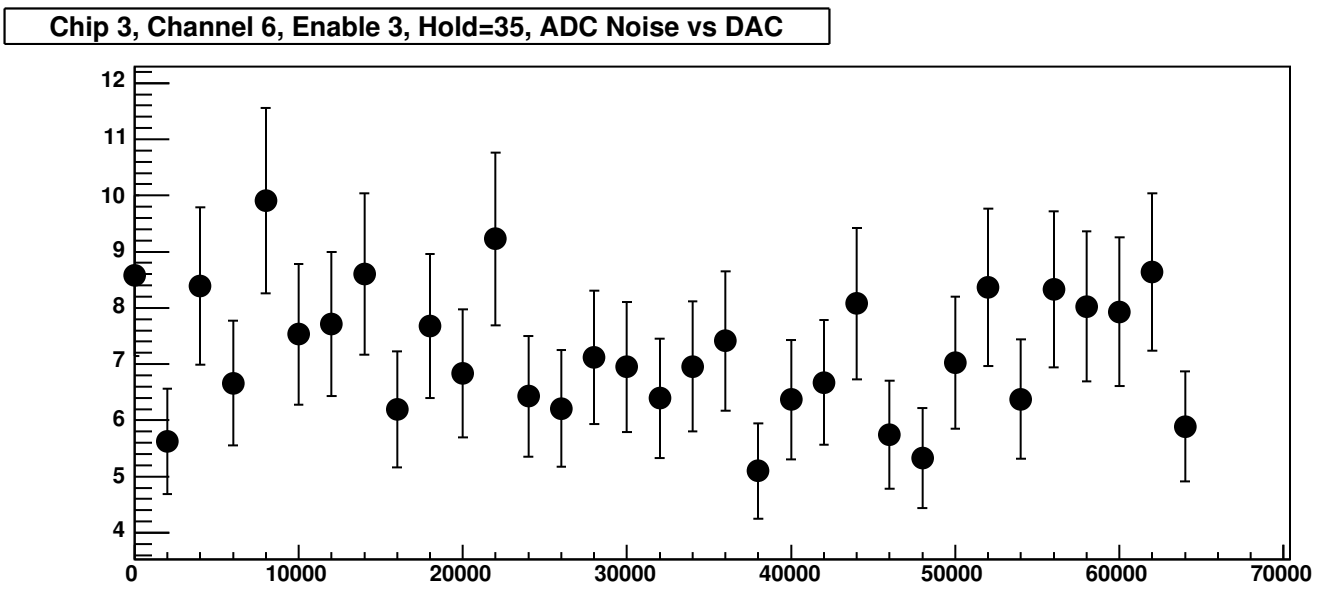
Chip 3, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC

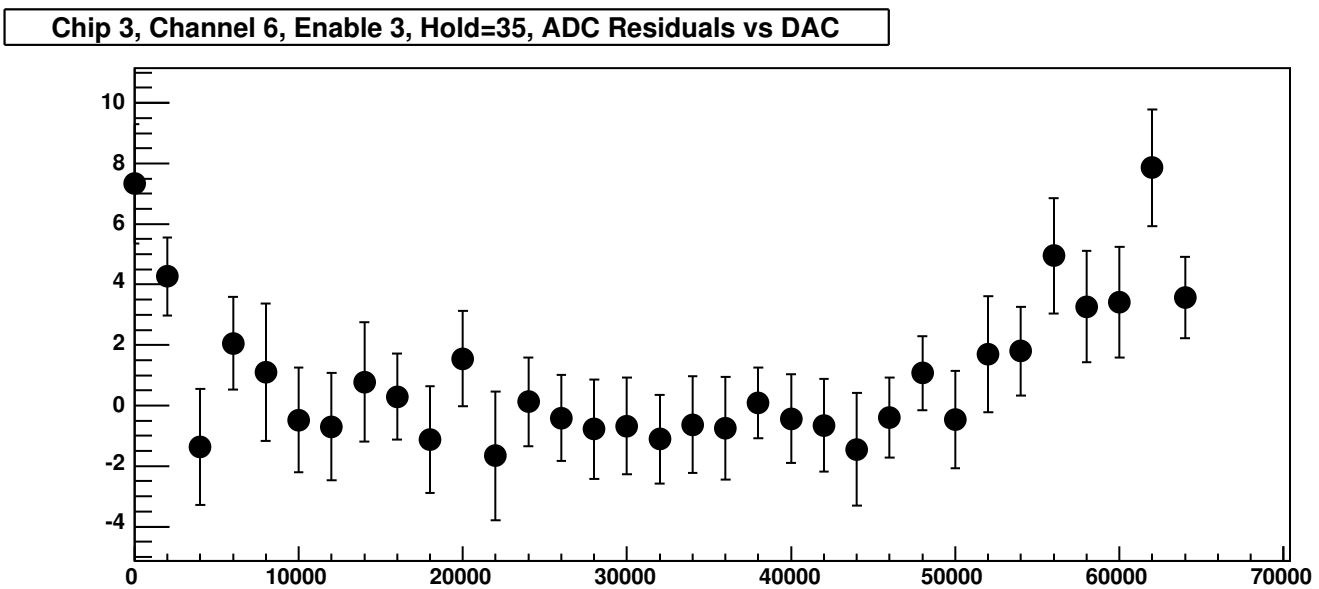


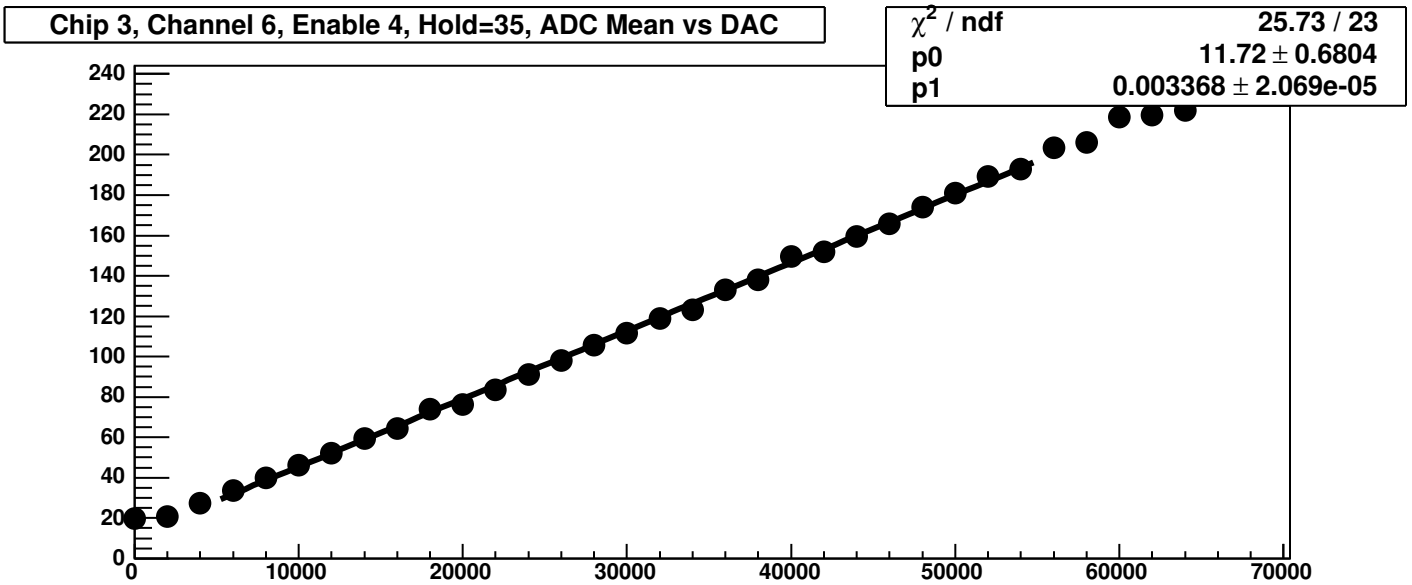
Chip 3, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC

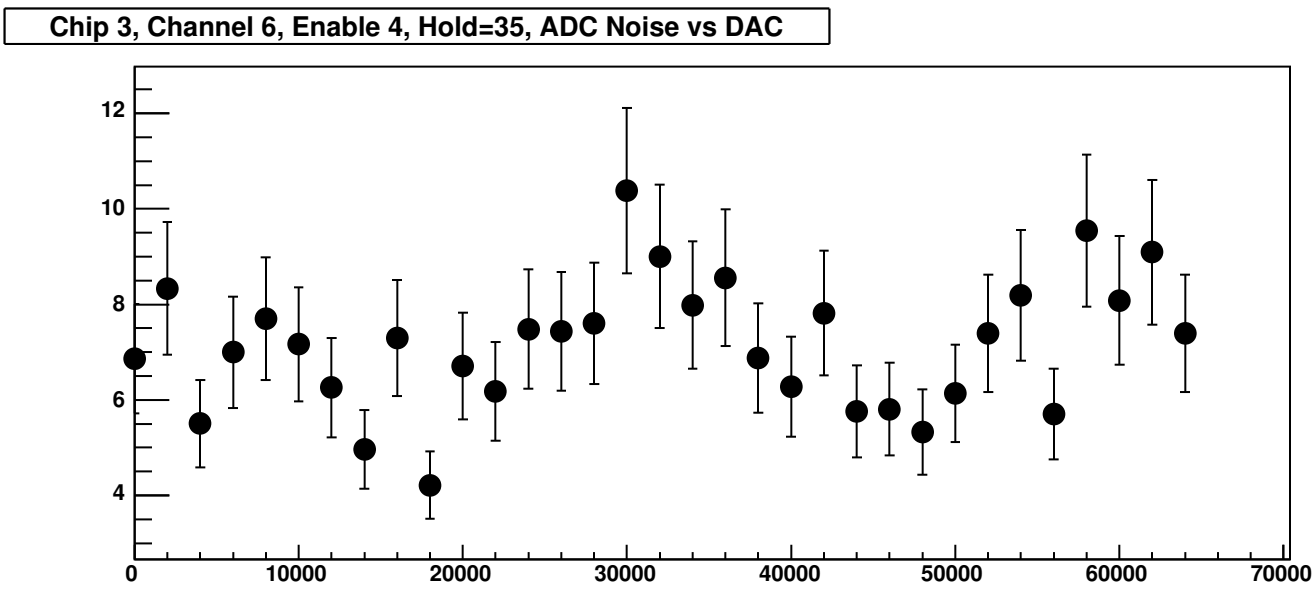


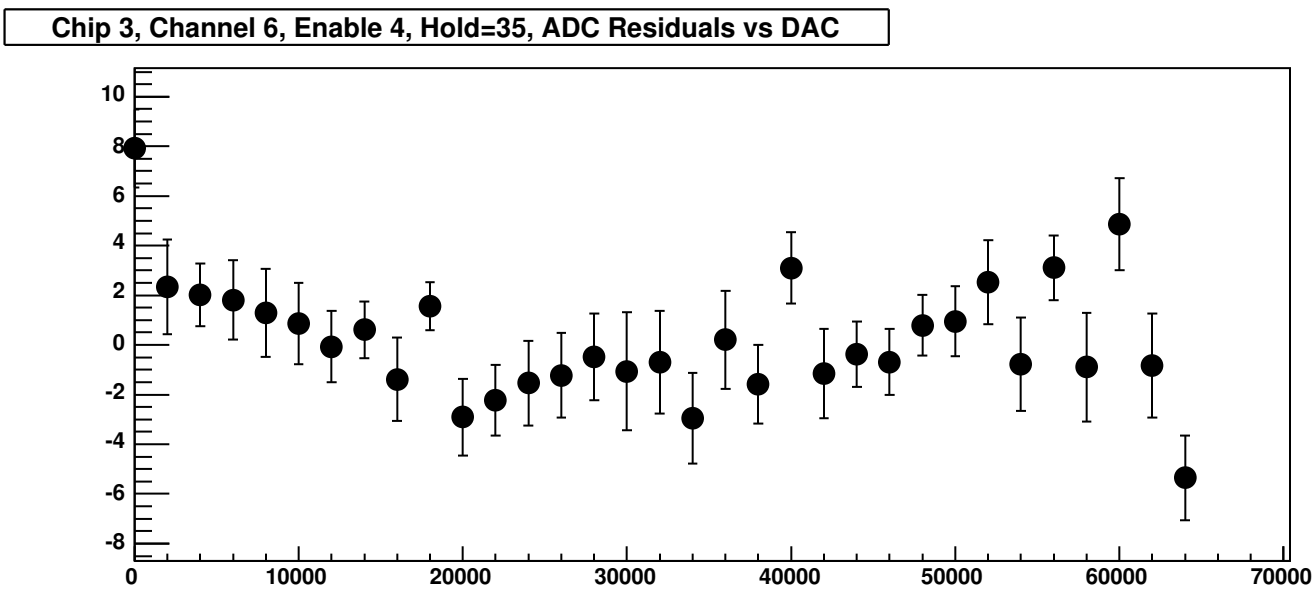

Chip 3, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 10.01 / 23
p0 12.51 ± 0.7763
p1 0.003213 ± 2.241e-05
240
220
200
180
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000
70000

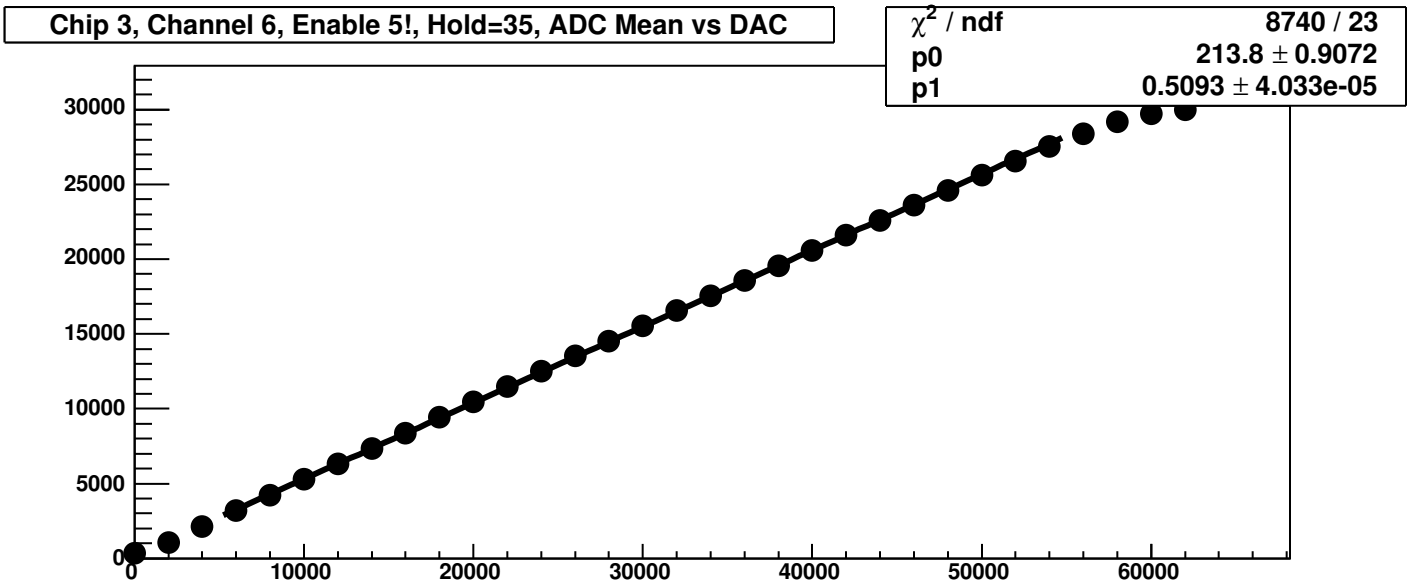

Chip 3, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

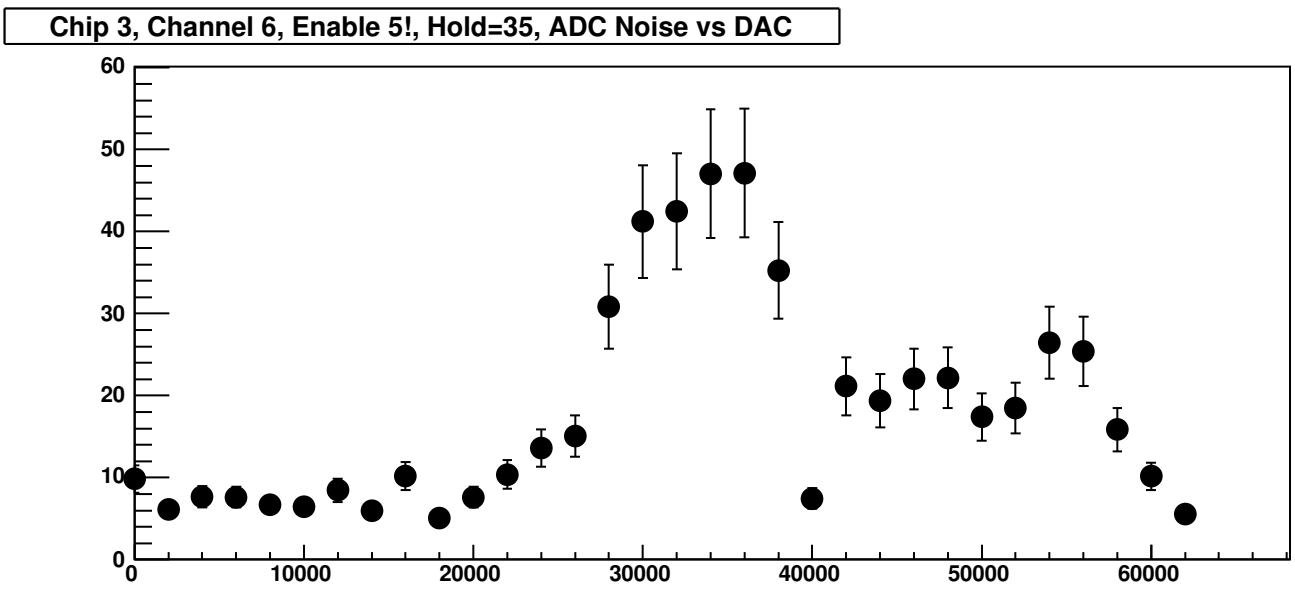

Chip 3, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
0
10000
20000
30000
40000
50000
60000
70000

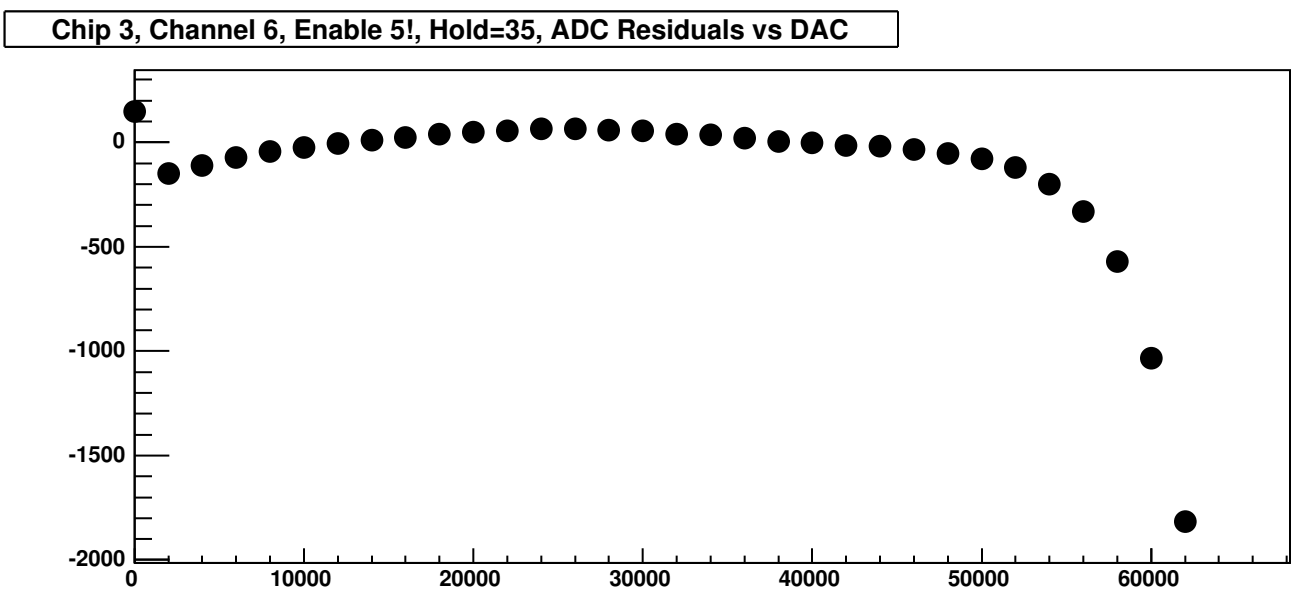

Chip 3, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 25.73 / 23
p0 11.72 ± 0.6804
p1 0.003368 ± 2.069e-05
240
220
200
180
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000
70000

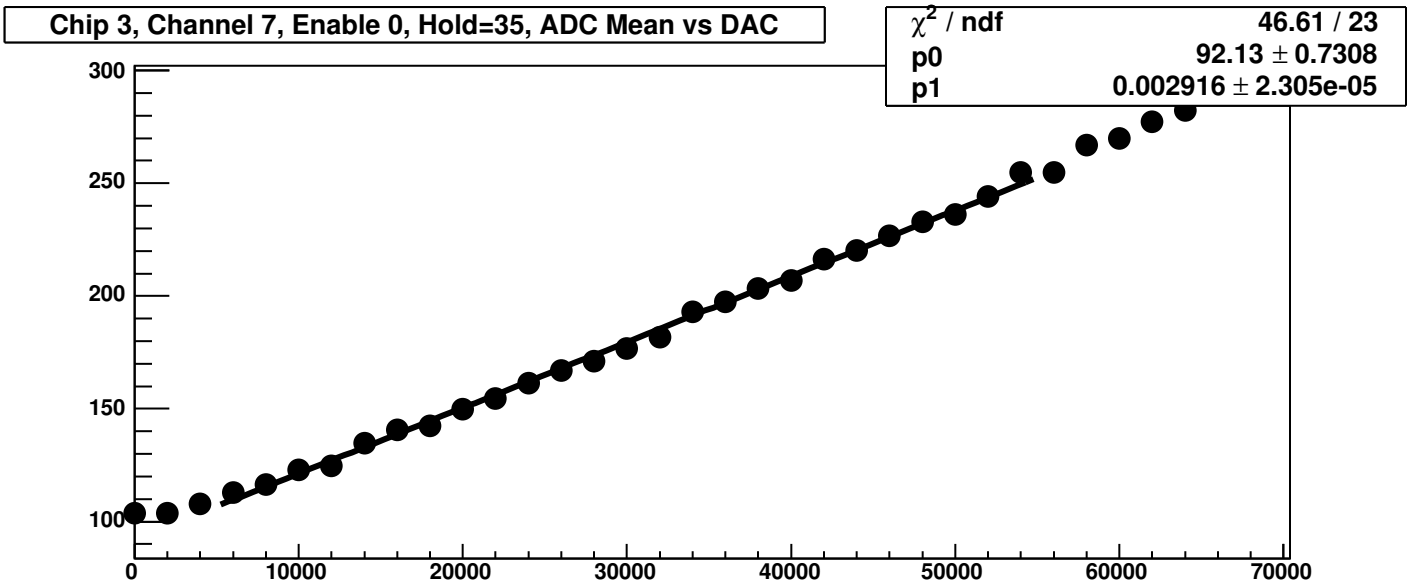

Chip 3, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

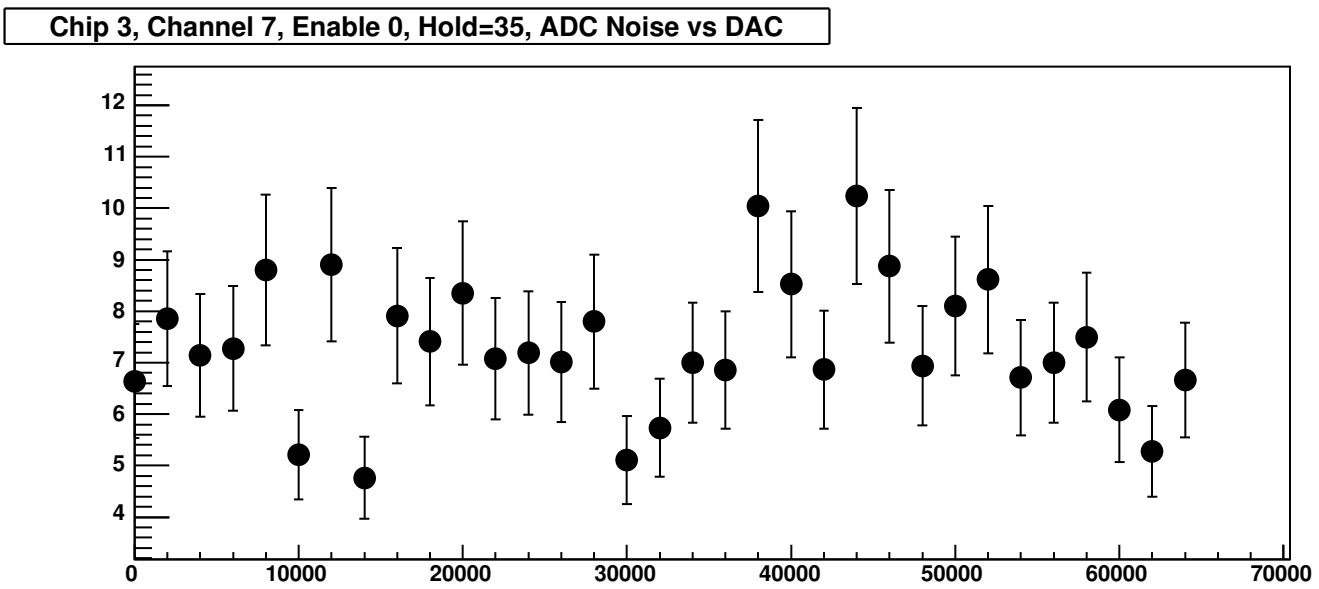

Chip 3, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

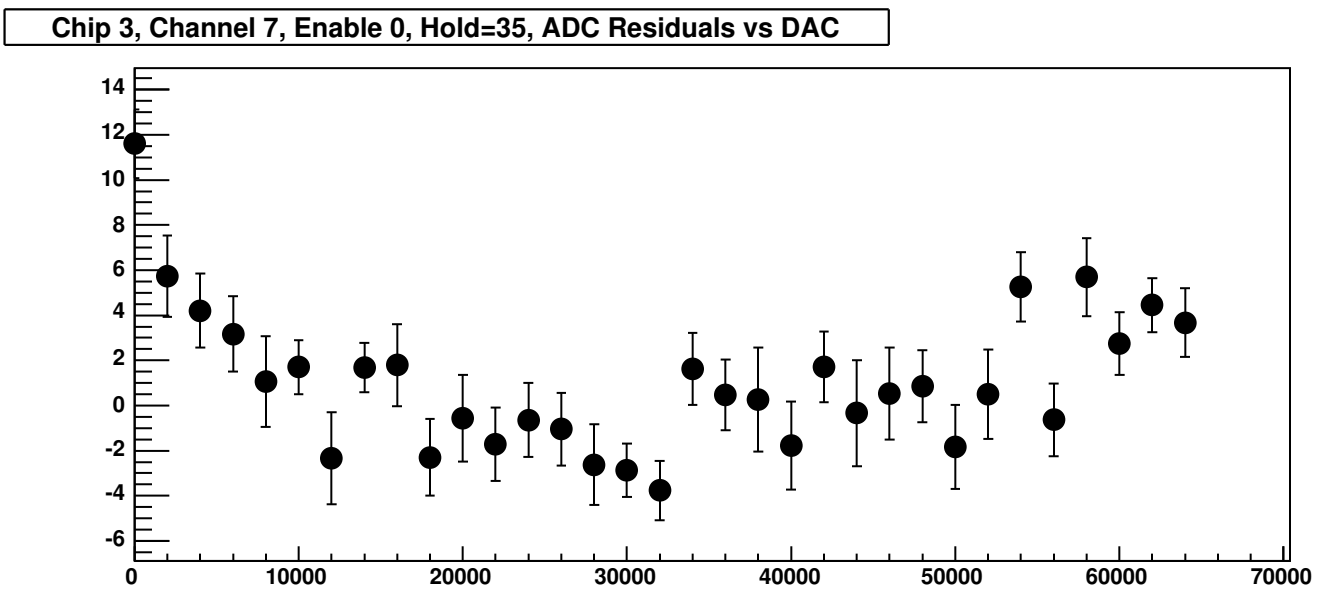

Chip 3, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 8740 / 23
p0 213.8 ± 0.9072
p1 0.5093 ± 4.033e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

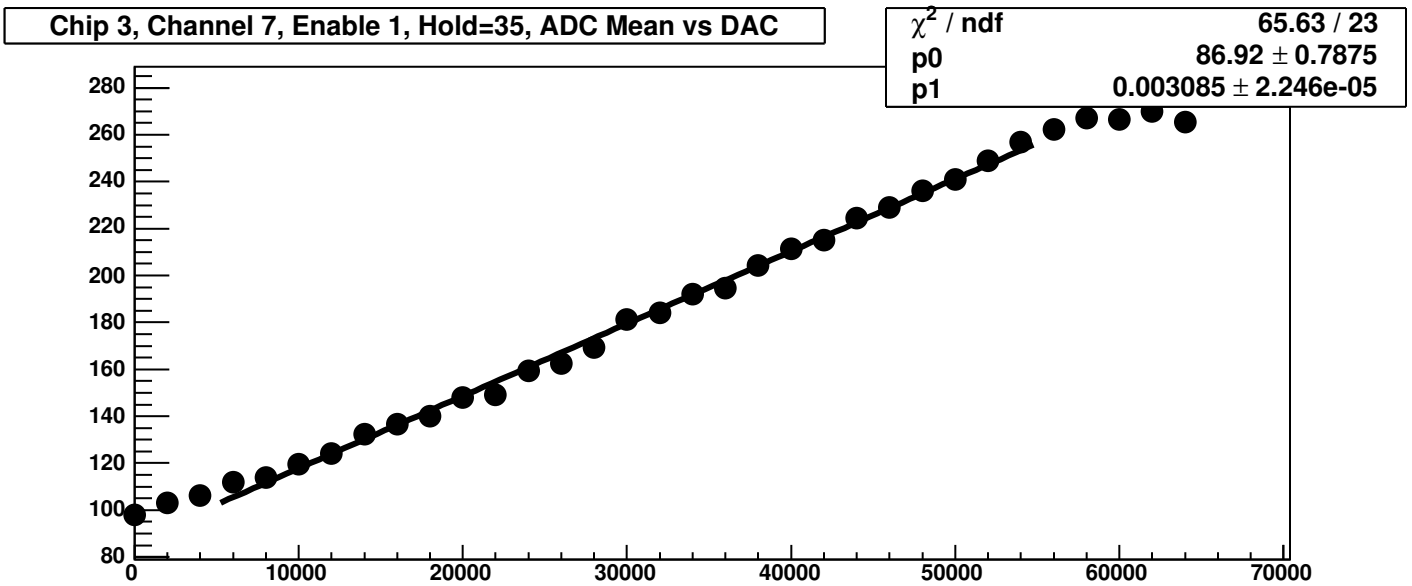

Chip 3, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000

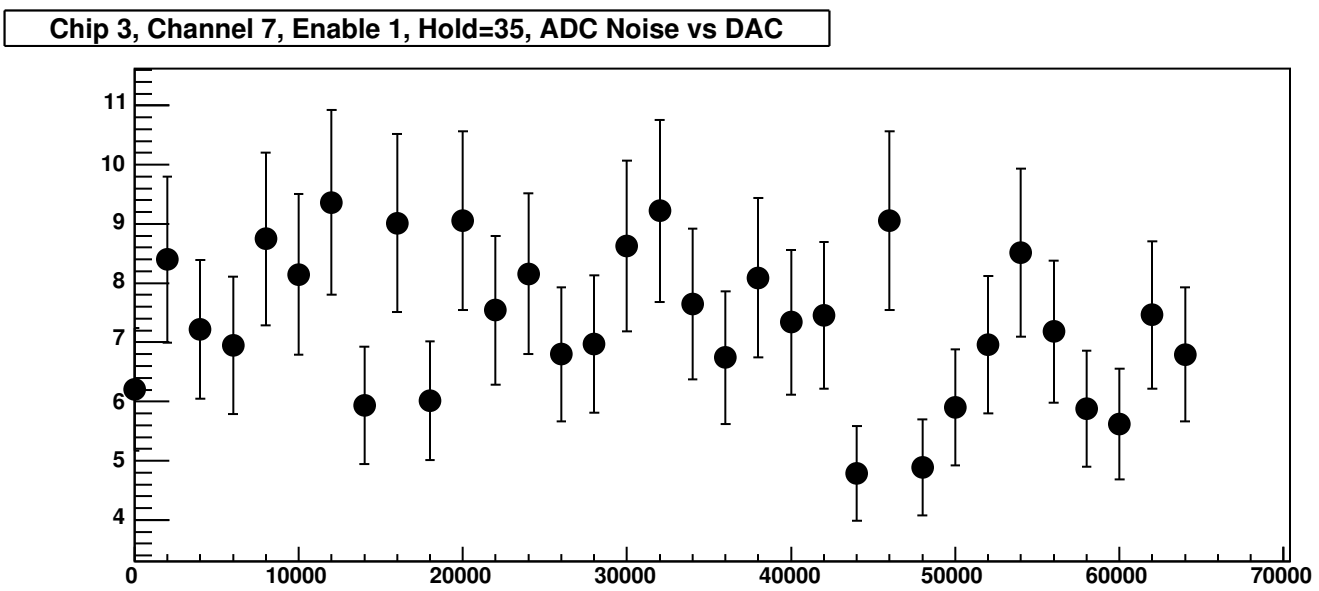

Chip 3, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000

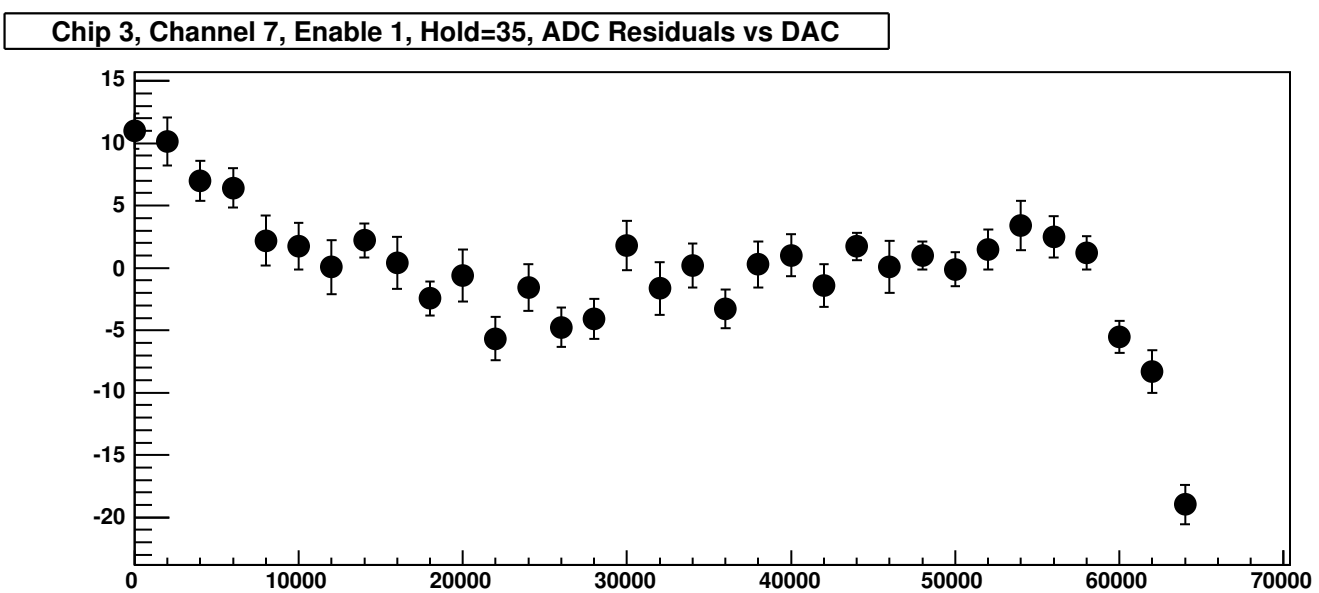

Chip 3, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 46.61 / 23
p0 92.13 ± 0.7308
p1 0.002916 ± 2.305e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

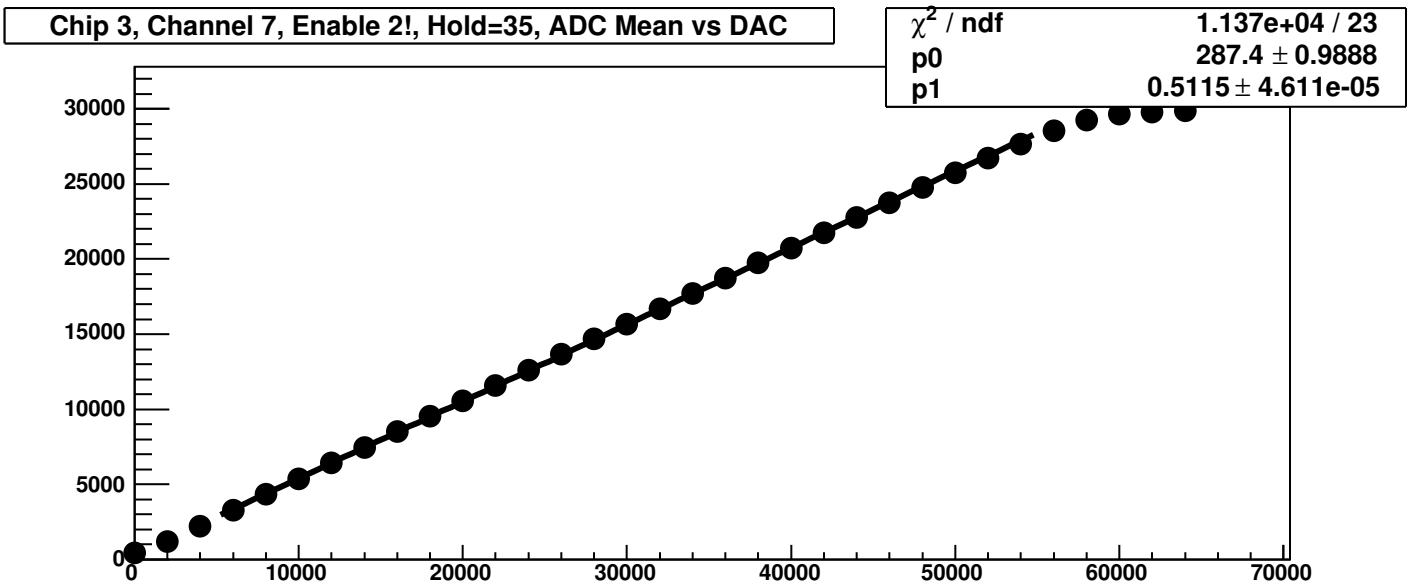

Chip 3, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

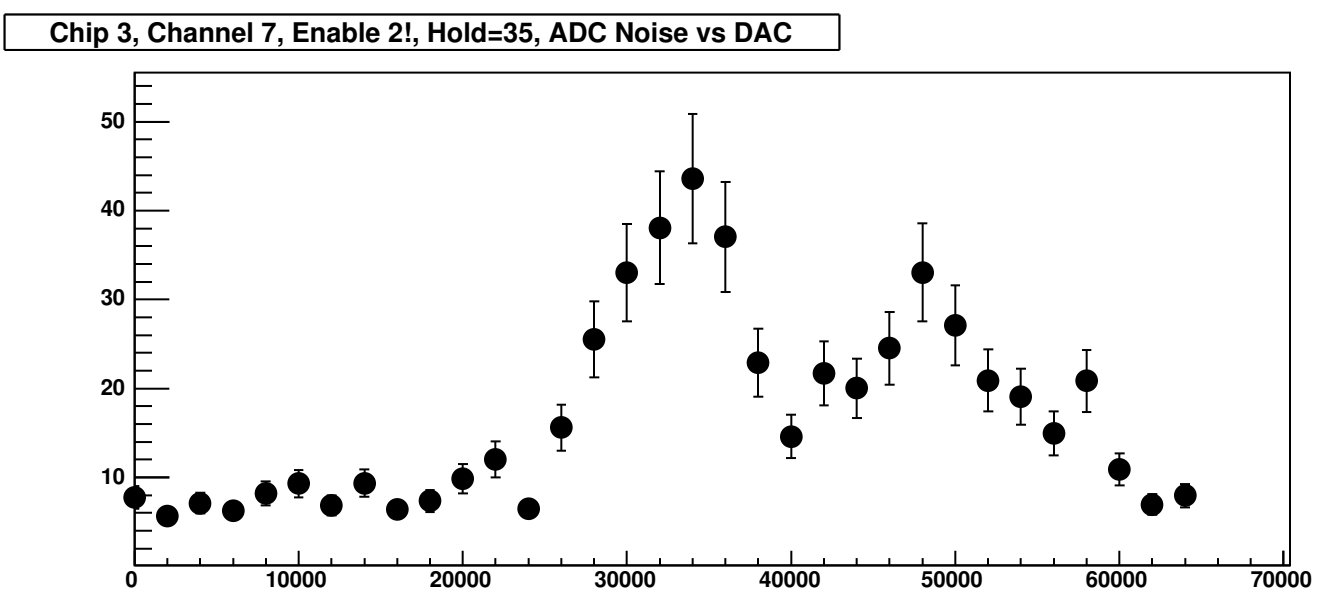

Chip 3, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

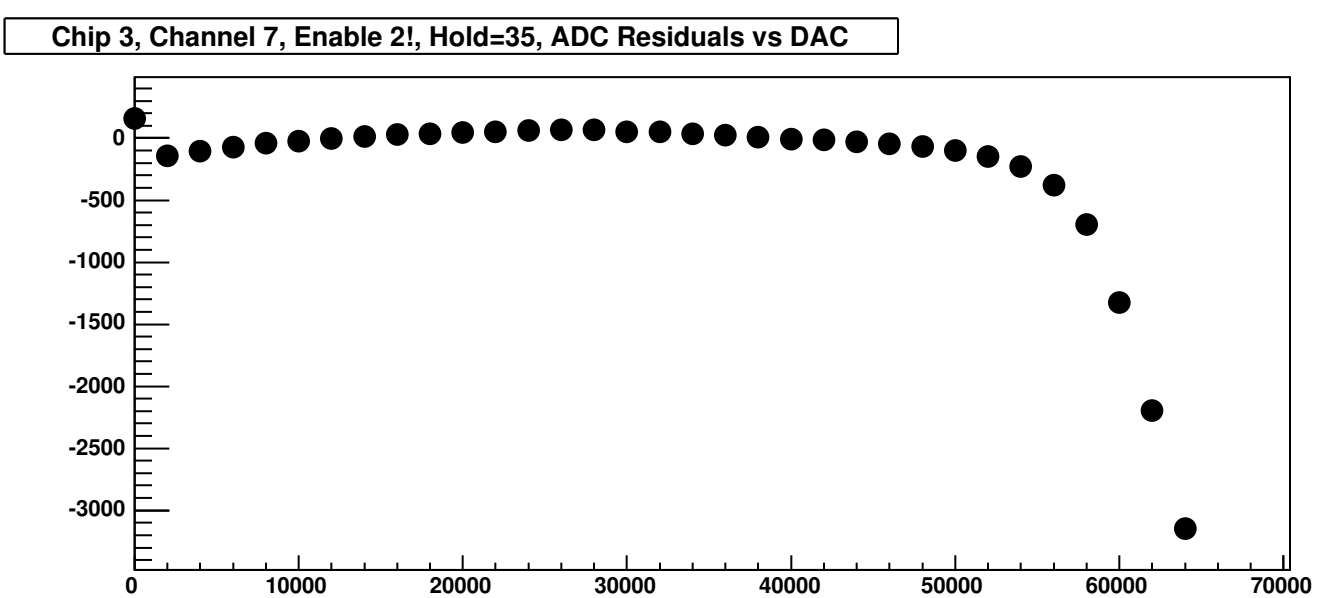

Chip 3, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 65.63 / 23
p0 86.92 ± 0.7875
p1 0.003085 ± 2.246e-05
280
260
240
220
200
180
160
140
120
100
80
0
10000
20000
30000
40000
50000
60000
70000

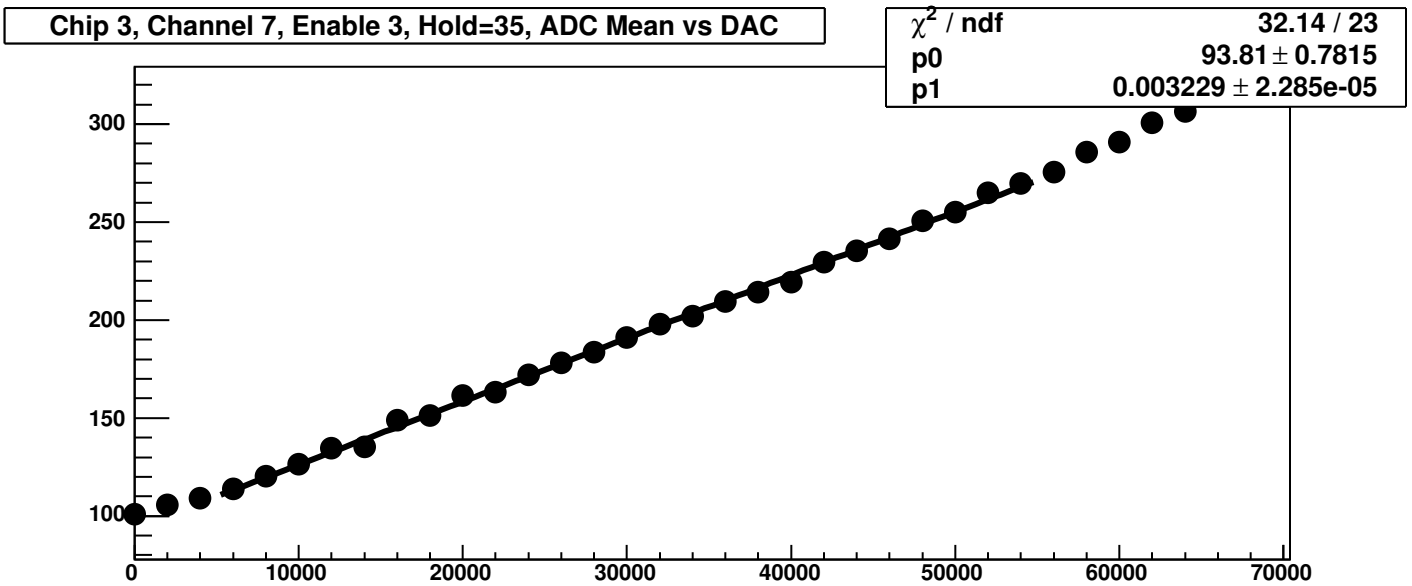

Chip 3, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

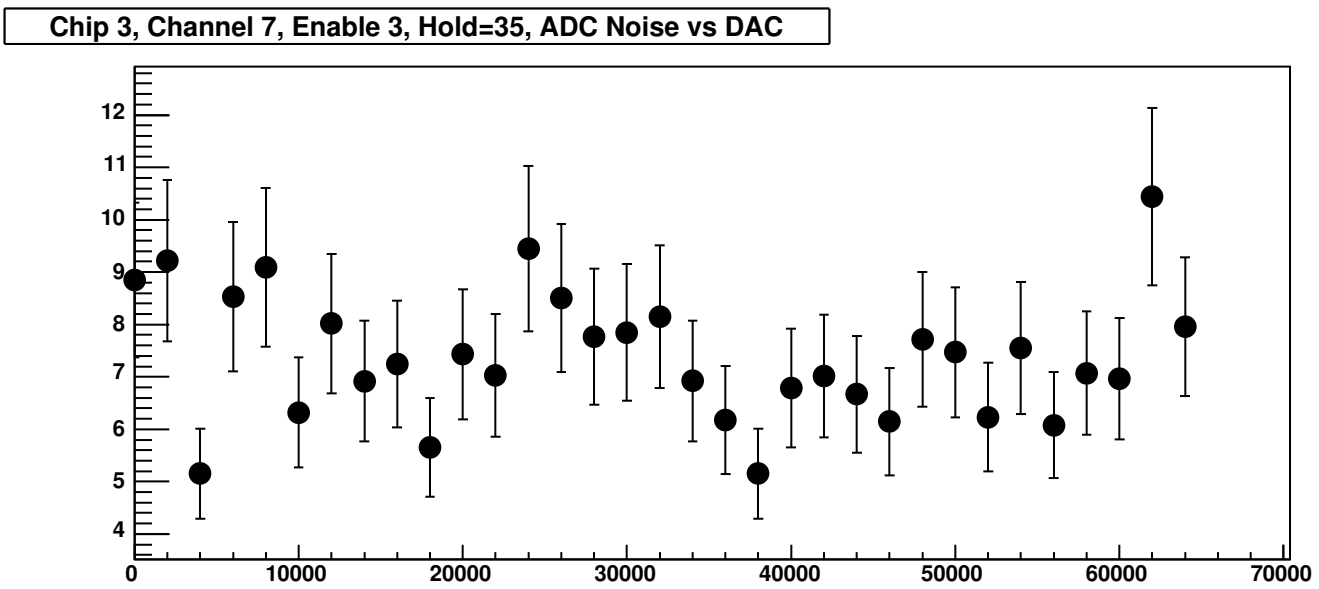

Chip 3, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
10000
20000
30000
40000
50000
60000
70000

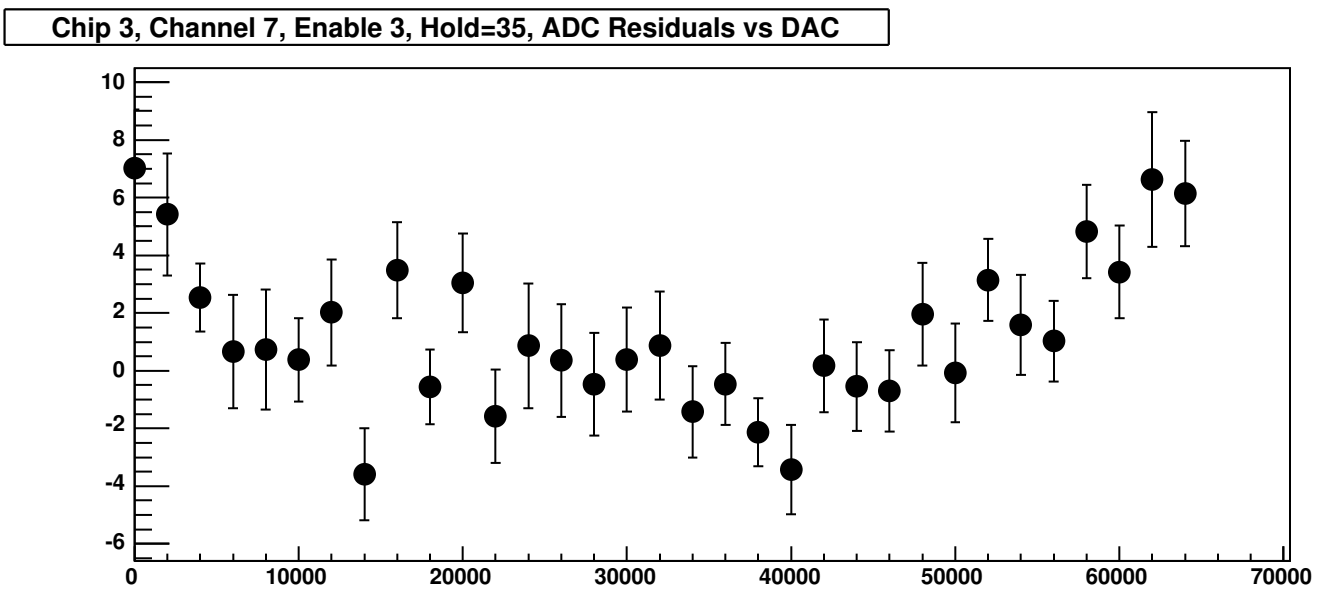

Chip 3, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 1.137e+04 / 23
p0 287.4 ± 0.9888
p1 0.5115 ± 4.611e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

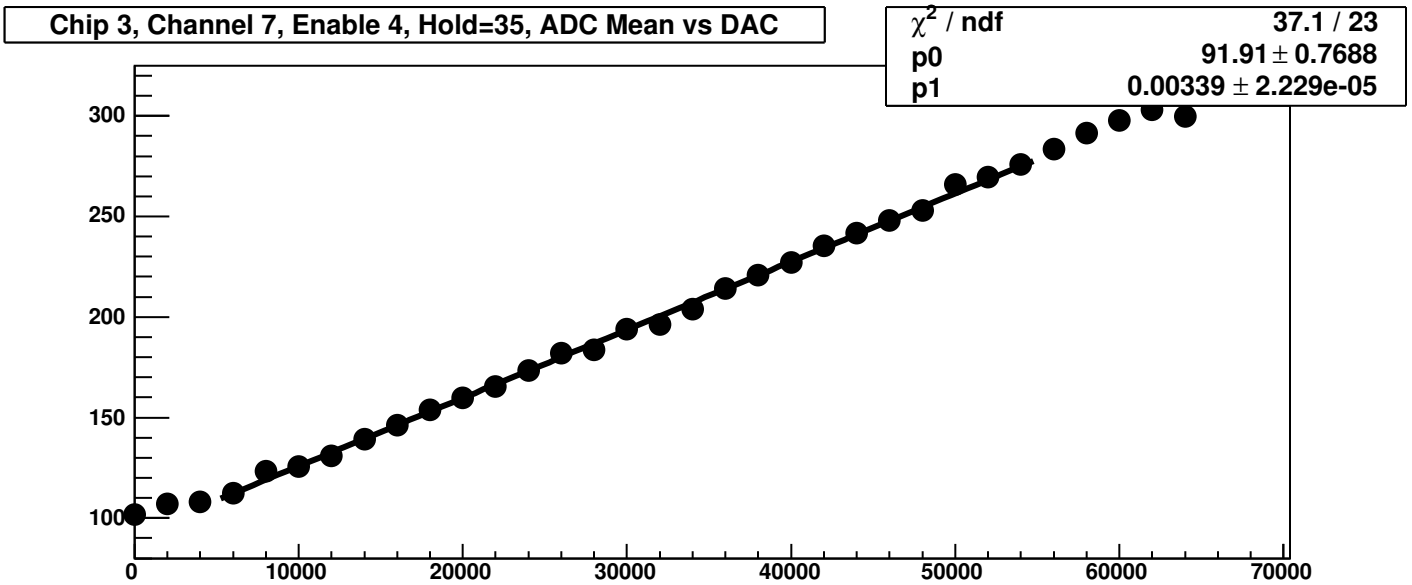

Chip 3, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000

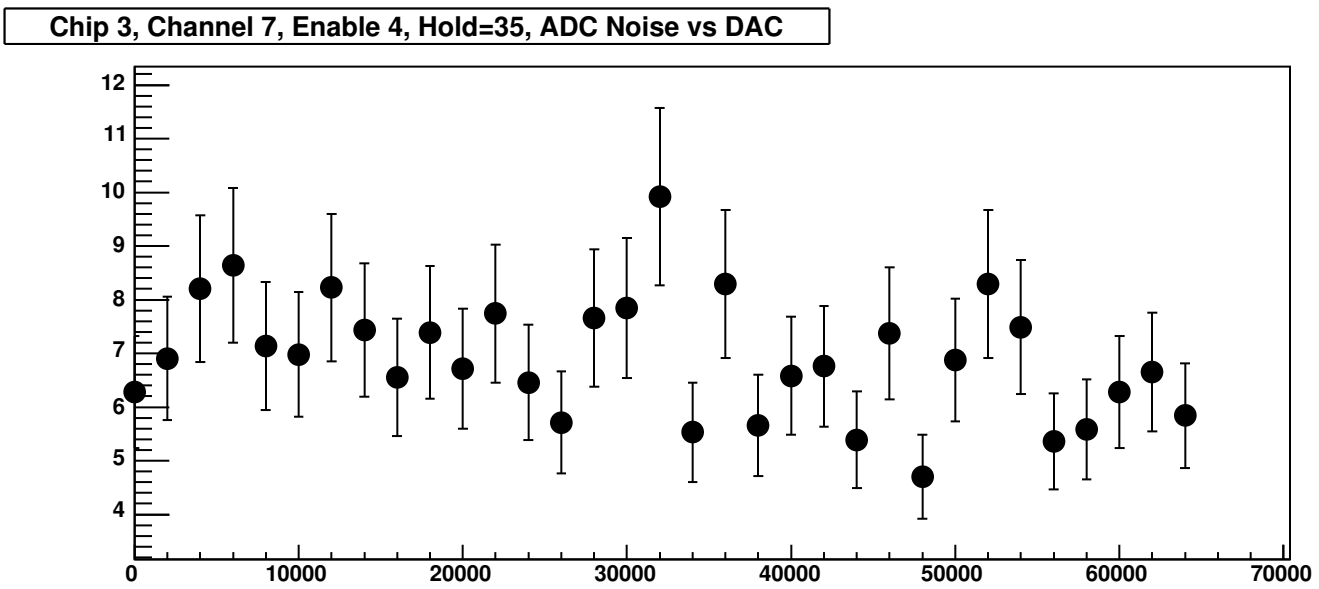

Chip 3, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
0
10000
20000
30000
40000
50000
60000
70000

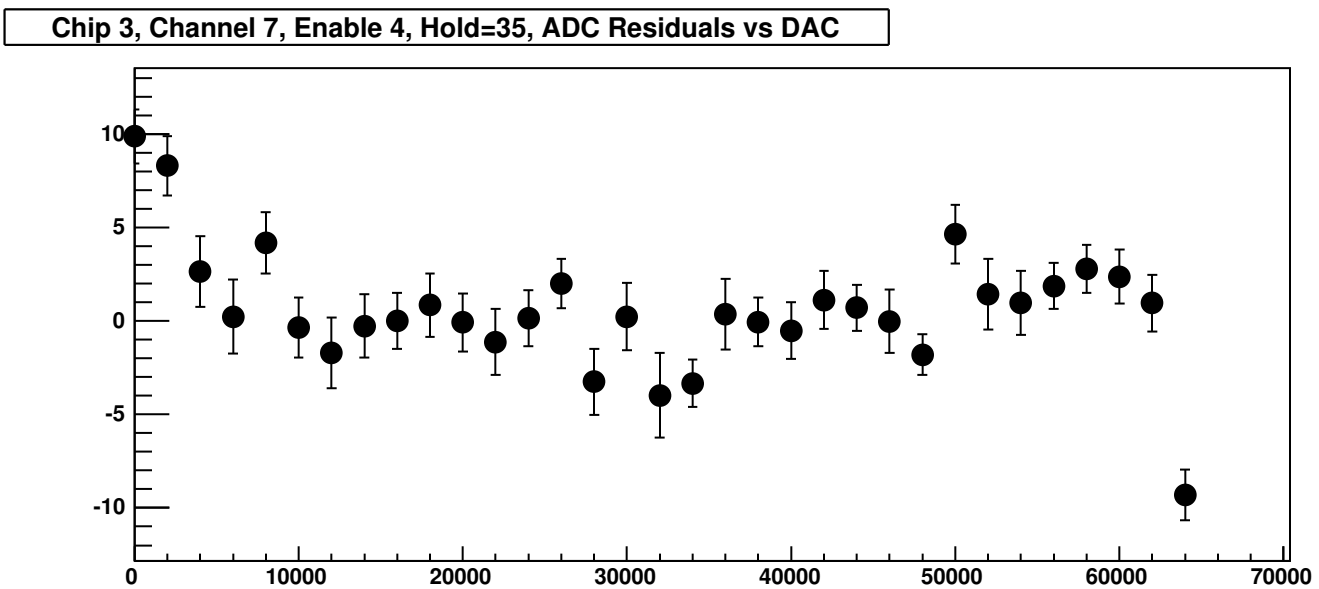

Chip 3, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 32.14 / 23
p0 93.81 ± 0.7815
p1 0.003229 ± 2.285e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

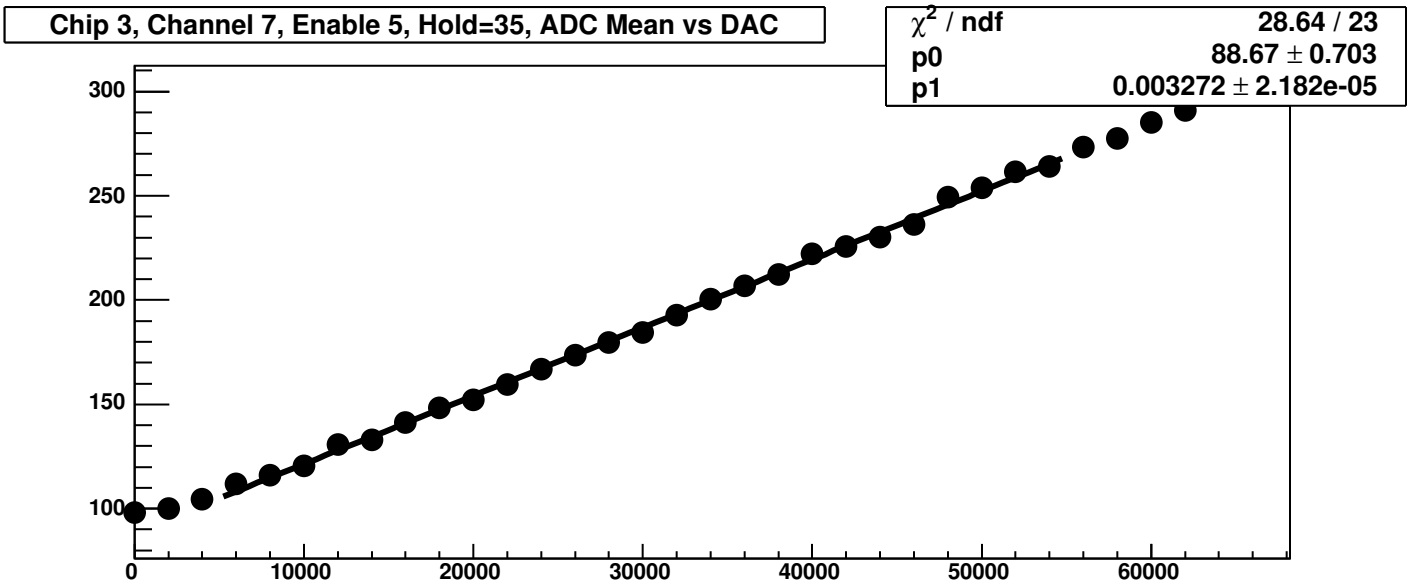

Chip 3, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

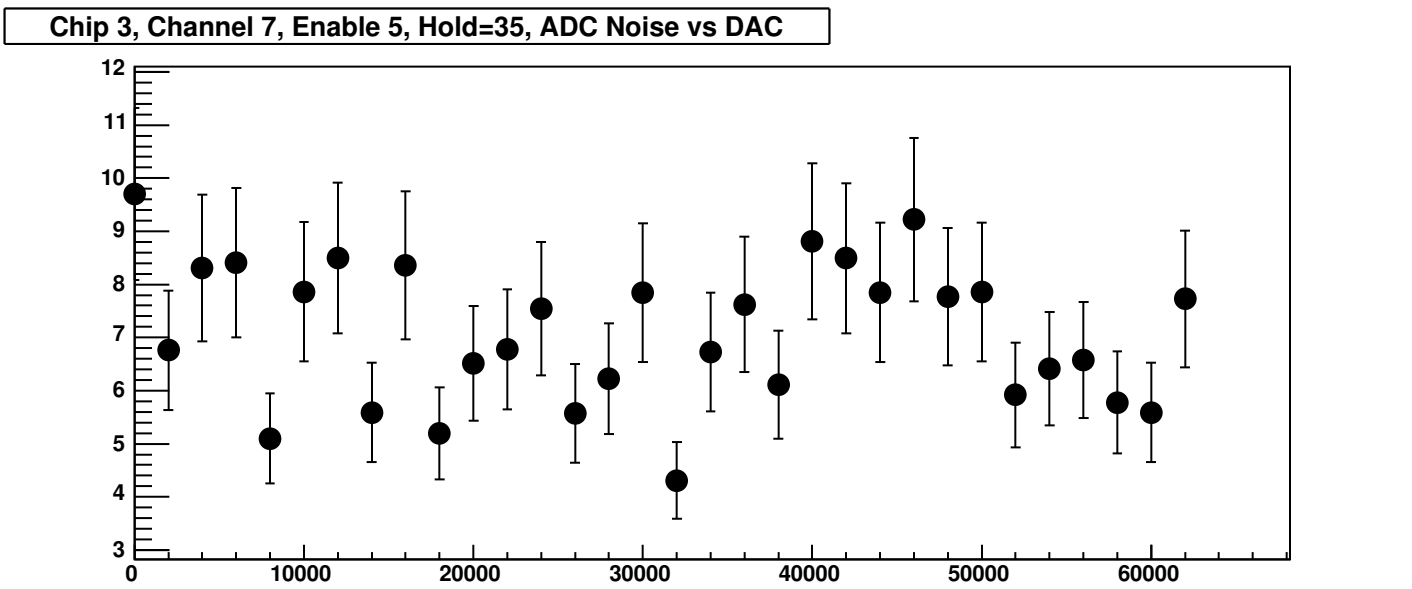

Chip 3, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

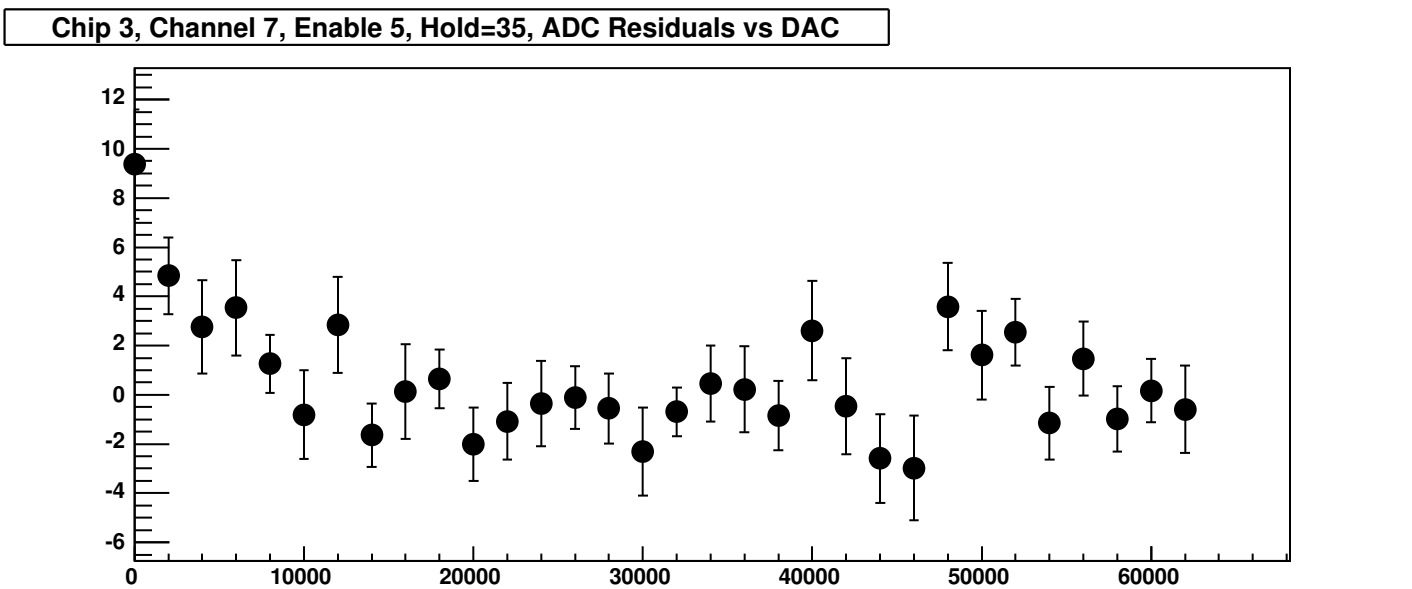

Chip 3, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 37.1 / 23
p0 91.91 ± 0.7688
p1 0.00339 ± 2.229e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

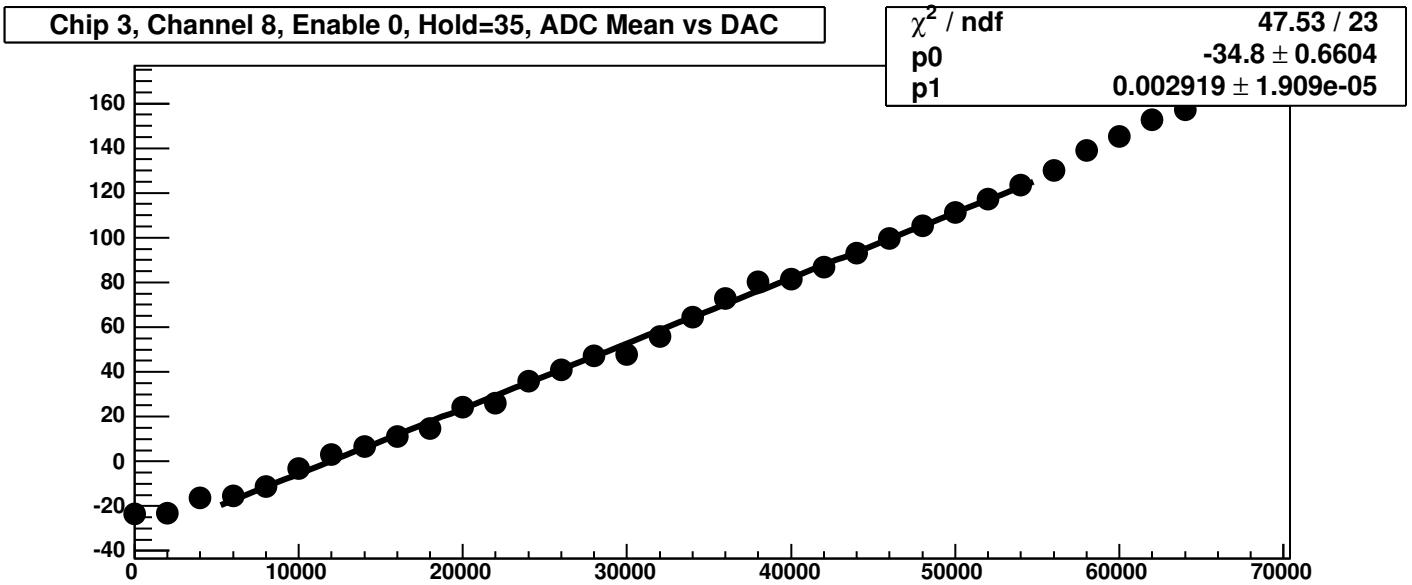

Chip 3, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

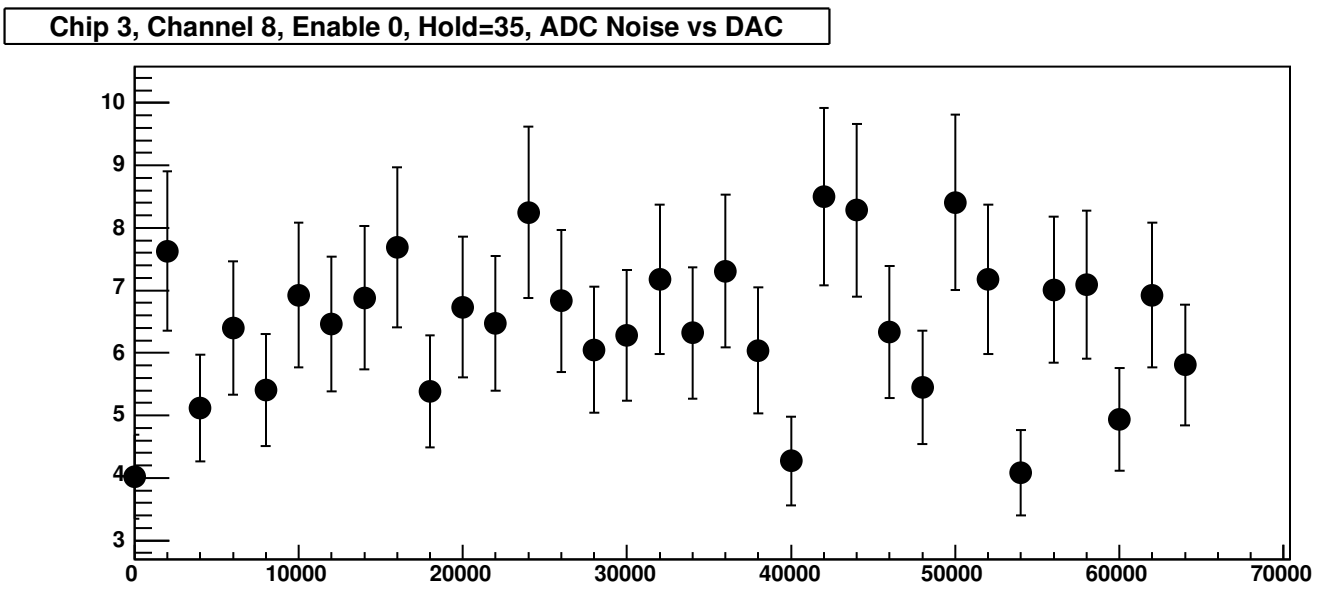

Chip 3, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

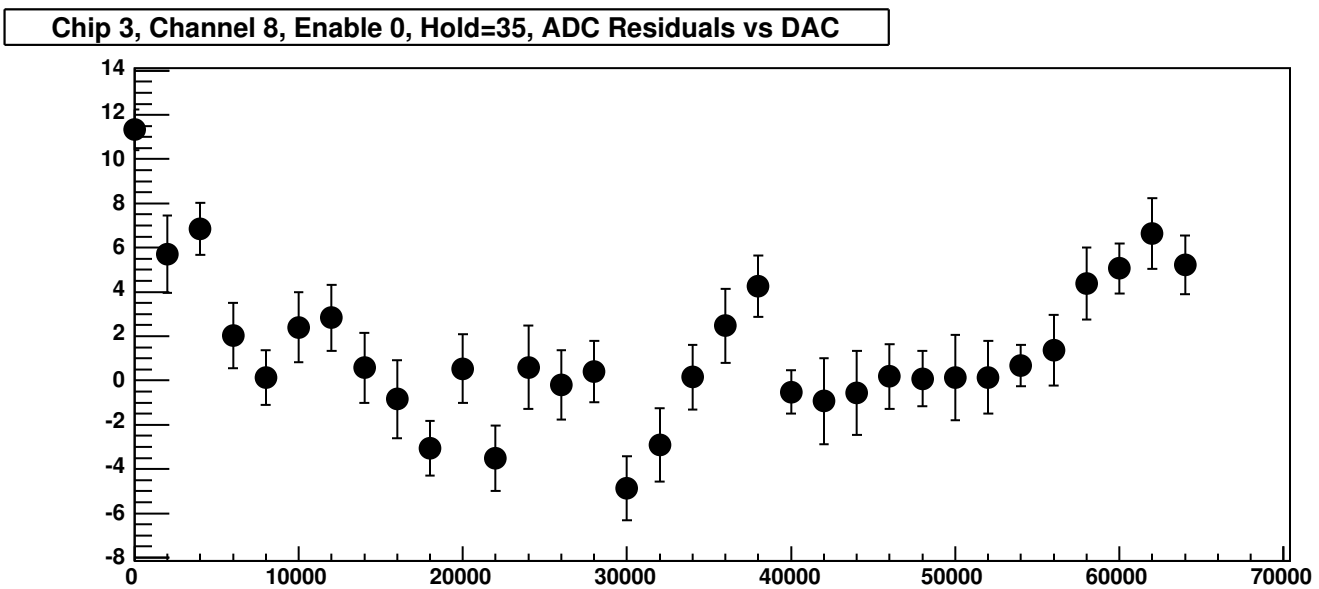

Chip 3, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 28.64 / 23
p0 88.67 ± 0.703
p1 0.003272 ± 2.182e-05
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000

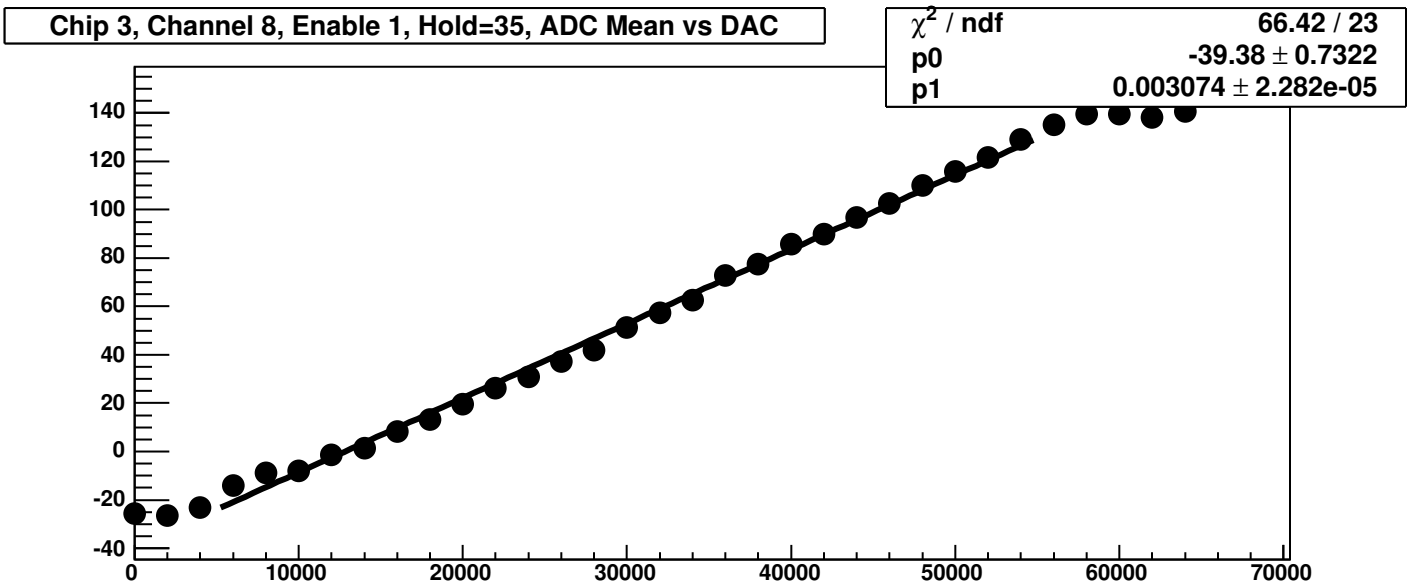

Chip 3, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000

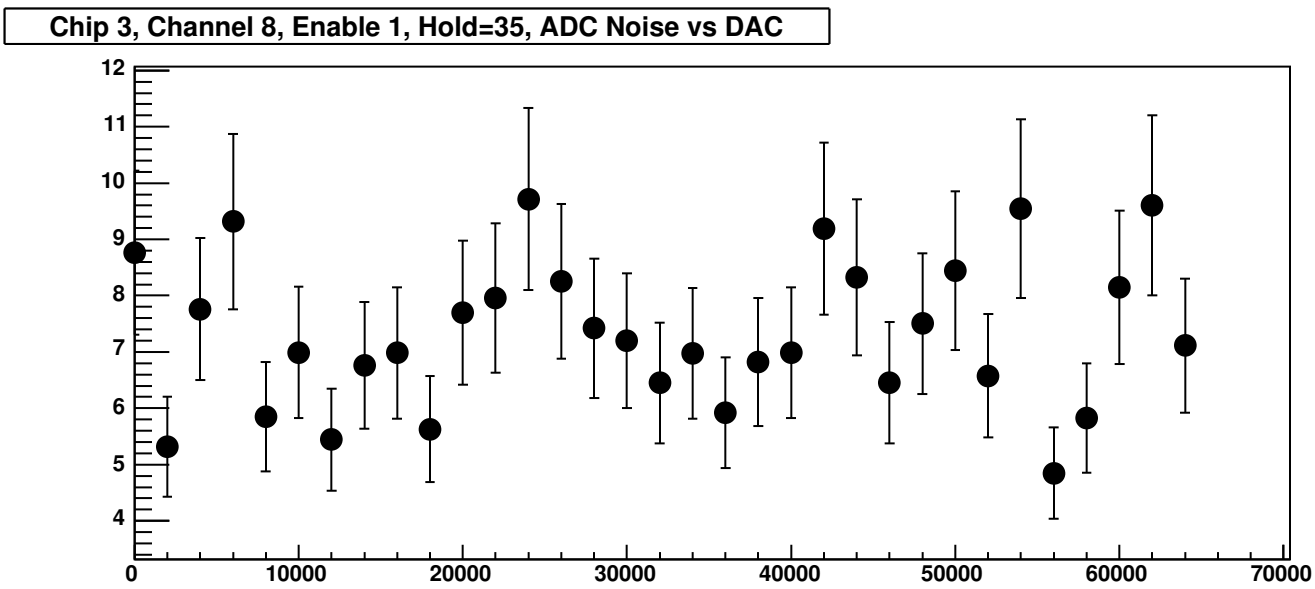

Chip 3, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000

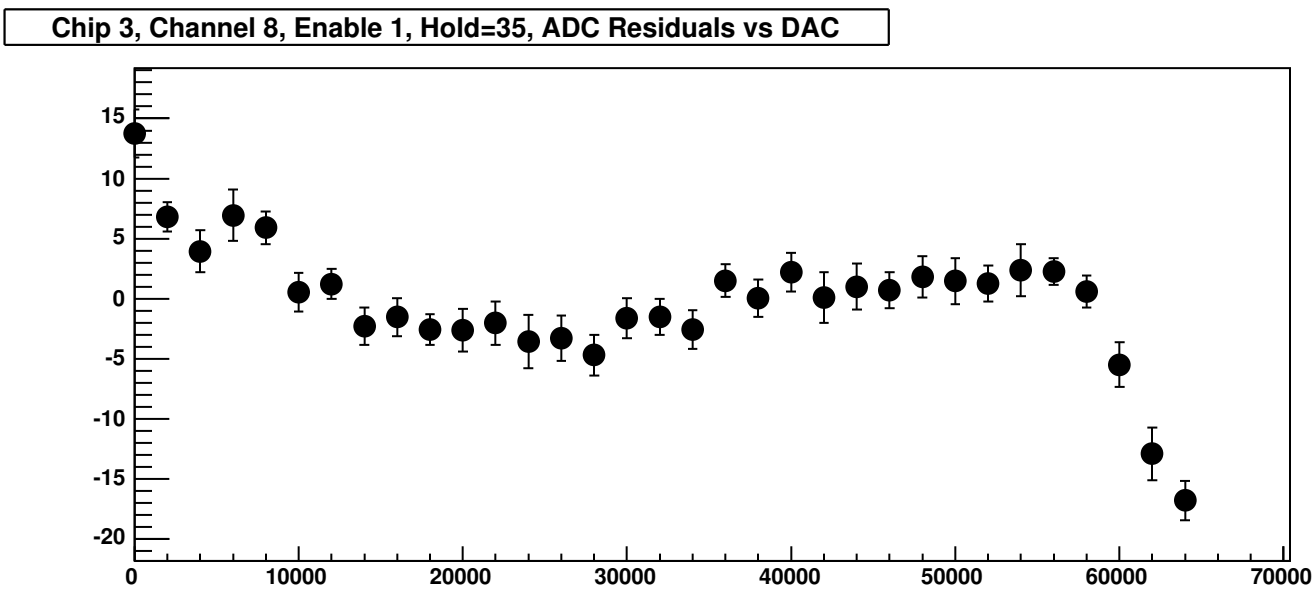

Chip 3, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 47.53 / 23
p0 -34.8 ± 0.6604
p1 0.002919 ± 1.909e-05
160
140
120
100
80
60
40
20
0
-20
-40
0
10000
20000
30000
40000
50000
60000
70000

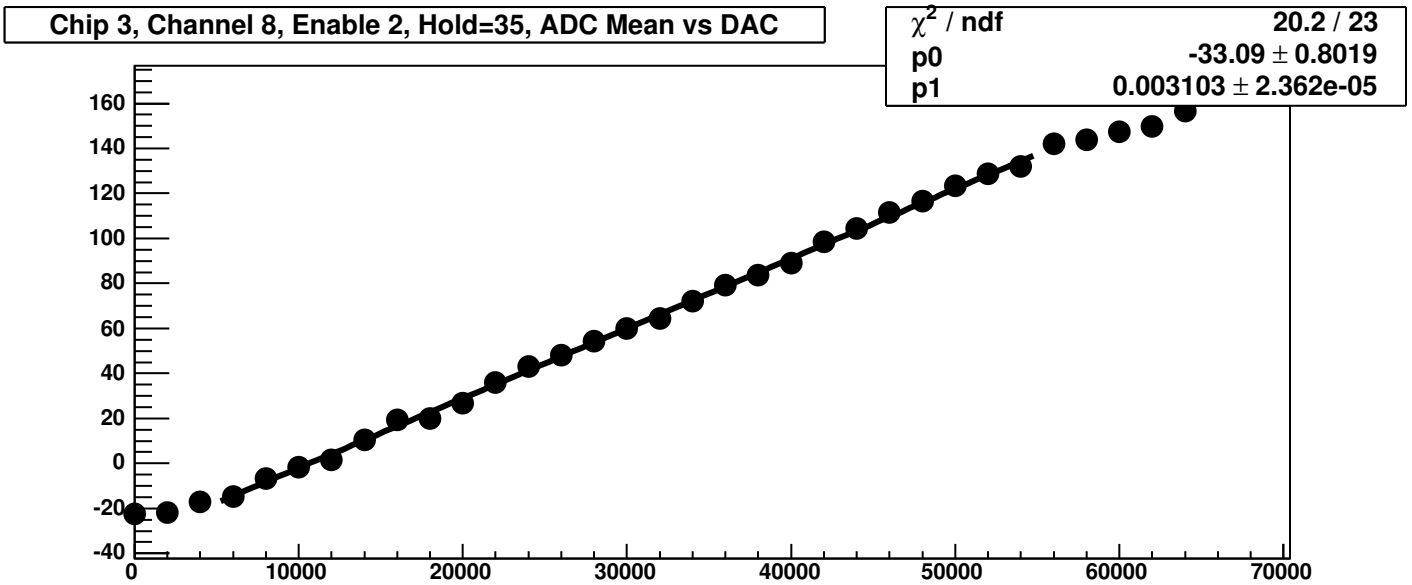

Chip 3, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

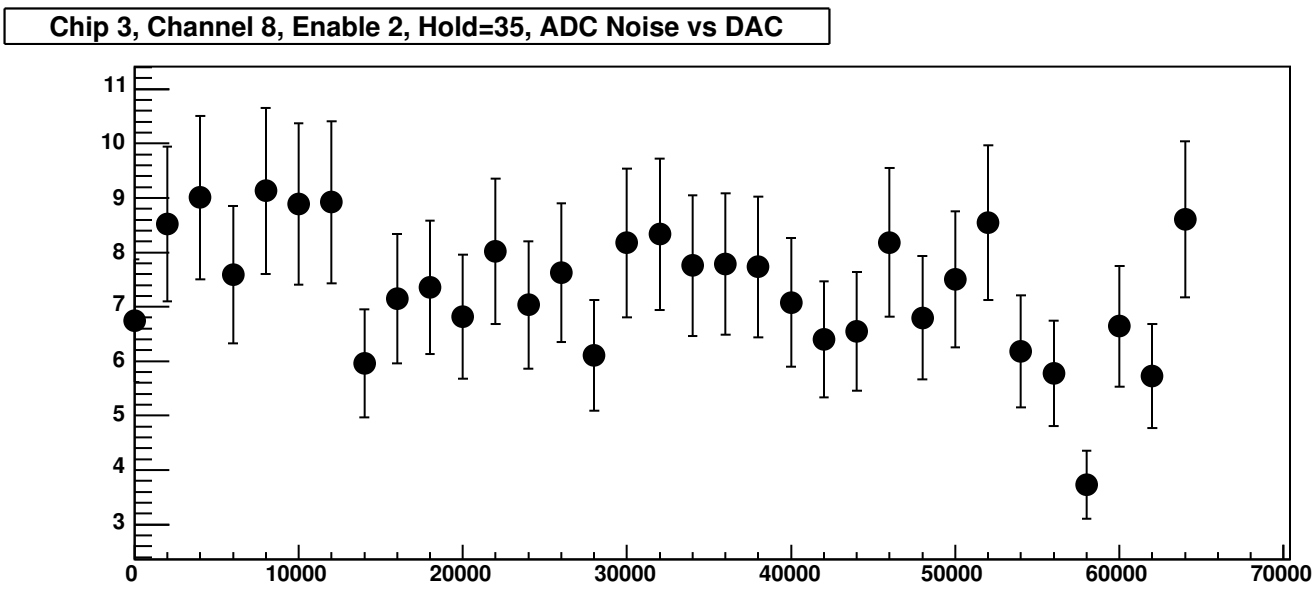

Chip 3, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

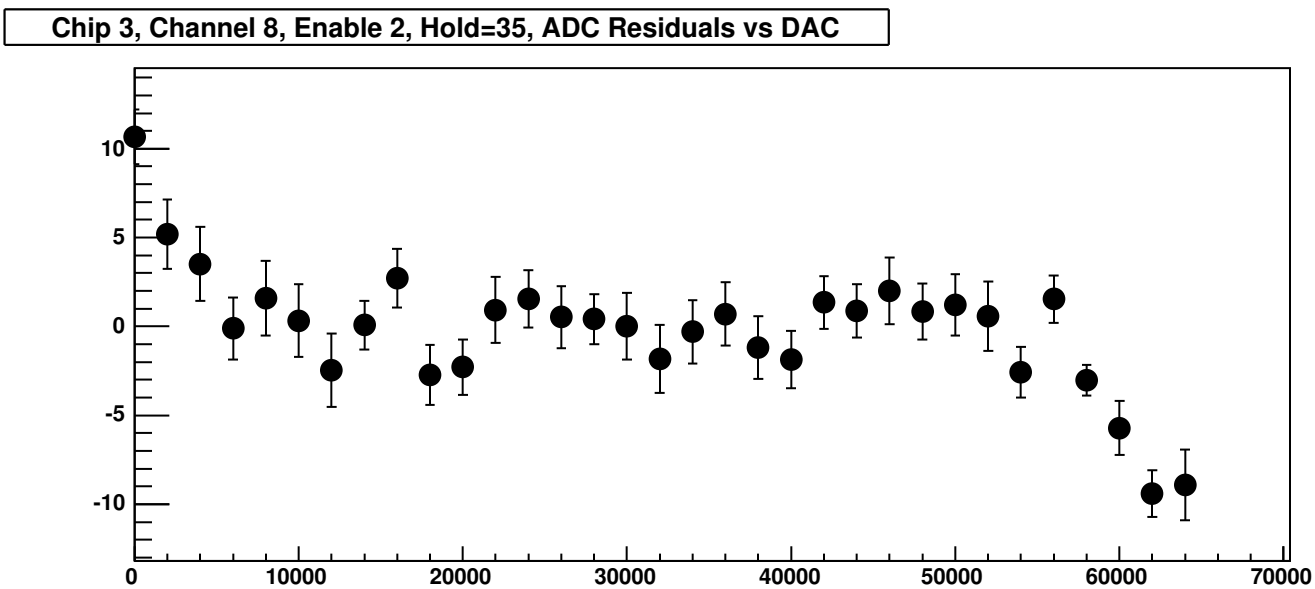

Chip 3, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 66.42 / 23
p0 -39.38 ± 0.7322
p1 0.003074 ± 2.282e-05
140
120
100
80
60
40
20
0
-20
-40
0
10000
20000
30000
40000
50000
60000
70000

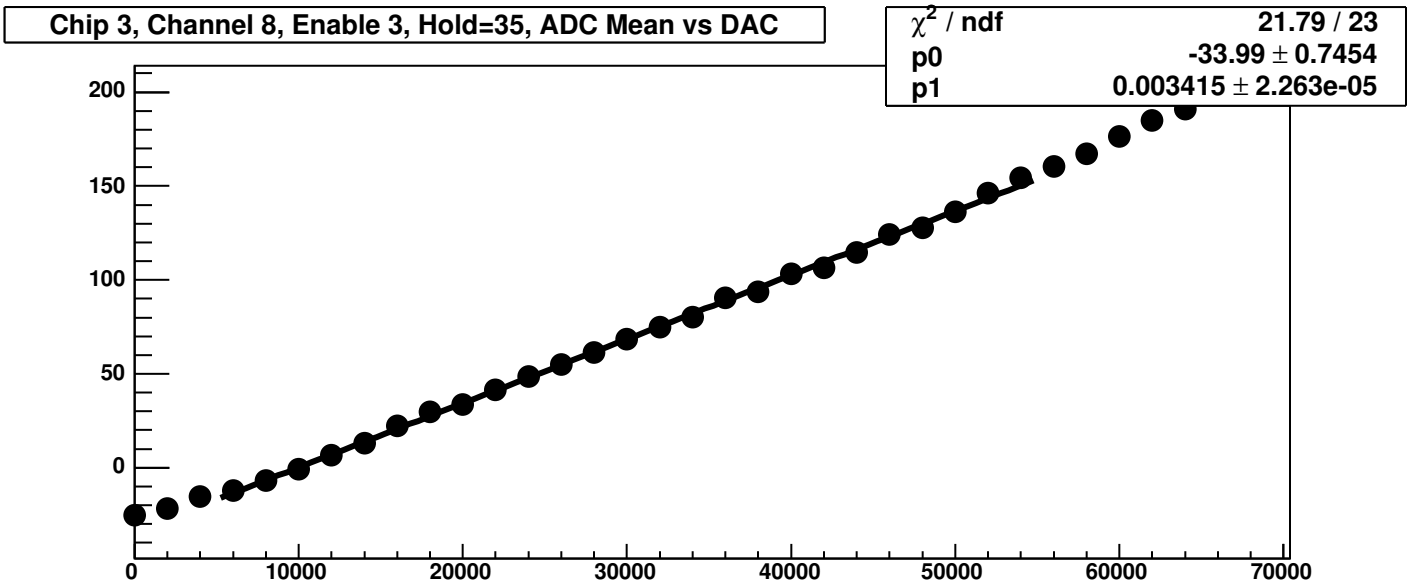

Chip 3, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

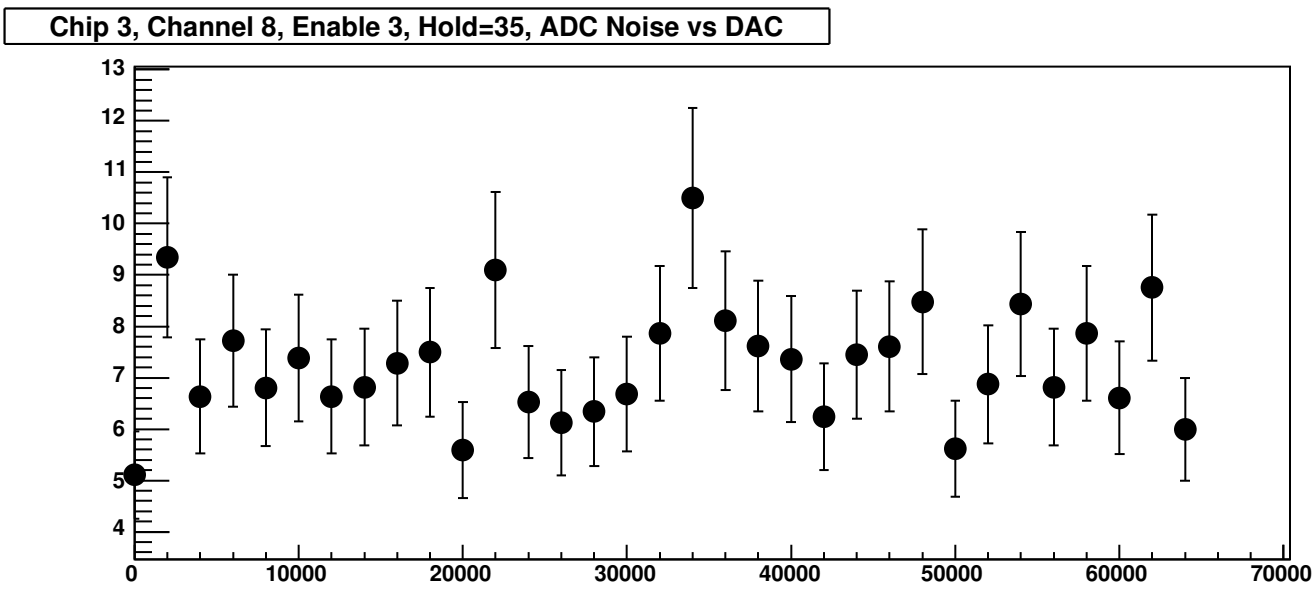

Chip 3, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
10000
20000
30000
40000
50000
60000
70000

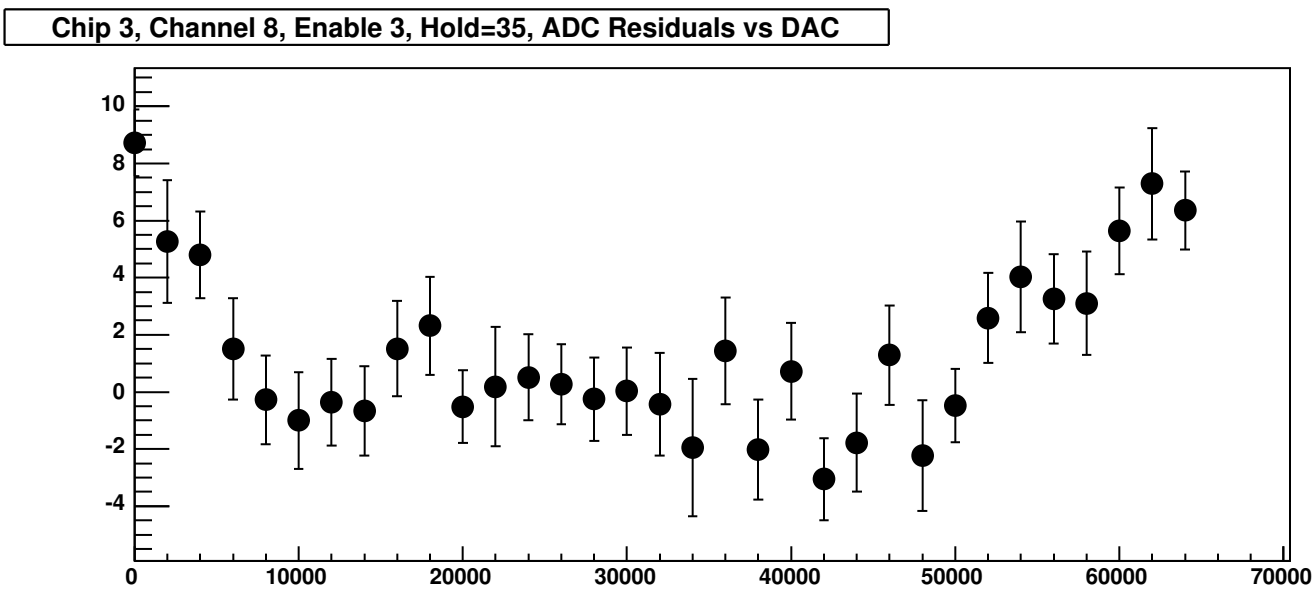

Chip 3, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 20.2 / 23
p0 -33.09 ± 0.8019
p1 0.003103 ± 2.362e-05
160
140
120
100
80
60
40
20
0
-20
-40
0
10000
20000
30000
40000
50000
60000
70000

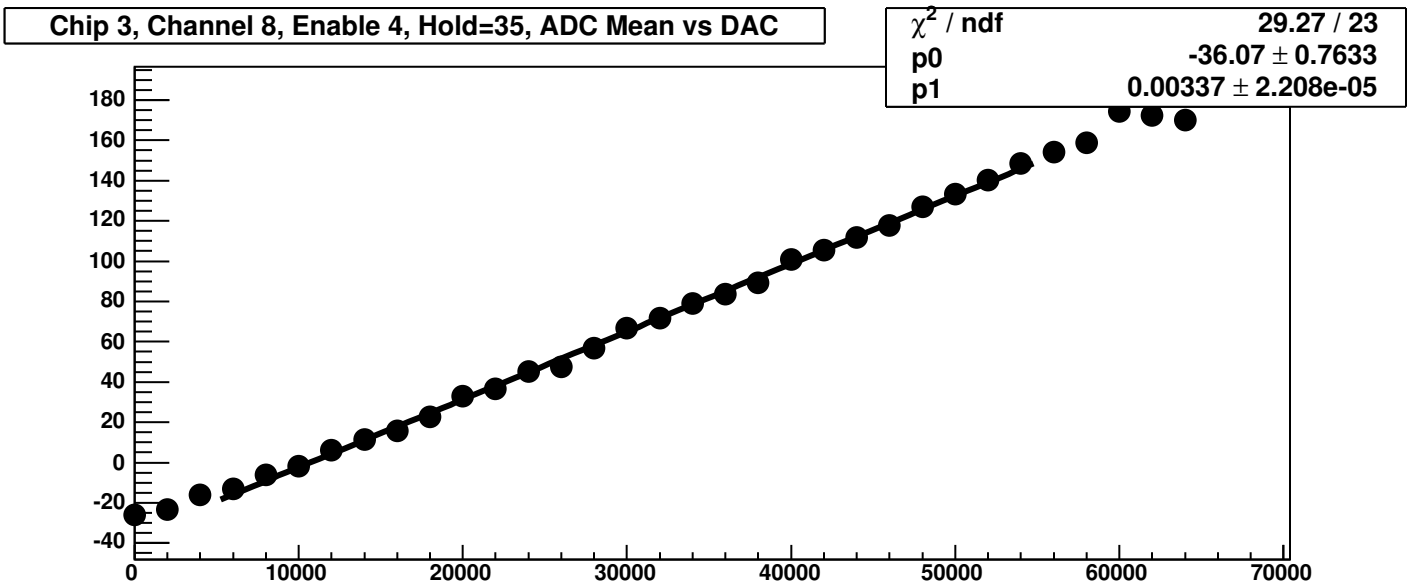

Chip 3, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

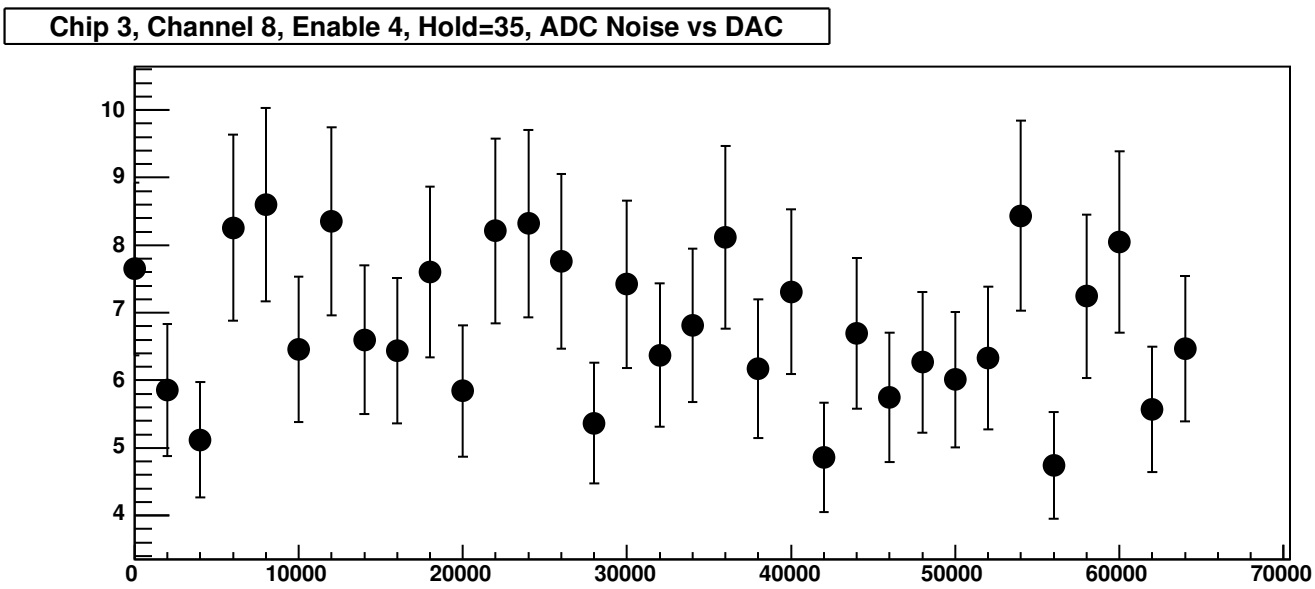

Chip 3, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

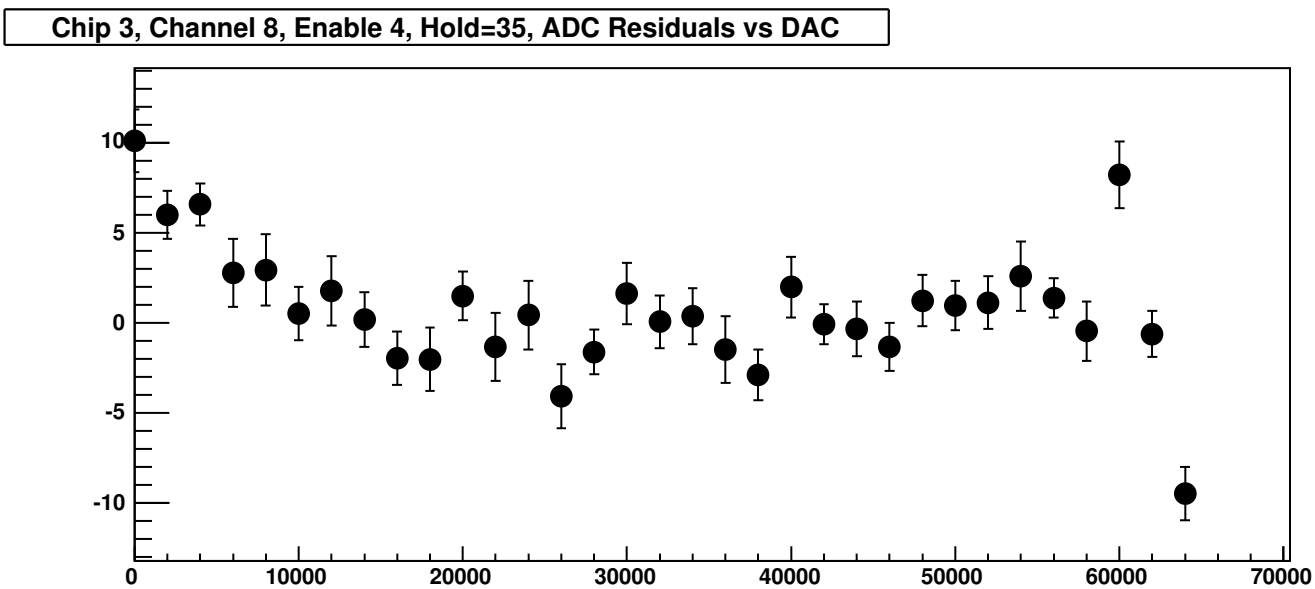

Chip 3, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.79 / 23
p0 -33.99 ± 0.7454
p1 0.003415 ± 2.263e-05
200
150
100
50
0
0
10000
20000
30000
40000
50000
60000
70000

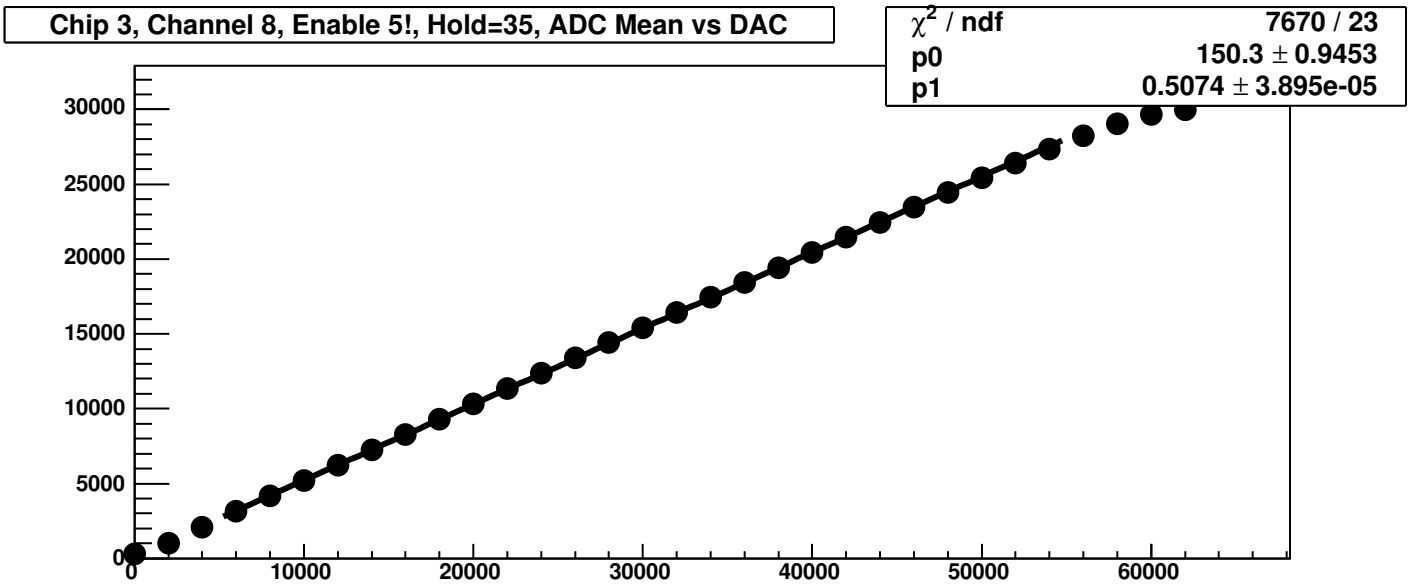

Chip 3, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

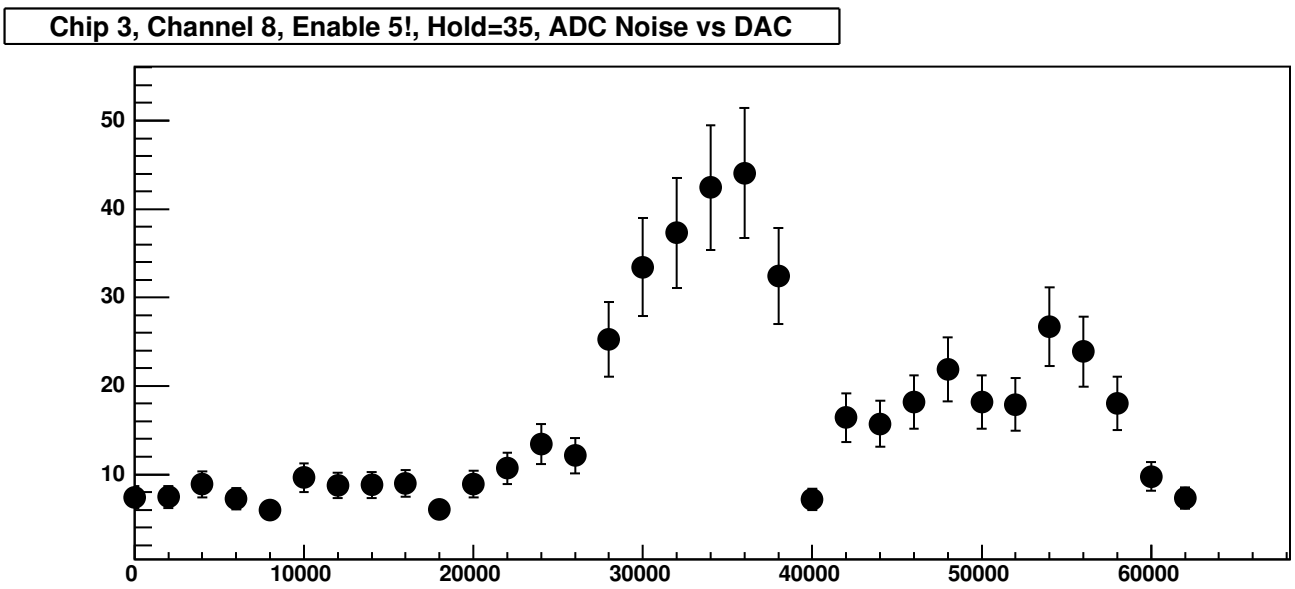

Chip 3, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
0
10000
20000
30000
40000
50000
60000
70000

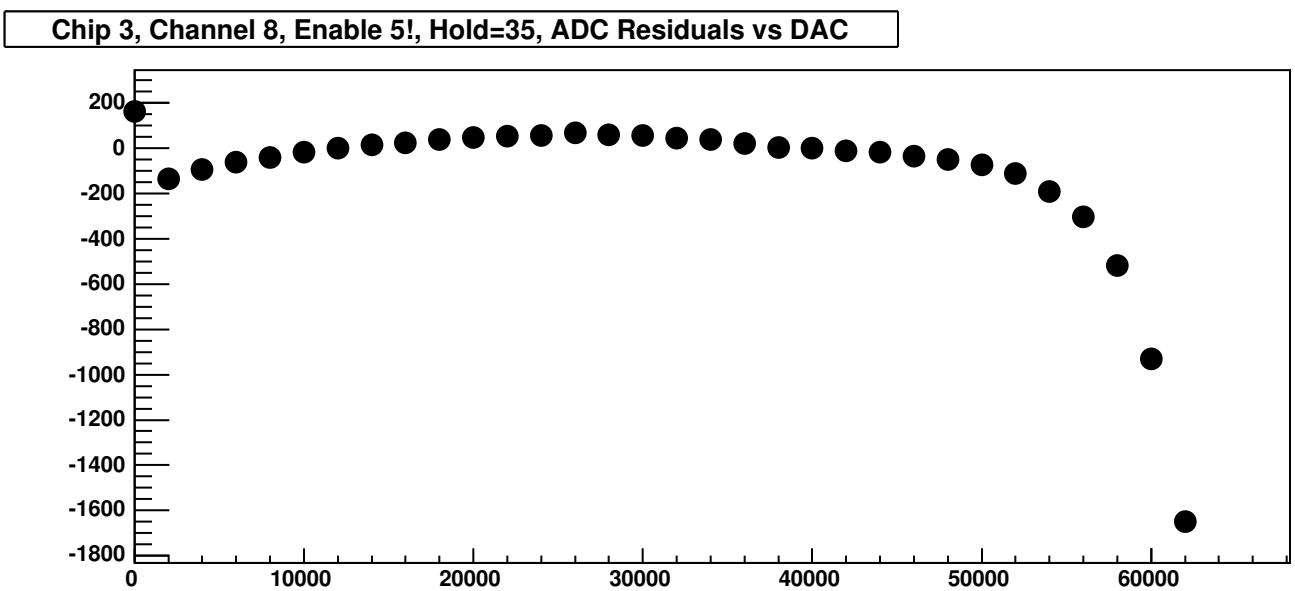

Chip 3, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.27 / 23
p0 -36.07 ± 0.7633
p1 0.00337 ± 2.208e-05
180
160
140
120
100
80
60
40
20
0
-20
-40
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

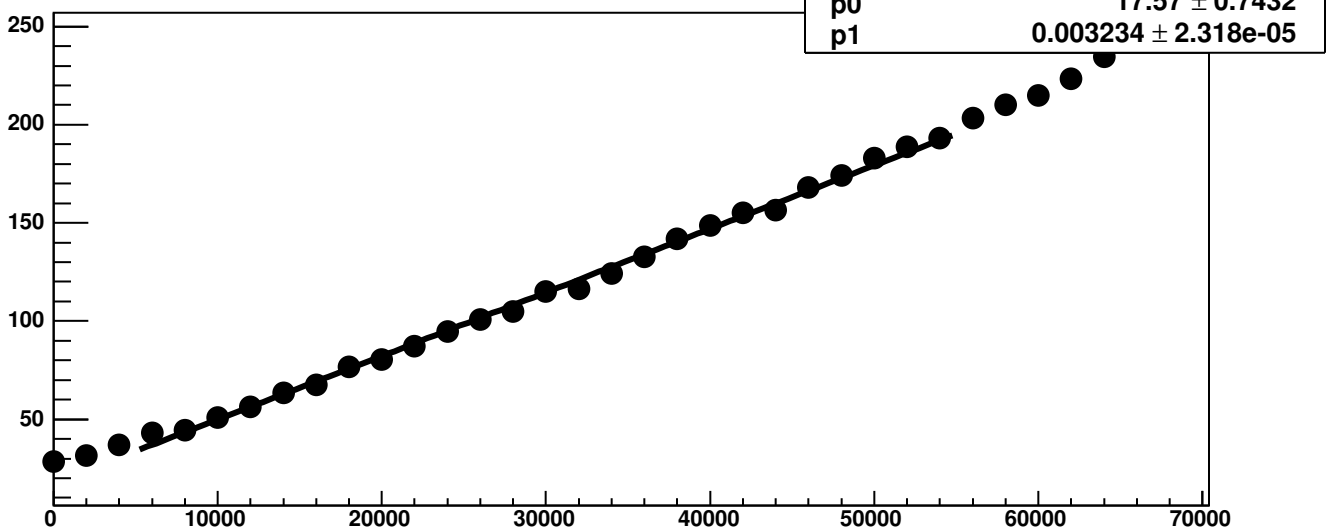

Chip 3, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 7670 / 23
p0 150.3 ± 0.9453
p1 0.5074 ± 3.895e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

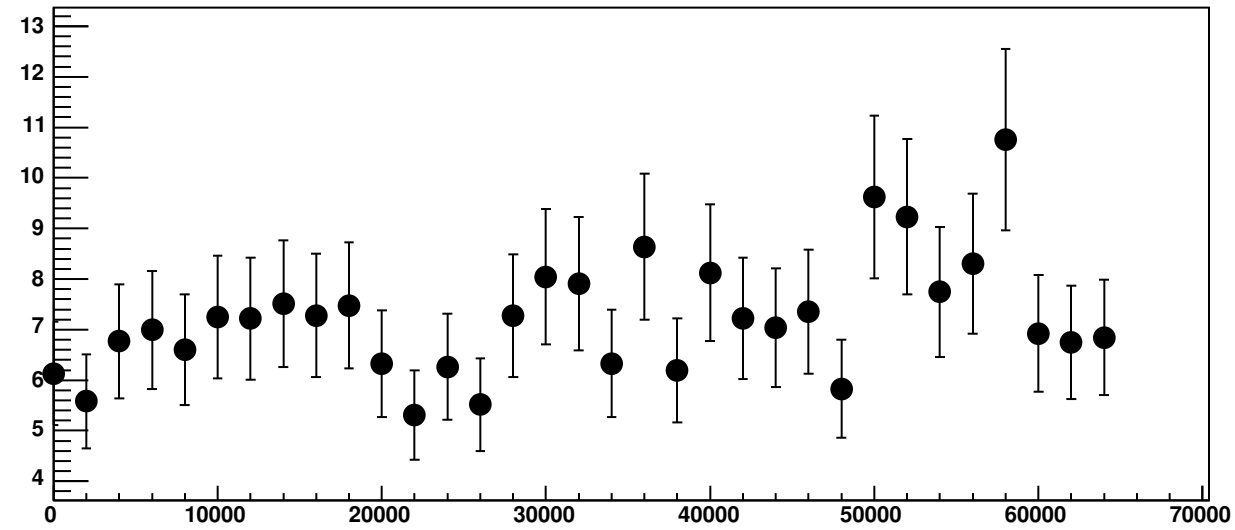

Chip 3, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
10000
20000
30000
40000
50000
60000

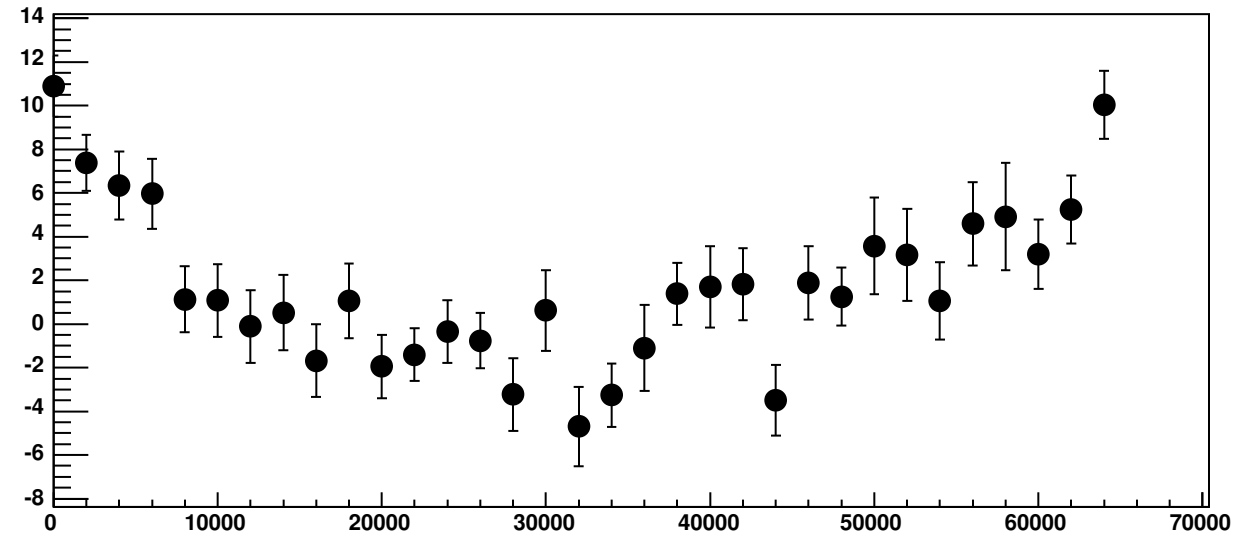
Chip 3, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

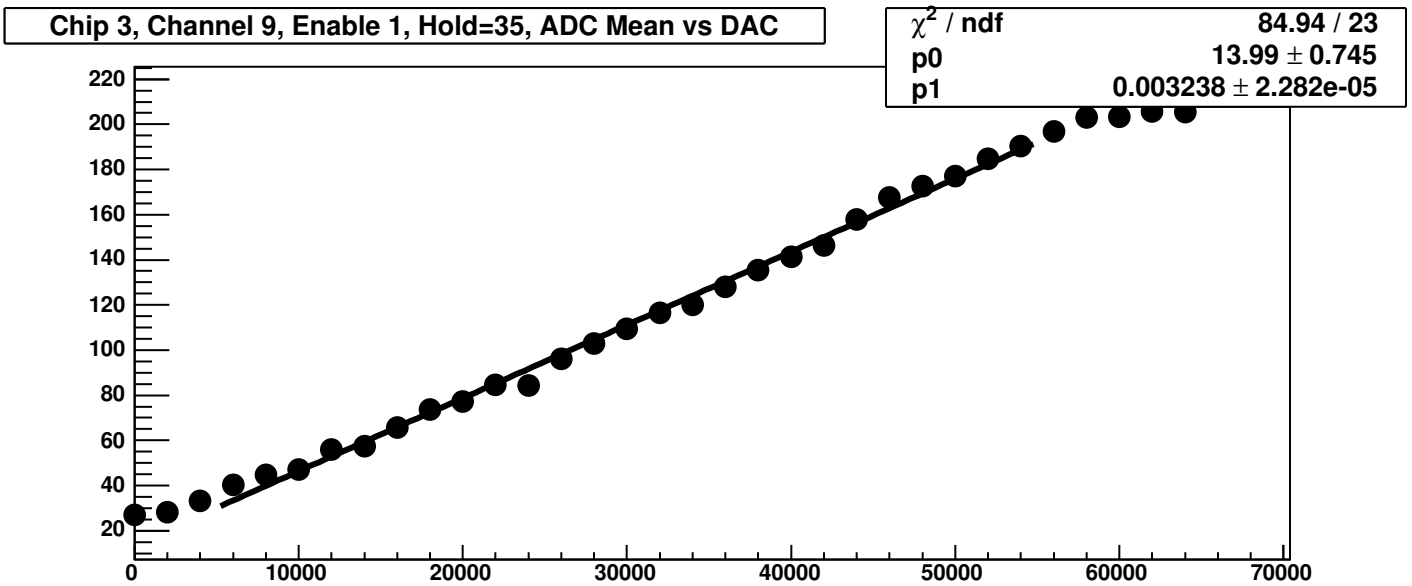


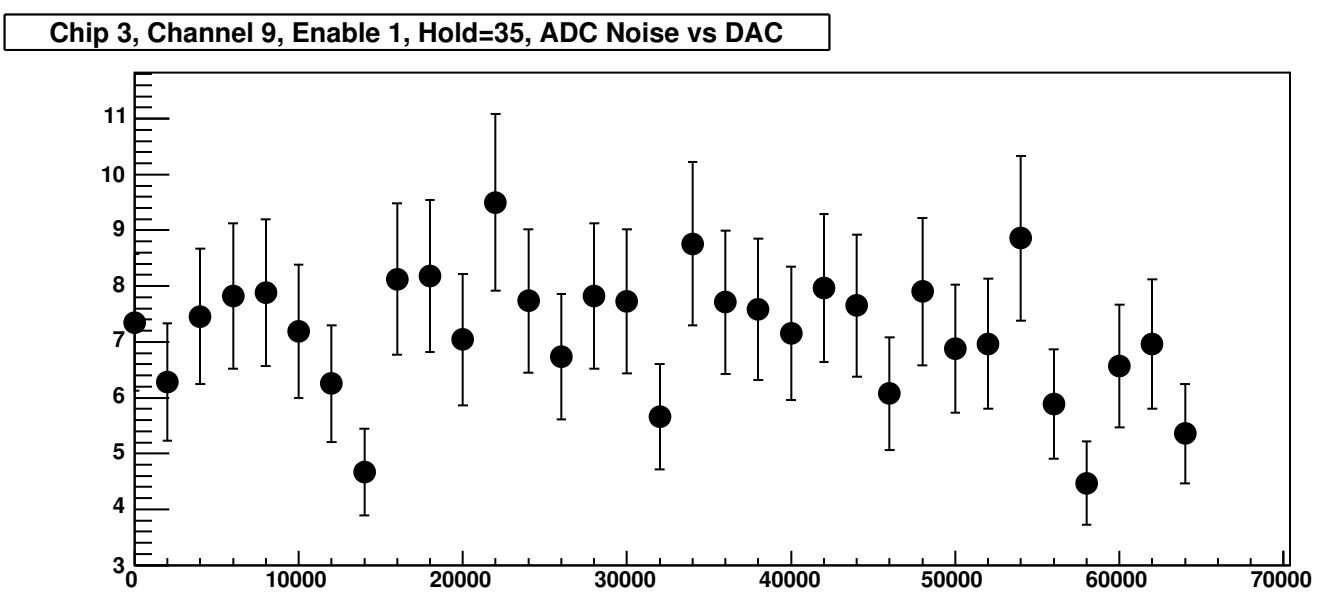
Chip 3, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC

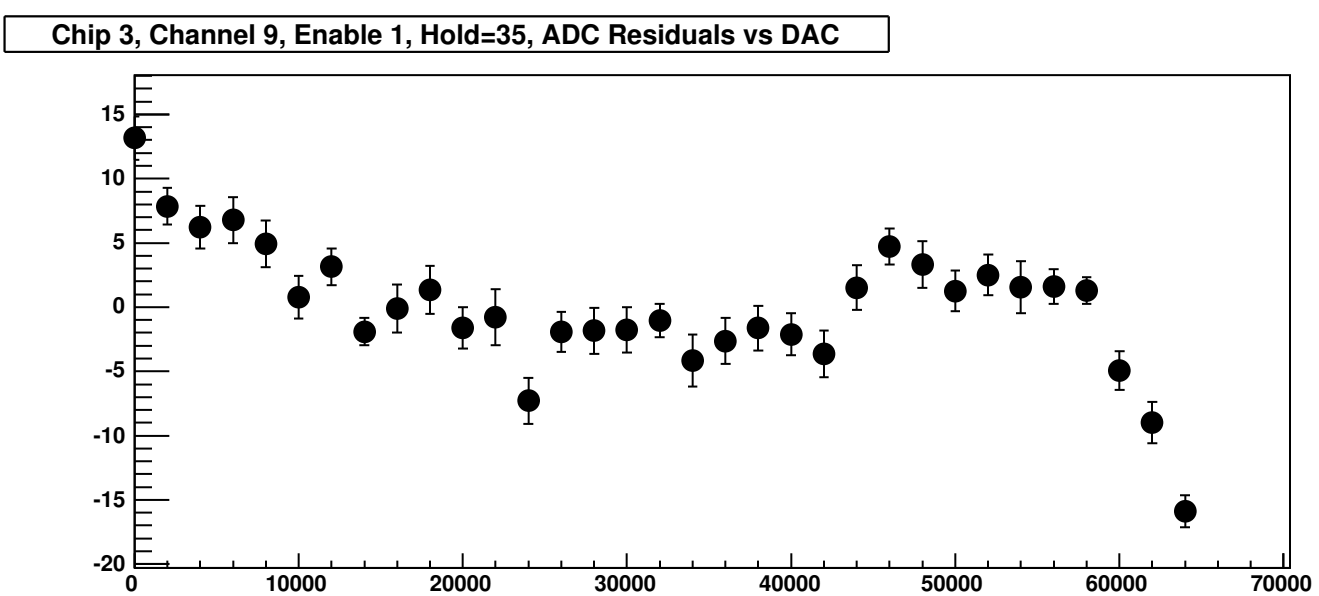


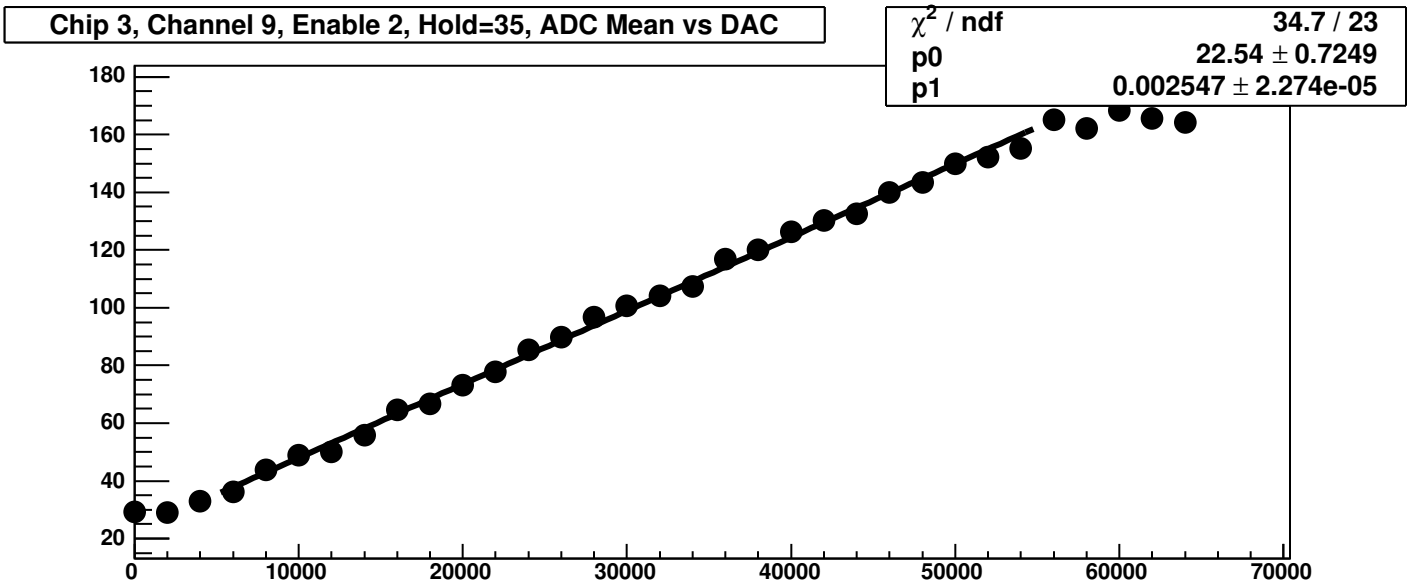
Chip 3, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

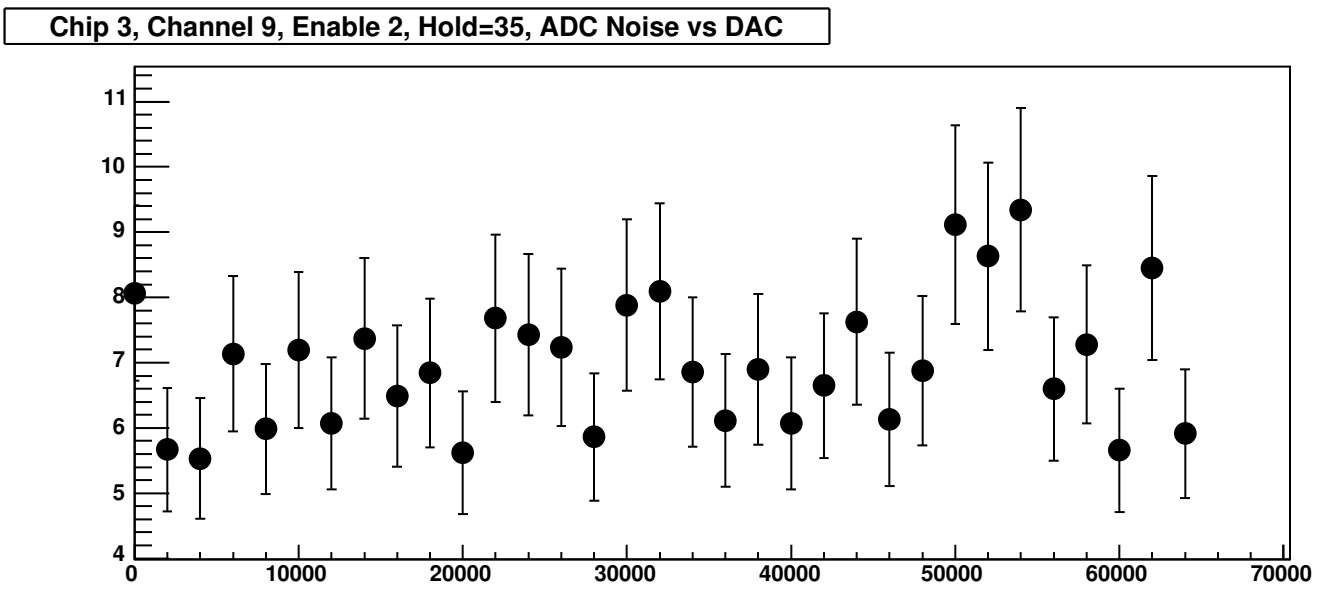


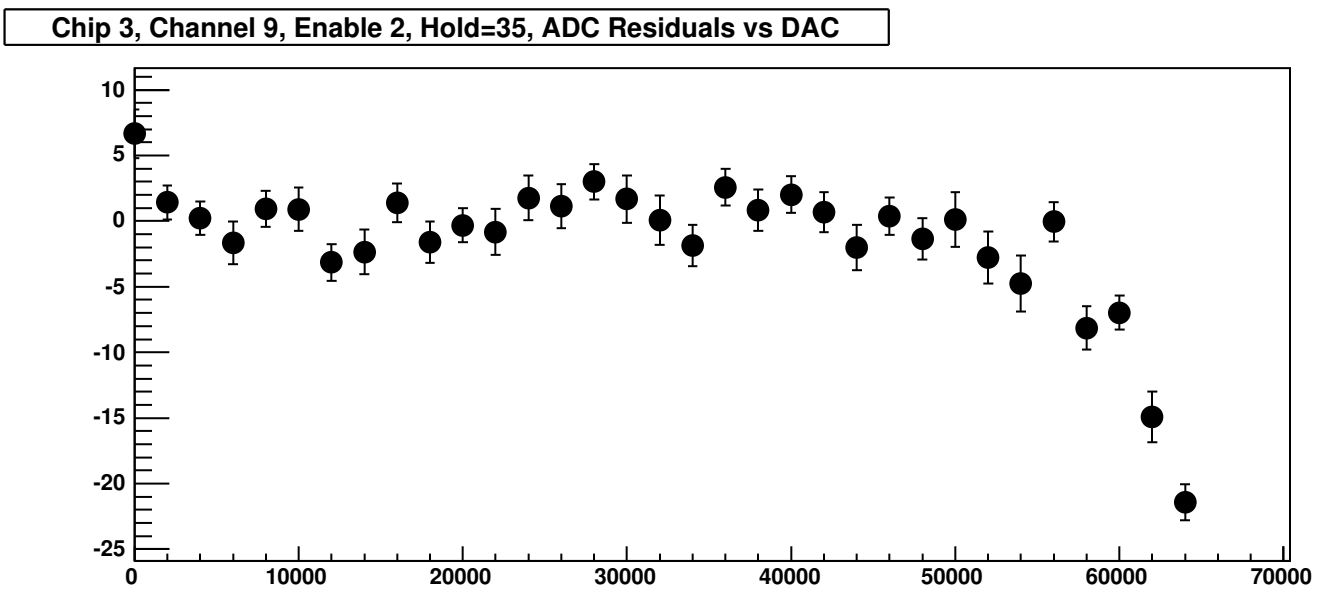

Chip 3, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 84.94 / 23
p0 13.99 ± 0.745
p1 0.003238 ± 2.282e-05
220
200
180
160
140
120
100
80
60
40
20
0
10000
20000
30000
40000
50000
60000
70000

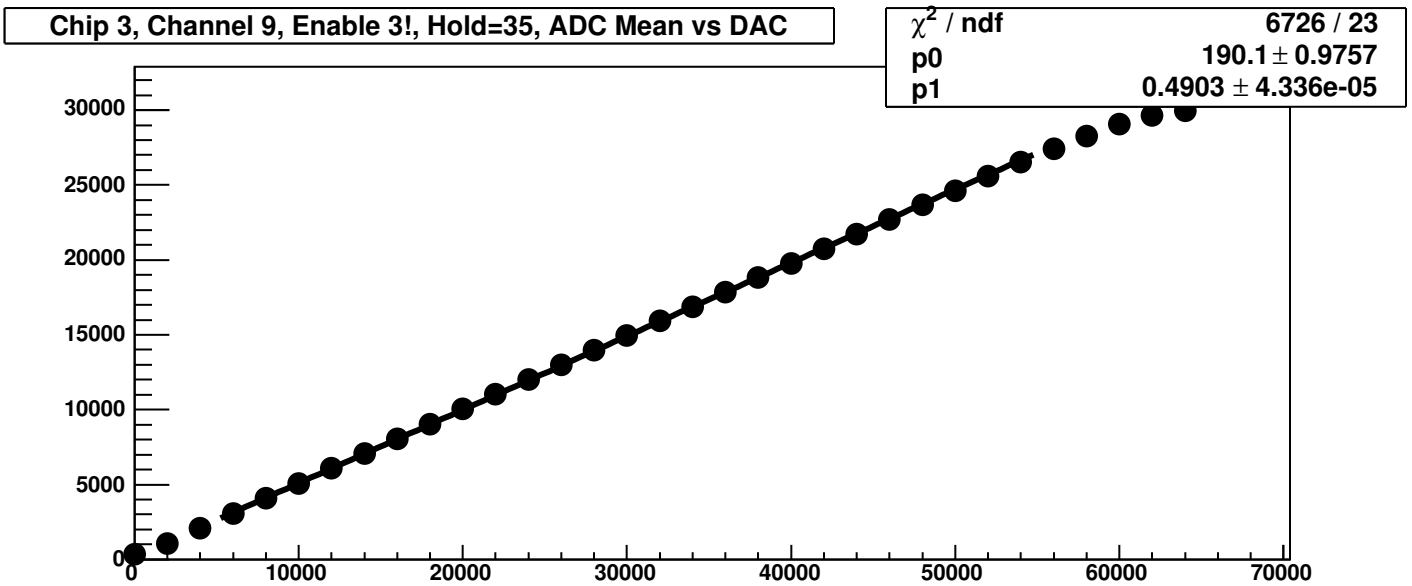

Chip 3, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

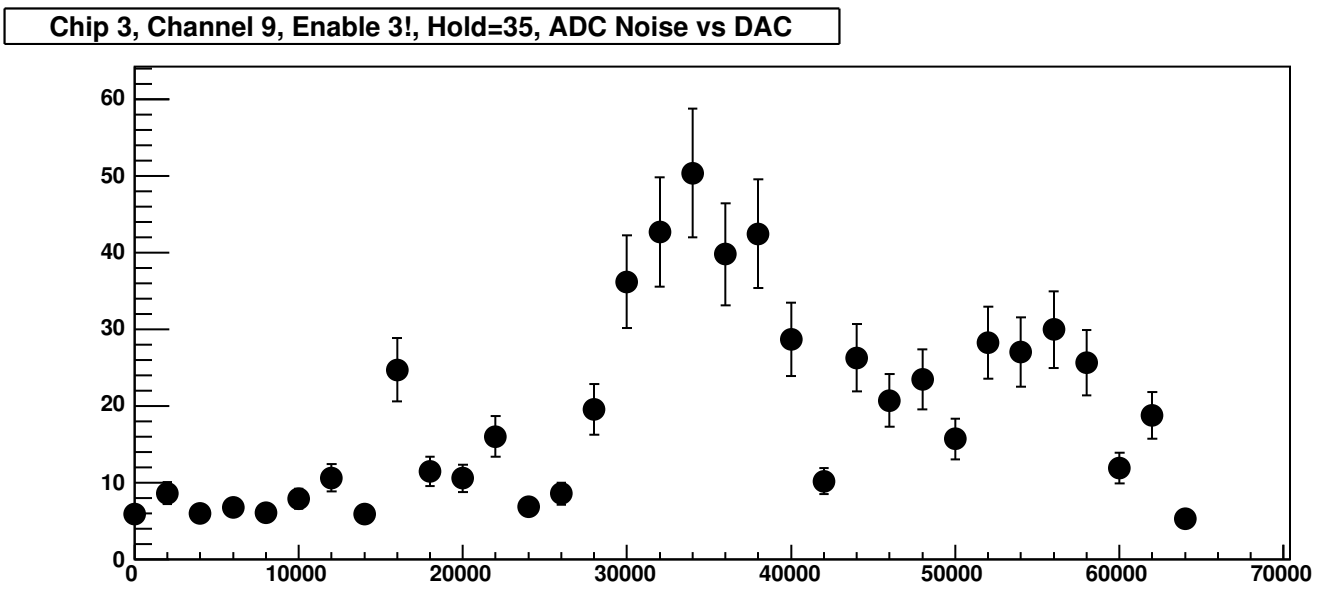

Chip 3, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
10000
20000
30000
40000
50000
60000
70000

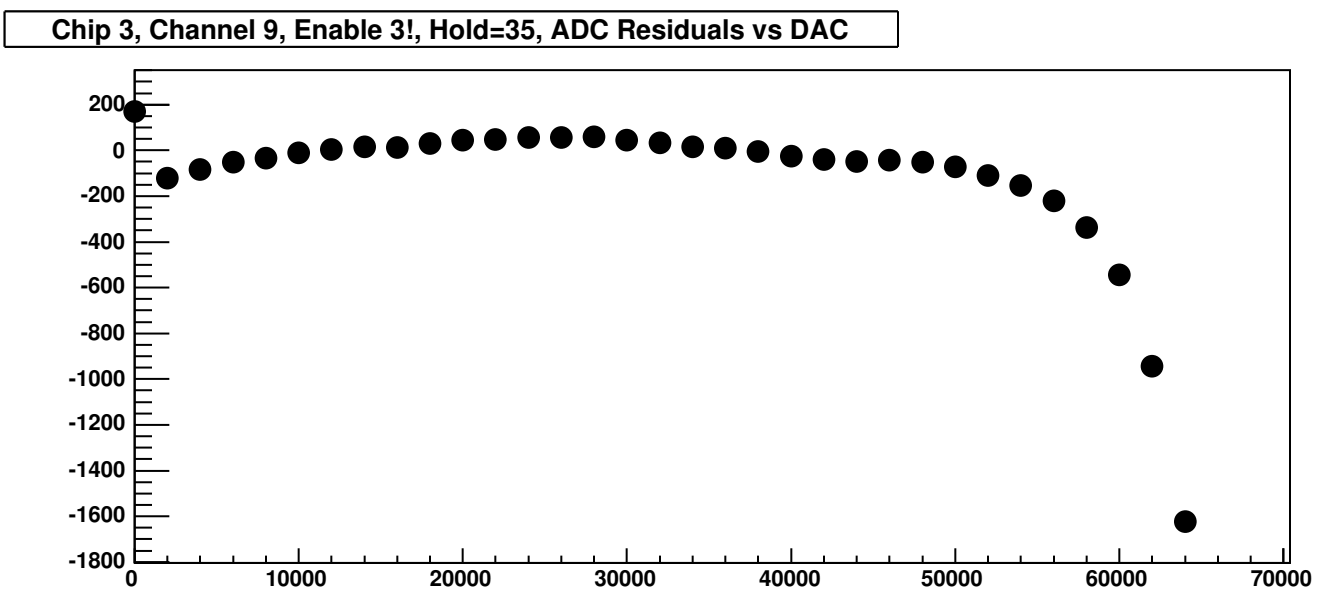

Chip 3, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 34.7 / 23
p0 22.54 ± 0.7249
p1 0.002547 ± 2.274e-05
180
160
140
120
100
80
60
40
20
0
10000
20000
30000
40000
50000
60000
70000

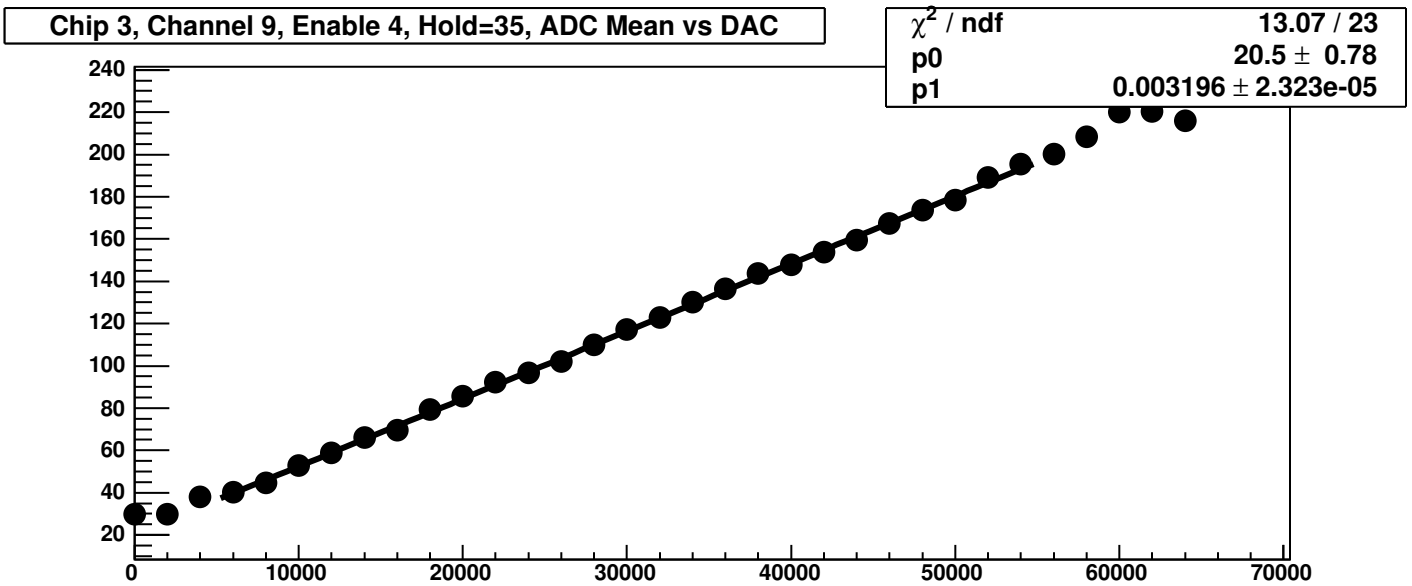

Chip 3, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

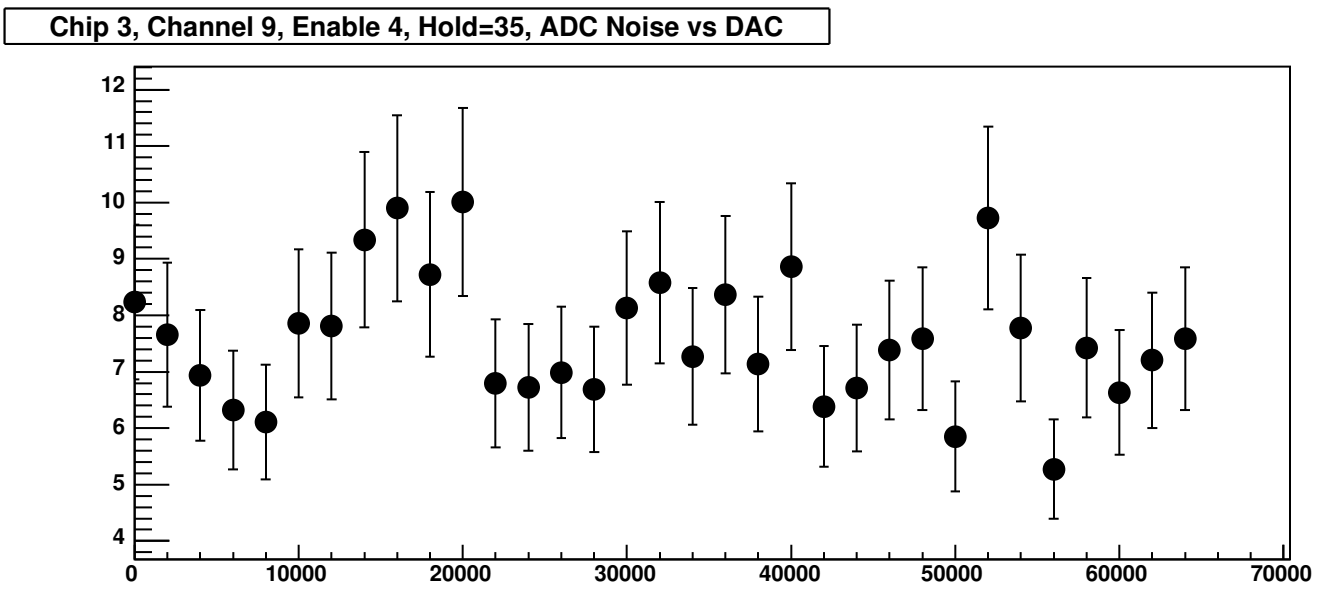

Chip 3, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
10000
20000
30000
40000
50000
60000
70000

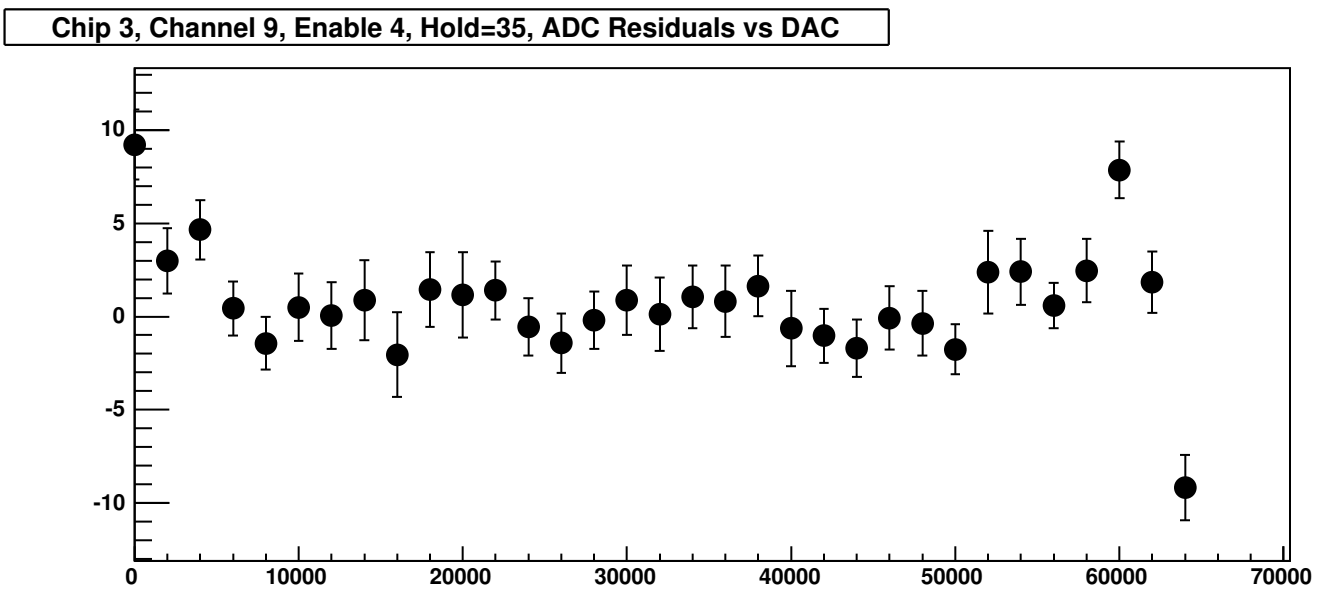

Chip 3, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 6726 / 23
p0 190.1 ± 0.9757
p1 0.4903 ± 4.336e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

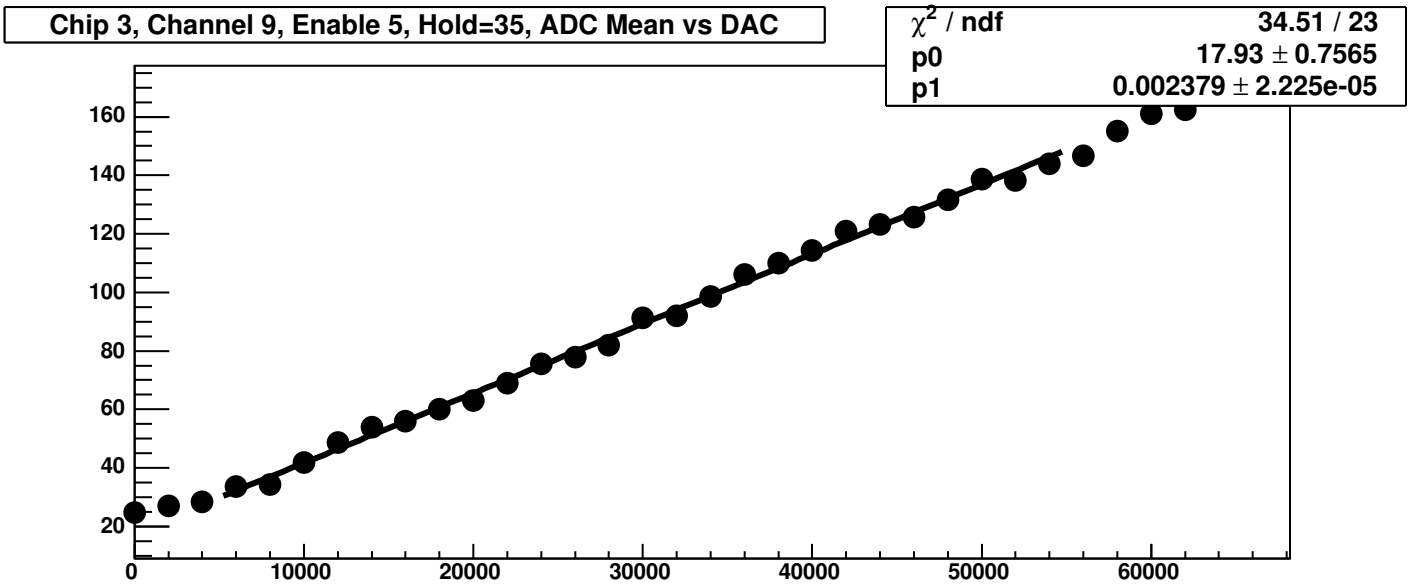

Chip 3, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

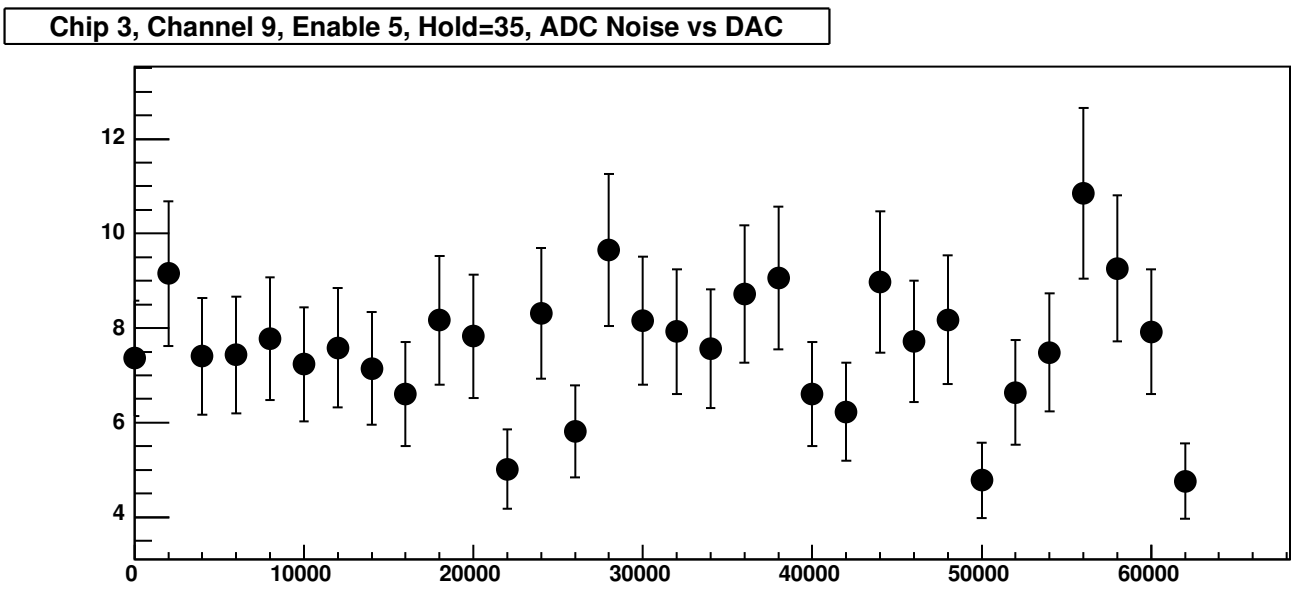

Chip 3, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
10000
20000
30000
40000
50000
60000
70000

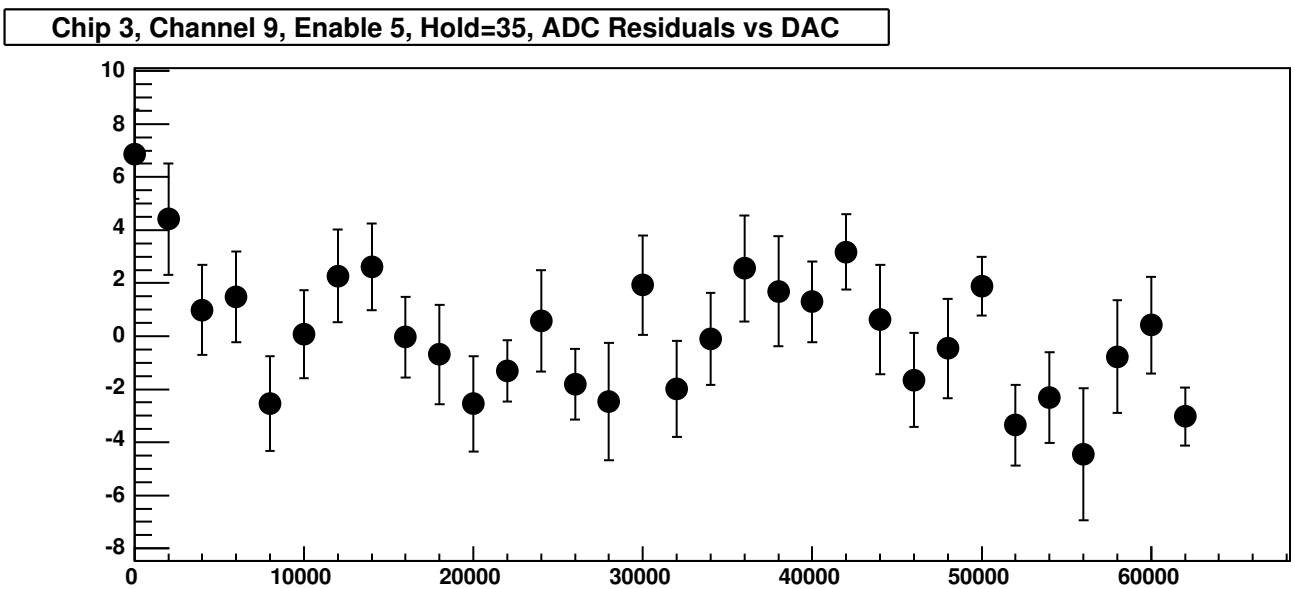

Chip 3, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 13.07 / 23
p0 20.5 ± 0.78
p1 0.003196 ± 2.323e-05
240
220
200
180
160
140
120
100
80
60
40
20
0
10000
20000
30000
40000
50000
60000
70000

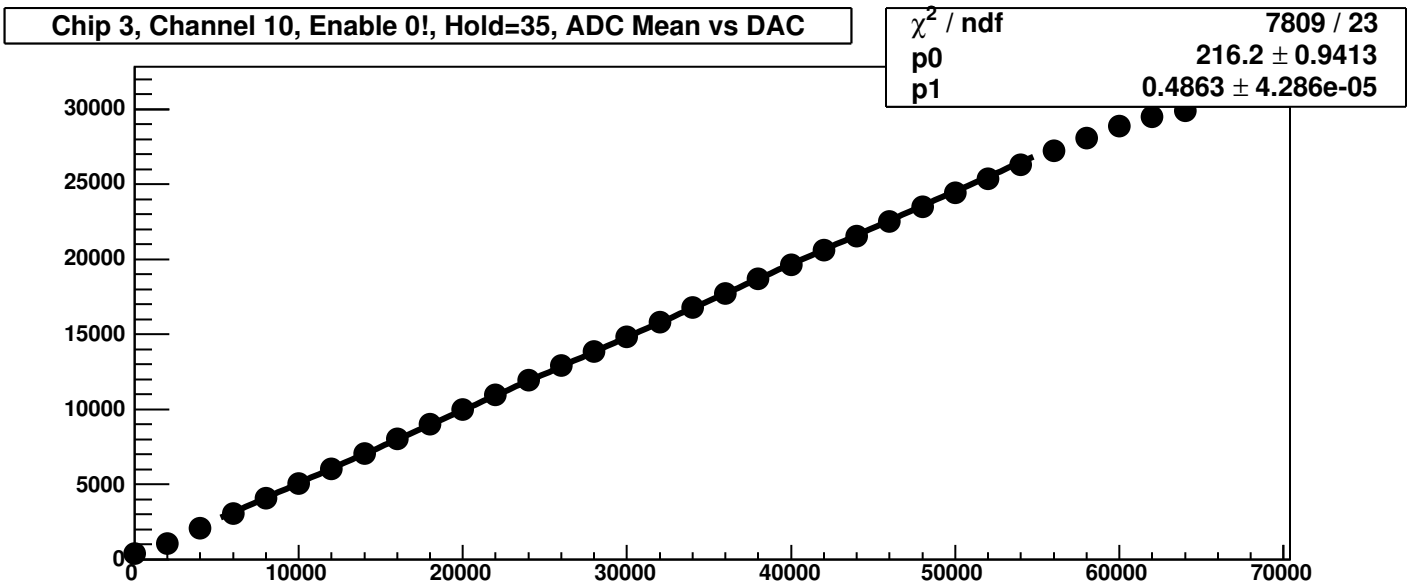

Chip 3, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

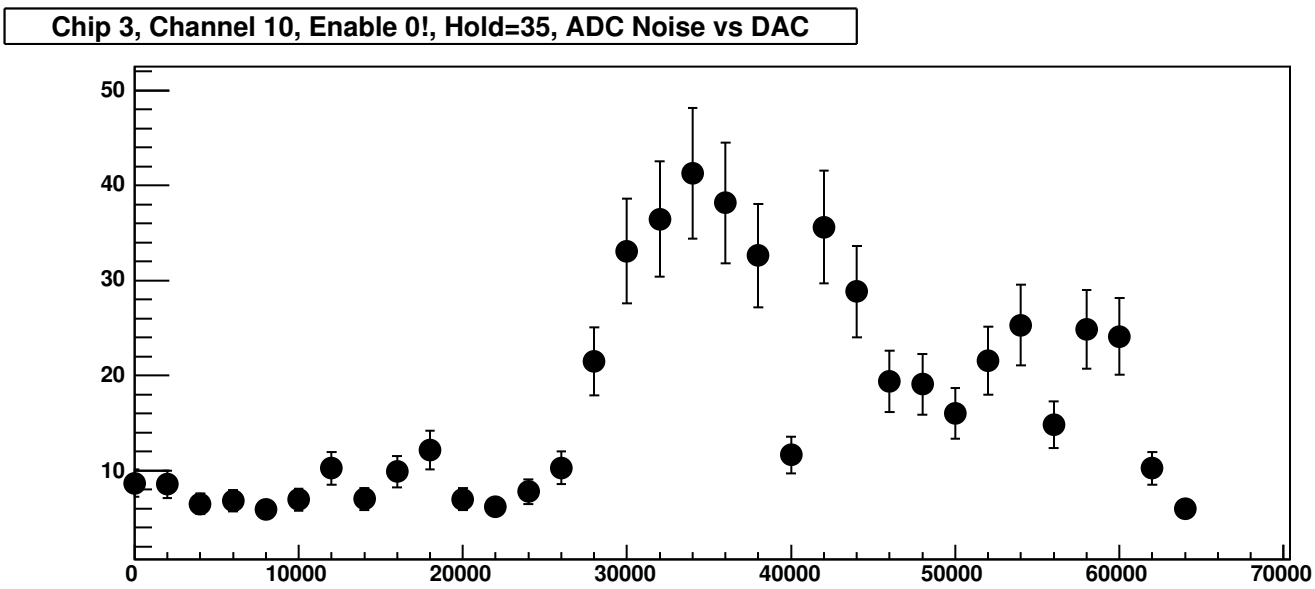

Chip 3, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

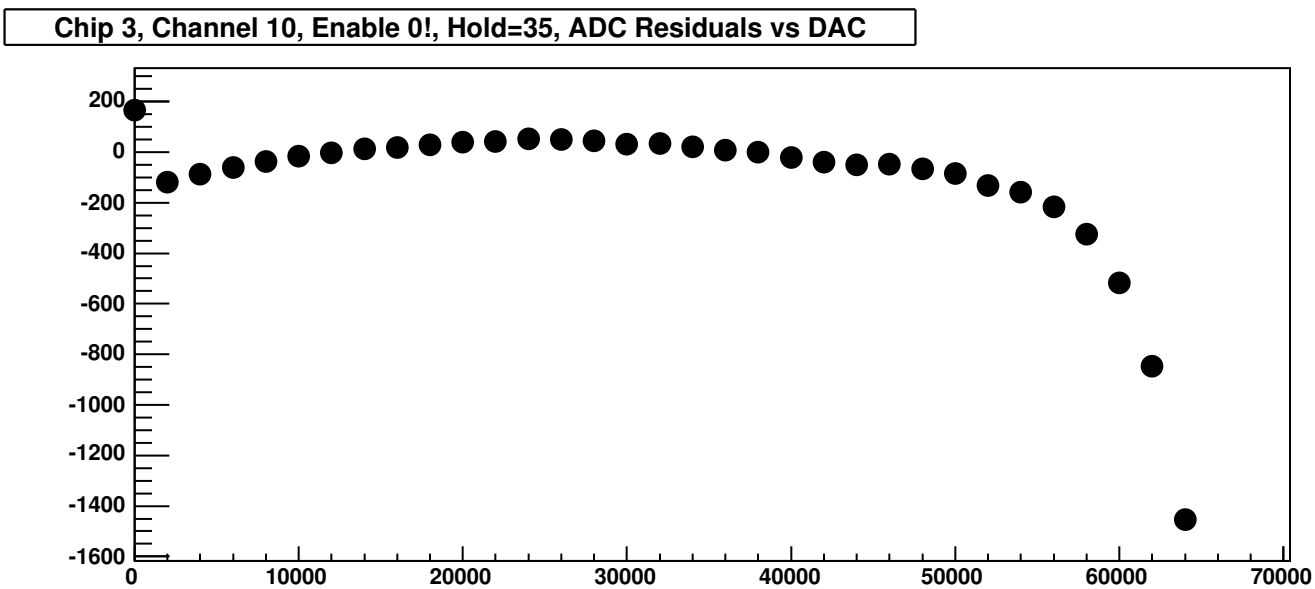

Chip 3, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 34.51 / 23
p0 17.93 ± 0.7565
p1 0.002379 ± 2.225e-05
160
140
120
100
80
60
40
20
0
10000
20000
30000
40000
50000
60000

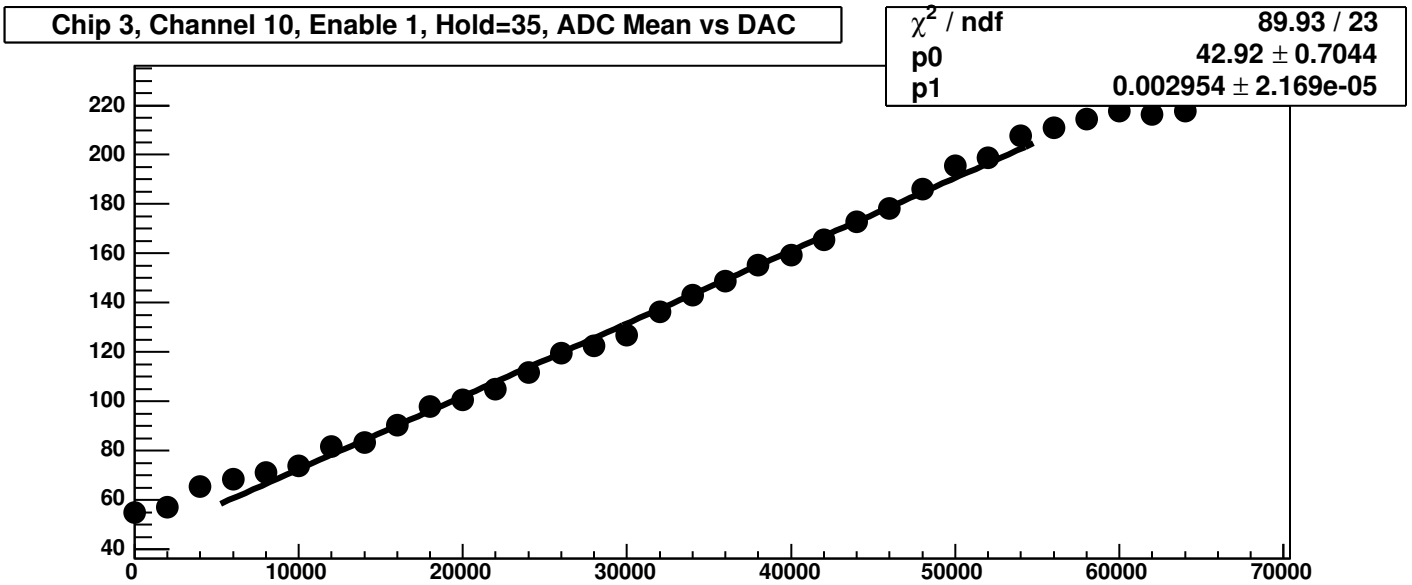

Chip 3, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000

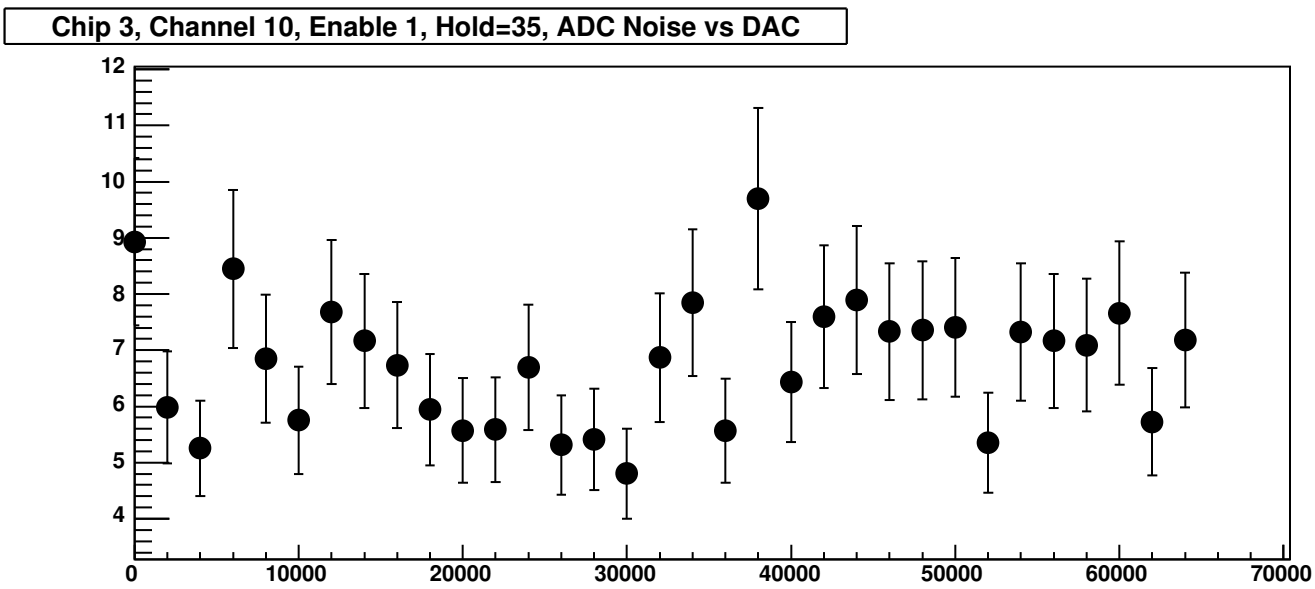

Chip 3, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000

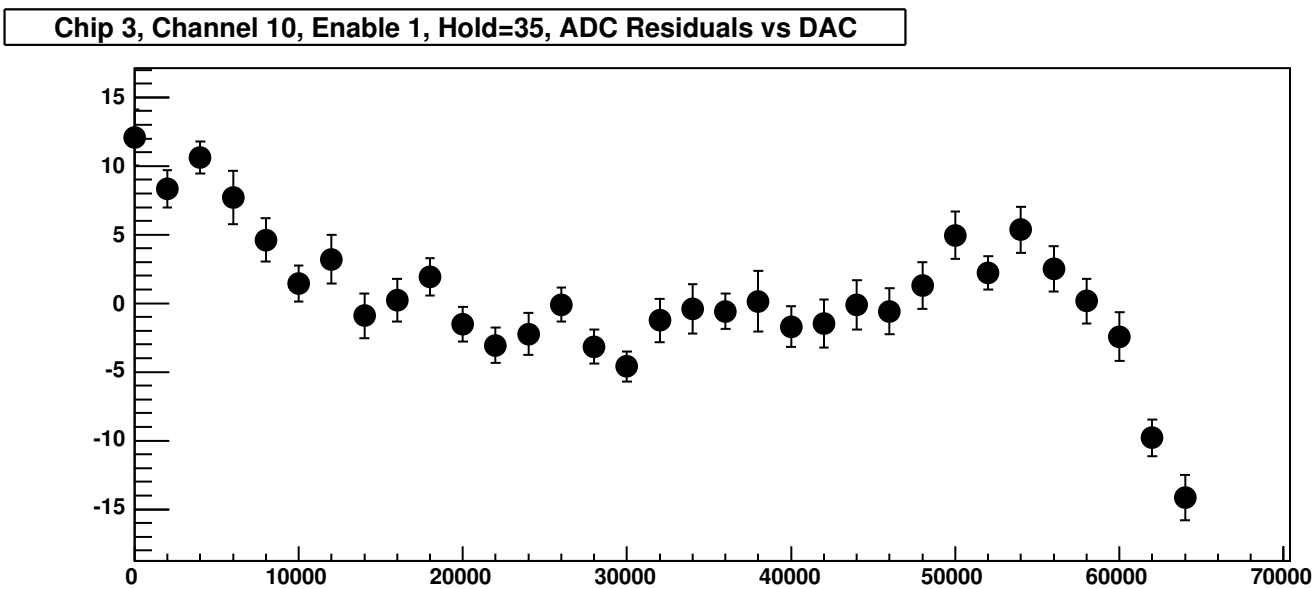

Chip 3, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 7809 / 23
p0 216.2 ± 0.9413
p1 0.4863 ± 4.286e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

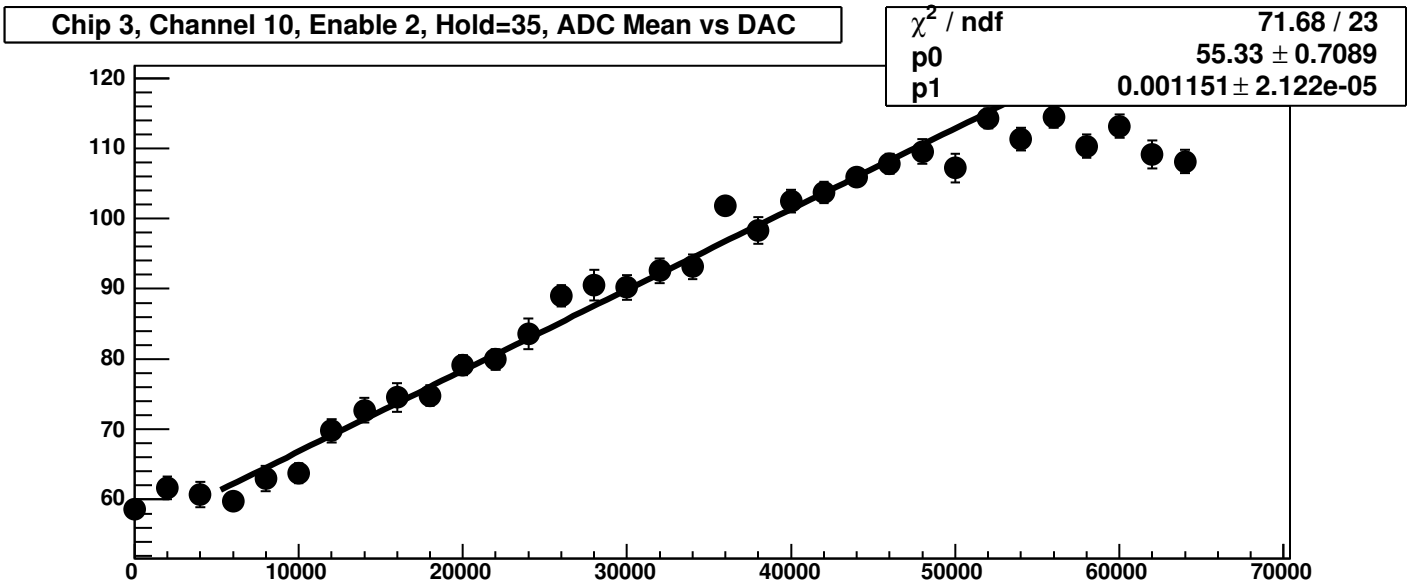

Chip 3, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000

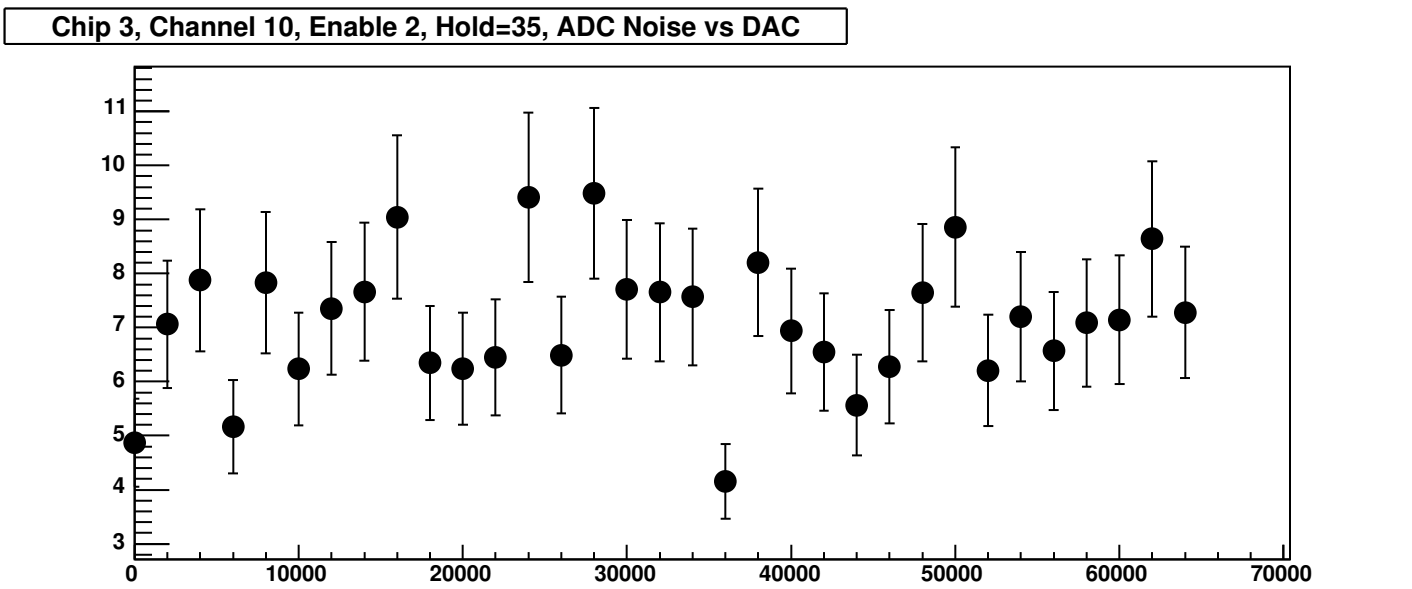

Chip 3, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
0
10000
20000
30000
40000
50000
60000
70000

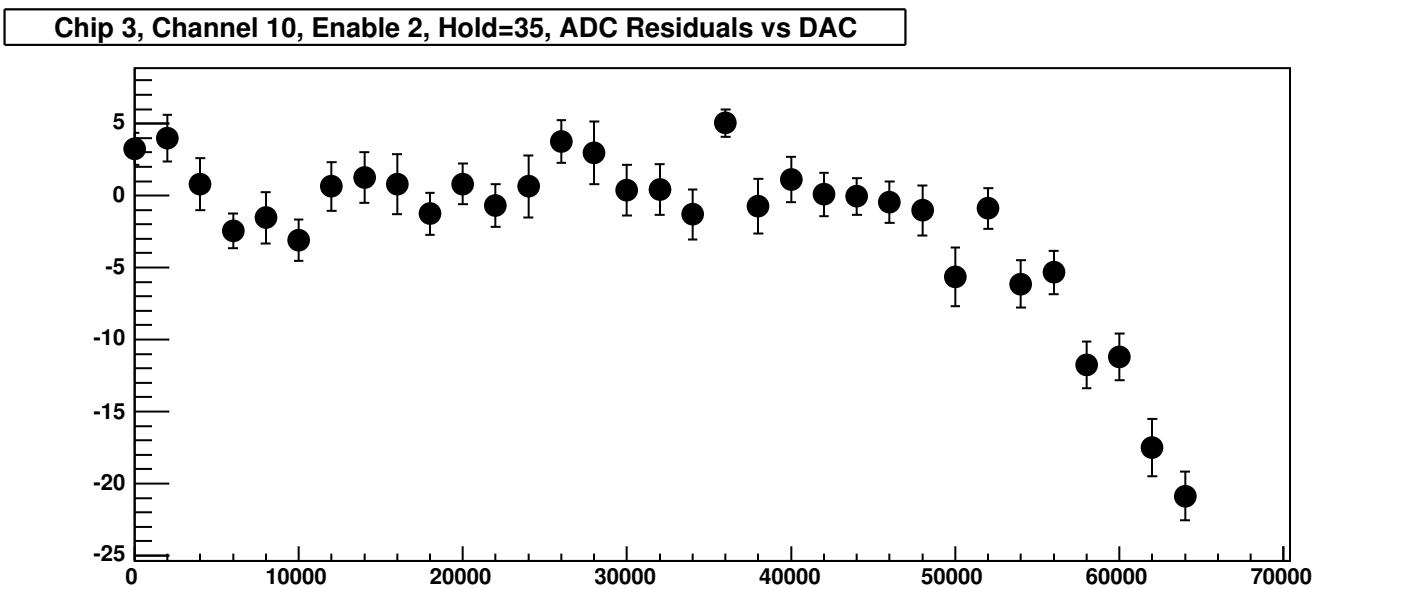

Chip 3, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 89.93 / 23
p0 42.92 ± 0.7044
p1 0.002954 ± 2.169e-05
220
200
180
160
140
120
100
80
60
40
0
10000
20000
30000
40000
50000
60000
70000

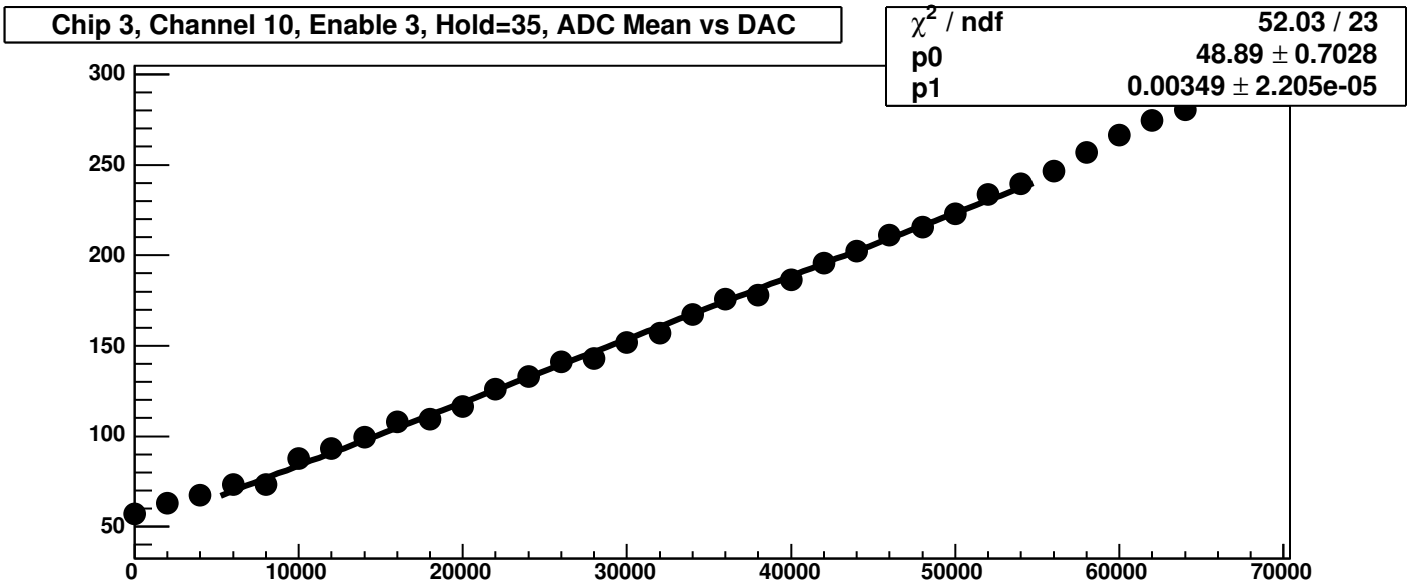

Chip 3, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

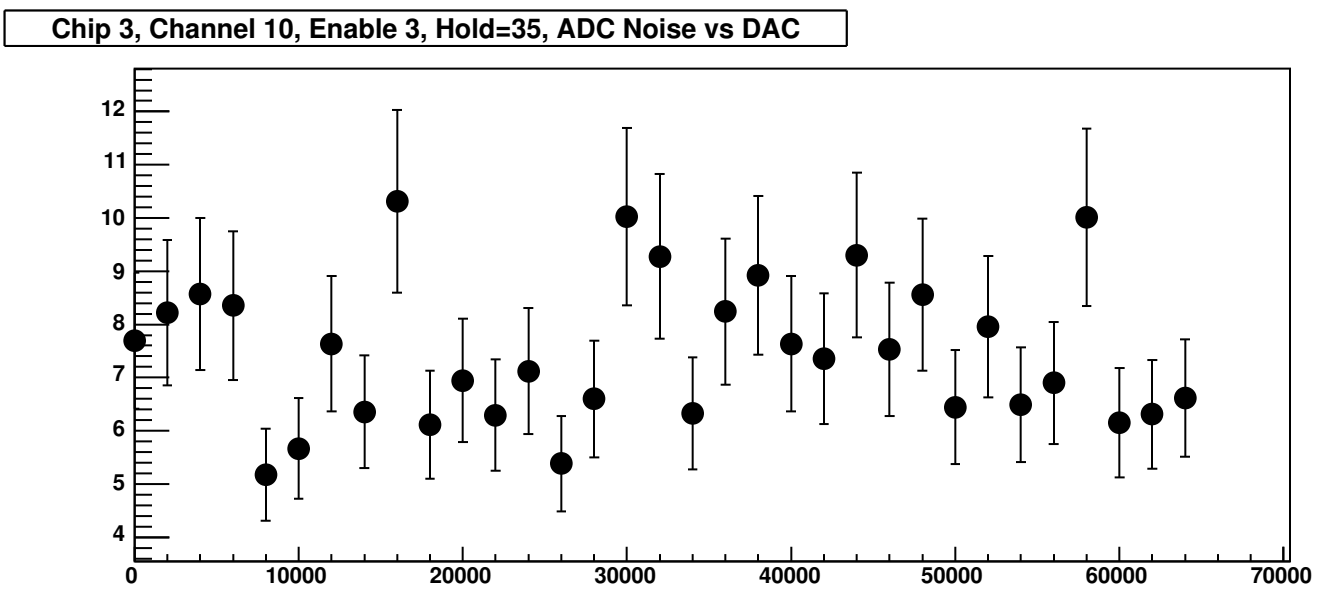

Chip 3, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

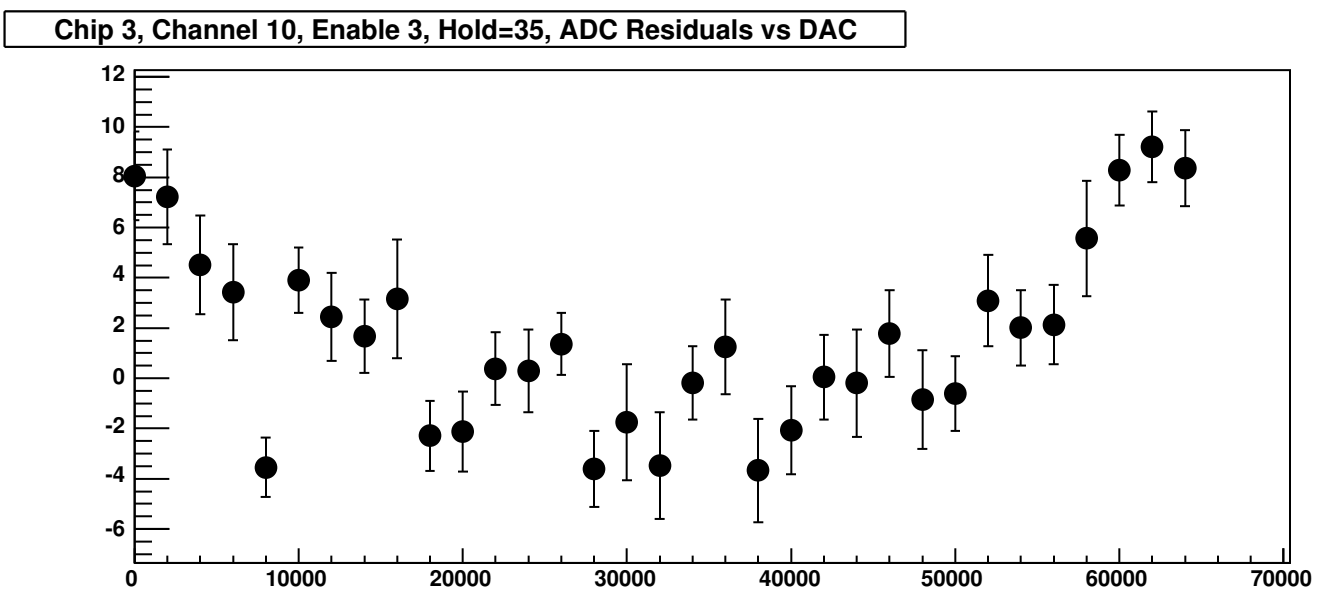

Chip 3, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 71.68 / 23
p0 55.33 ± 0.7089
p1 0.001151 ± 2.122e-05
120
110
100
90
80
70
60
0
10000
20000
30000
40000
50000
60000
70000

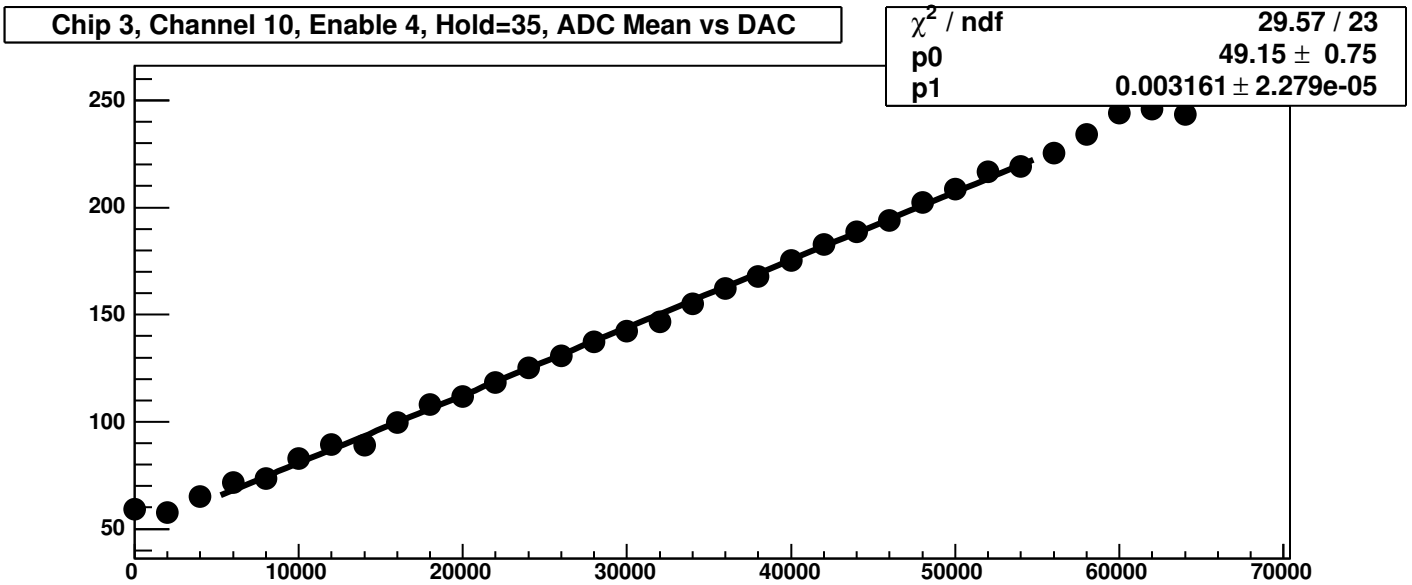

Chip 3, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

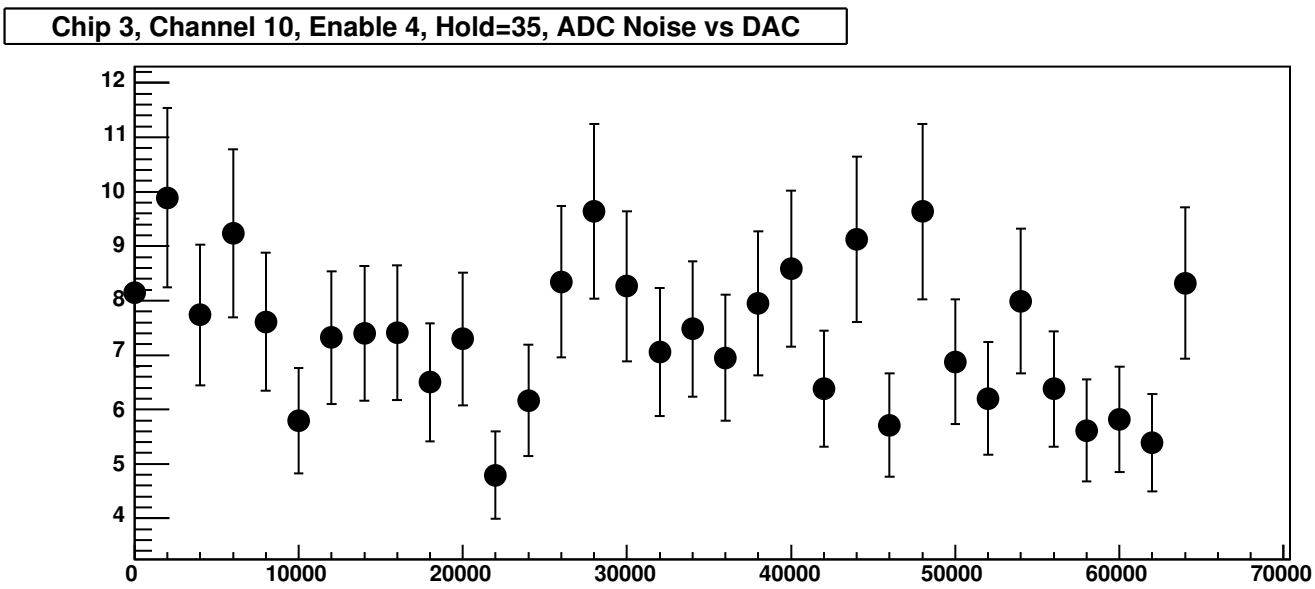

Chip 3, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
0
10000
20000
30000
40000
50000
60000
70000

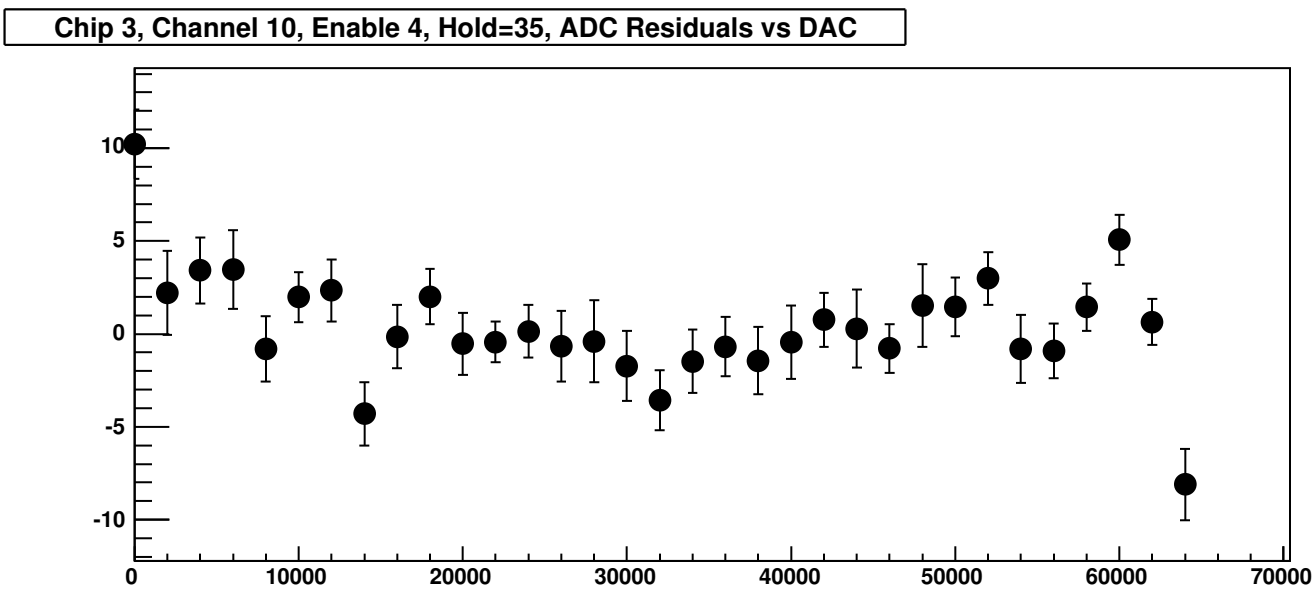

Chip 3, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 52.03 / 23
p0 48.89 ± 0.7028
p1 0.00349 ± 2.205e-05
300
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

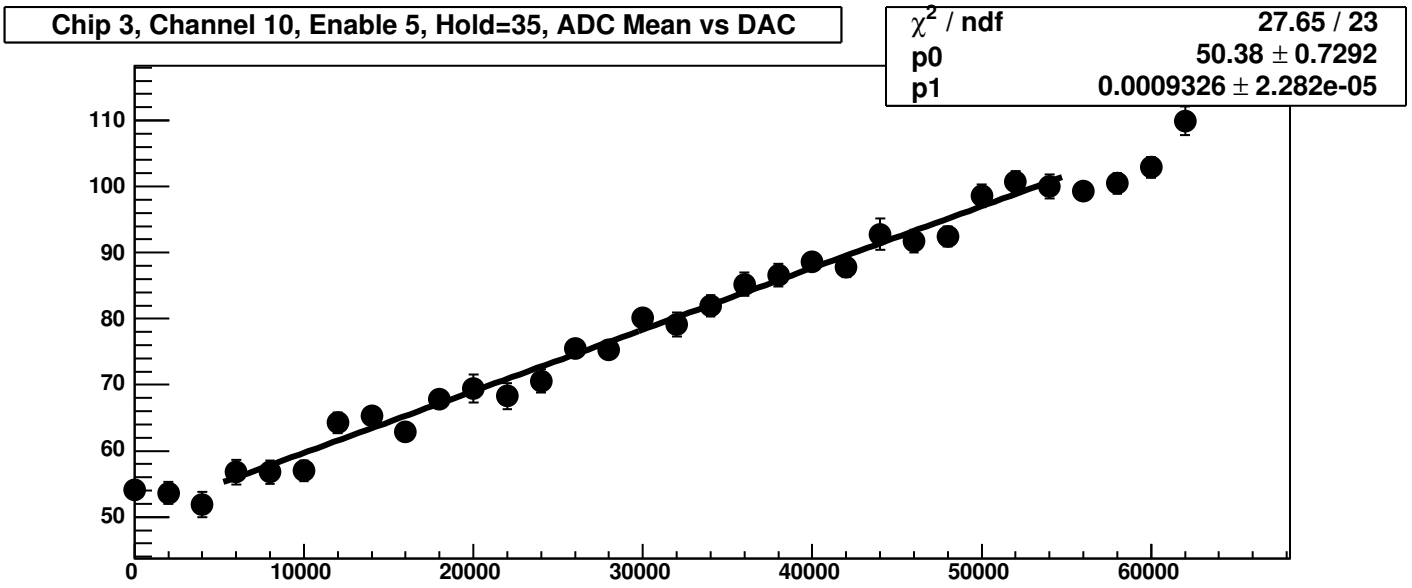

Chip 3, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

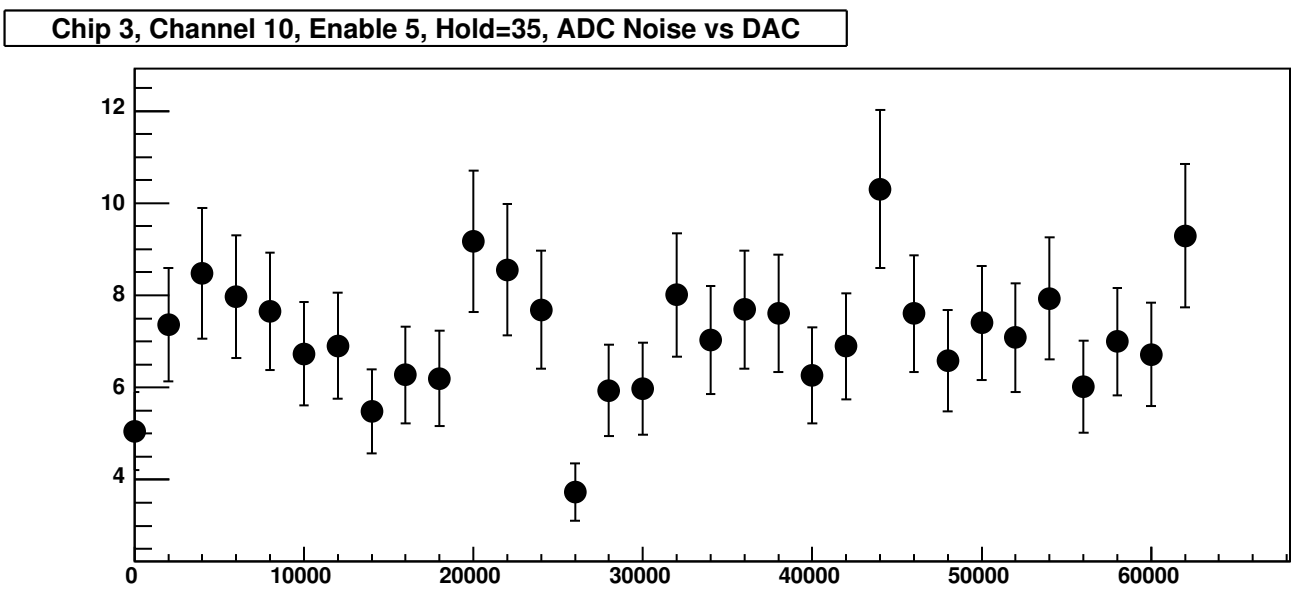

Chip 3, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

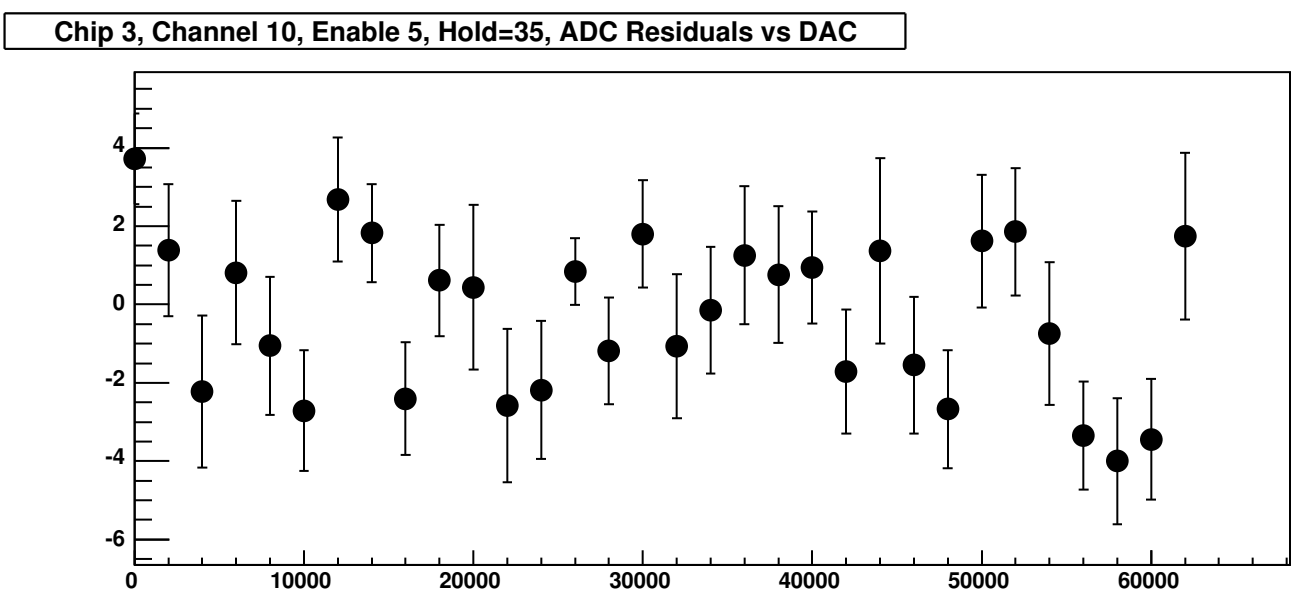

Chip 3, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 29.57 / 23
p0 49.15 ± 0.75
p1 0.003161 ± 2.279e-05
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

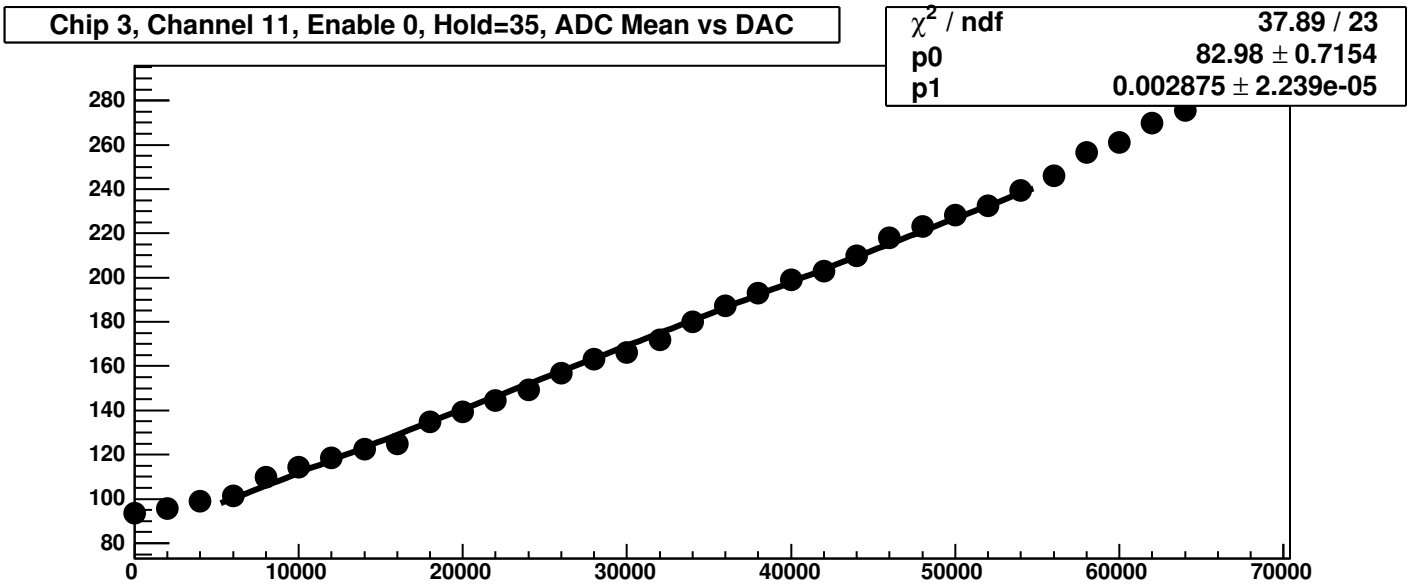

Chip 3, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

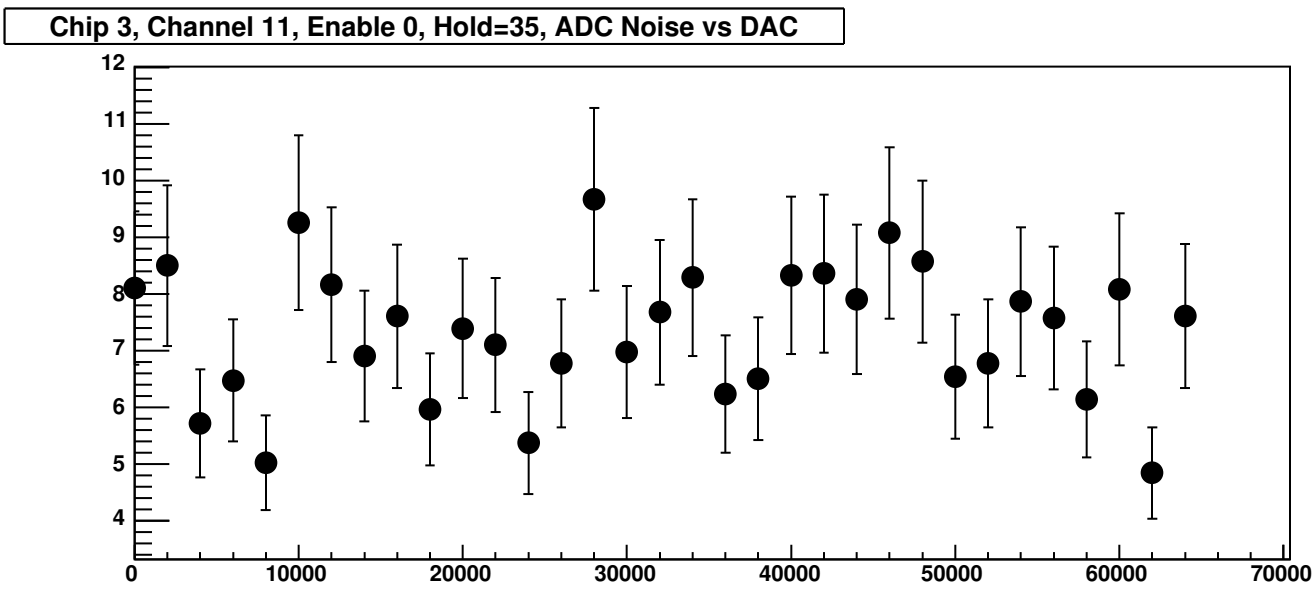

Chip 3, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

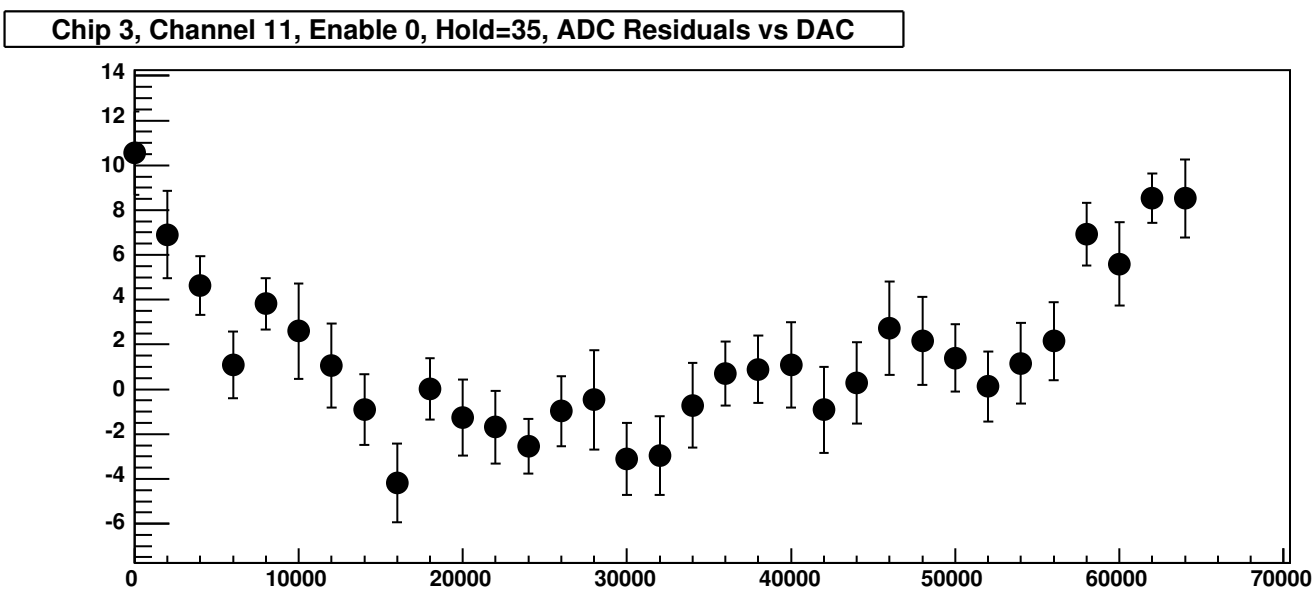

Chip 3, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 27.65 / 23
p0 50.38 ± 0.7292
p1 0.0009326 ± 2.282e-05
110
100
90
80
70
60
50
0
10000
20000
30000
40000
50000
60000

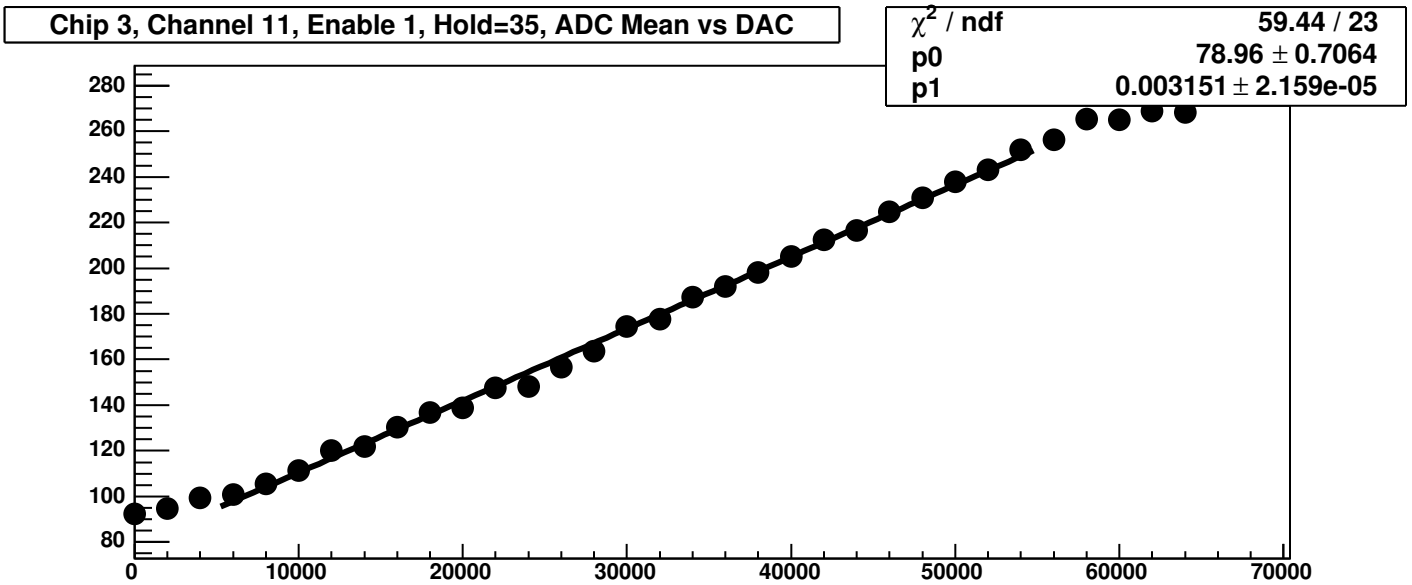

Chip 3, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000

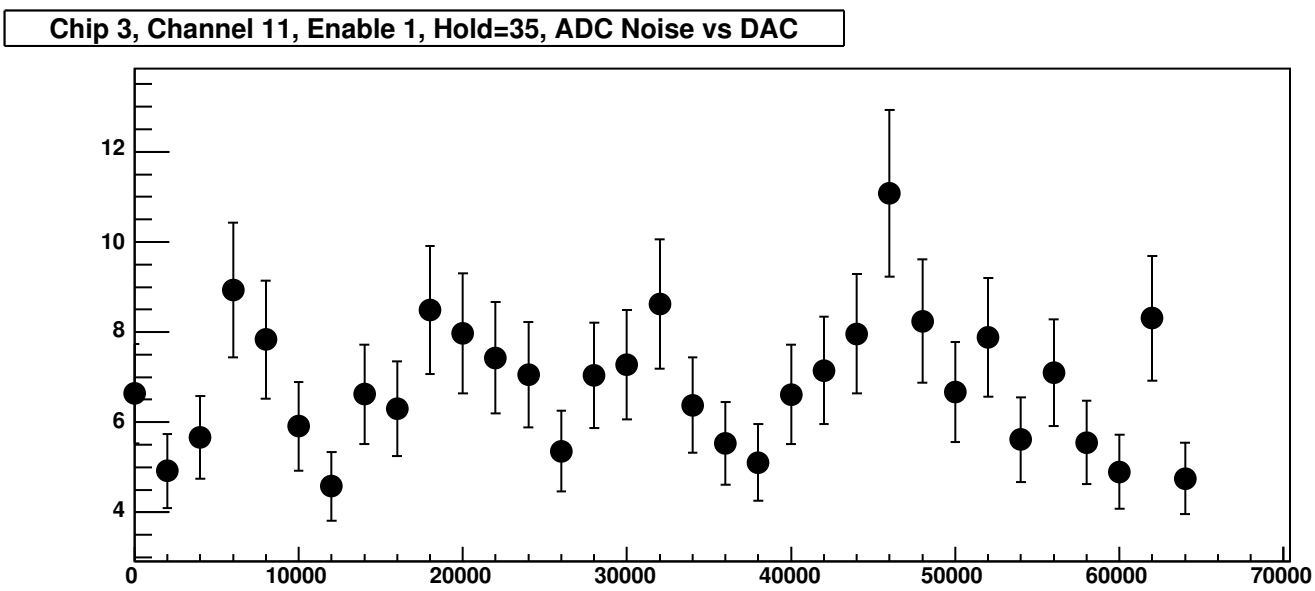

Chip 3, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000

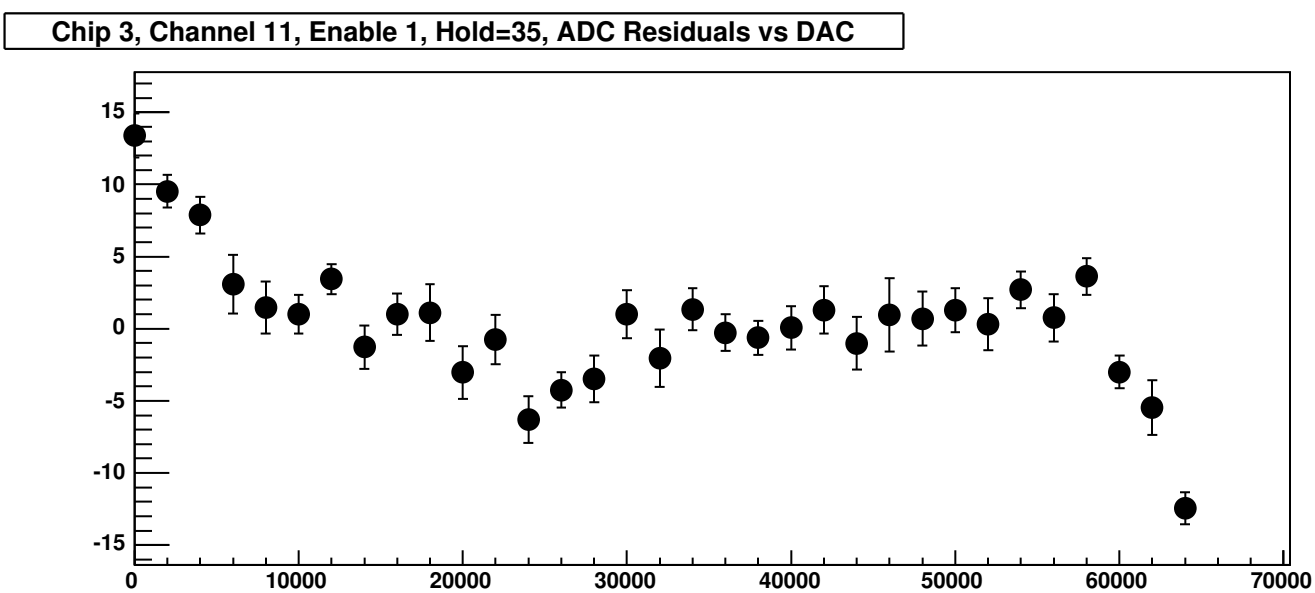

Chip 3, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 37.89 / 23
p0 82.98 ± 0.7154
p1 0.002875 ± 2.239e-05
280
260
240
220
200
180
160
140
120
100
80
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

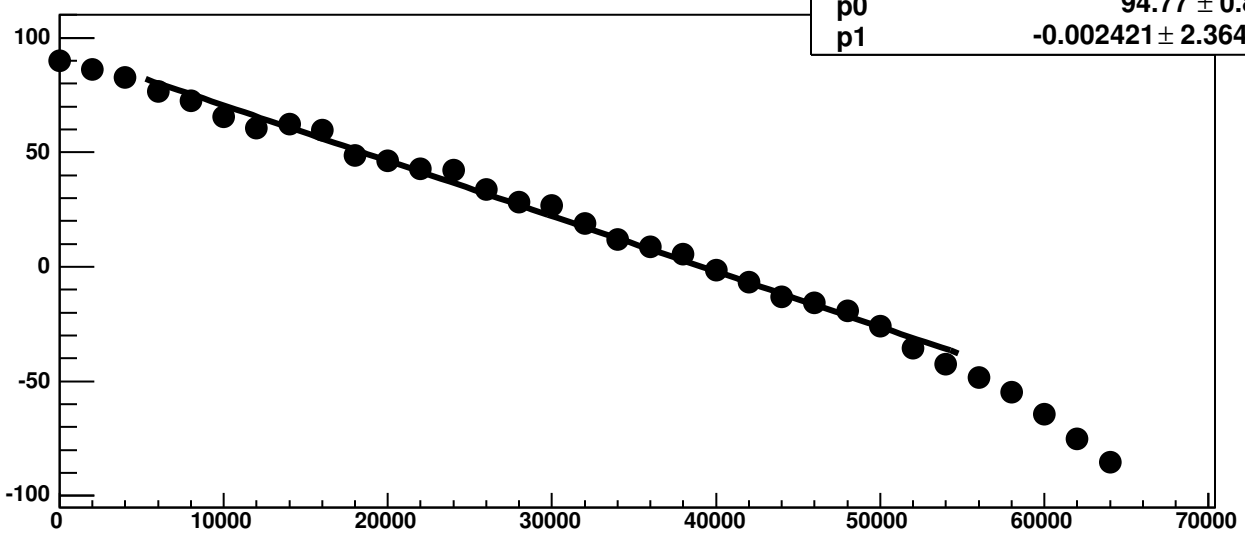

Chip 3, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 59.44 / 23
p0 78.96 ± 0.7064
p1 0.003151 ± 2.159e-05
280
260
240
220
200
180
160
140
120
100
80
0
10000
20000
30000
40000
50000
60000
70000

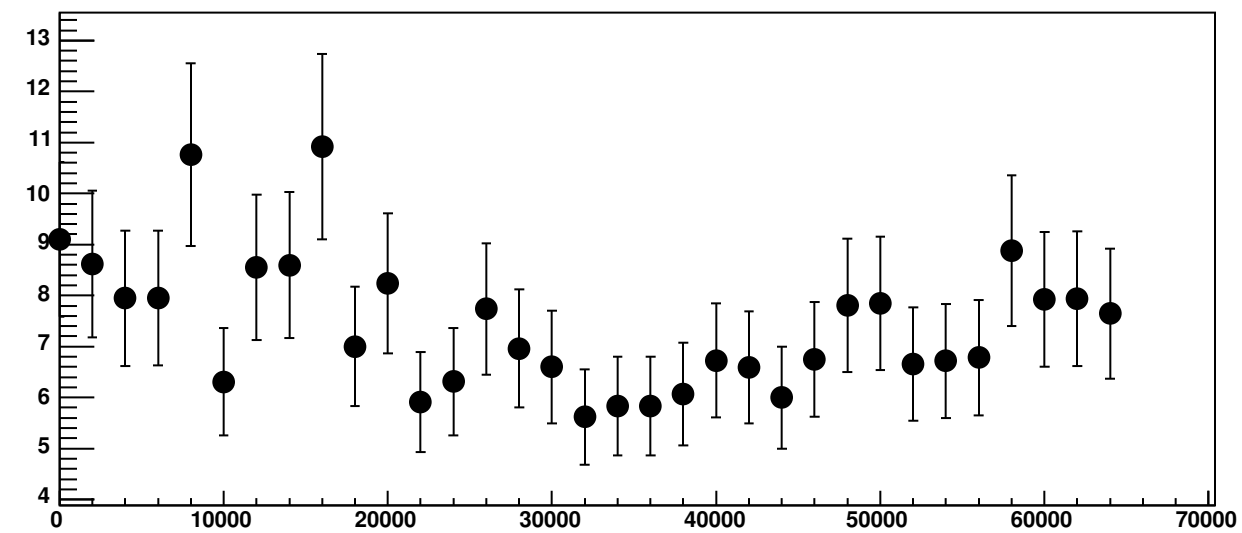

Chip 3, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

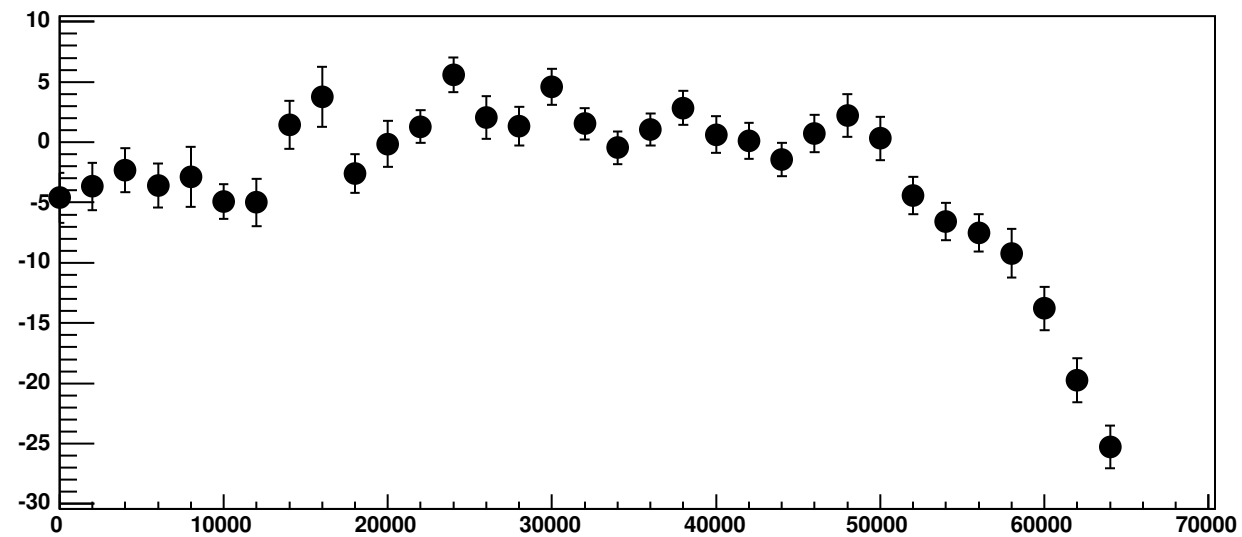
Chip 3, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

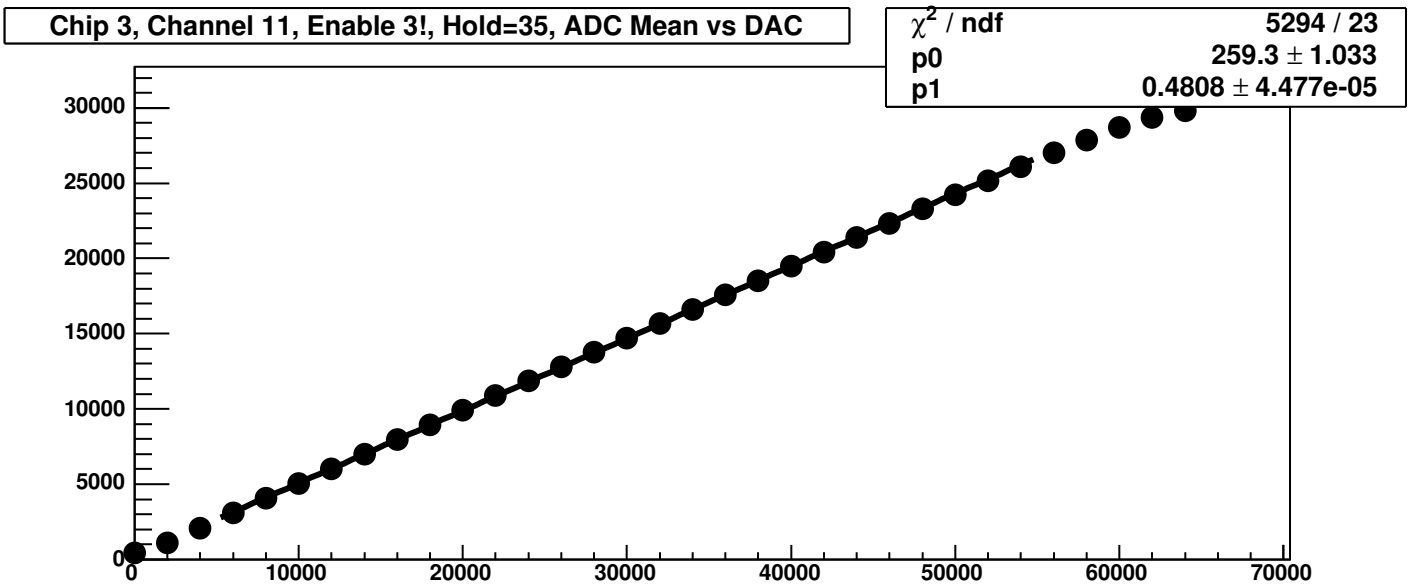


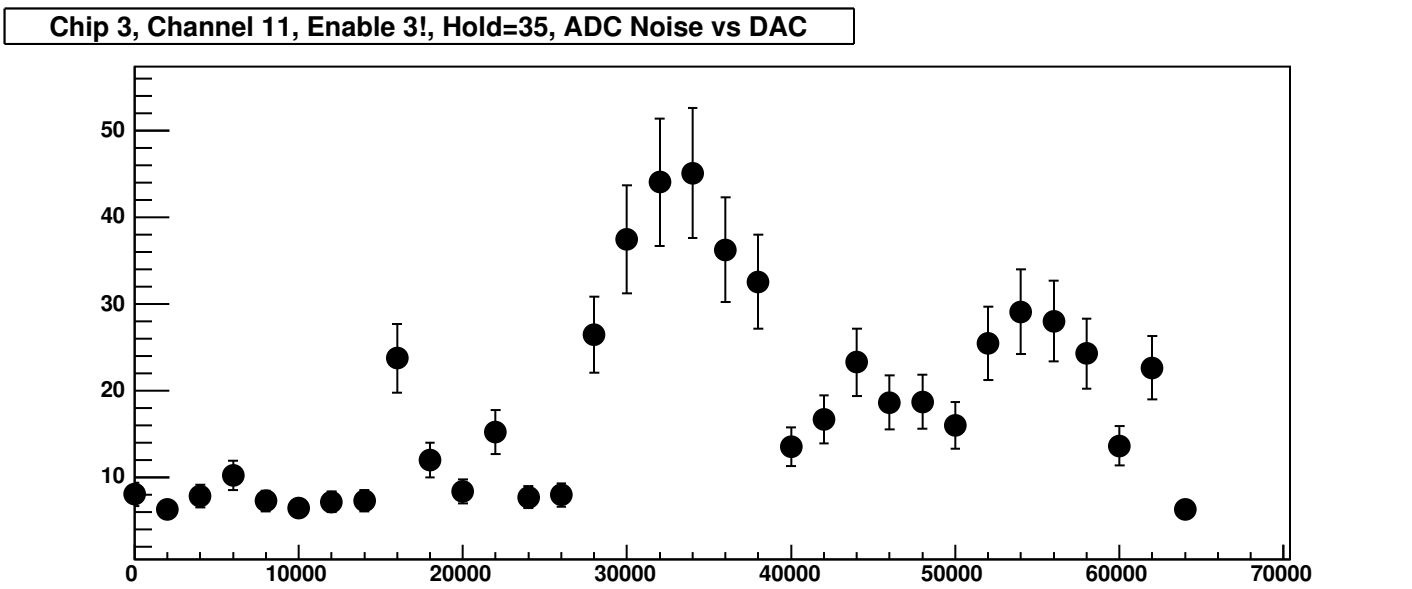
Chip 3, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

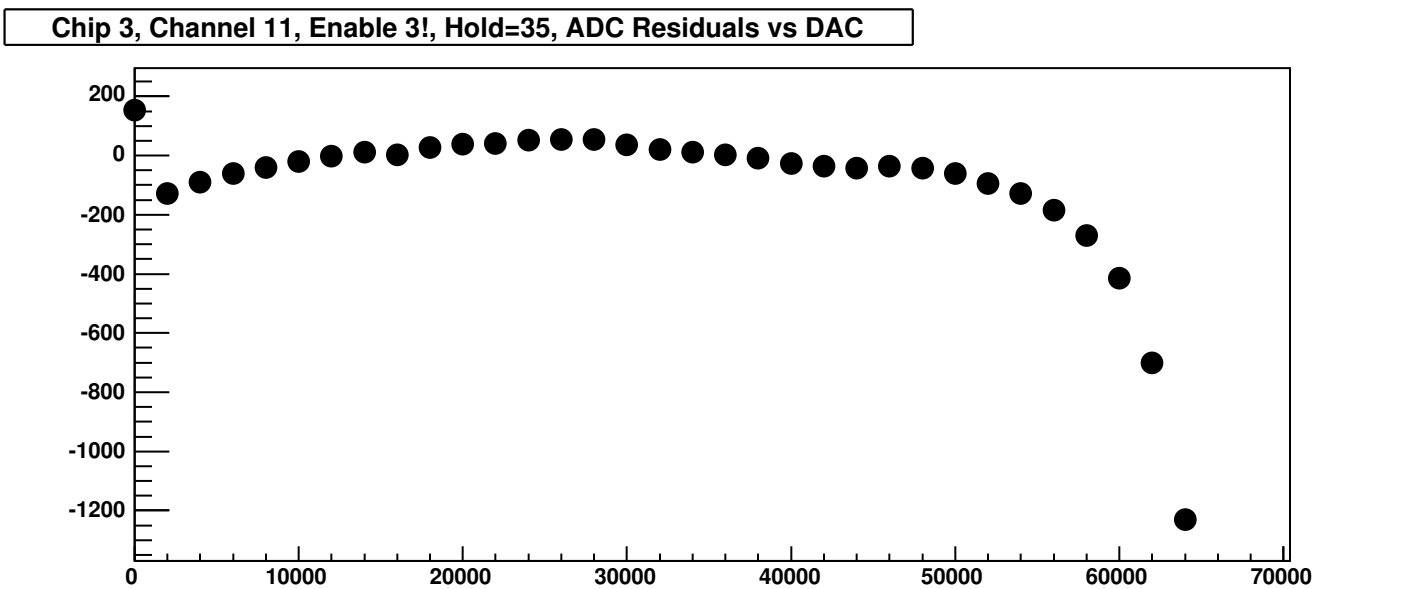


Chip 3, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

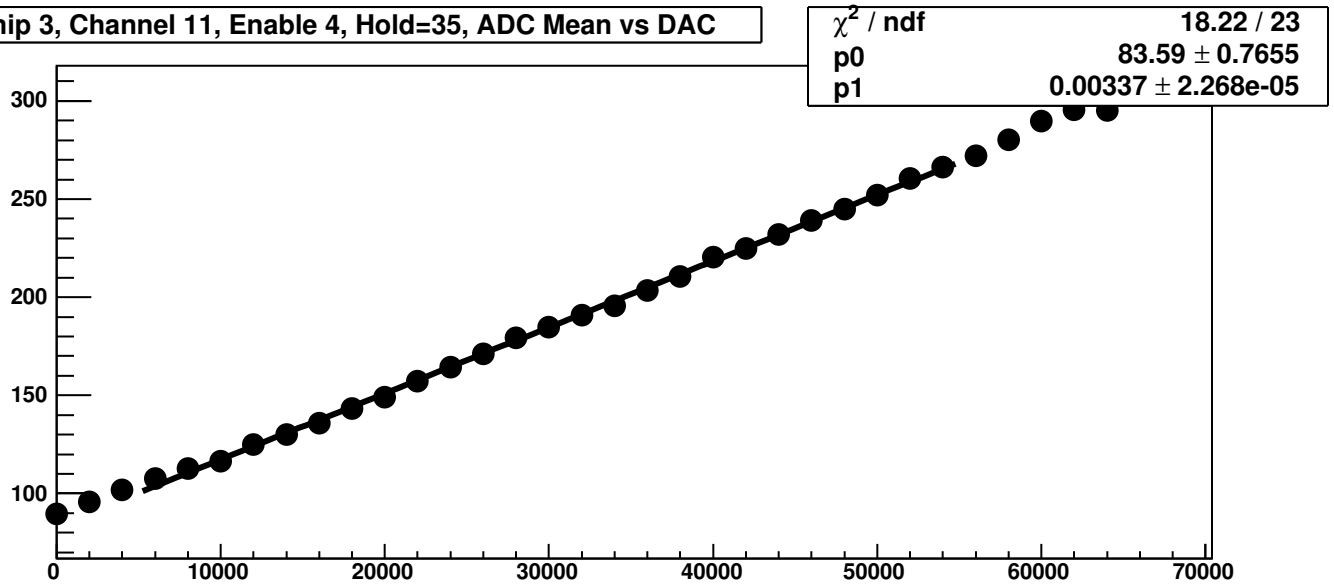



Chip 3, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 5294 / 23
p0 259.3 ± 1.033
p1 0.4808 ± 4.477e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

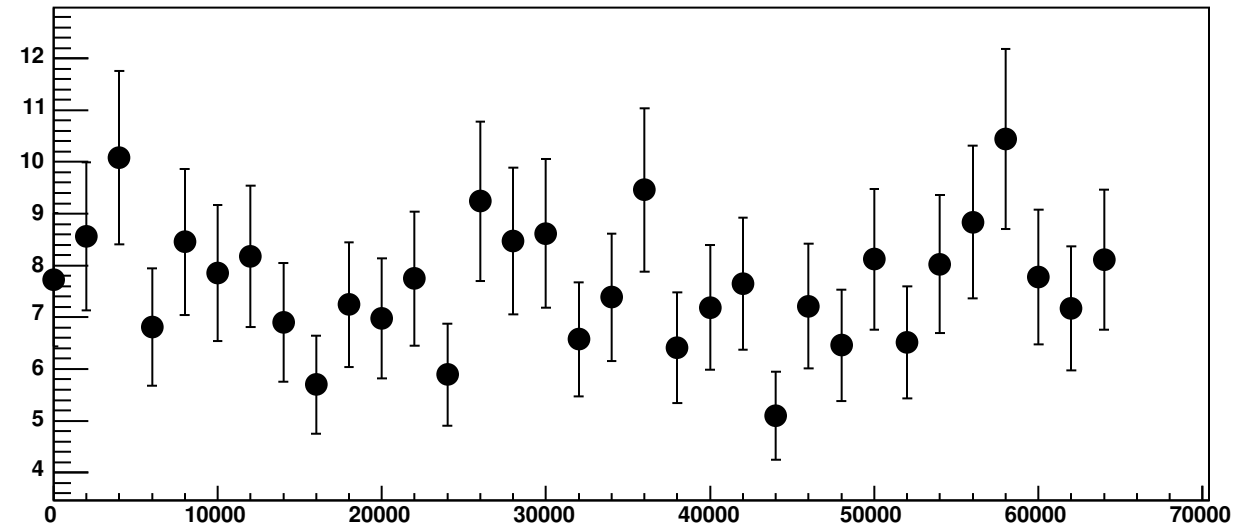

Chip 3, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
0
10000
20000
30000
40000
50000
60000
70000

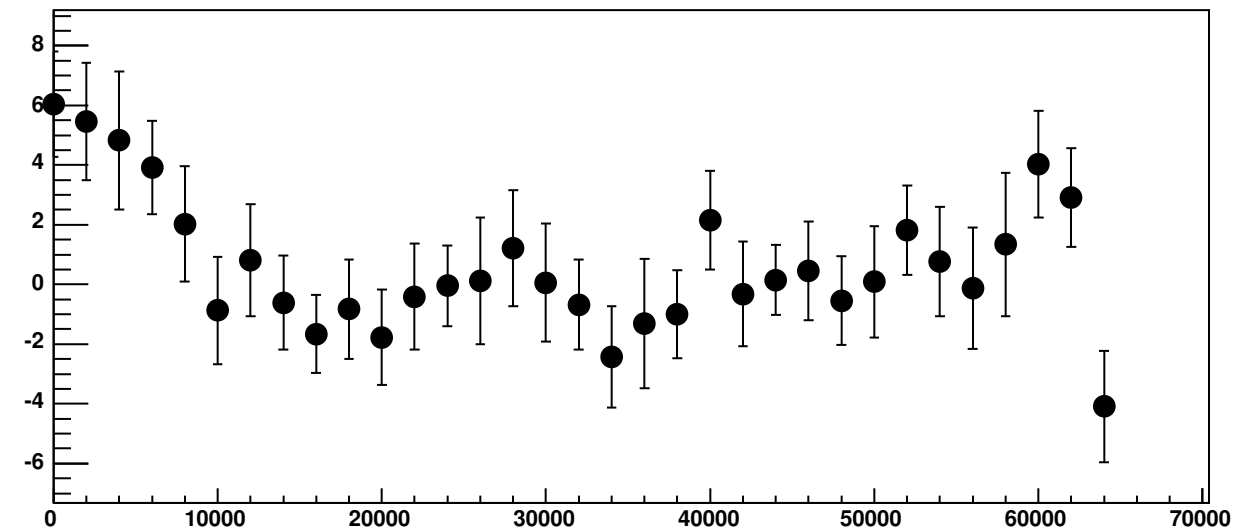
Chip 3, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC

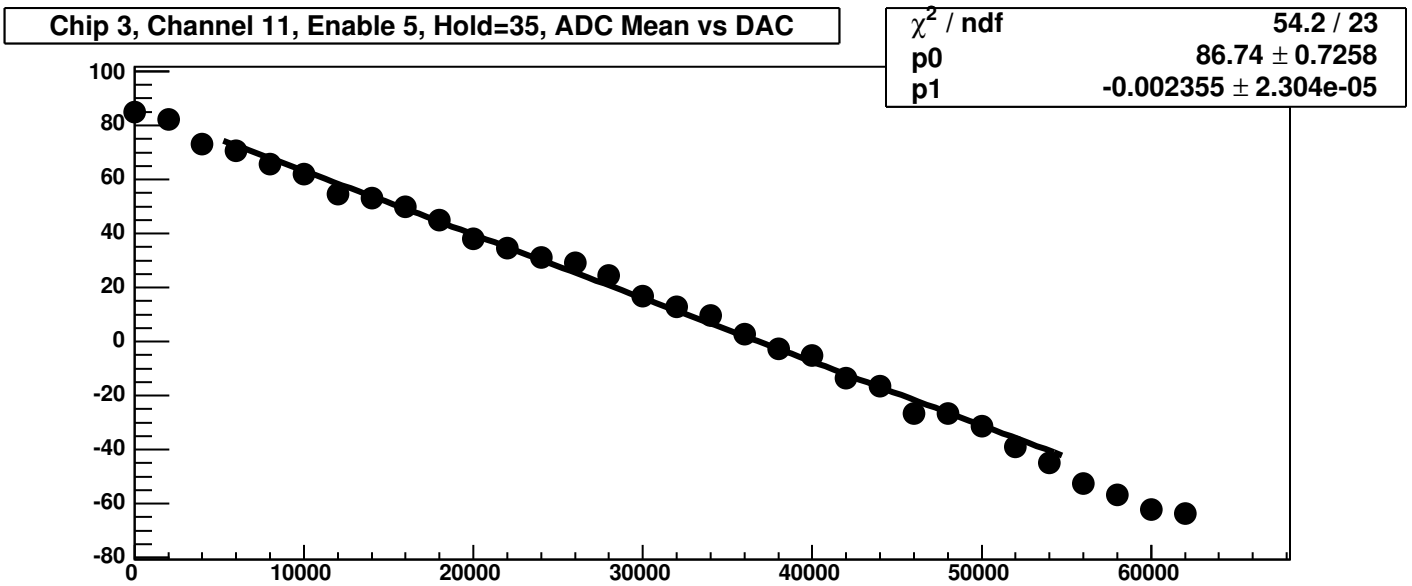


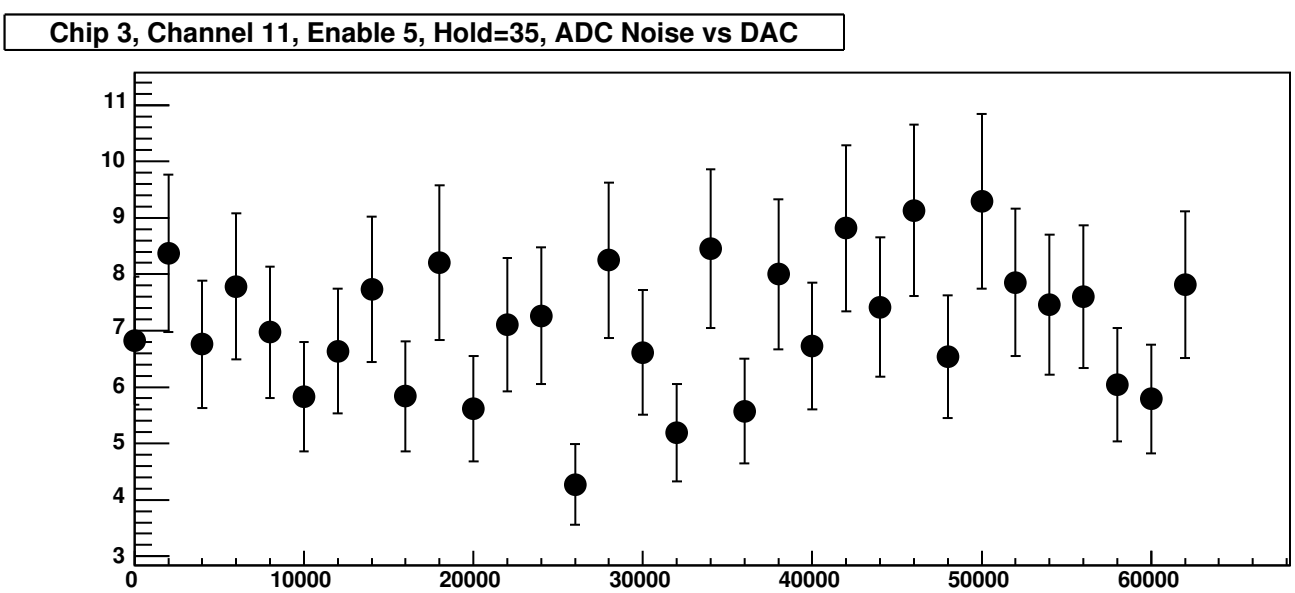
Chip 3, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC

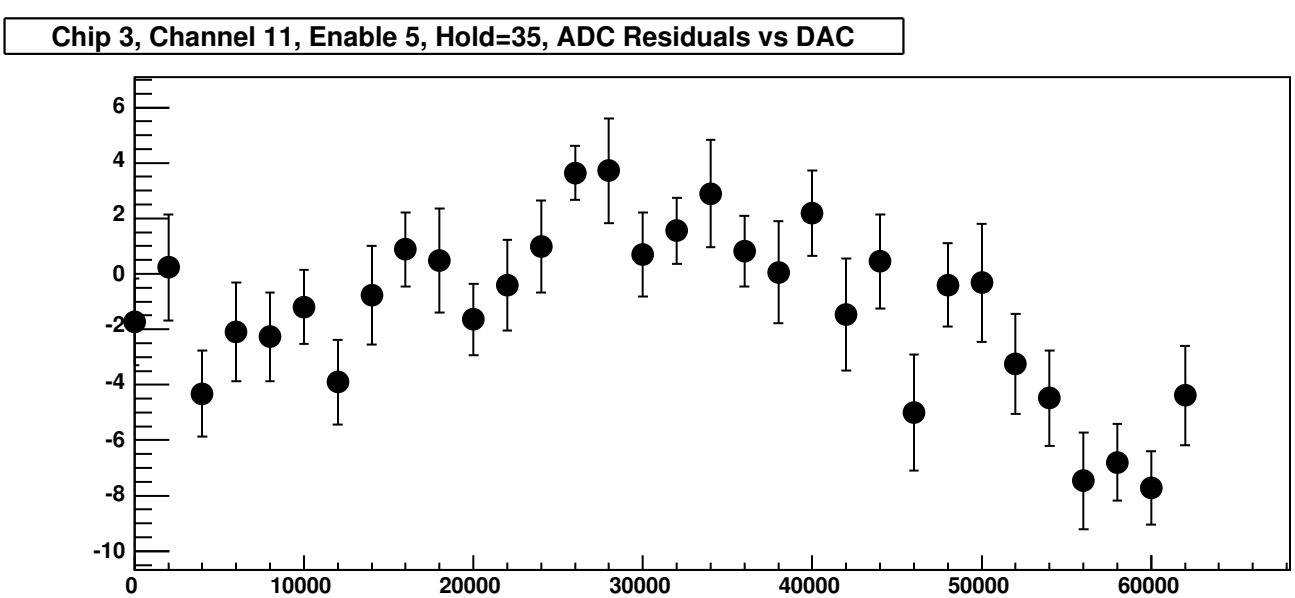


Chip 3, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC

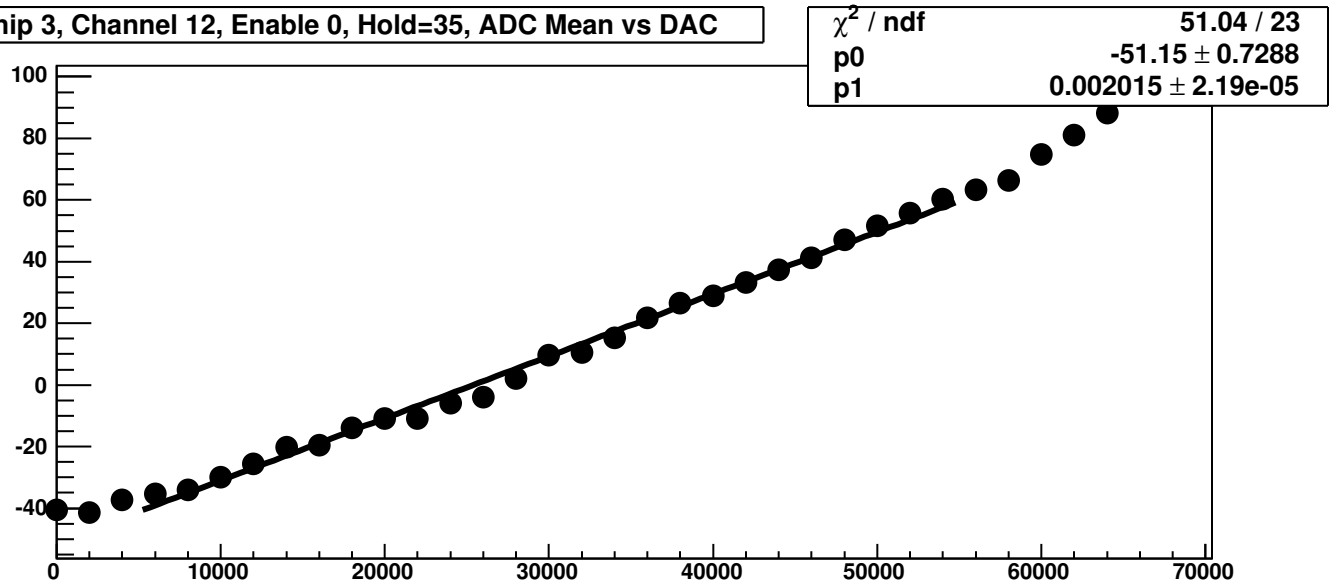



Chip 3, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 54.2 / 23
p0 86.74 ± 0.7258
p1 -0.002355 ± 2.304e-05
100
80
60
40
20
0
-20
-40
-60
-80
0
10000
20000
30000
40000
50000
60000

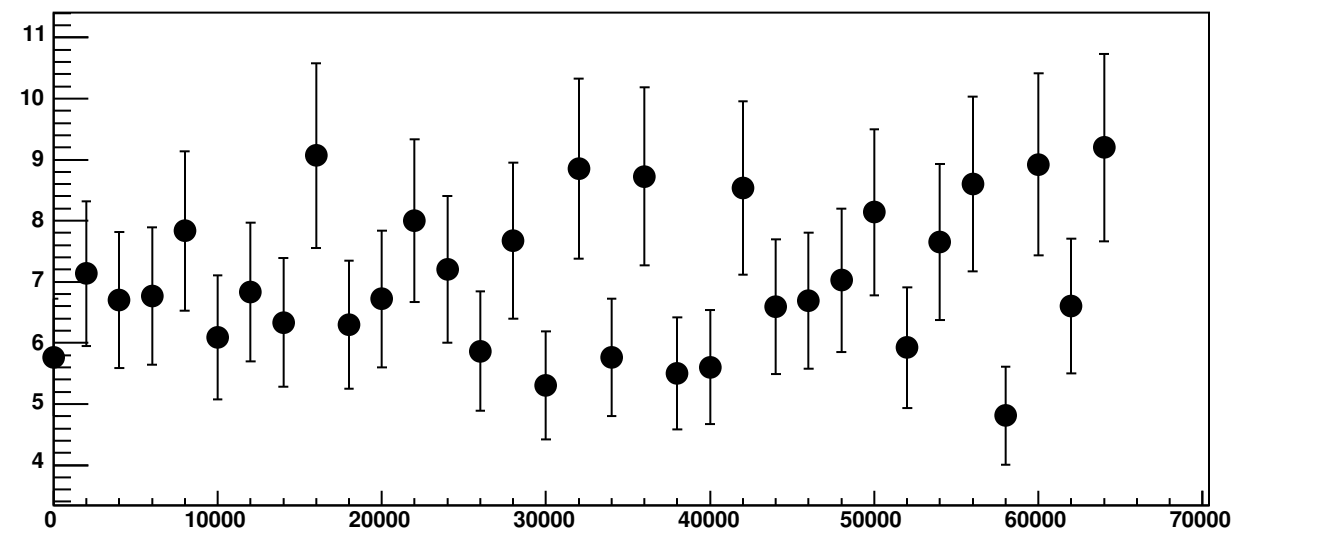

Chip 3, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
0
10000
20000
30000
40000
50000
60000

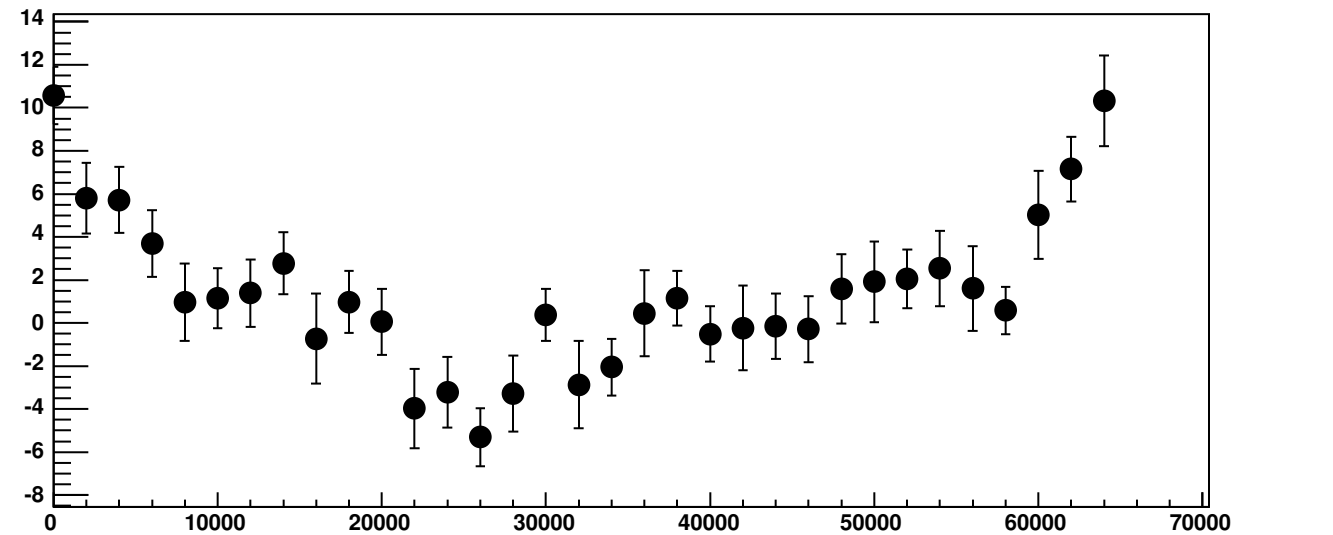
Chip 3, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC

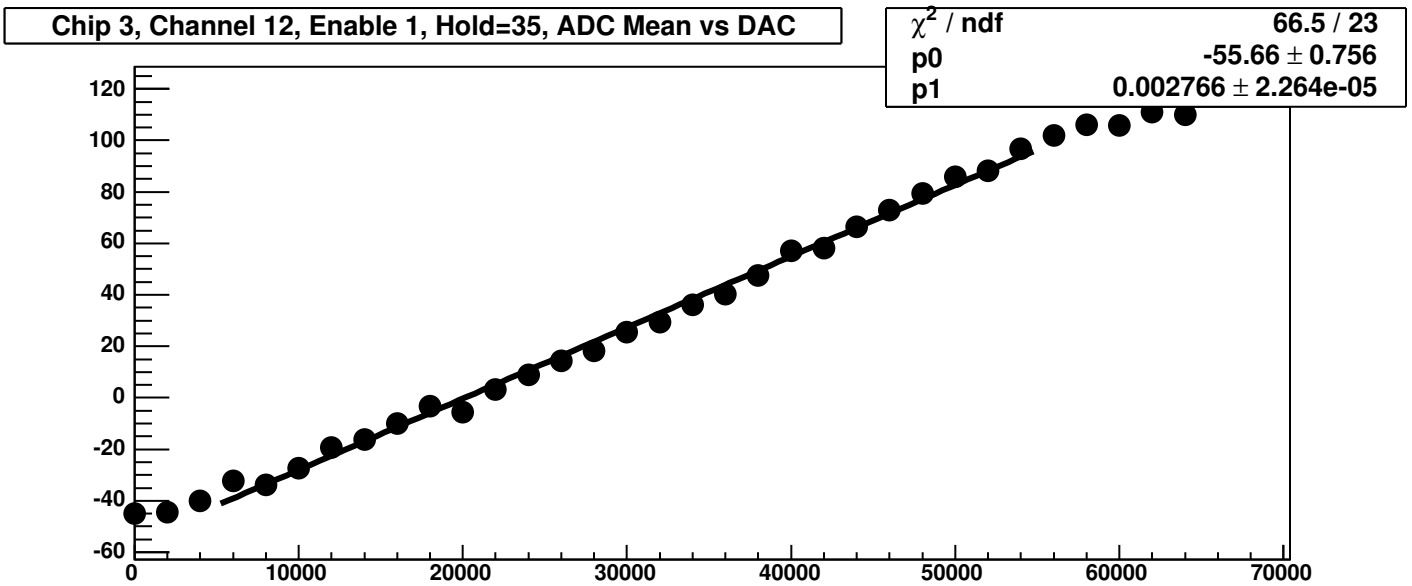


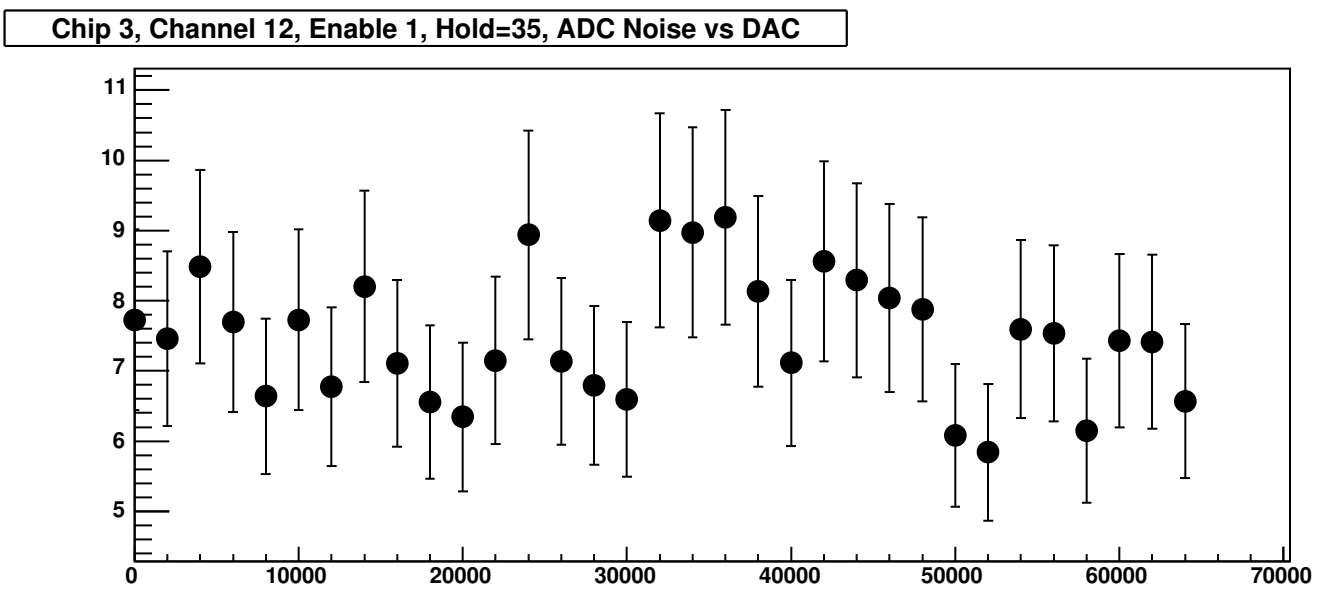
Chip 3, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC

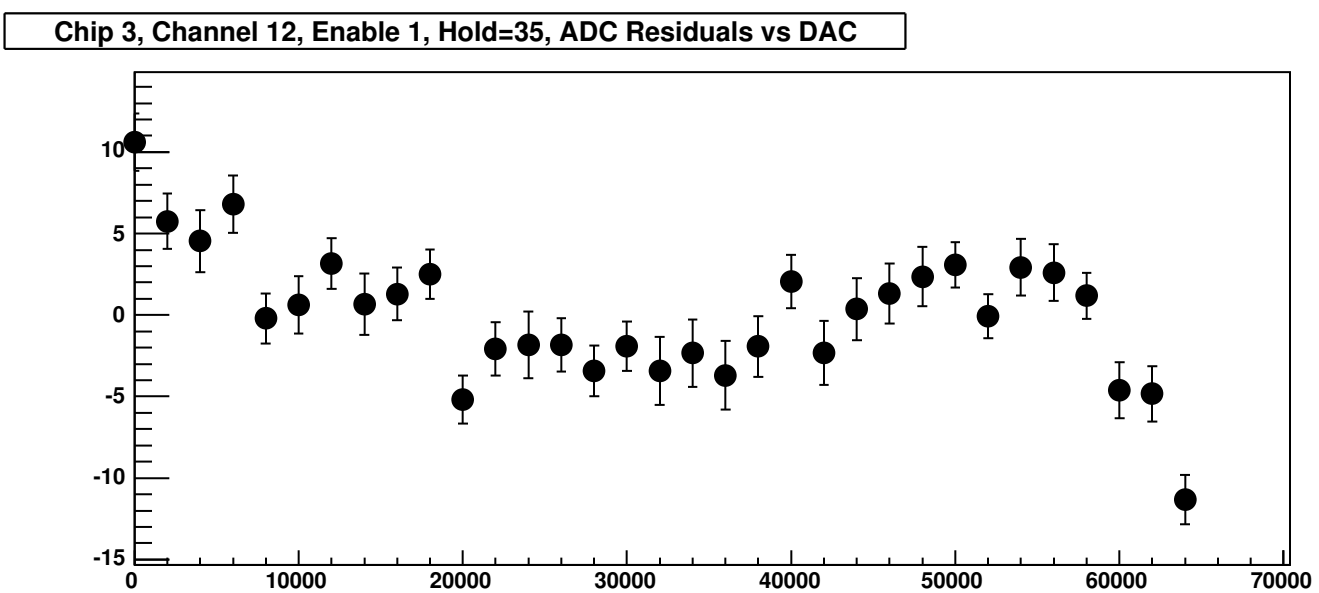


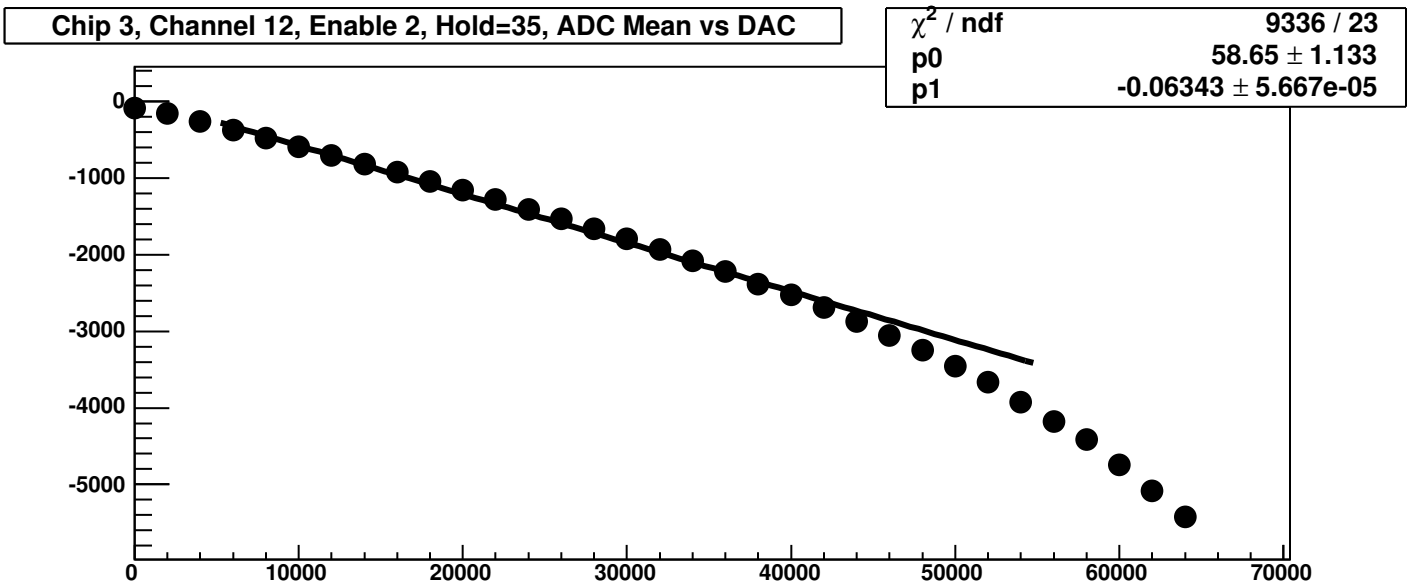
Chip 3, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC

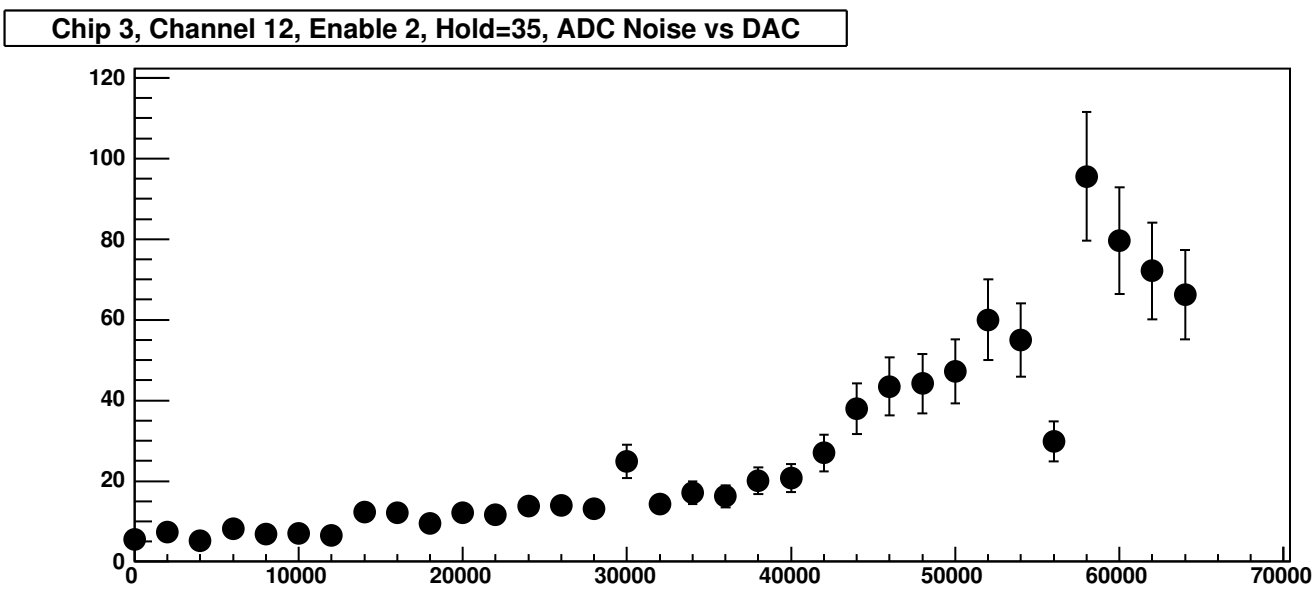


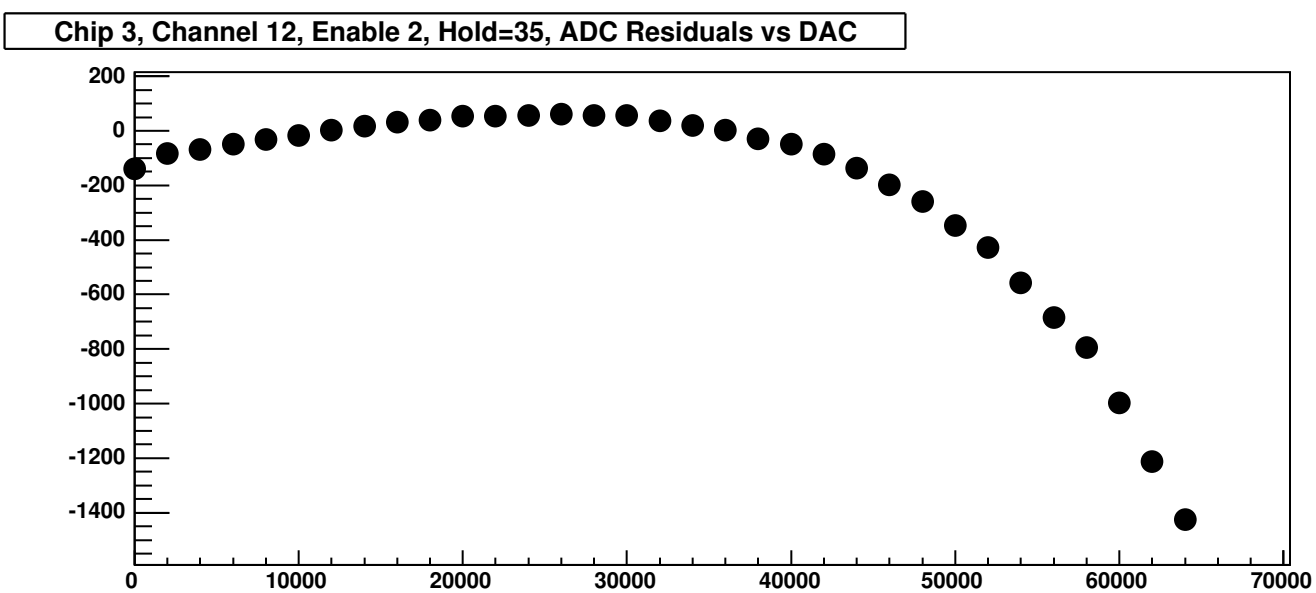

Chip 3, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 66.5 / 23
p0 -55.66 ± 0.756
p1 0.002766 ± 2.264e-05
120
100
80
60
40
20
0
-20
-40
-60
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000

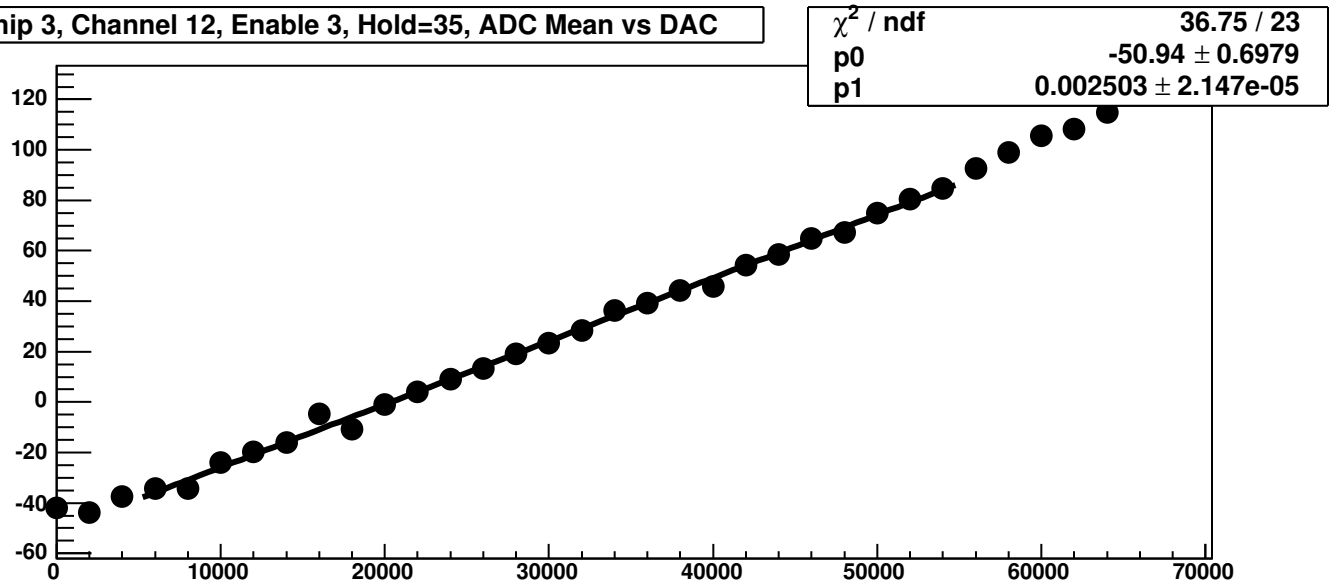

Chip 3, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 9336 / 23
p0 58.65 ± 1.133
p1 -0.06343 ± 5.667e-05
0
-1000
-2000
-3000
-4000
-5000
0
10000
20000
30000
40000
50000
60000
70000

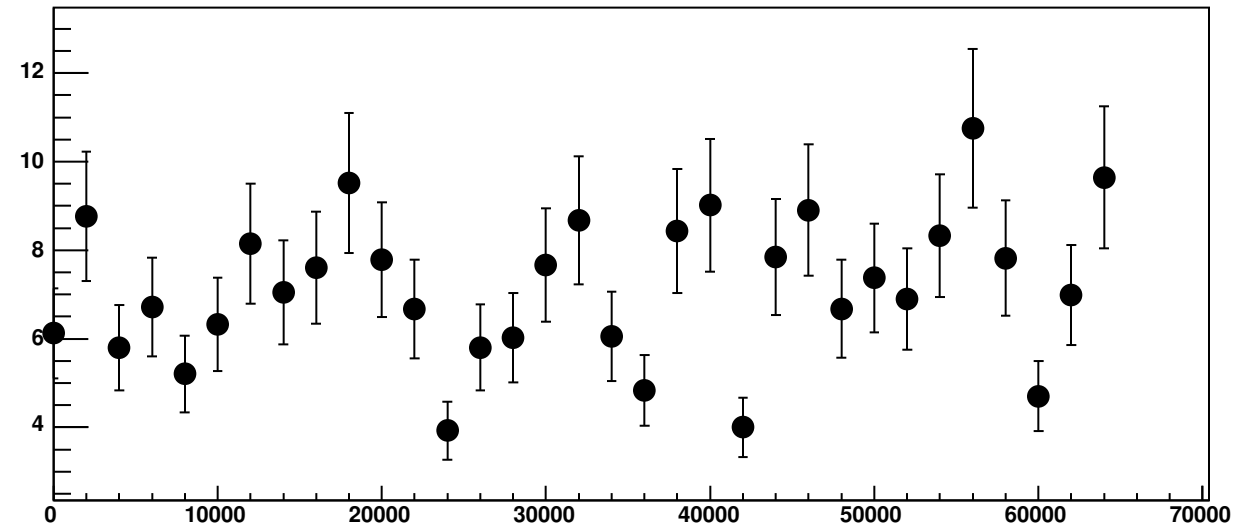

Chip 3, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
10000
20000
30000
40000
50000
60000
70000

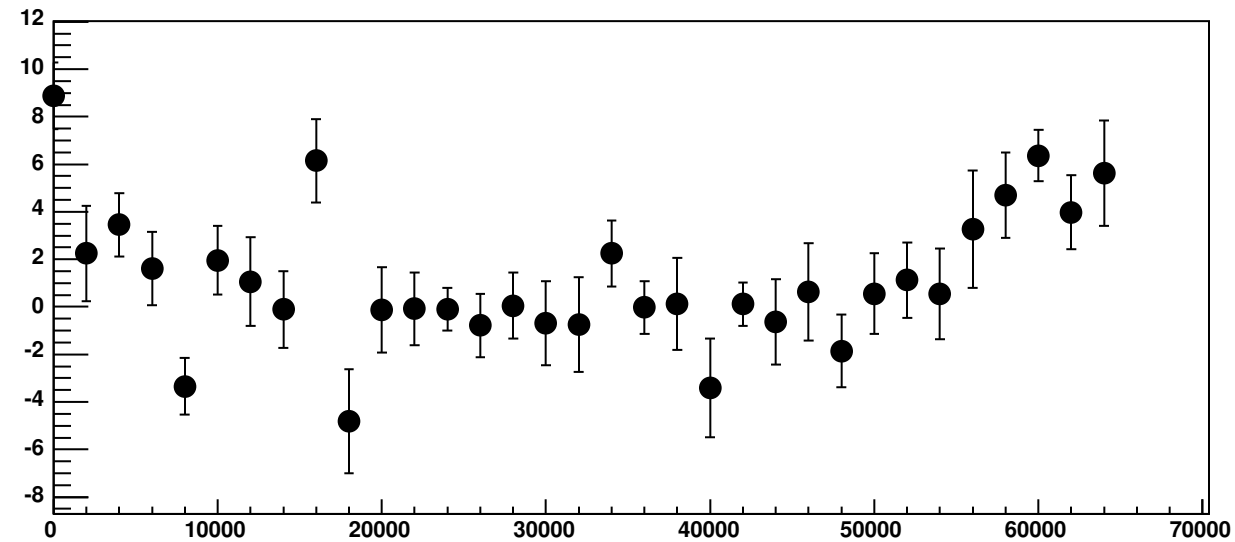
Chip 3, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC

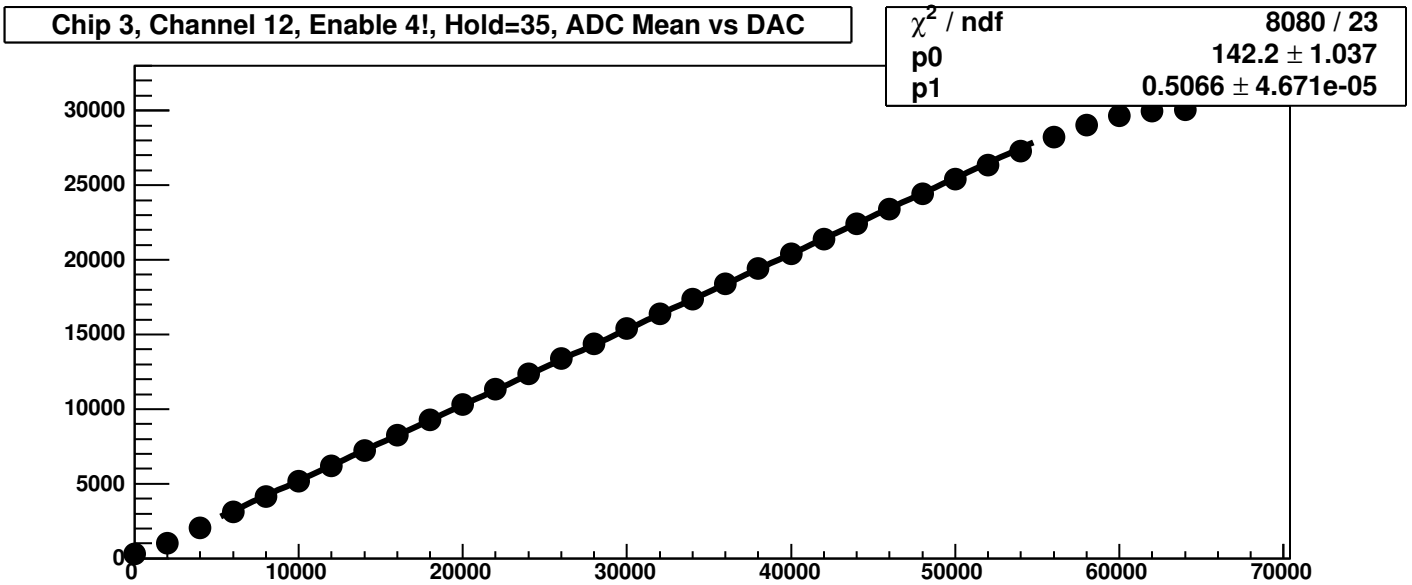


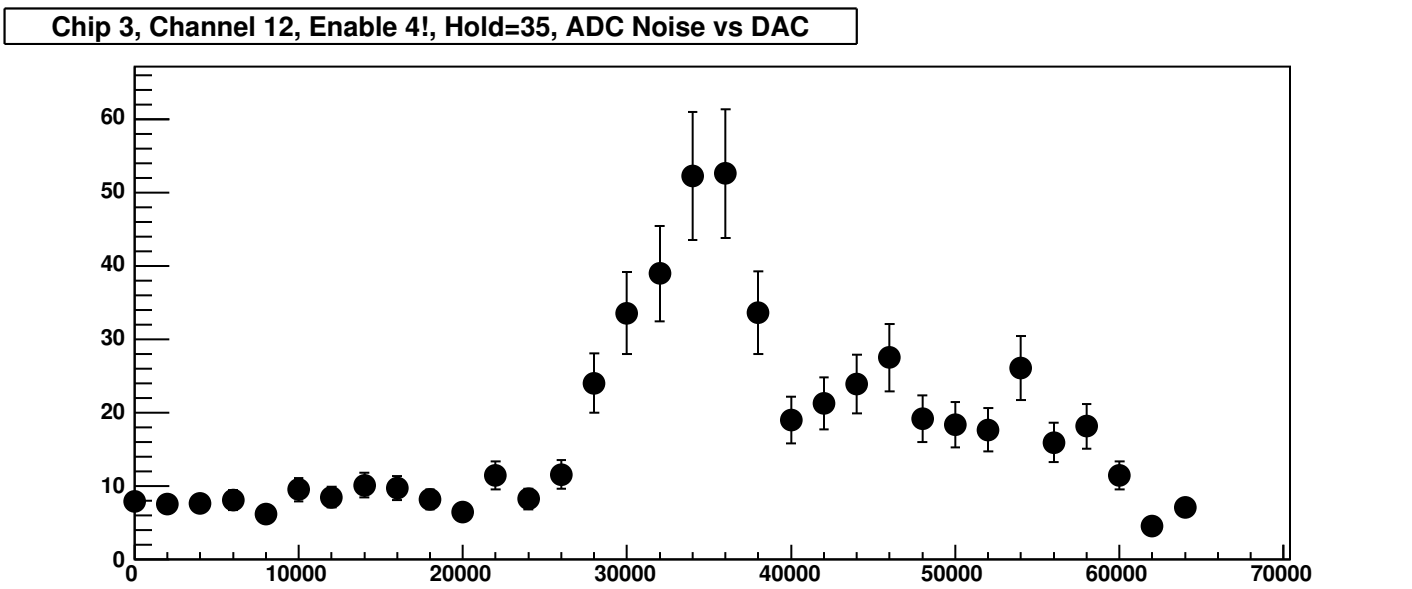
Chip 3, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC

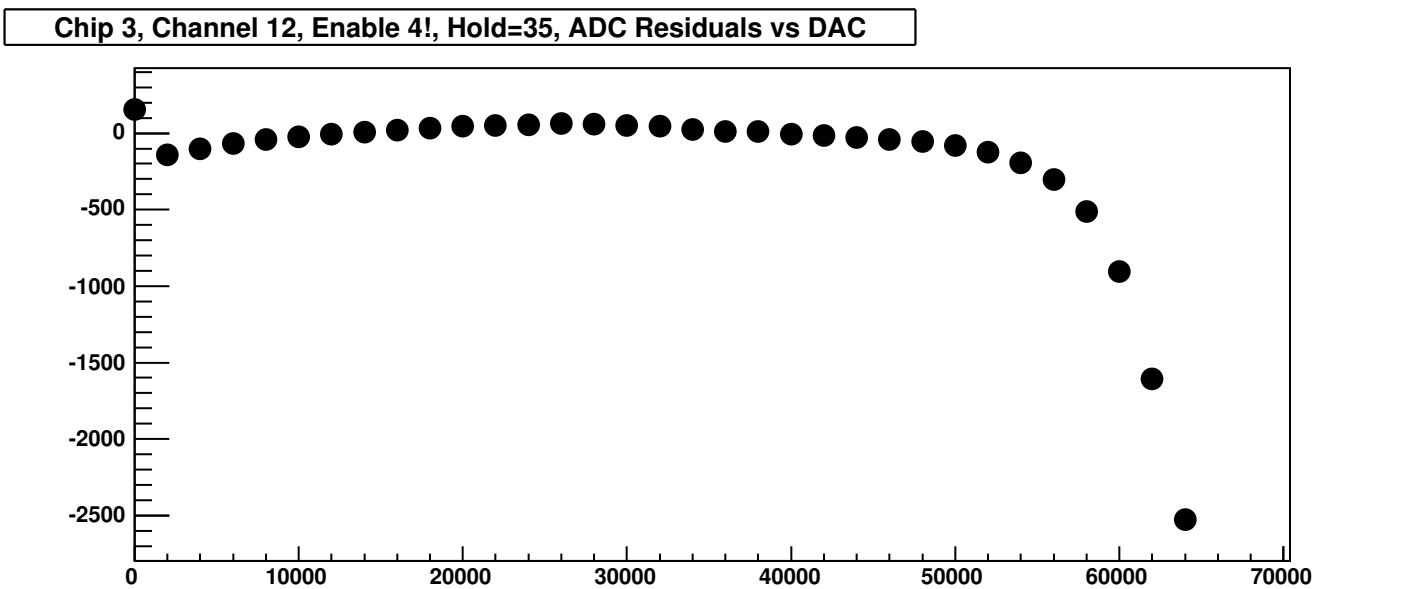


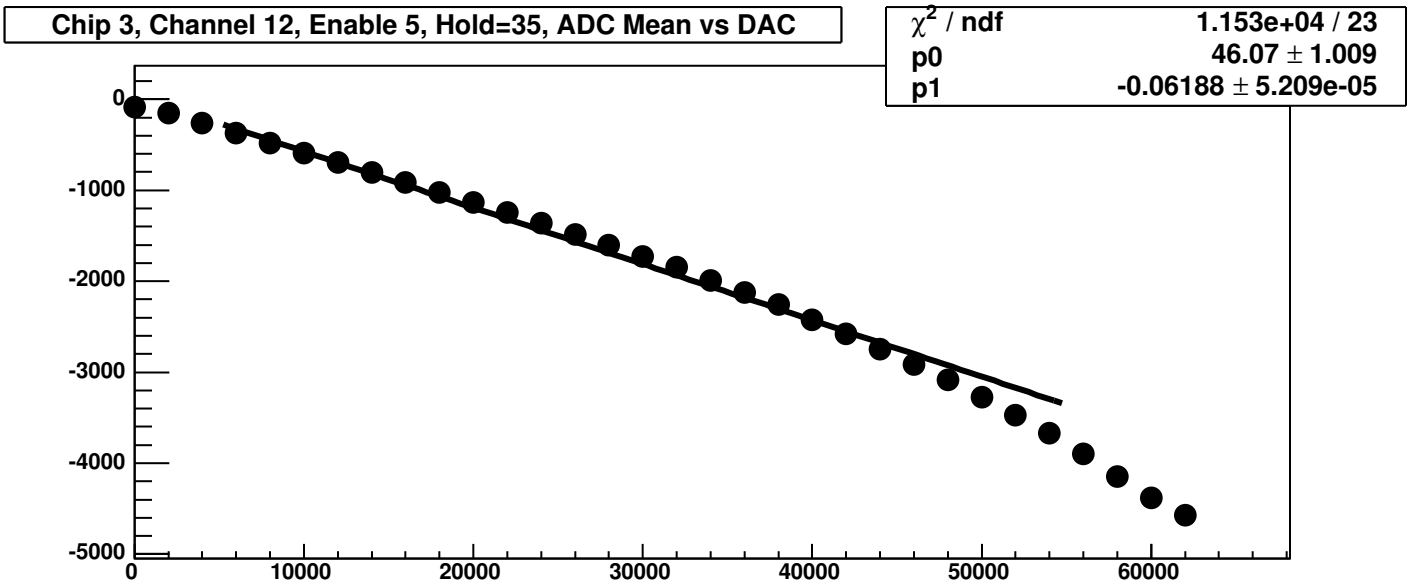
Chip 3, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC

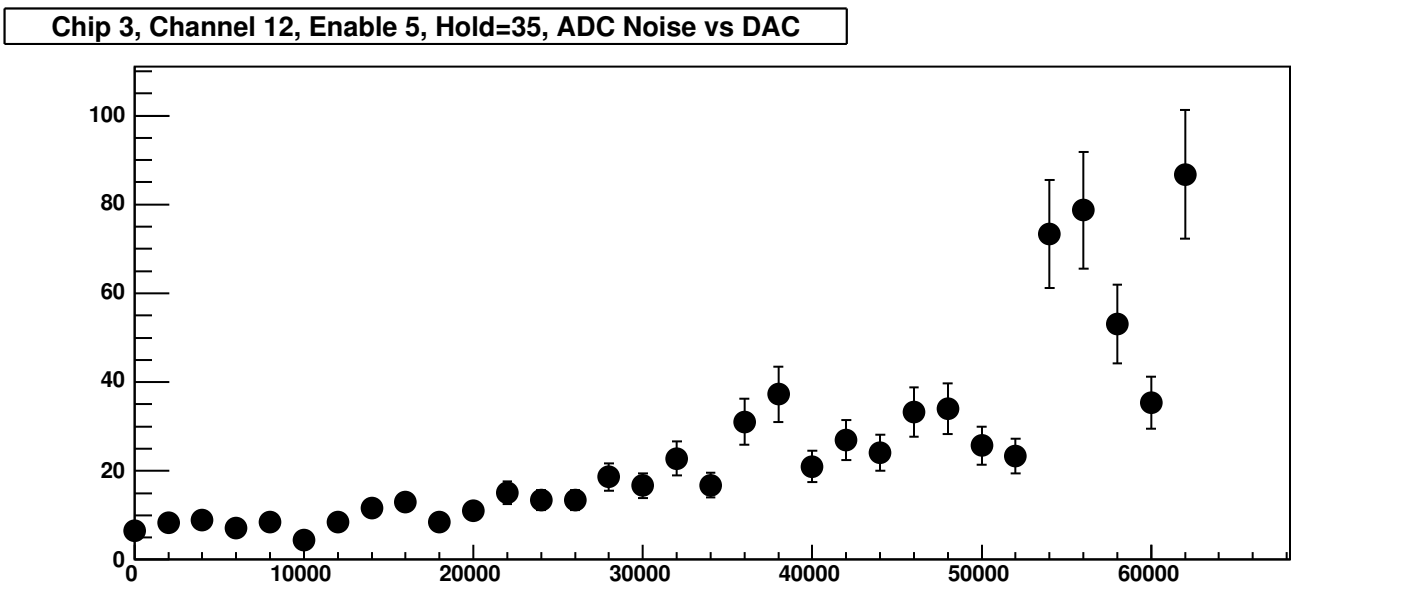


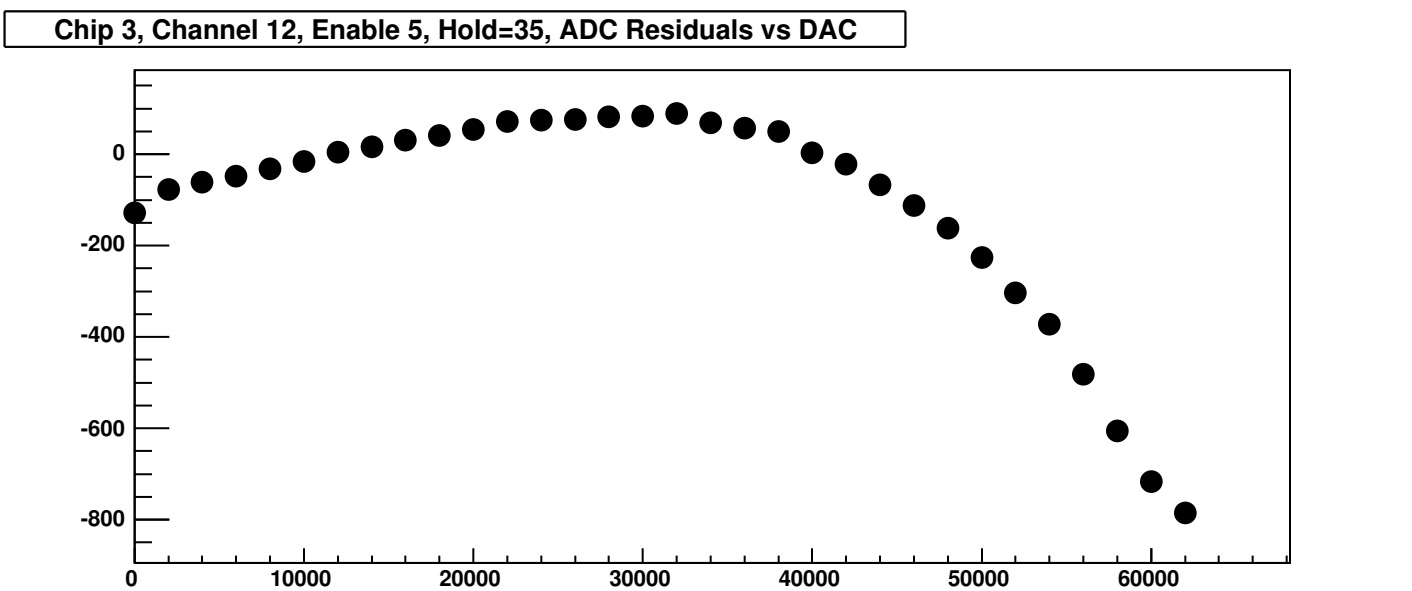

Chip 3, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 8080 / 23
p0 142.2 ± 1.037
p1 0.5066 ± 4.671e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

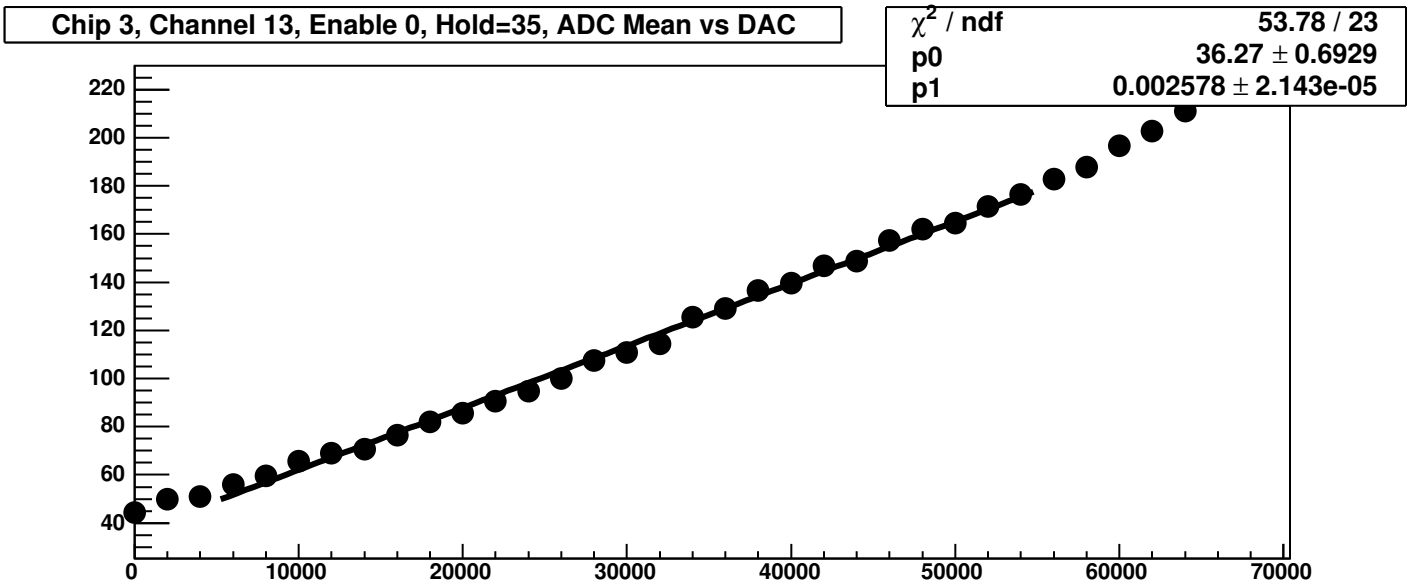

Chip 3, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

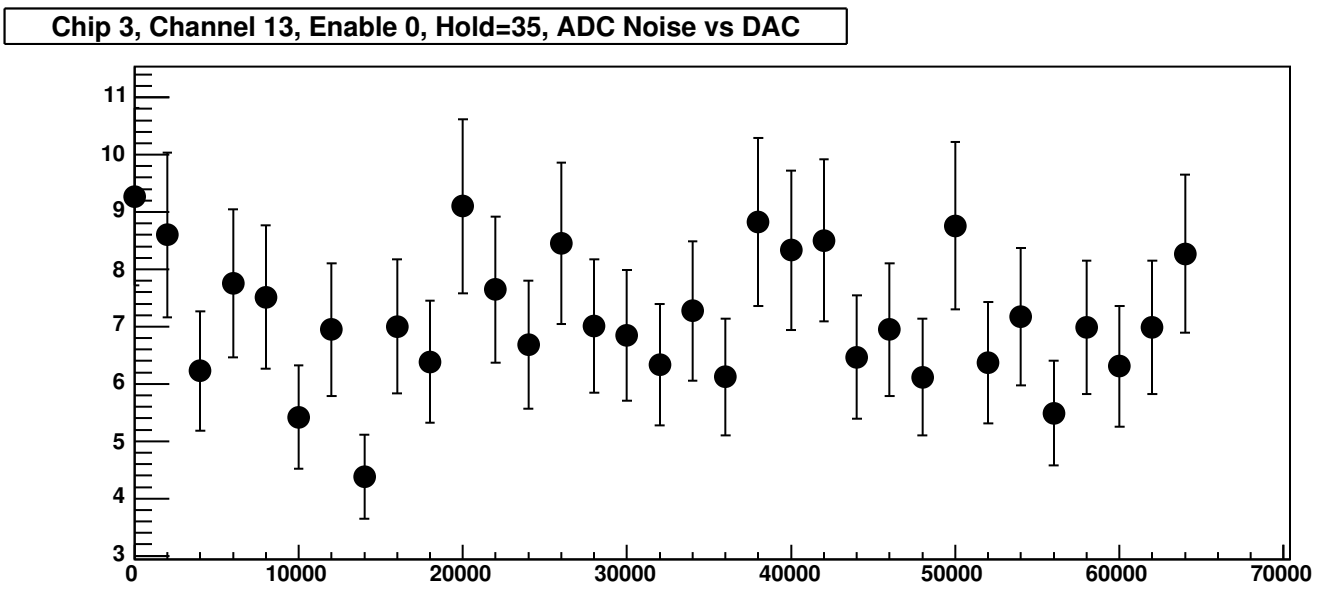

Chip 3, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
0
10000
20000
30000
40000
50000
60000
70000

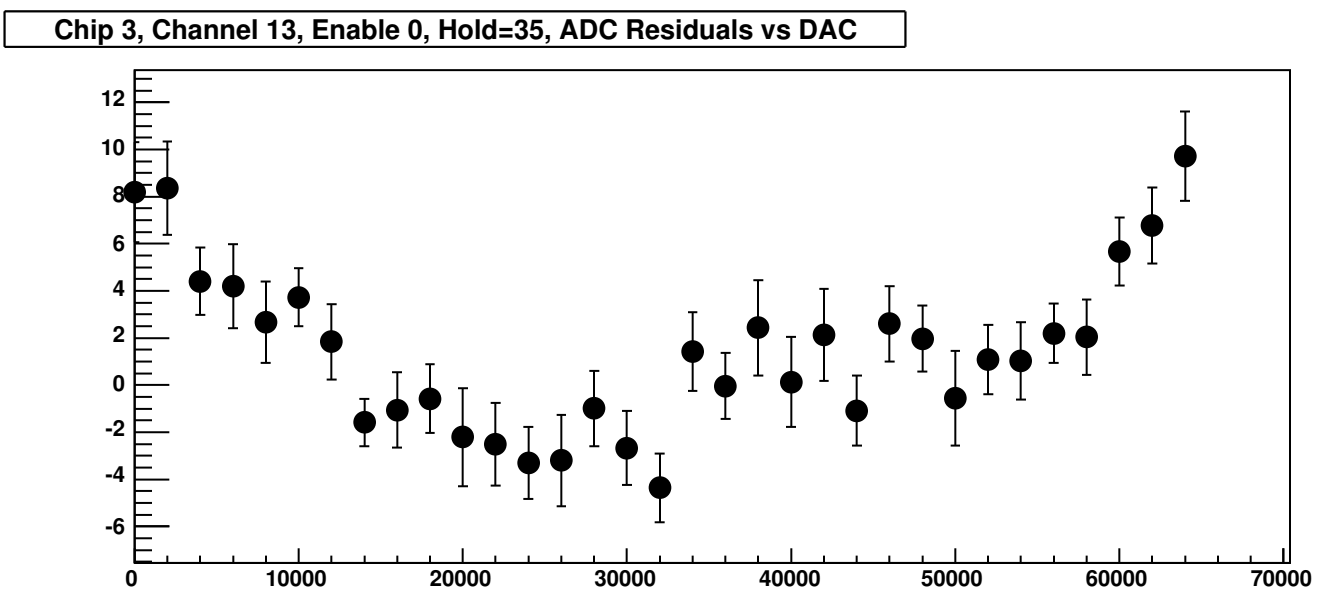

Chip 3, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.153e+04 / 23
p0 46.07 ± 1.009
p1 -0.06188 ± 5.209e-05
0
-1000
-2000
-3000
-4000
-5000
0
10000
20000
30000
40000
50000
60000

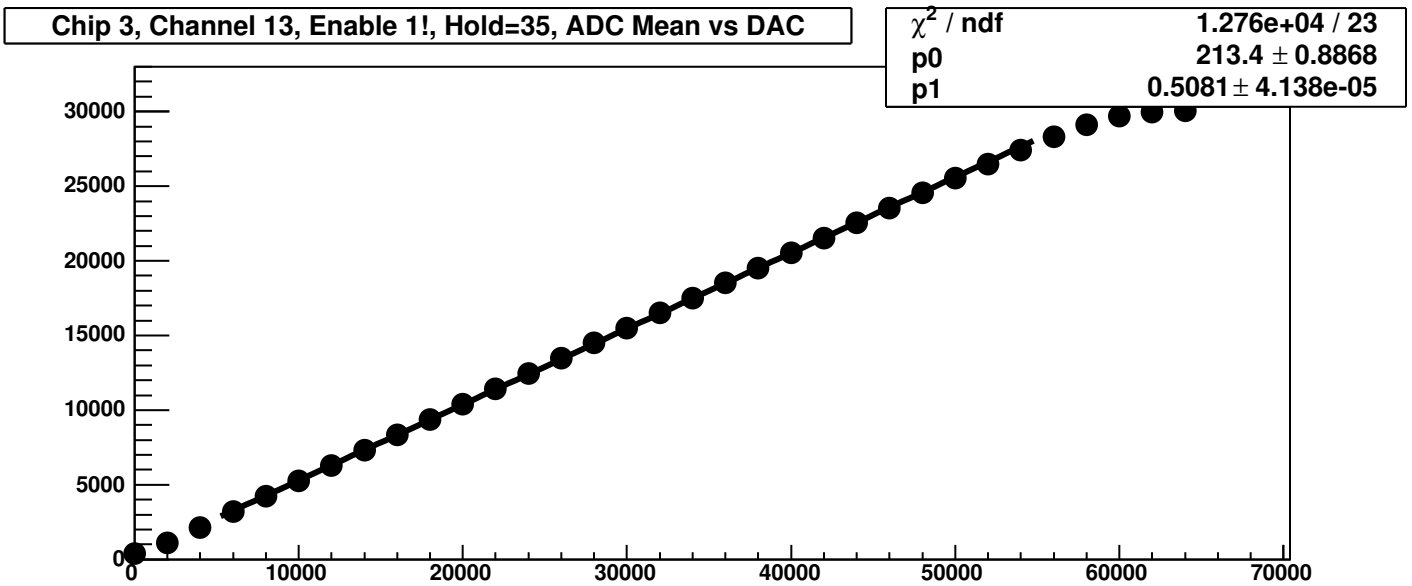

Chip 3, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

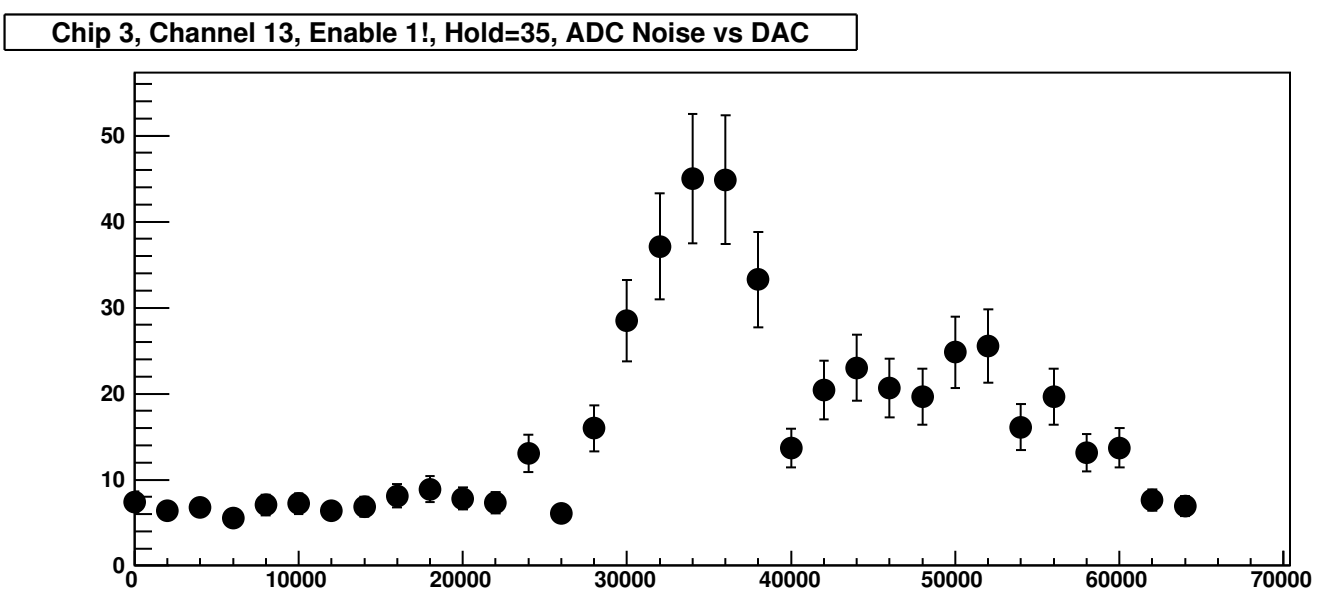

Chip 3, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
0
-200
-400
-600
-800
0
10000
20000
30000
40000
50000
60000

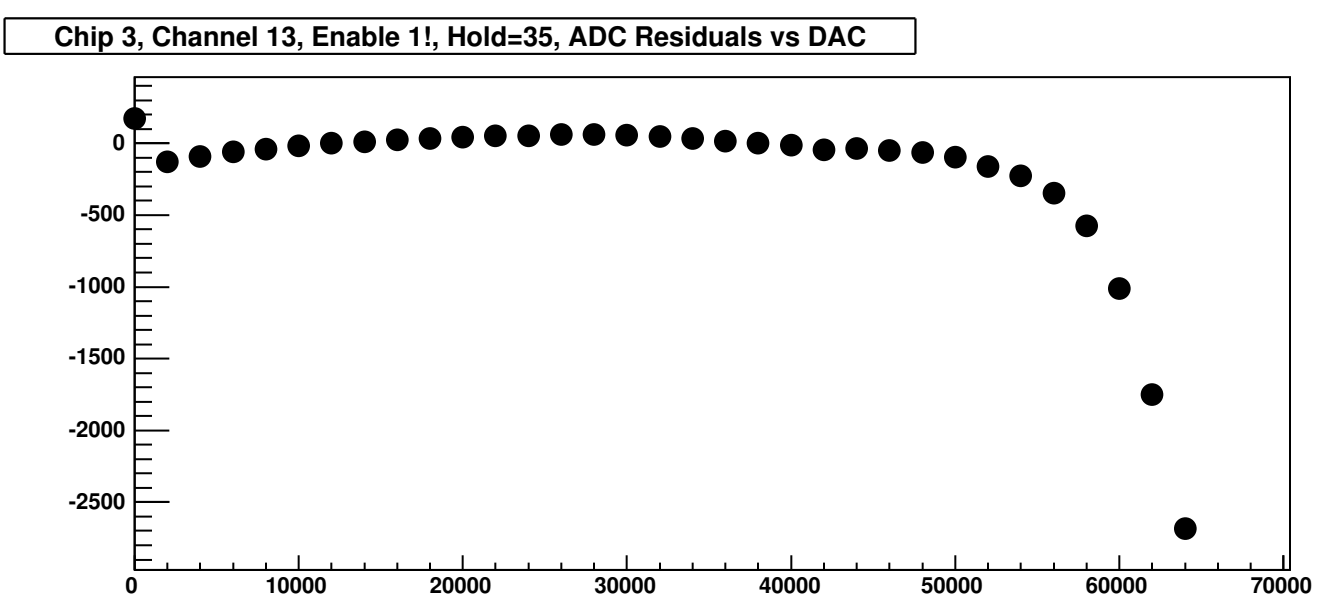

Chip 3, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 53.78 / 23
p0 36.27 ± 0.6929
p1 0.002578 ± 2.143e-05
220
200
180
160
140
120
100
80
60
40
0
10000
20000
30000
40000
50000
60000
70000

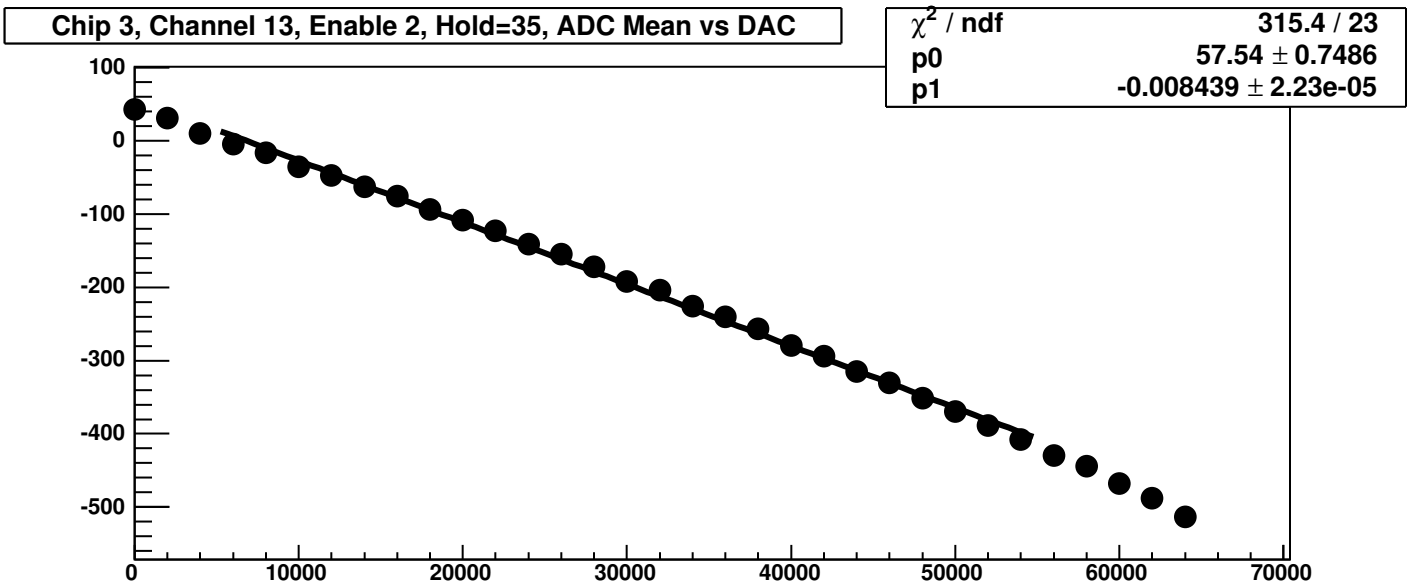

Chip 3, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

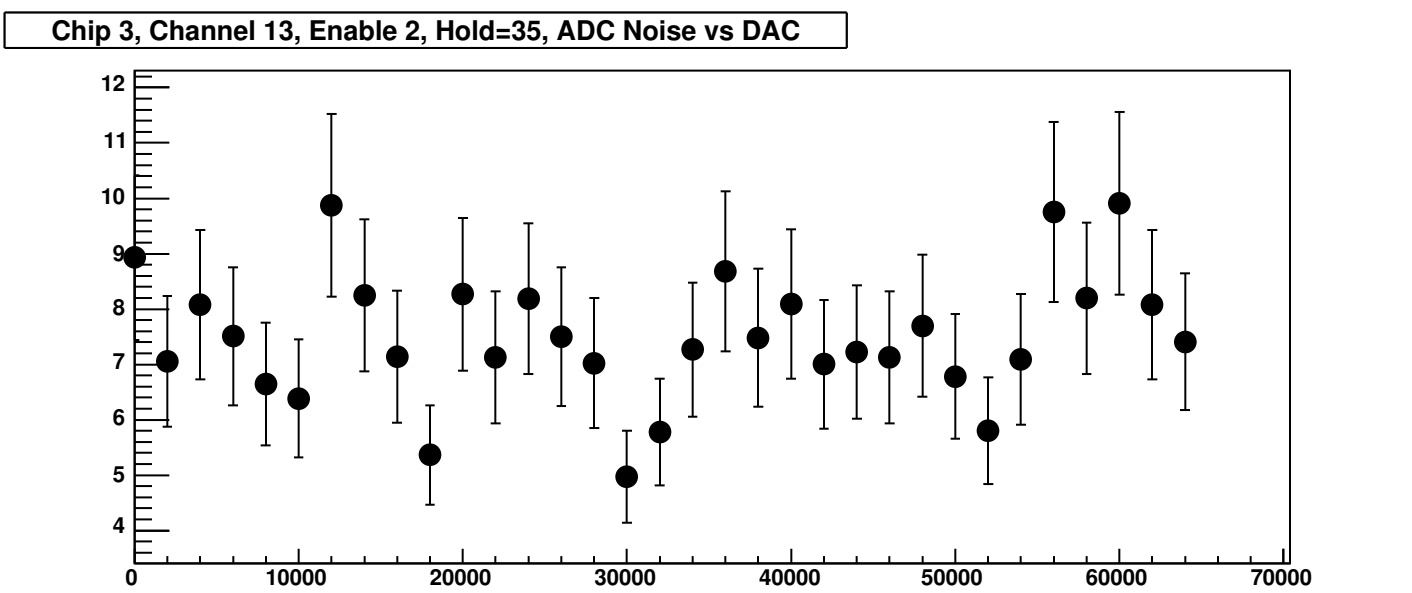

Chip 3, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

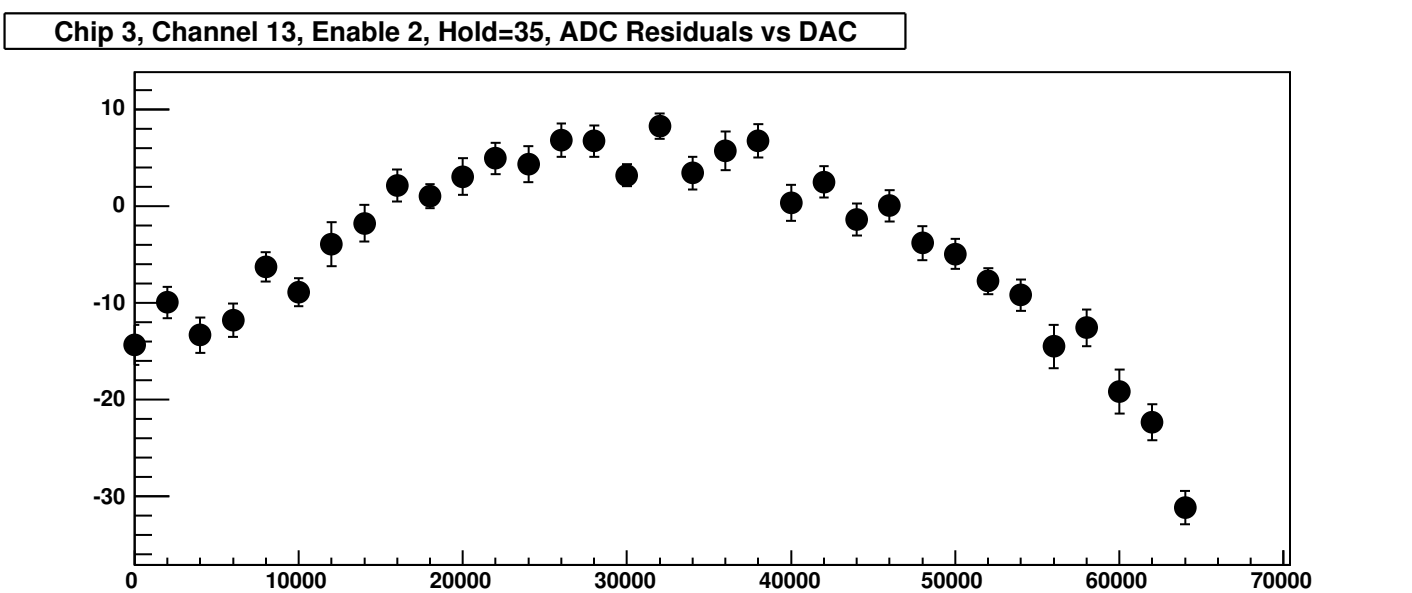

Chip 3, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 1.276e+04 / 23
p0 213.4 ± 0.8868
p1 0.5081 ± 4.138e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

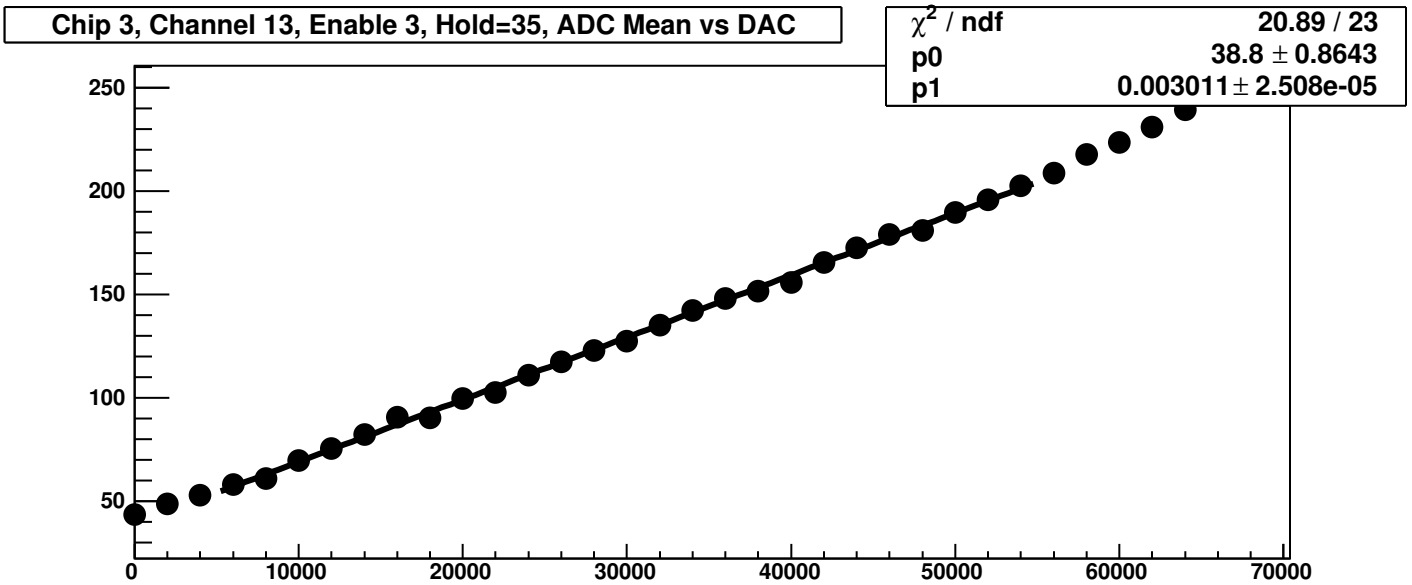

Chip 3, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

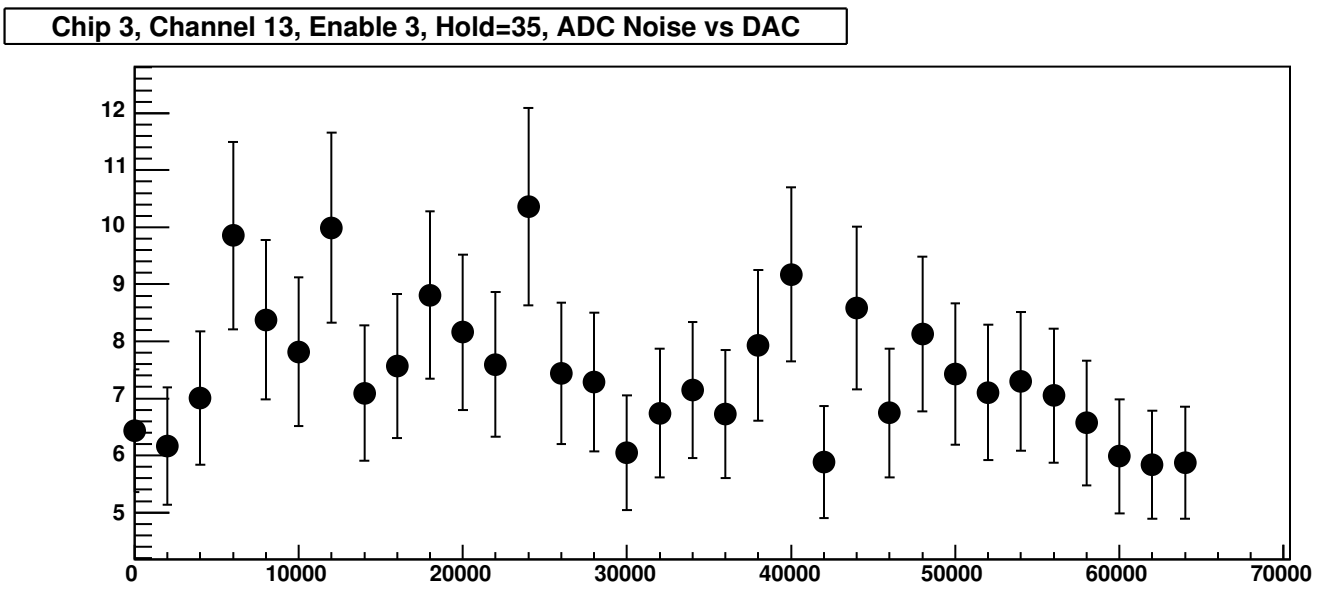

Chip 3, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
0
10000
20000
30000
40000
50000
60000
70000

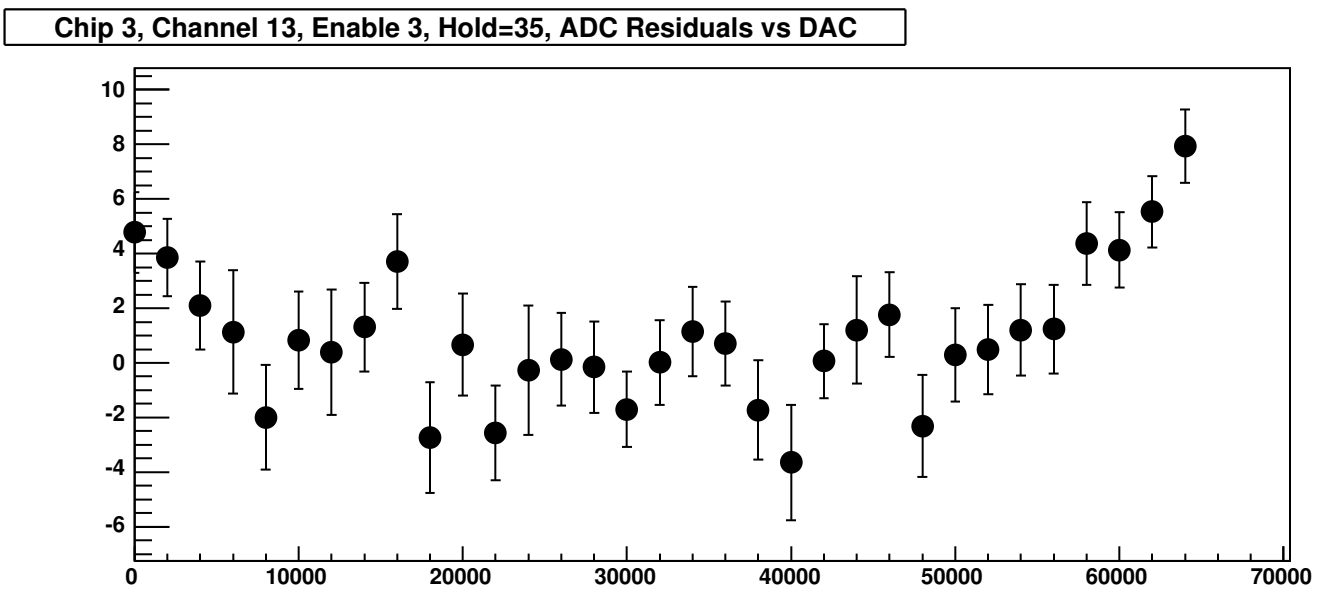

Chip 3, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 315.4 / 23
p0 57.54 ± 0.7486
p1 -0.008439 ± 2.23e-05
100
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000
70000

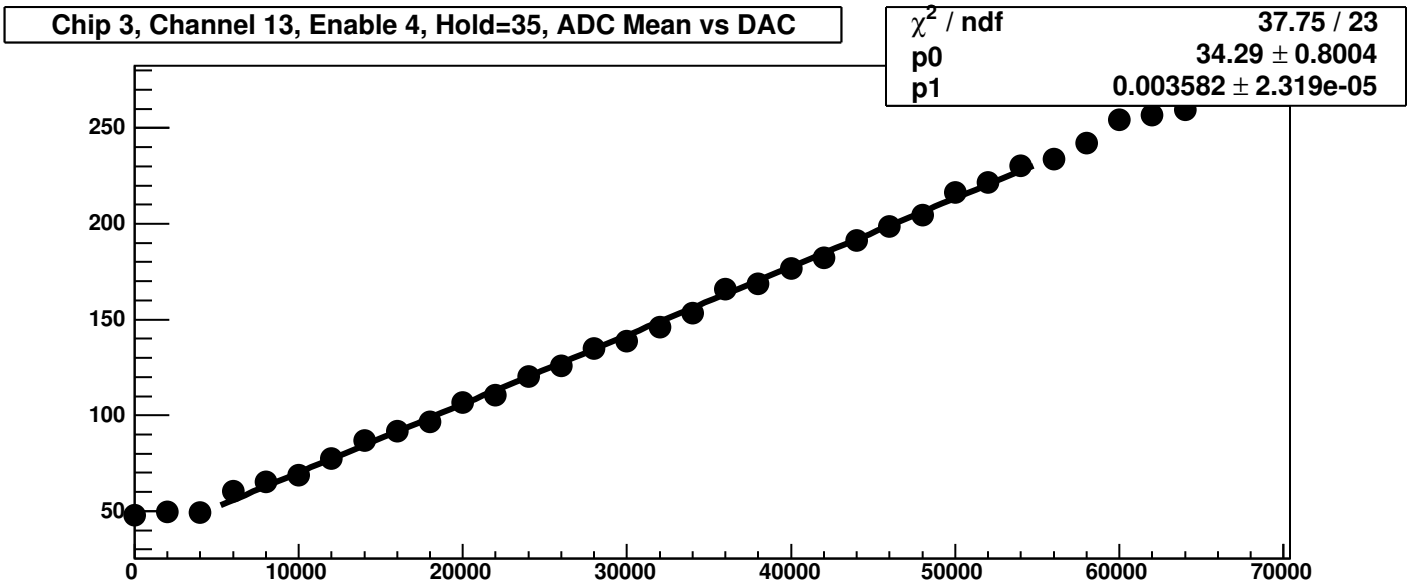

Chip 3, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

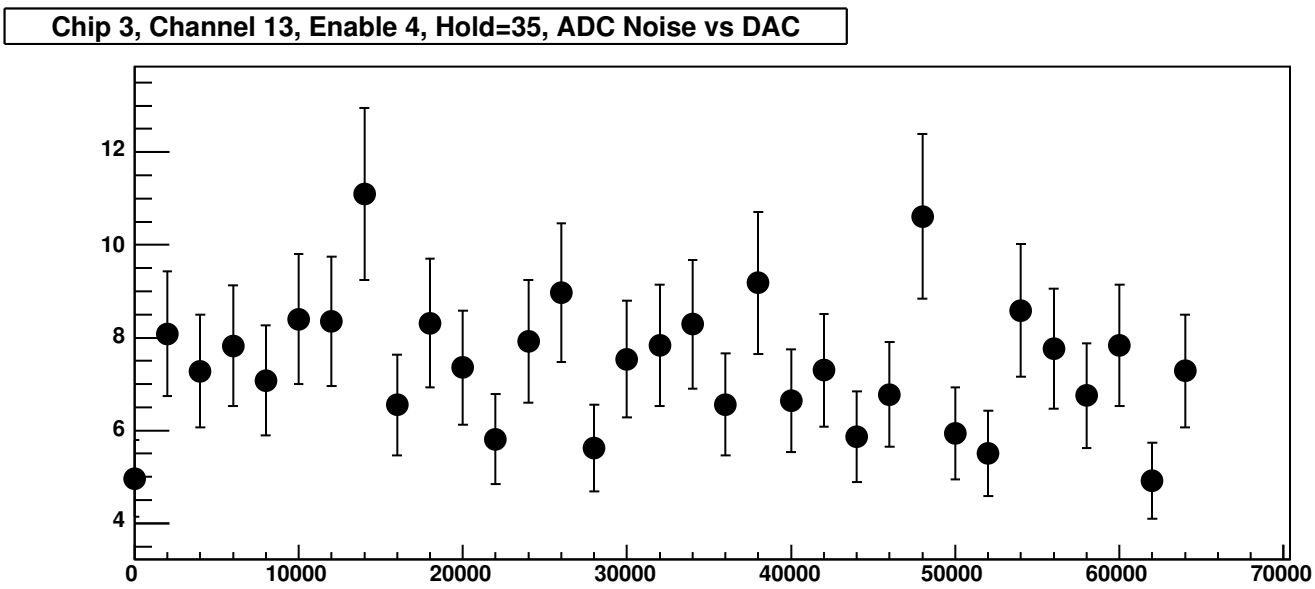

Chip 3, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
10000
20000
30000
40000
50000
60000
70000

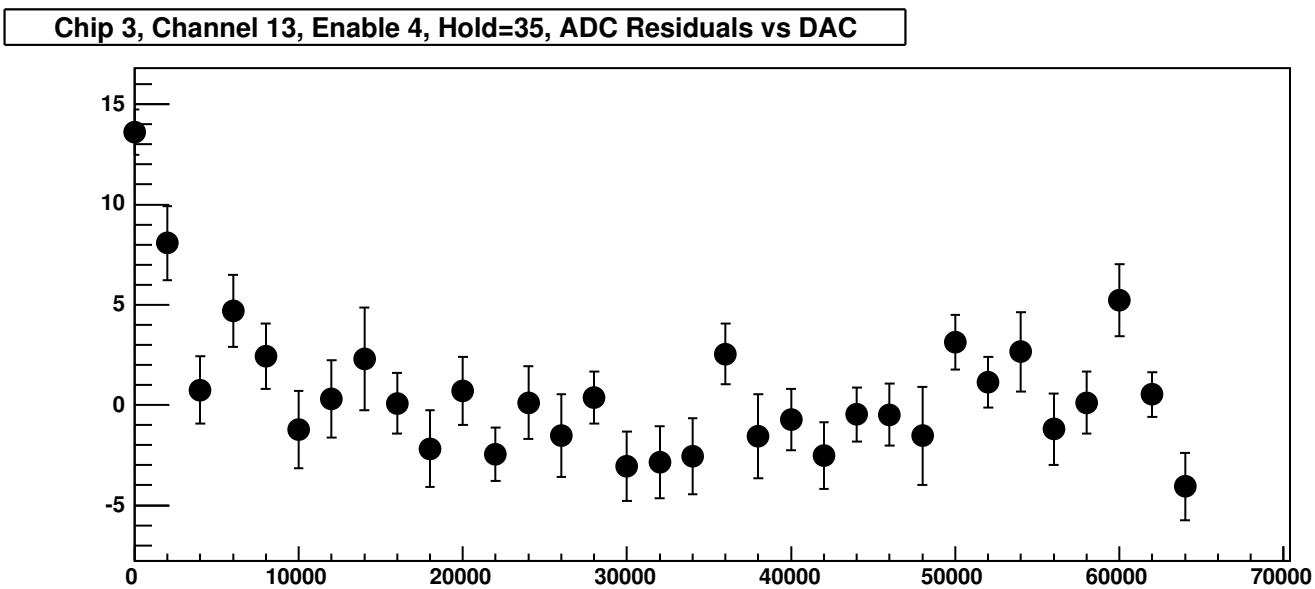

Chip 3, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 20.89 / 23
p0 38.8 ± 0.8643
p1 0.003011 ± 2.508e-05
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

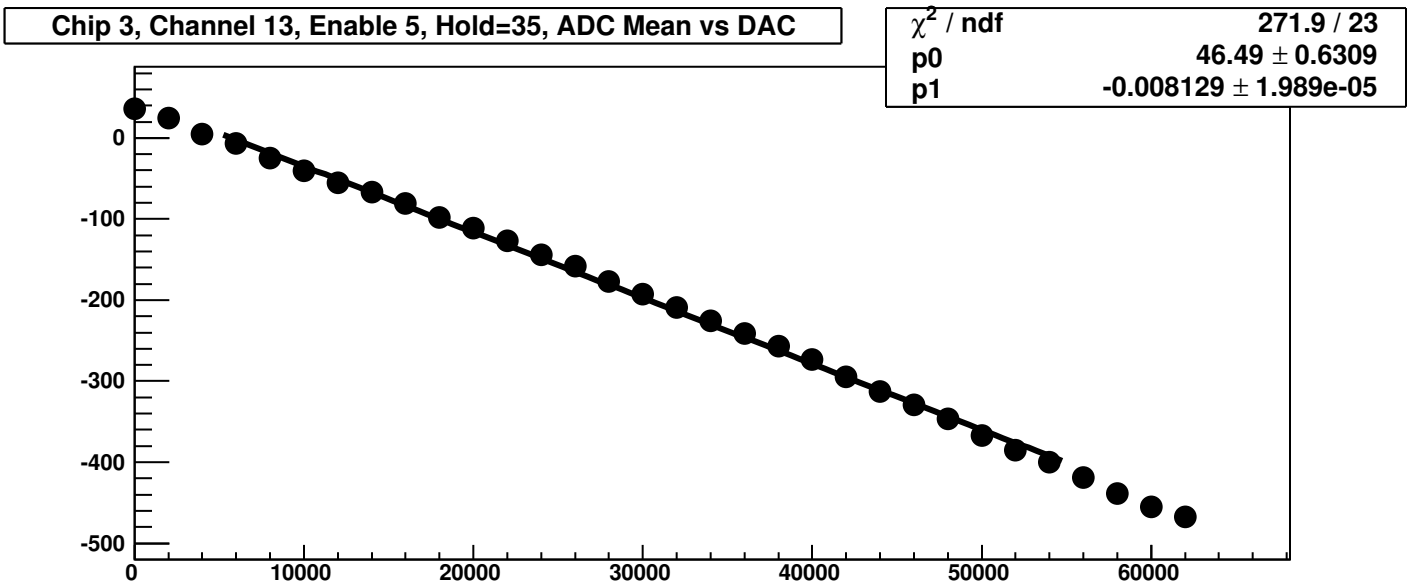

Chip 3, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000

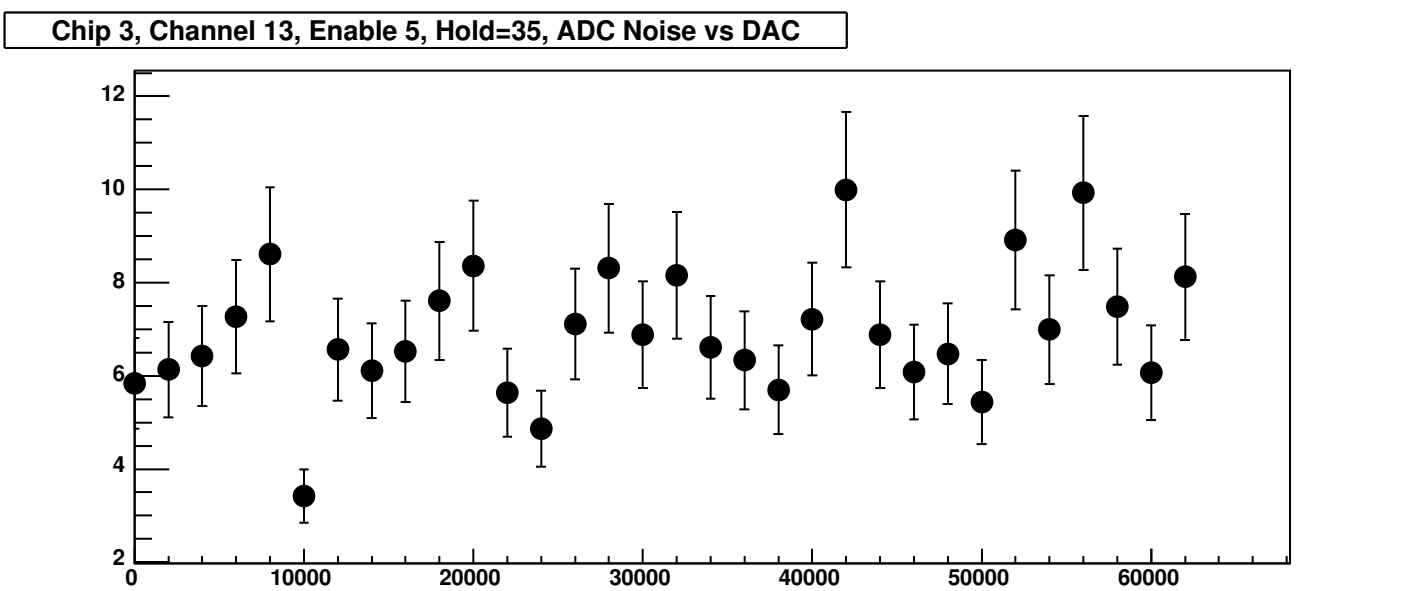

Chip 3, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

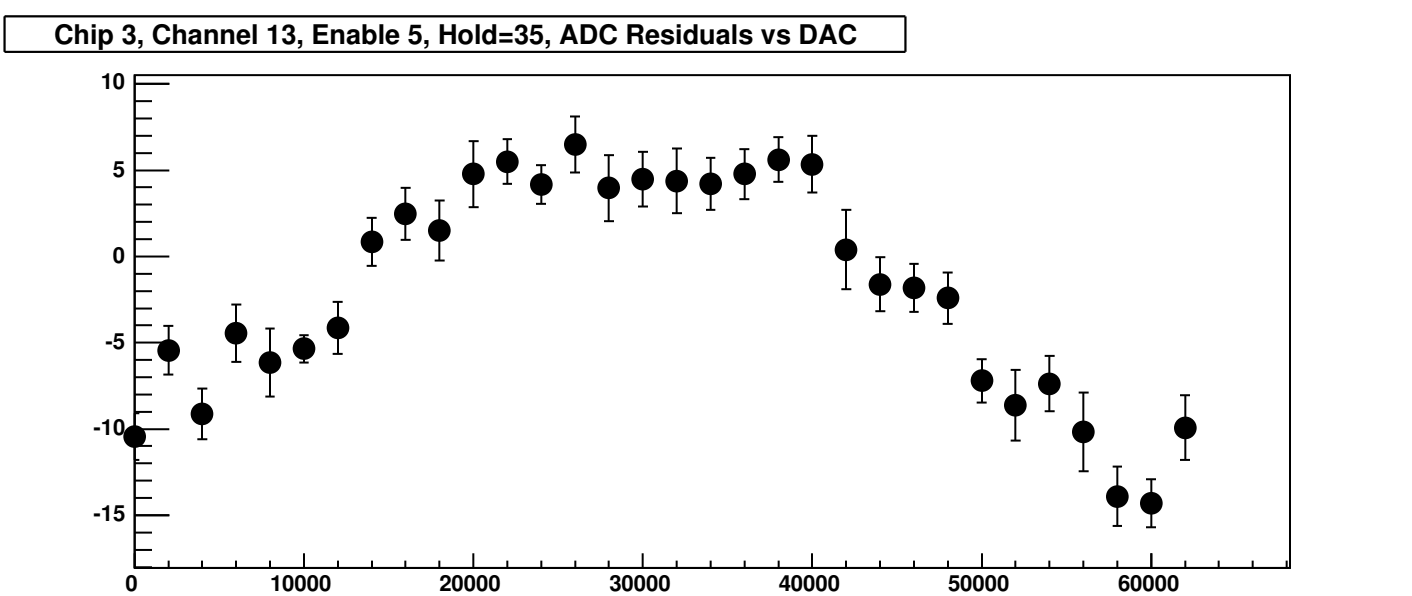

Chip 3, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 37.75 / 23
p0 34.29 ± 0.8004
p1 0.003582 ± 2.319e-05
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

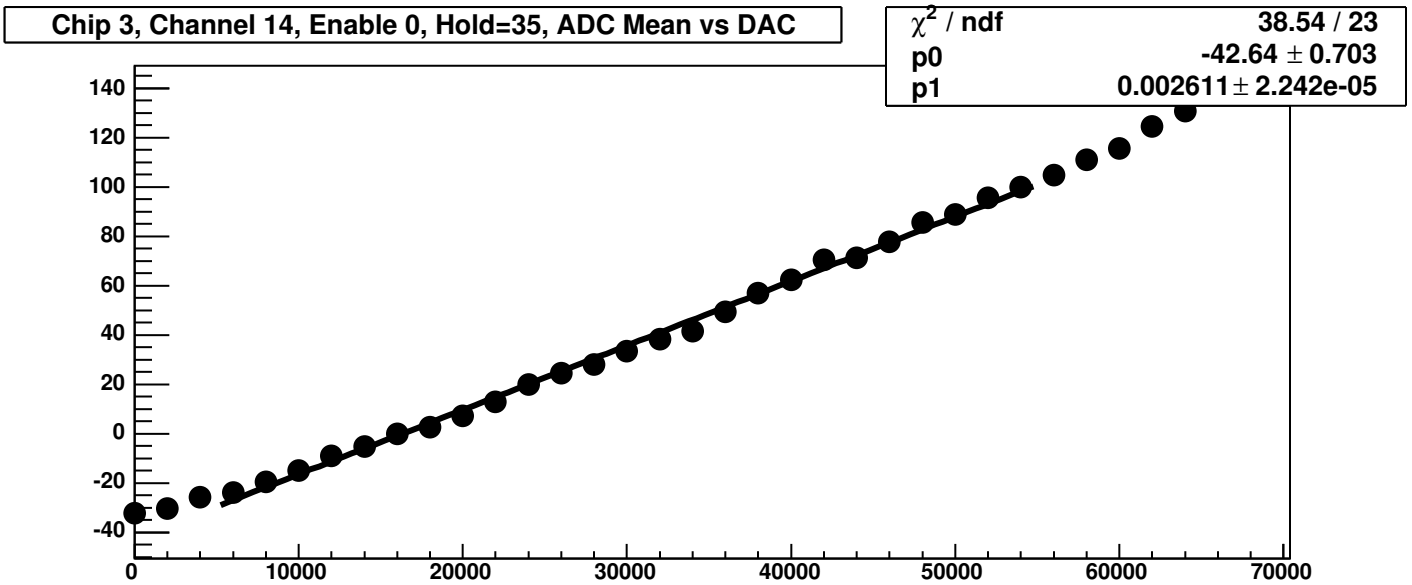

Chip 3, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

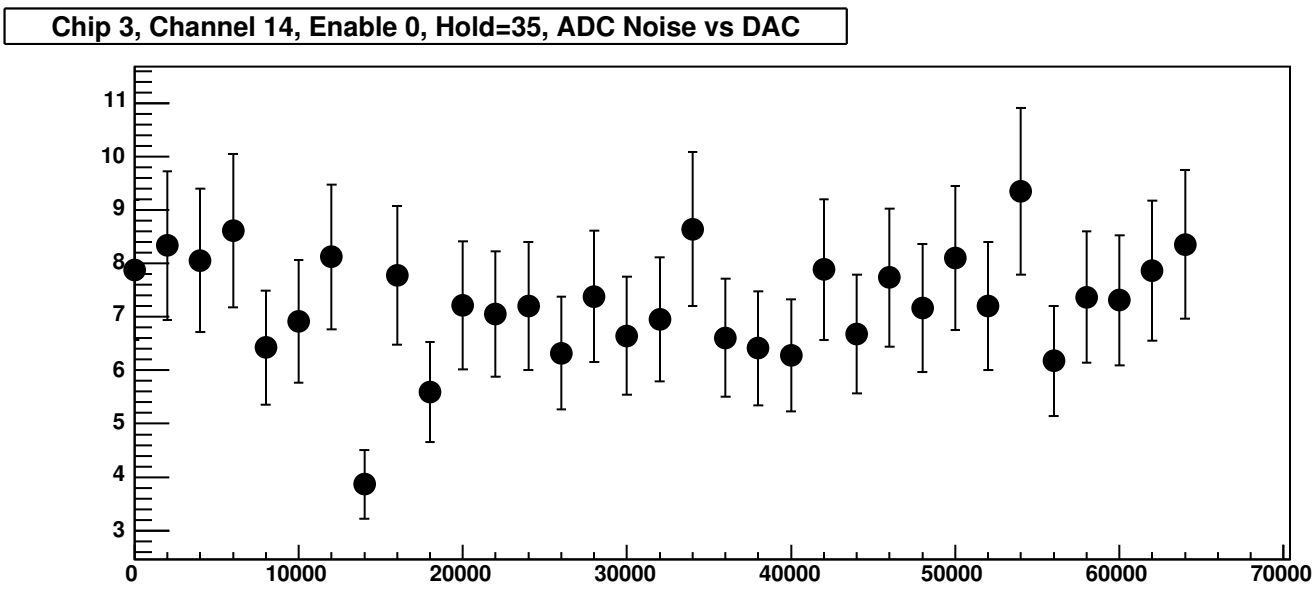

Chip 3, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

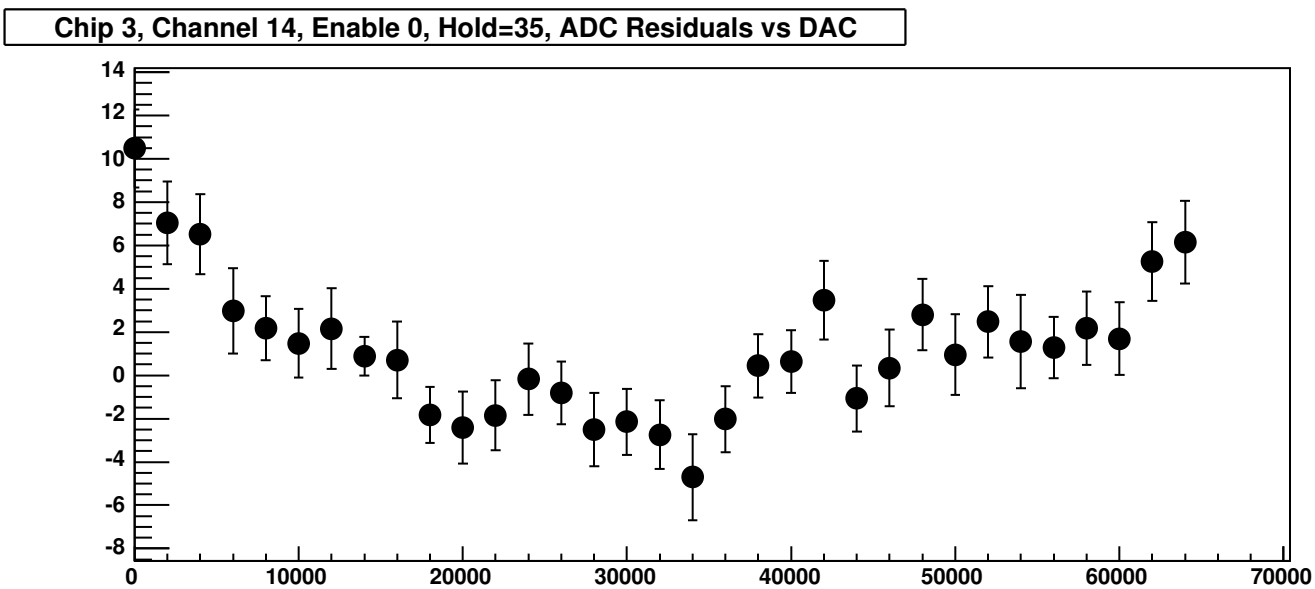

Chip 3, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 271.9 / 23
p0 46.49 ± 0.6309
p1 -0.008129 ± 1.989e-05
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000

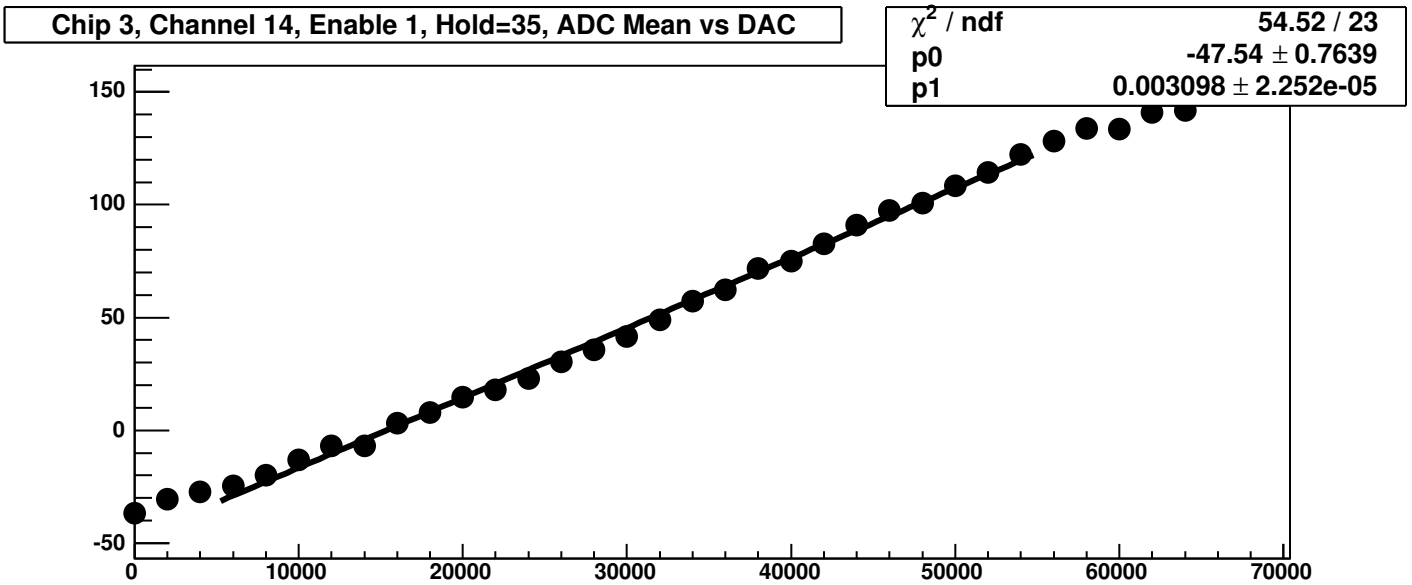

Chip 3, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
2
0
10000
20000
30000
40000
50000
60000

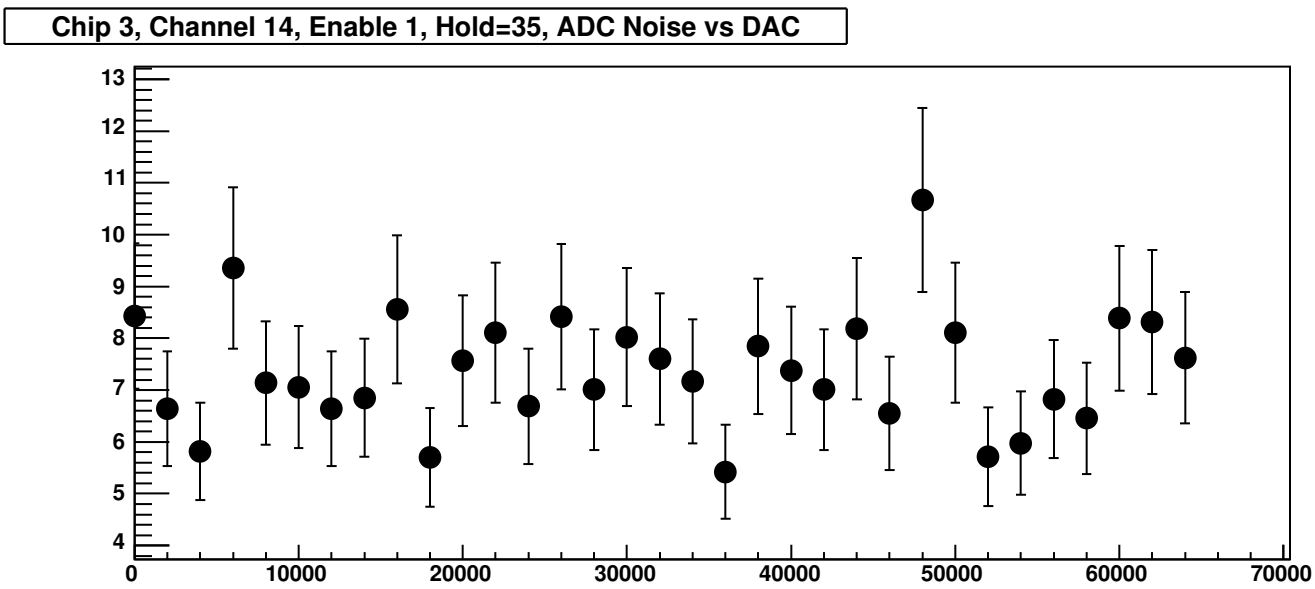

Chip 3, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000

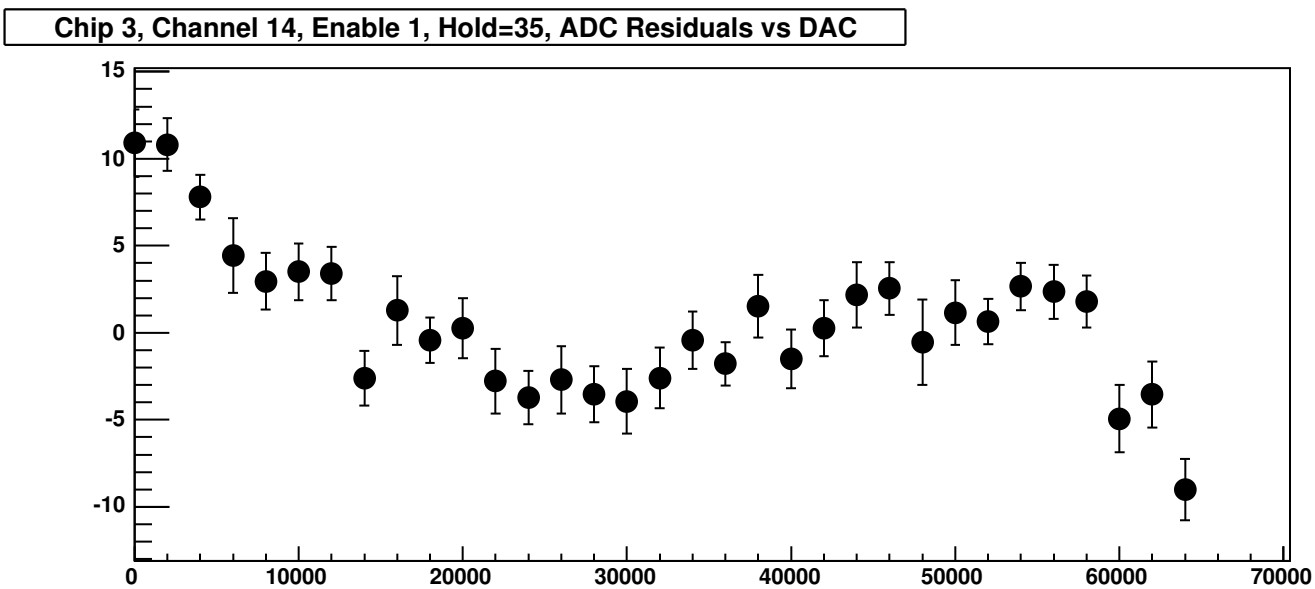

Chip 3, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 38.54 / 23
p0 -42.64 ± 0.703
p1 0.002611 ± 2.242e-05
140
120
100
80
60
40
20
0
-20
-40
0
10000
20000
30000
40000
50000
60000
70000

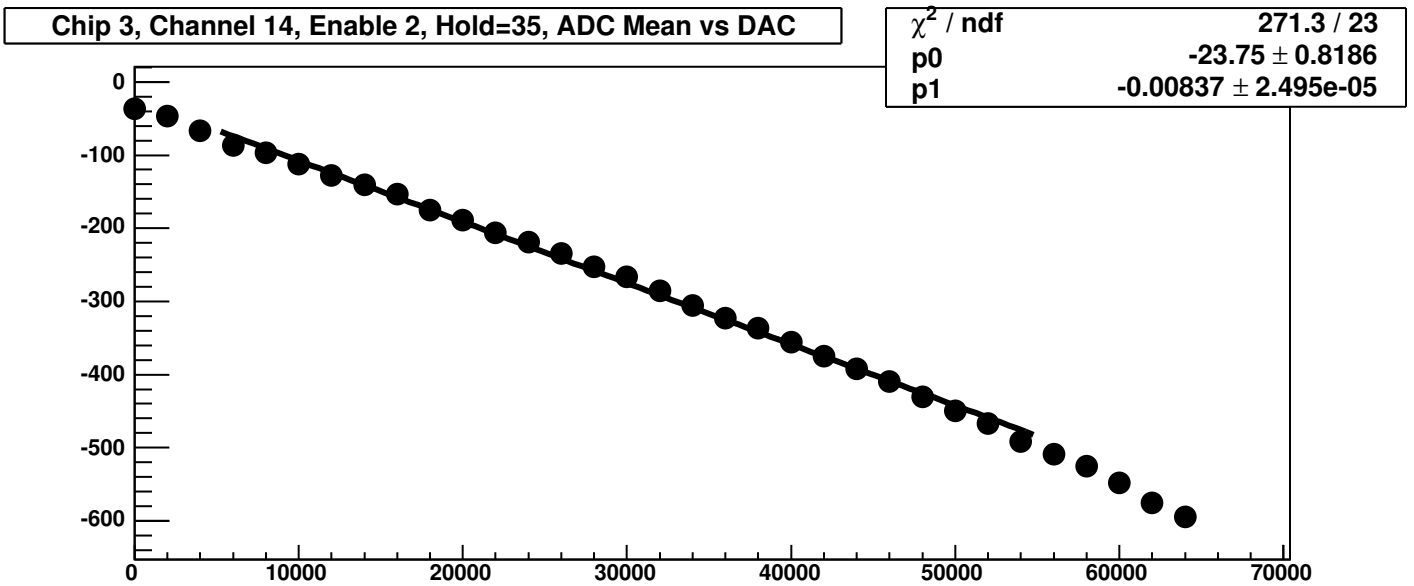

Chip 3, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

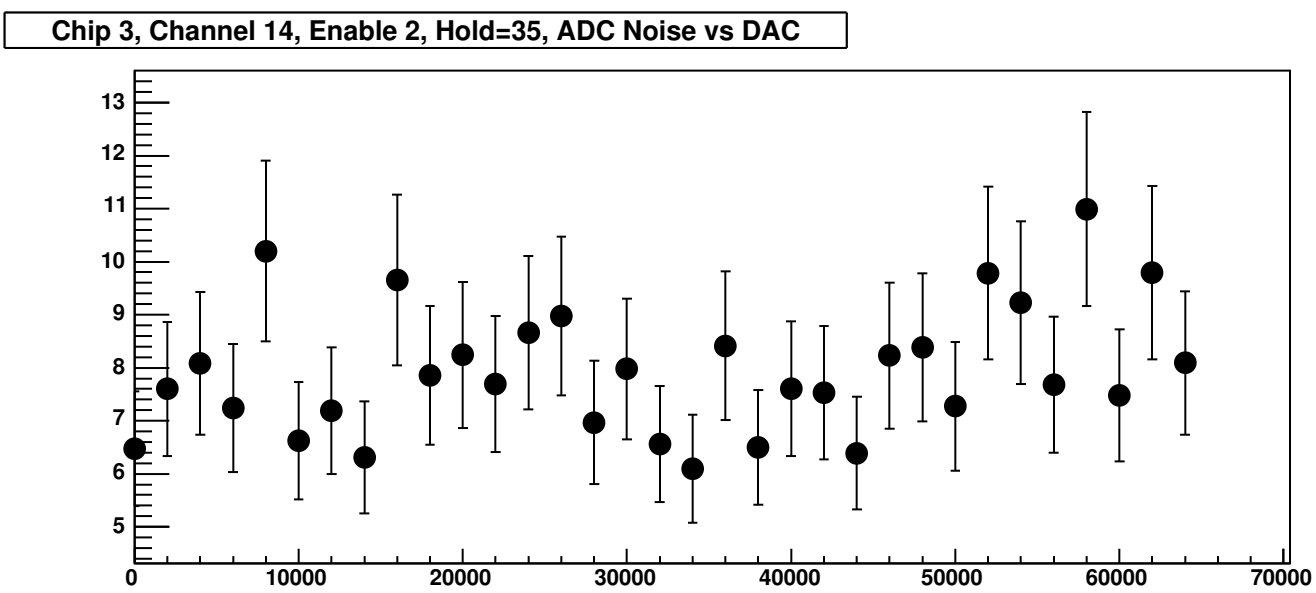

Chip 3, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

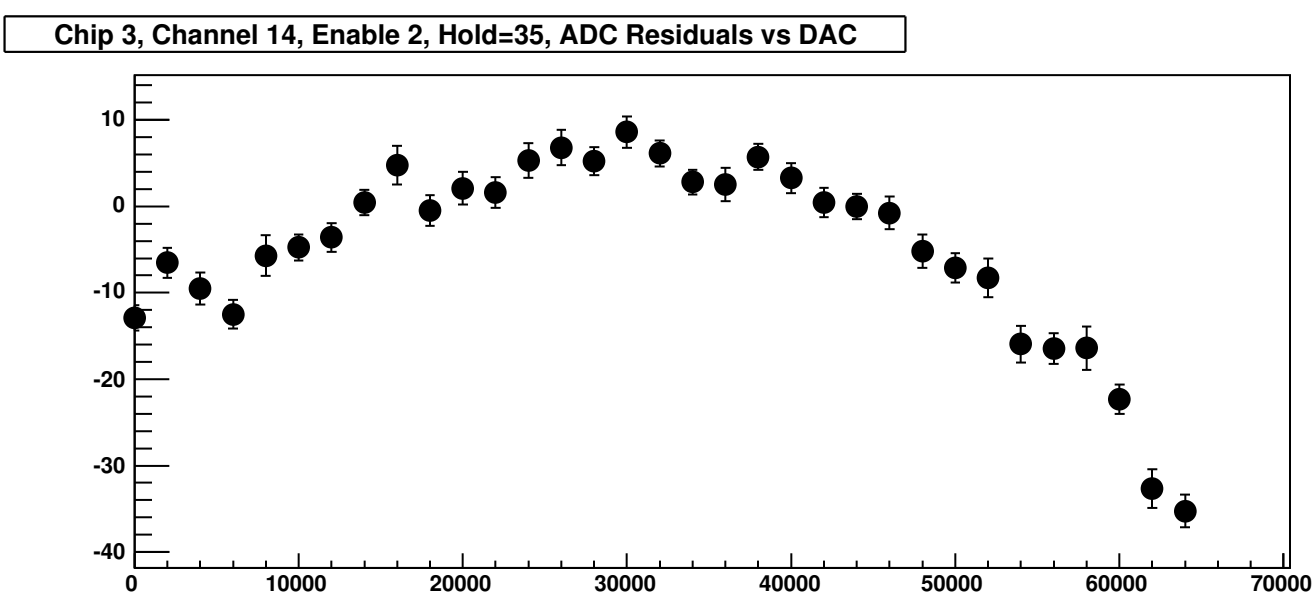

Chip 3, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 54.52 / 23
p0 -47.54 ± 0.7639
p1 0.003098 ± 2.252e-05
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000

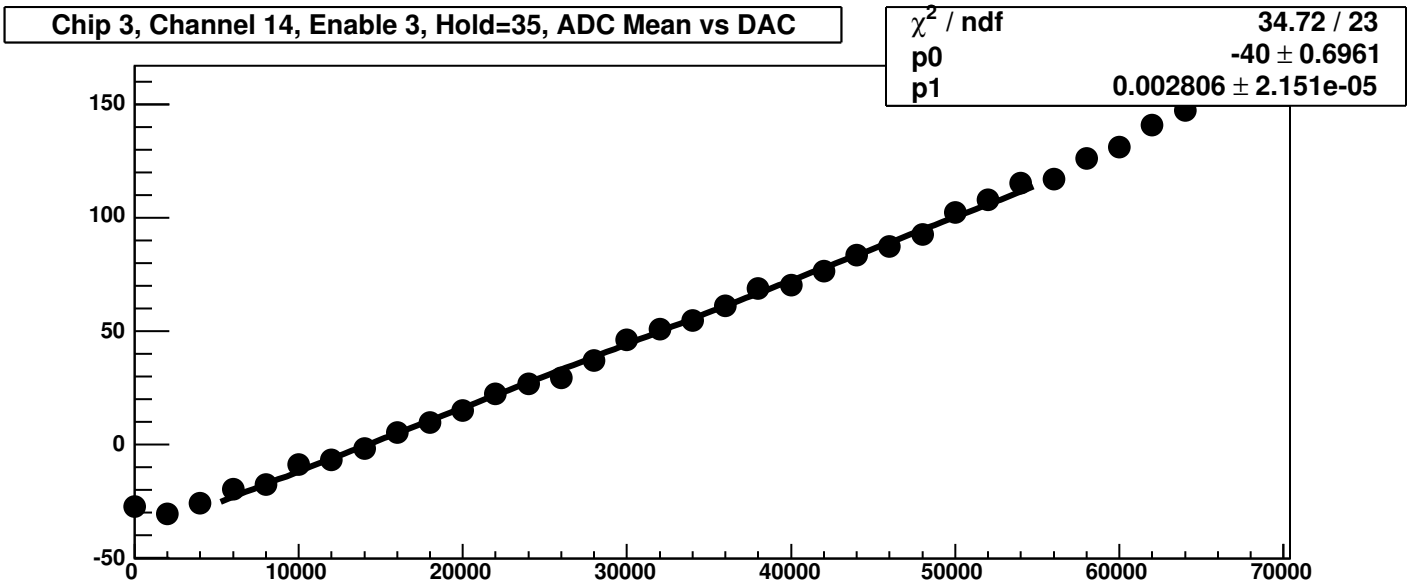

Chip 3, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

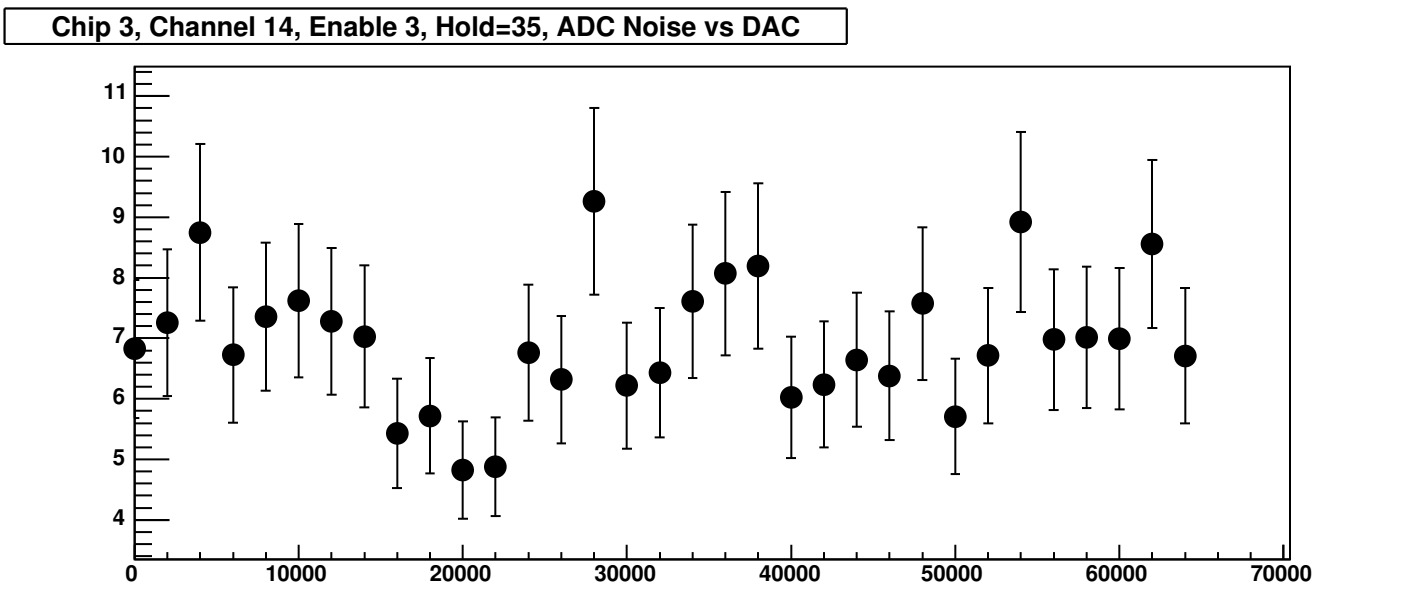

Chip 3, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

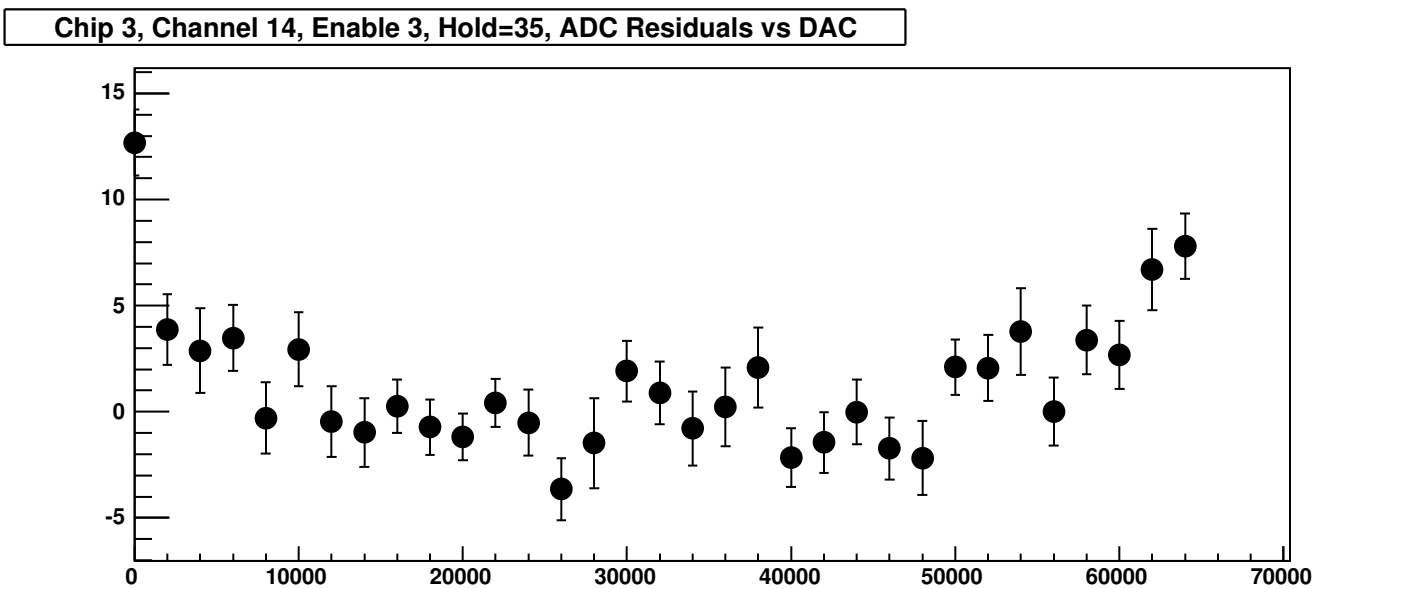

Chip 3, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 271.3 / 23
p0 -23.75 ± 0.8186
p1 -0.00837 ± 2.495e-05
0
-100
-200
-300
-400
-500
-600
0
10000
20000
30000
40000
50000
60000
70000

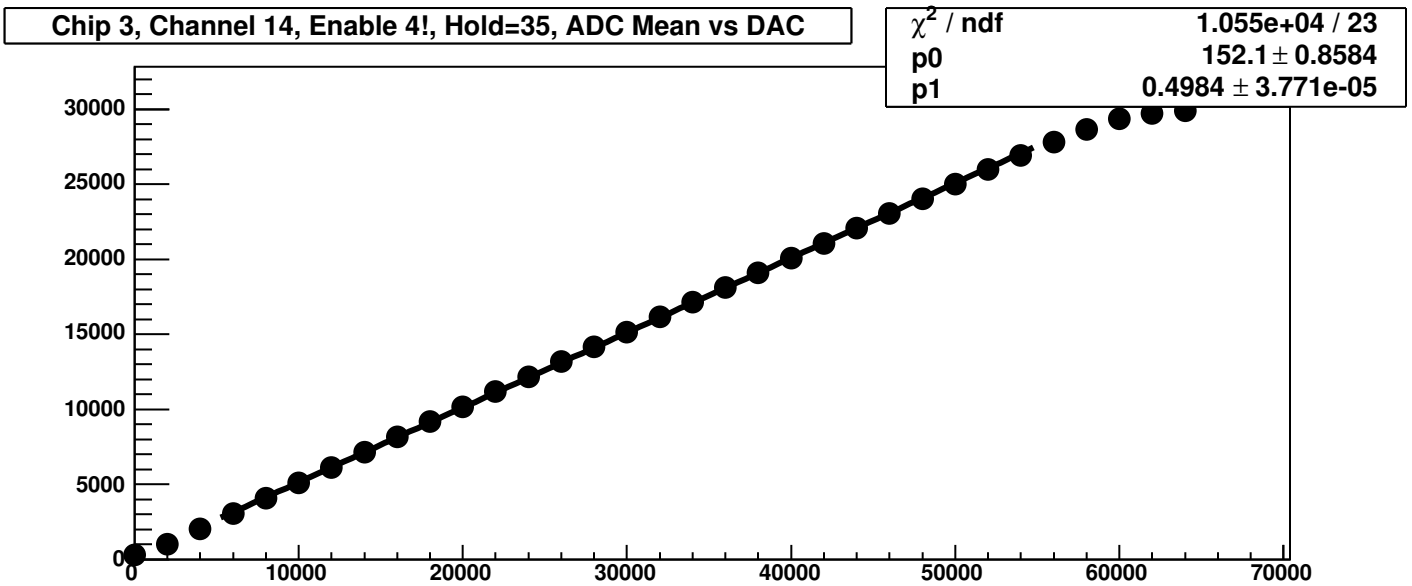

Chip 3, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000

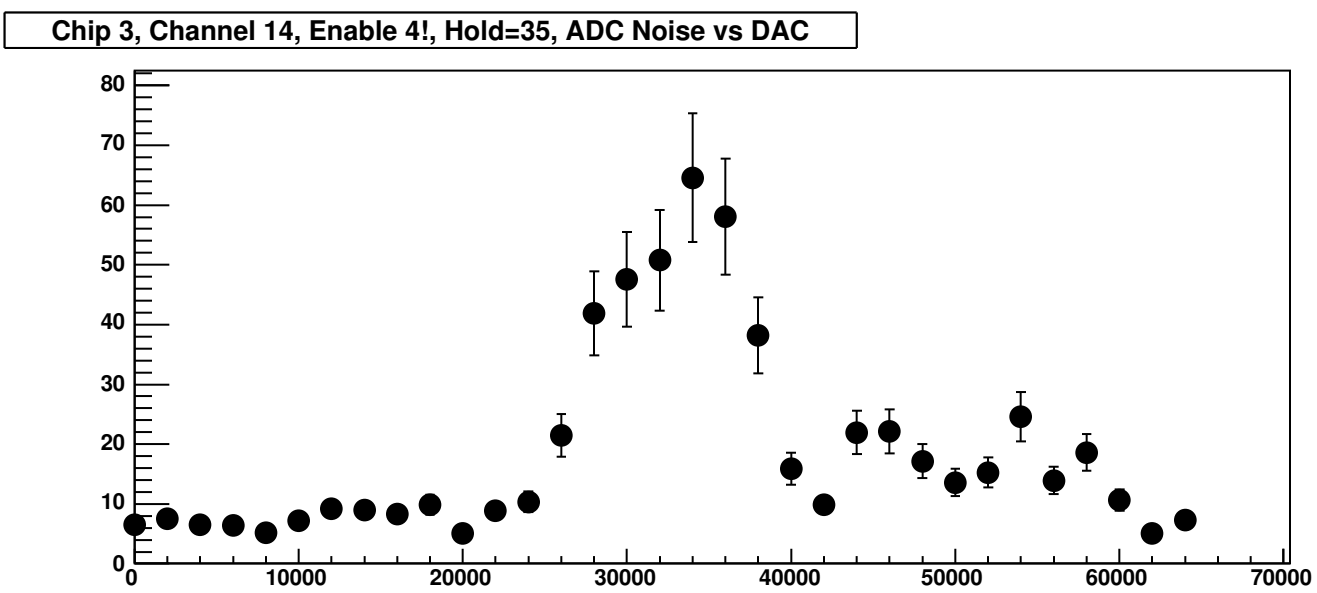

Chip 3, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
10000
20000
30000
40000
50000
60000
70000

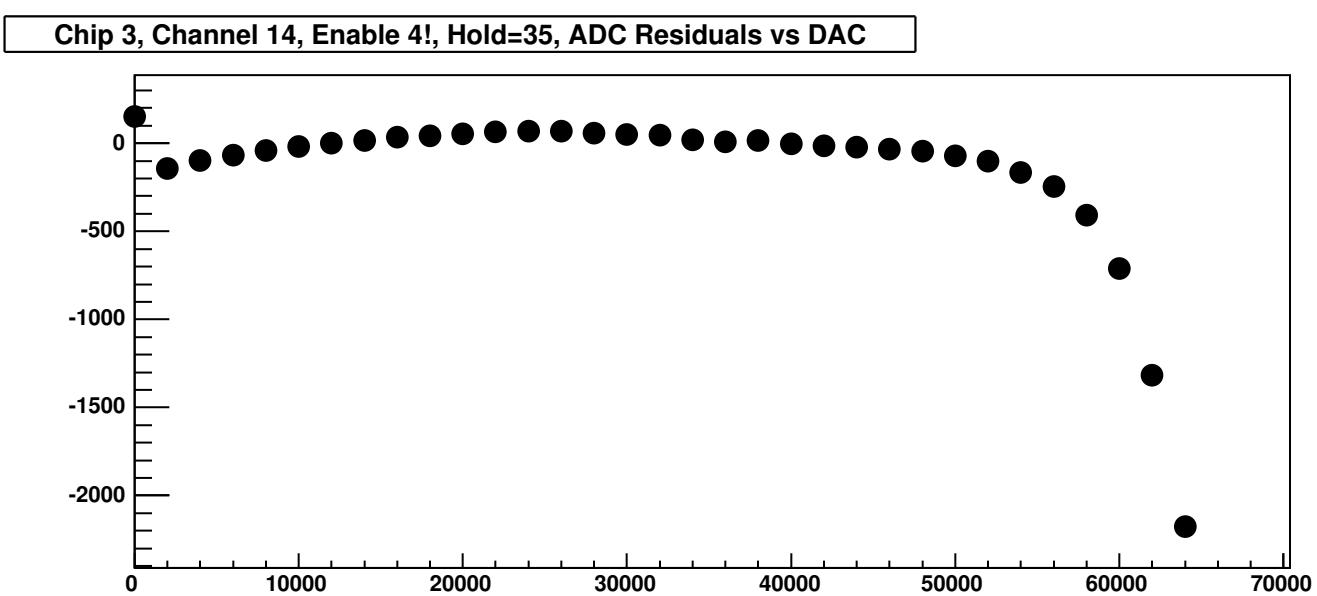

Chip 3, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 34.72 / 23
p0 -40 ± 0.6961
p1 0.002806 ± 2.151e-05
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000

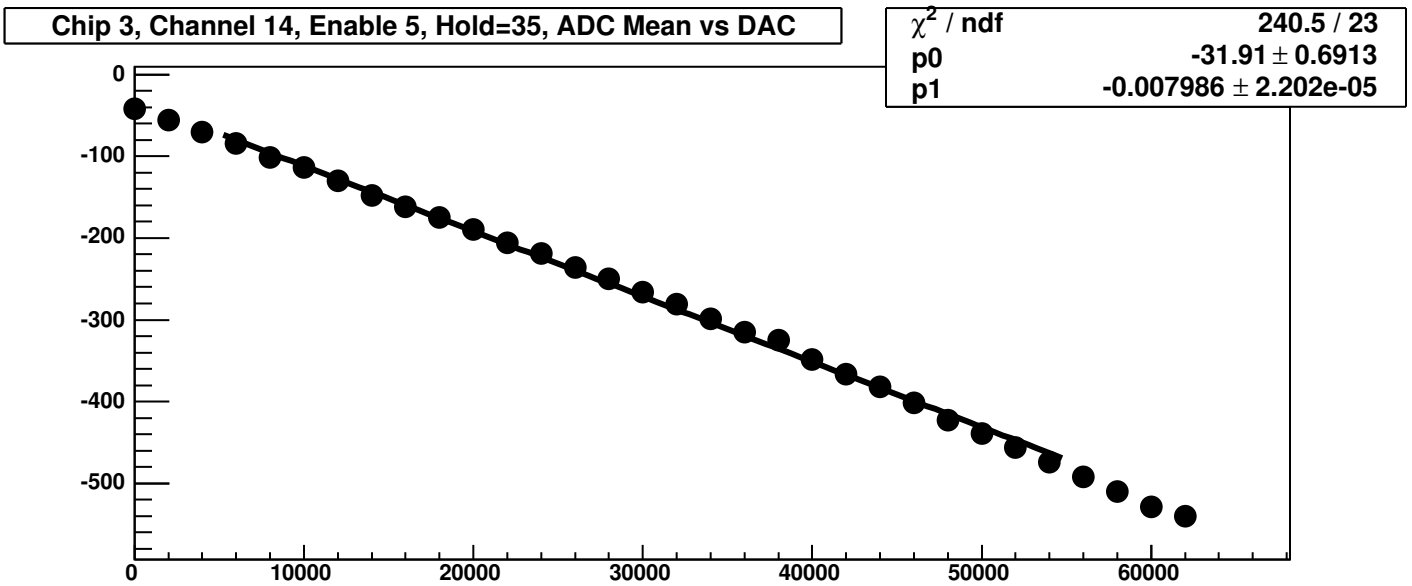

Chip 3, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

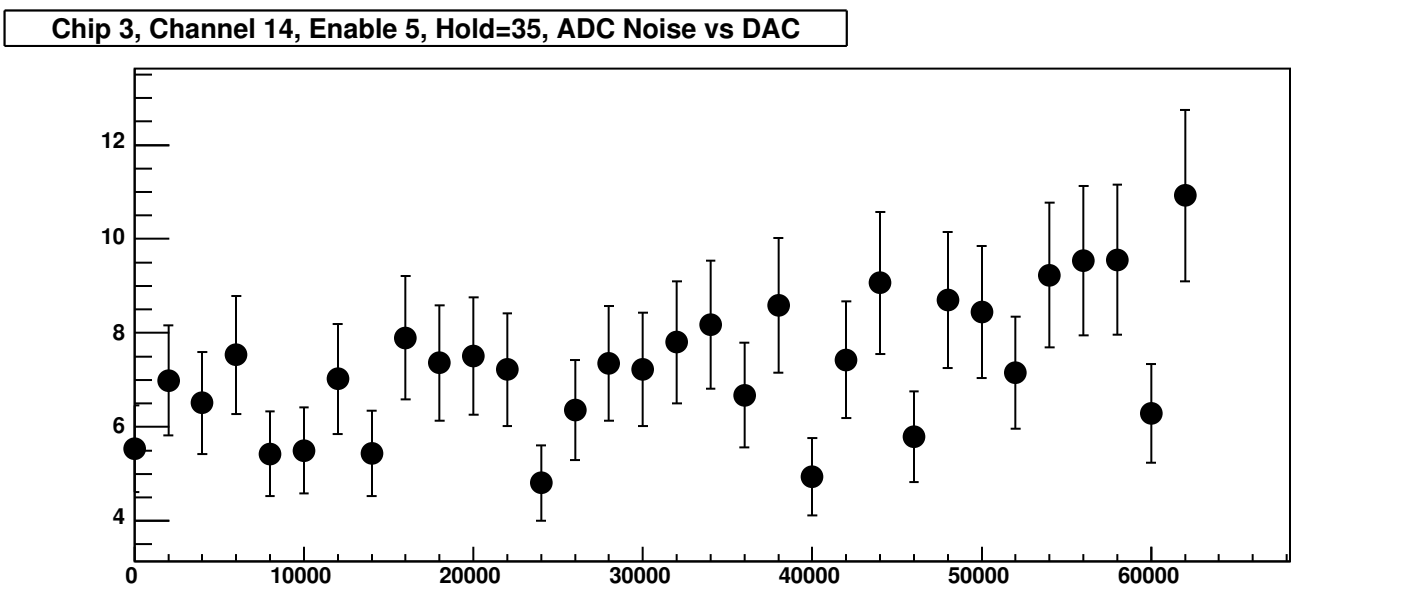

Chip 3, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

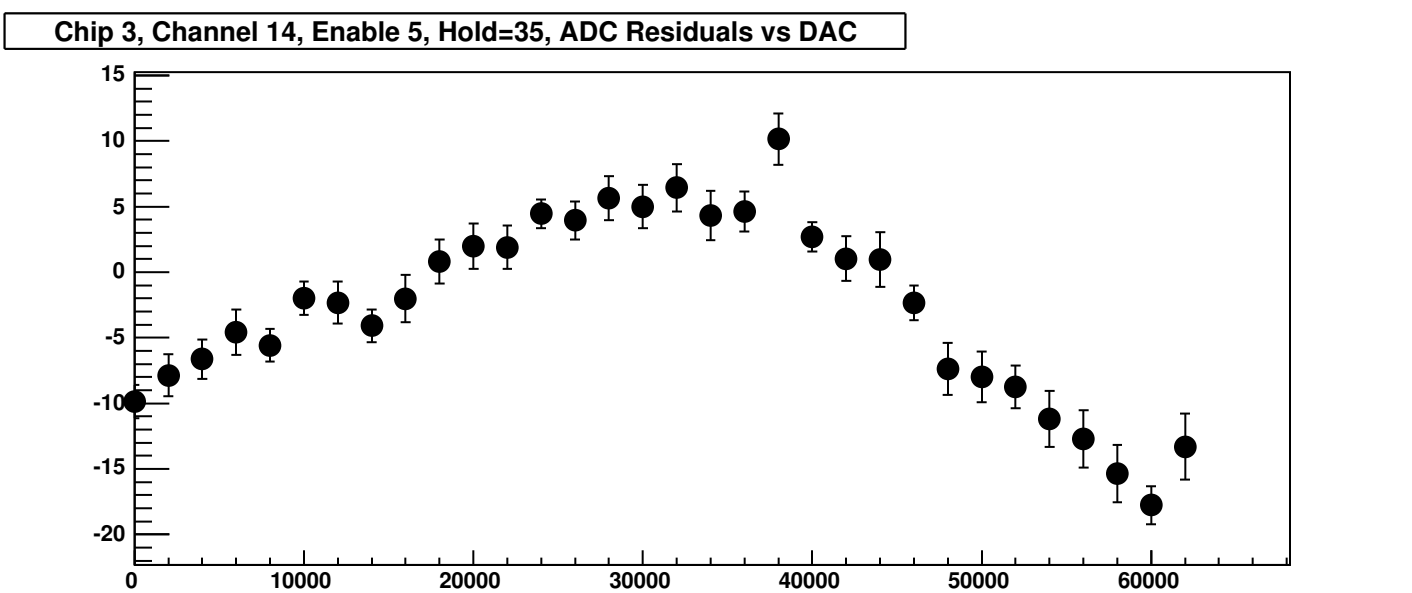

Chip 3, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 1.055e+04 / 23
p0 152.1 ± 0.8584
p1 0.4984 ± 3.771e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

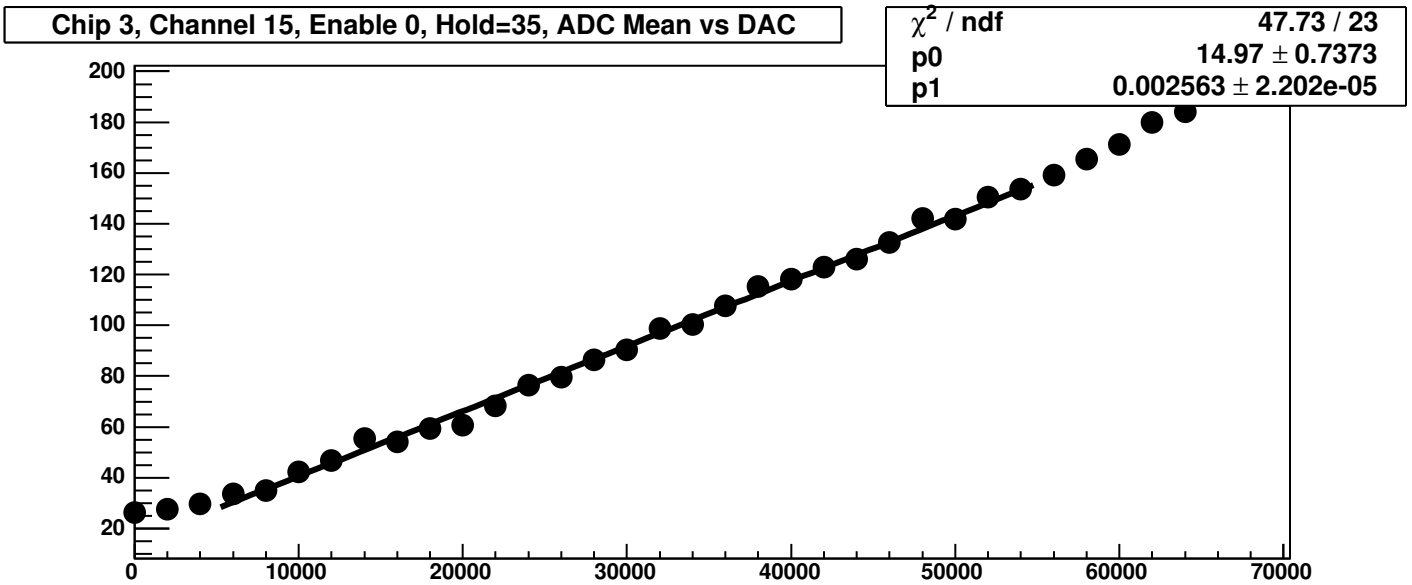

Chip 3, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
80
70
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

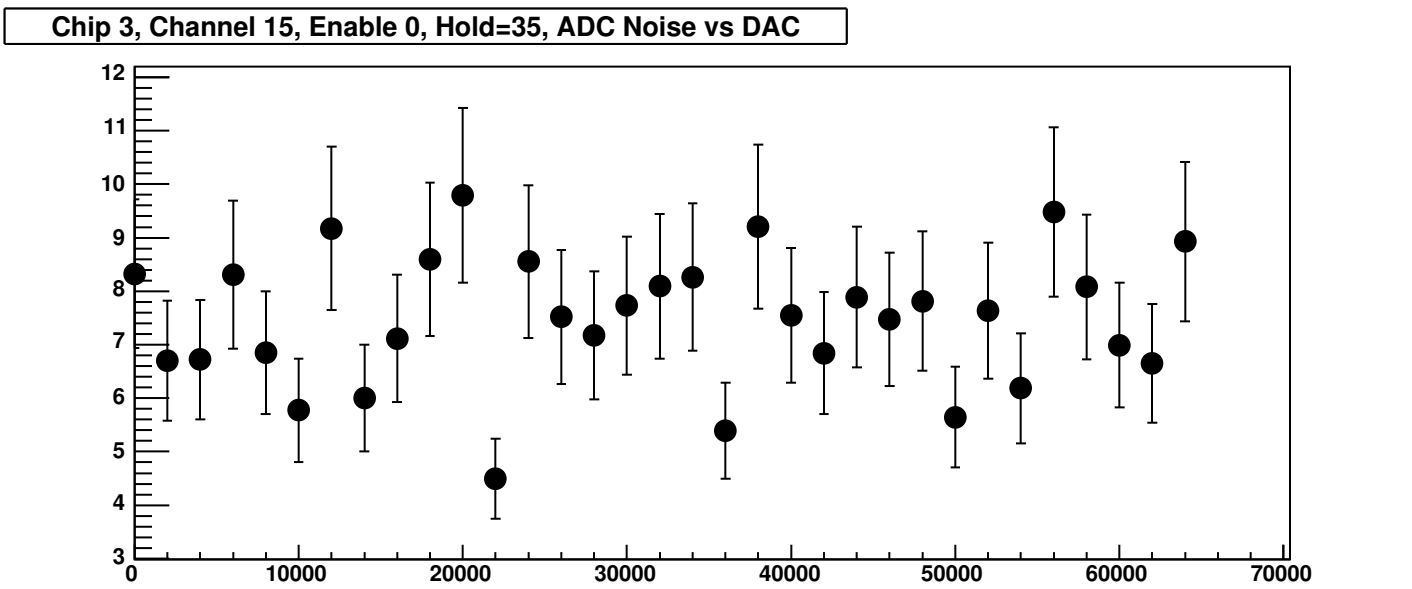

Chip 3, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000

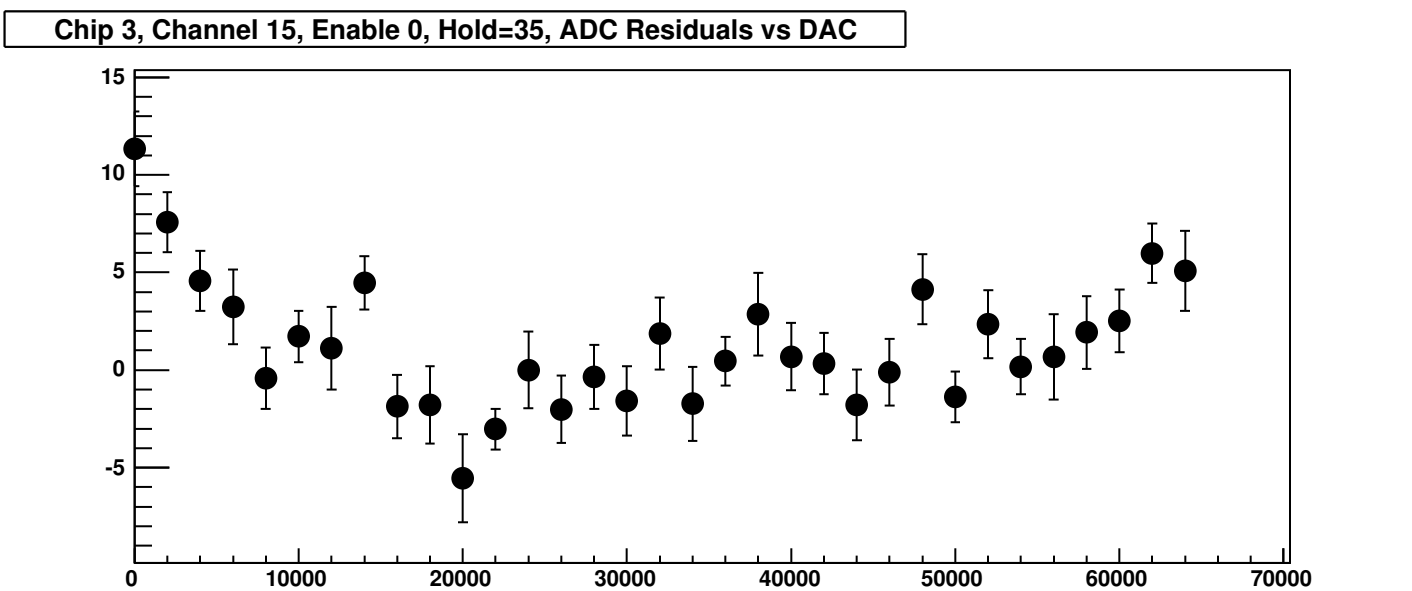

Chip 3, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 240.5 / 23
p0 -31.91 ± 0.6913
p1 -0.007986 ± 2.202e-05
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000

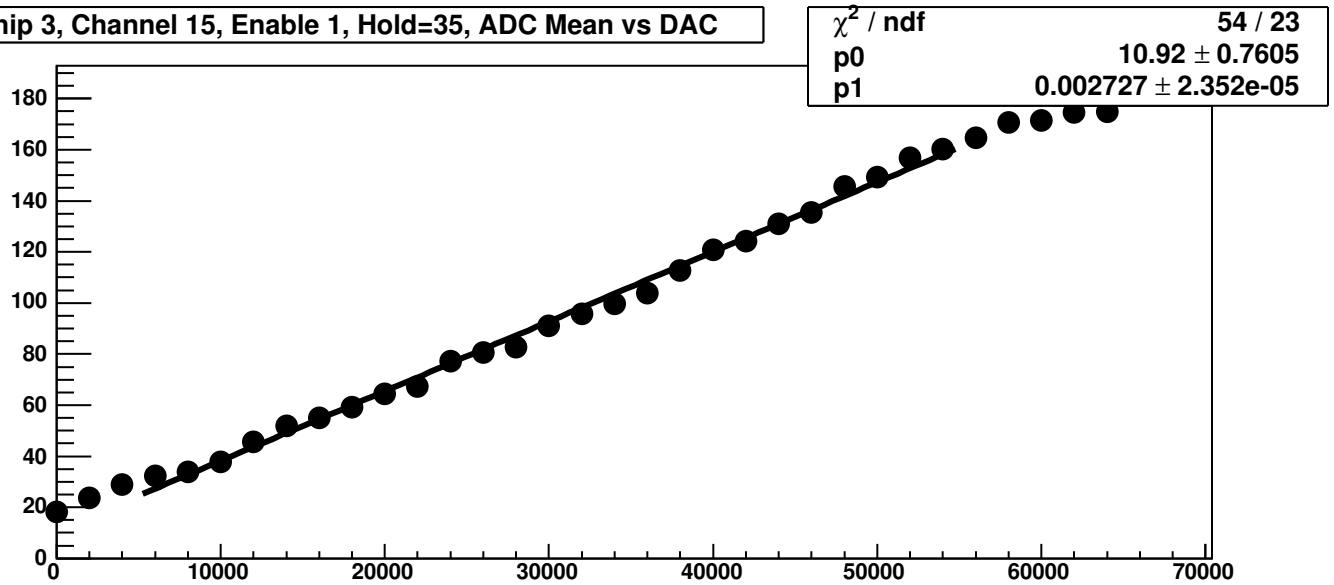

Chip 3, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
-15
-20
0
10000
20000
30000
40000
50000
60000


Chip 3, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 47.73 / 23
p0 14.97 ± 0.7373
p1 0.002563 ± 2.202e-05
200
180
160
140
120
100
80
60
40
20
0
10000
20000
30000
40000
50000
60000
70000

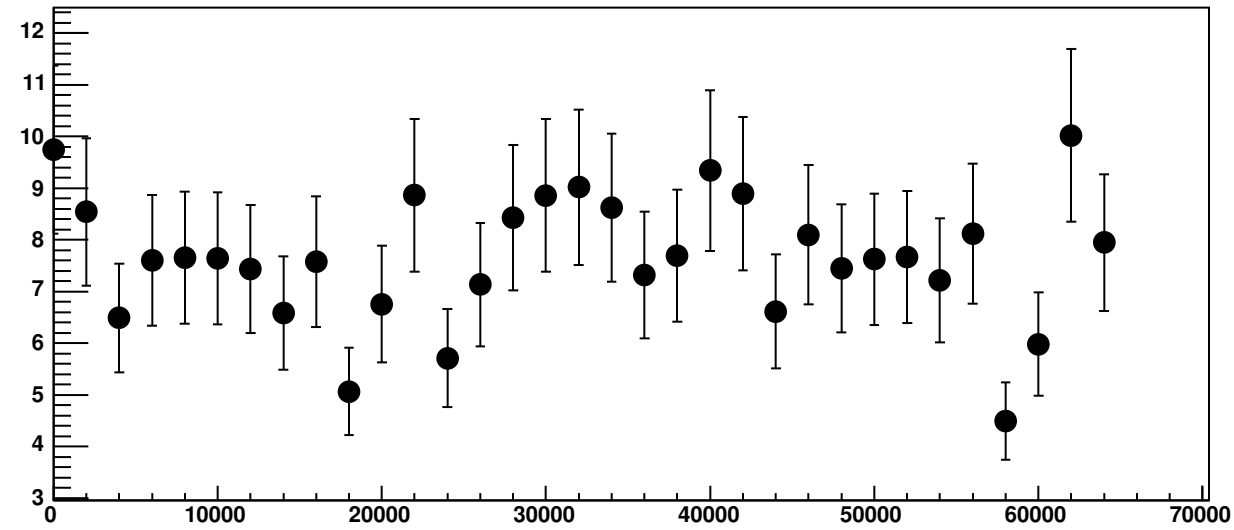

Chip 3, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

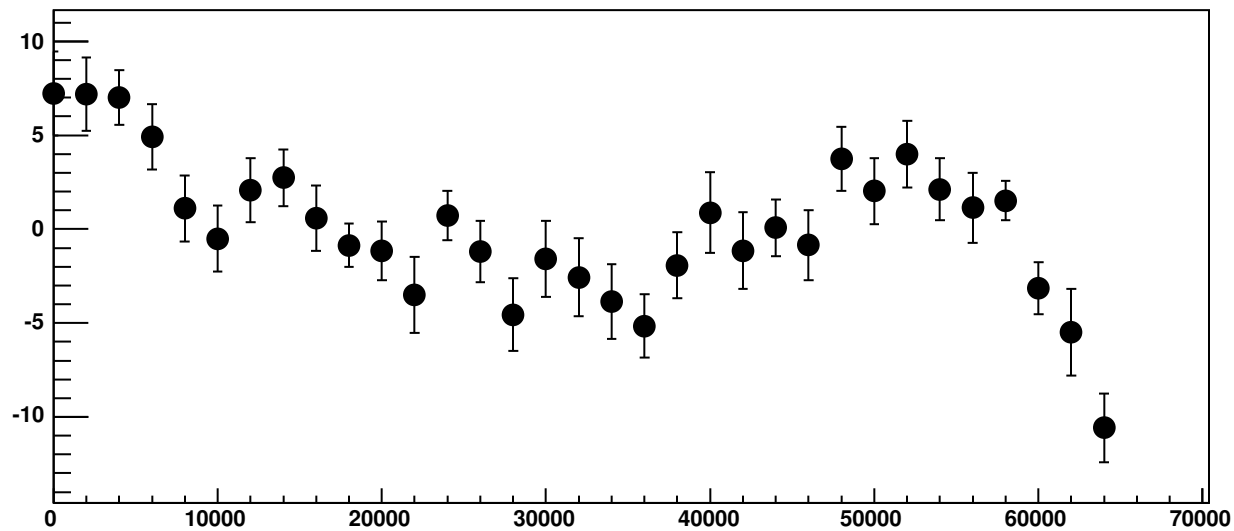
Chip 3, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC

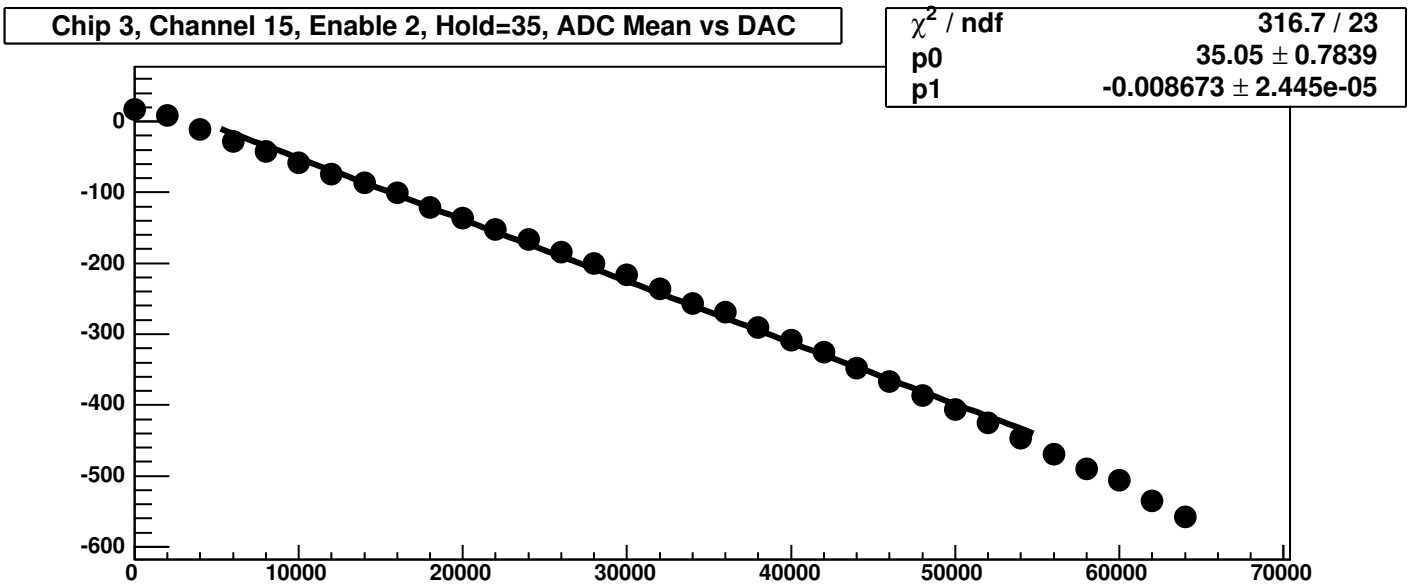


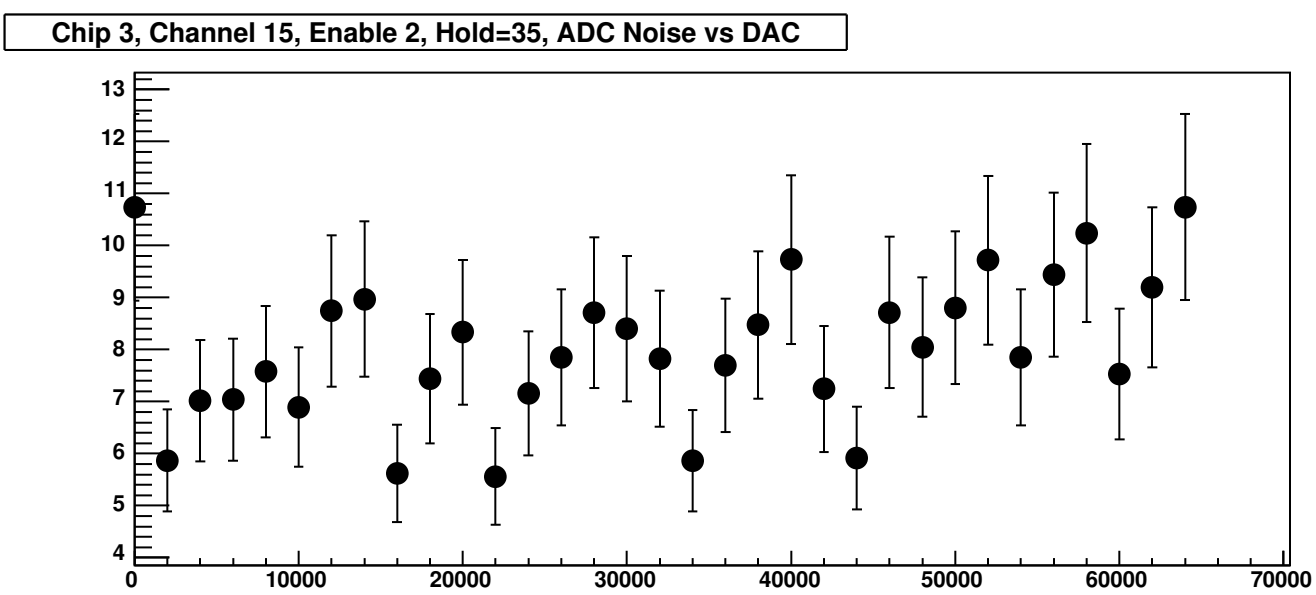
Chip 3, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC

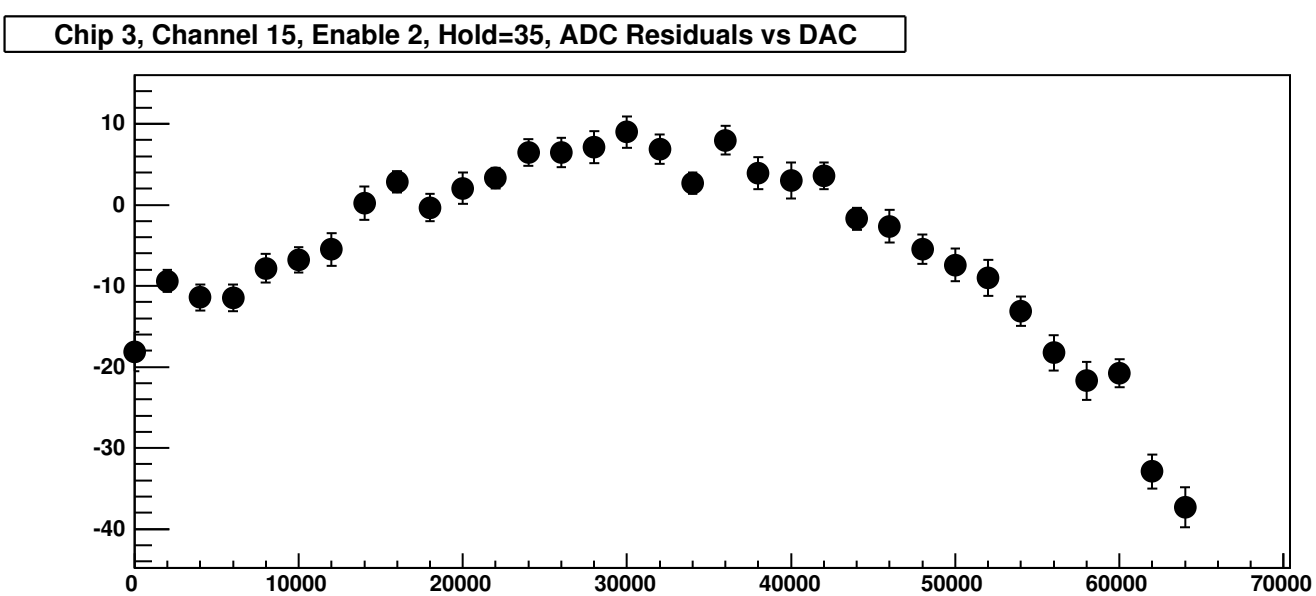


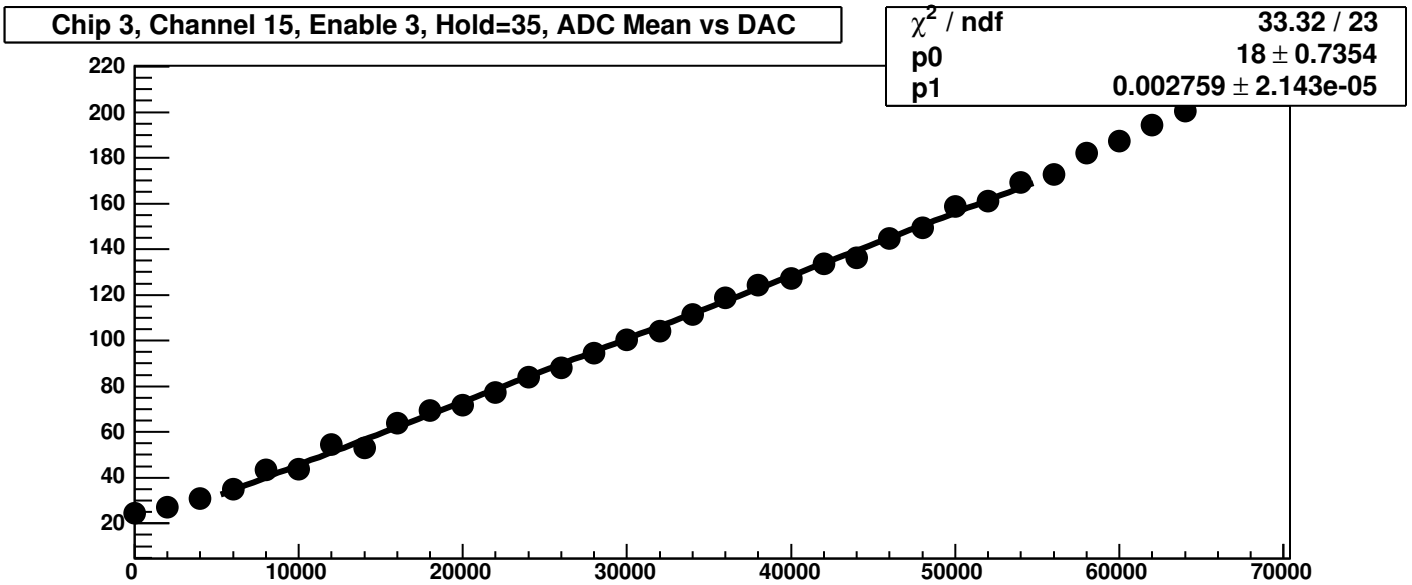
Chip 3, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC

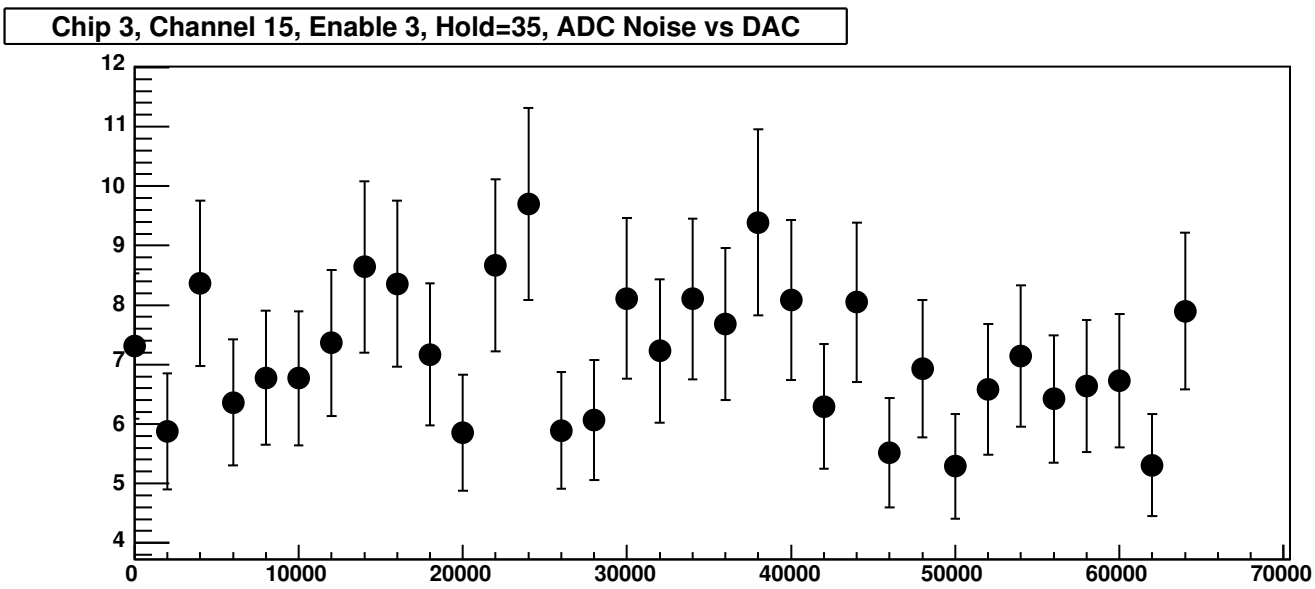


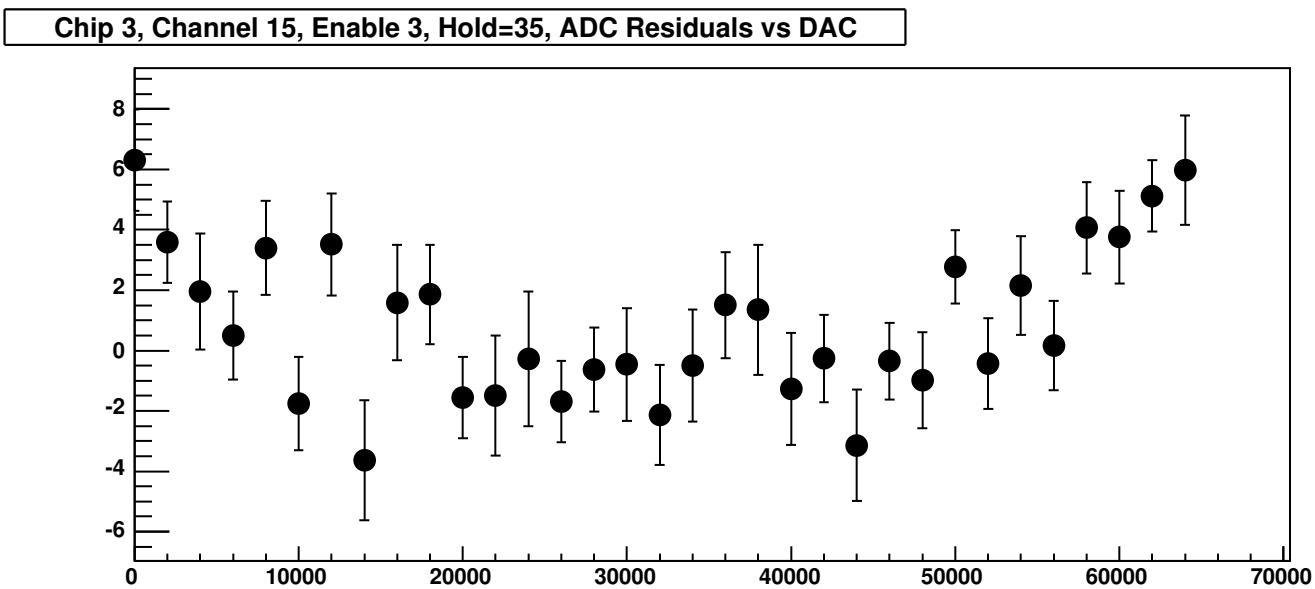

Chip 3, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 316.7 / 23
p0 35.05 ± 0.7839
p1 -0.008673 ± 2.445e-05
0
-100
-200
-300
-400
-500
-600
0
10000
20000
30000
40000
50000
60000
70000

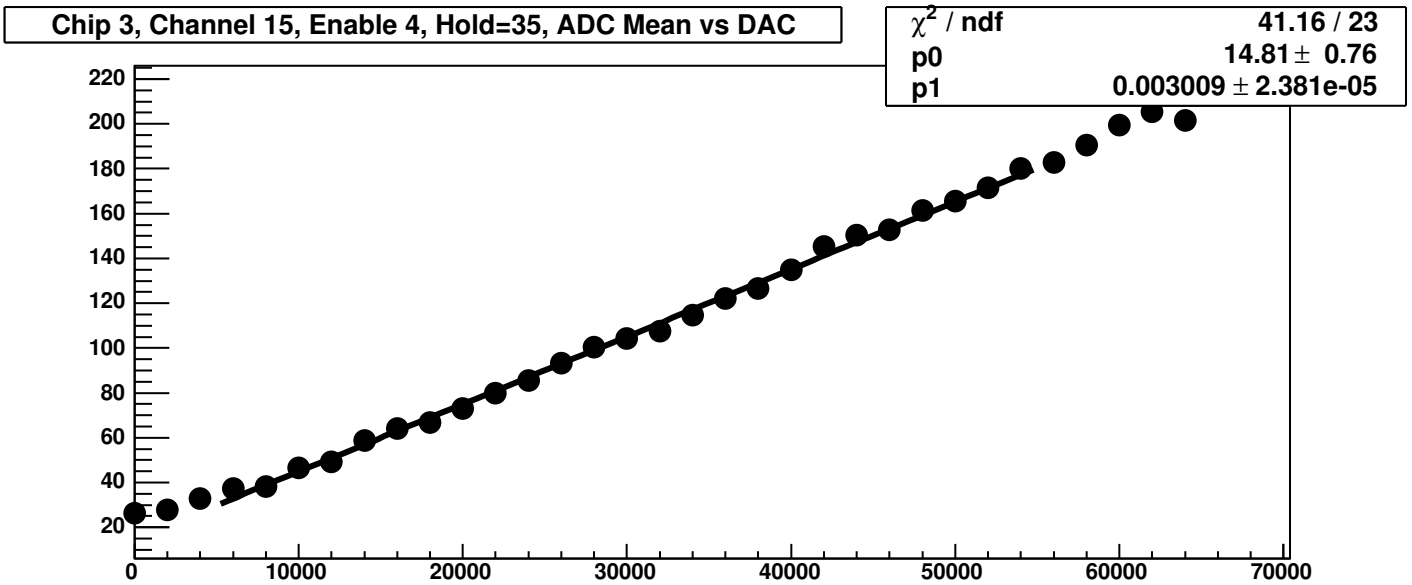

Chip 3, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

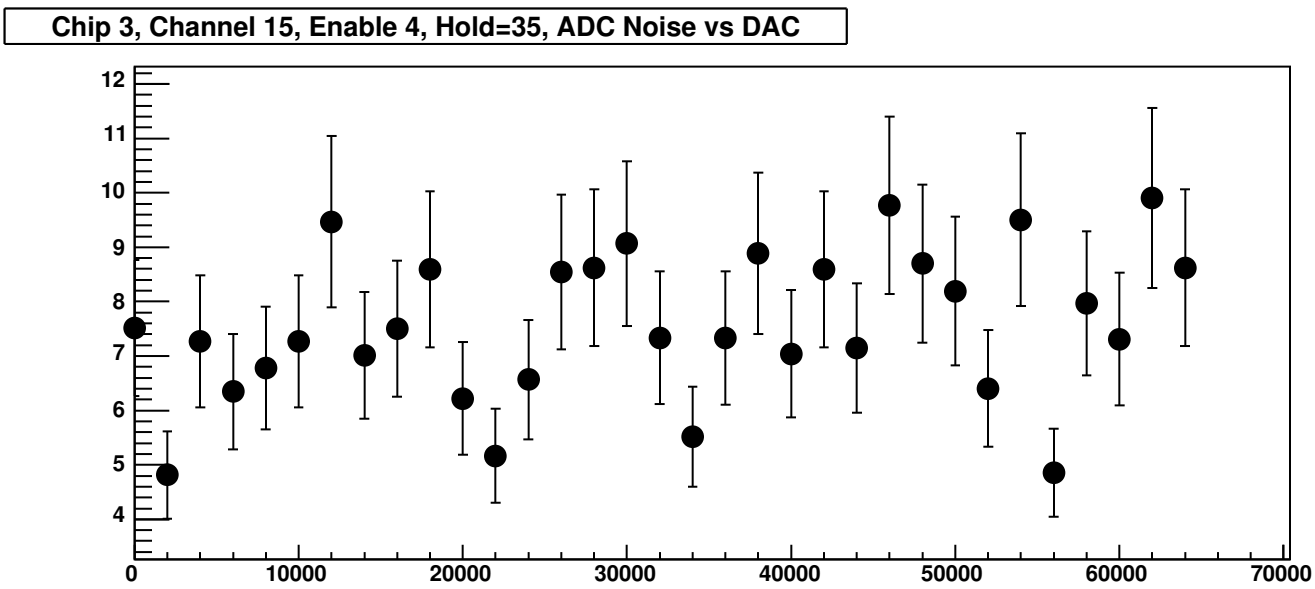

Chip 3, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
10000
20000
30000
40000
50000
60000
70000

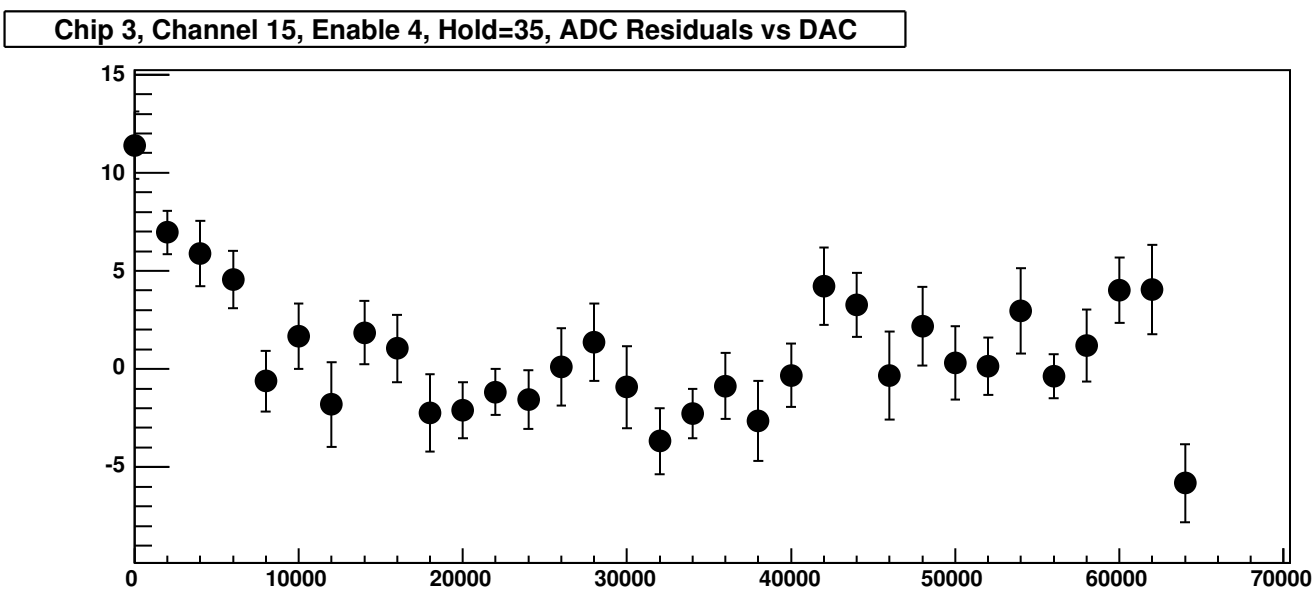

Chip 3, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 33.32 / 23
p0 18 ± 0.7354
p1 0.002759 ± 2.143e-05
220
200
180
160
140
120
100
80
60
40
20
0
10000
20000
30000
40000
50000
60000
70000

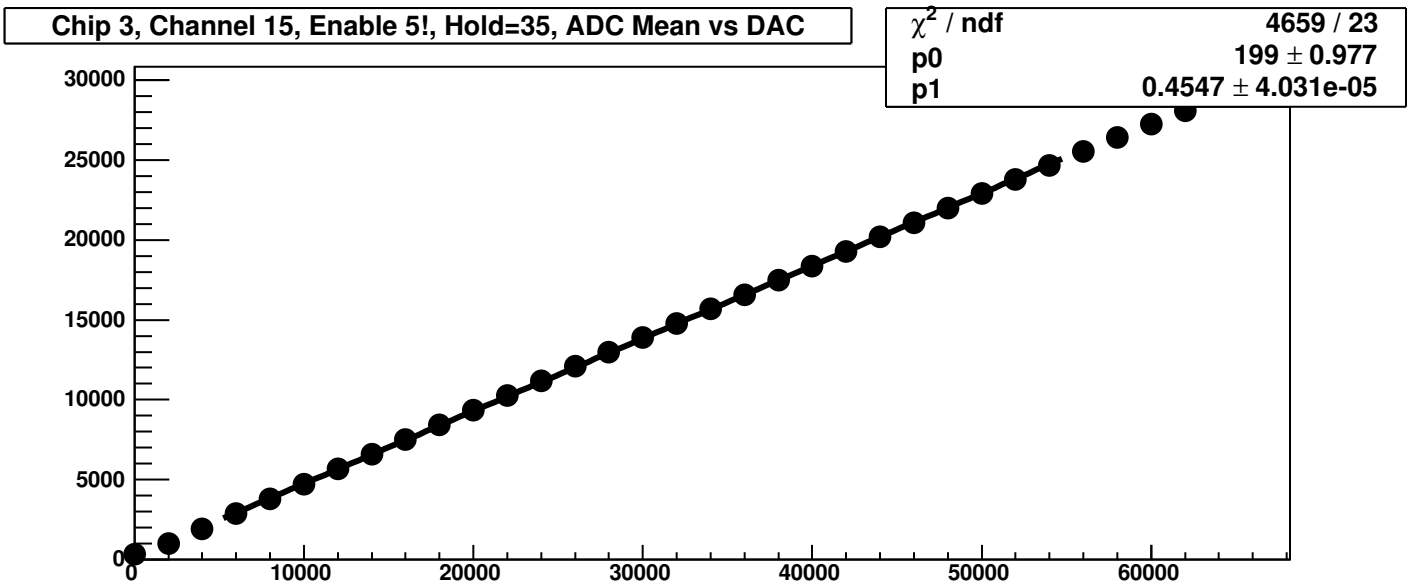

Chip 3, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

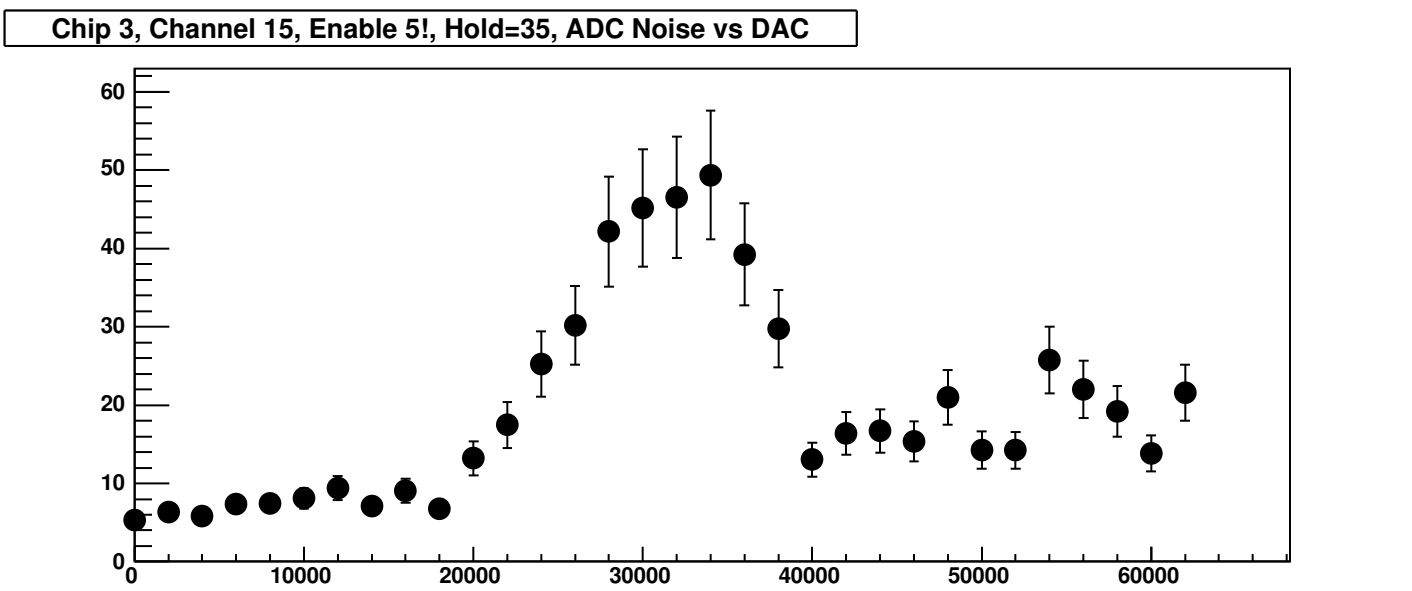

Chip 3, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

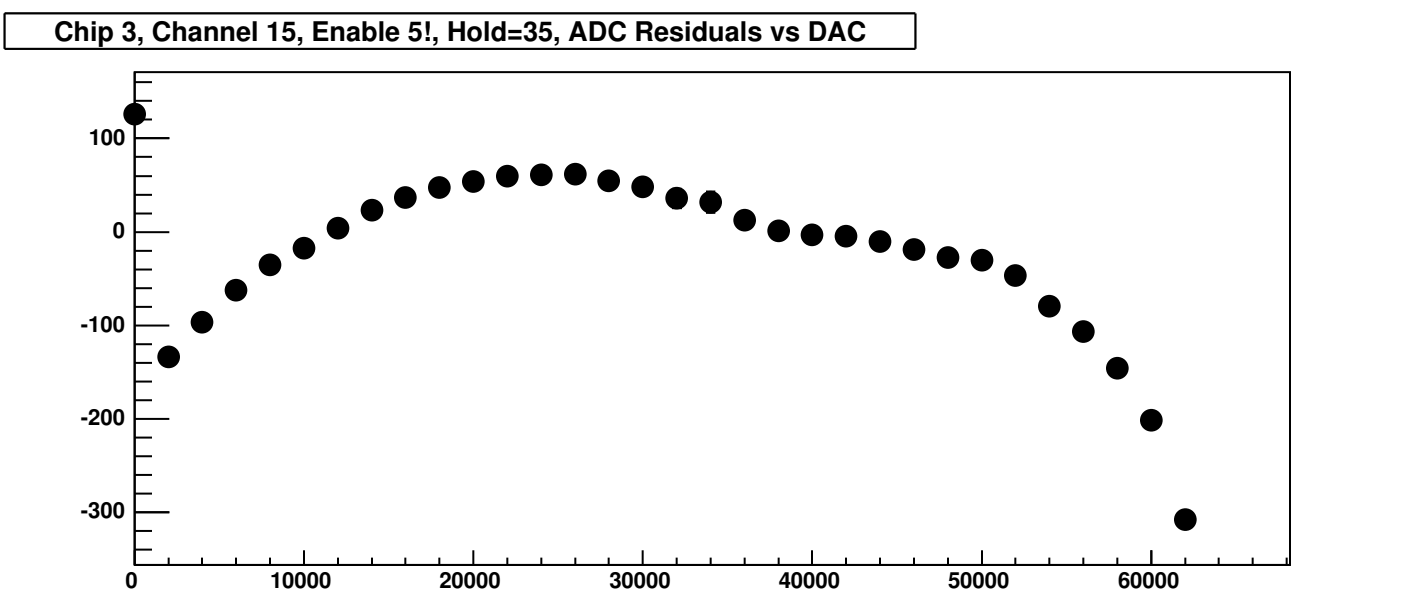

Chip 3, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 41.16 / 23
p0 14.81 ± 0.76
p1 0.003009 ± 2.381e-05
220
200
180
160
140
120
100
80
60
40
20
0
10000
20000
30000
40000
50000
60000
70000

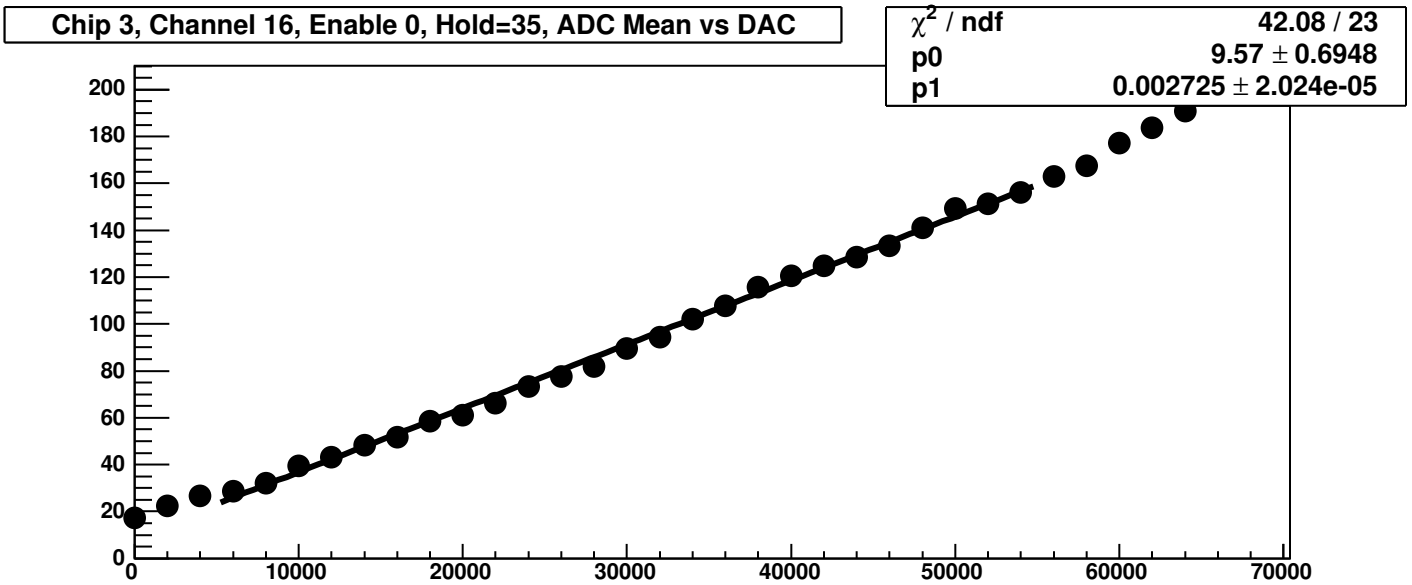

Chip 3, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

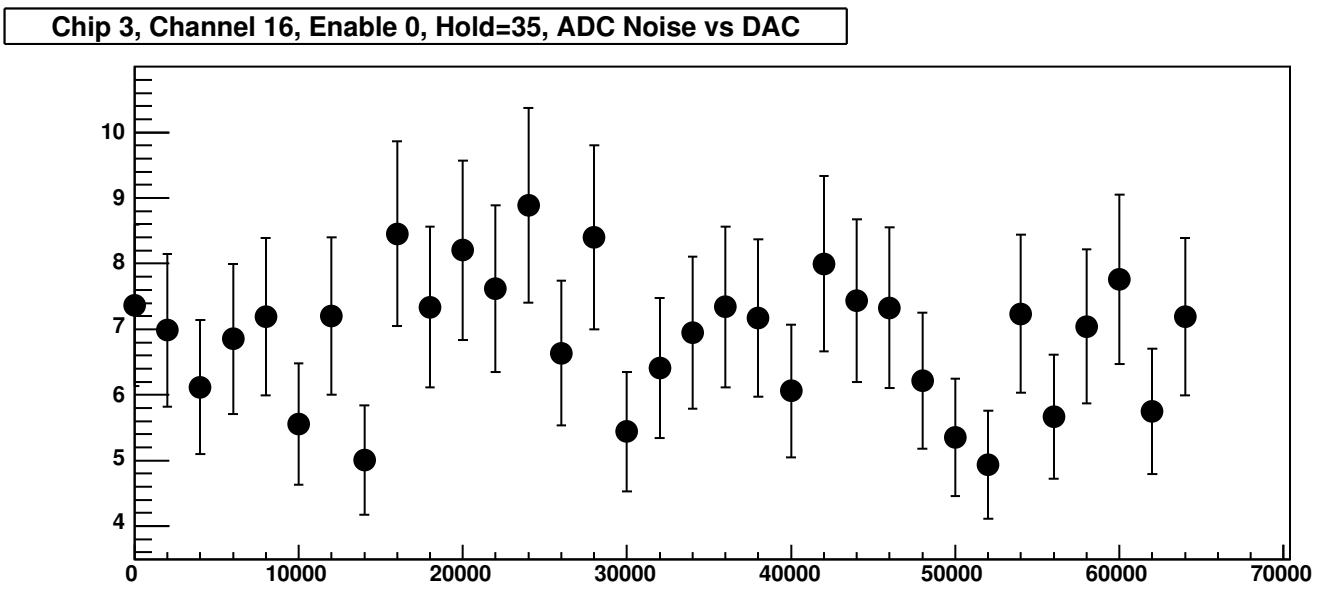

Chip 3, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

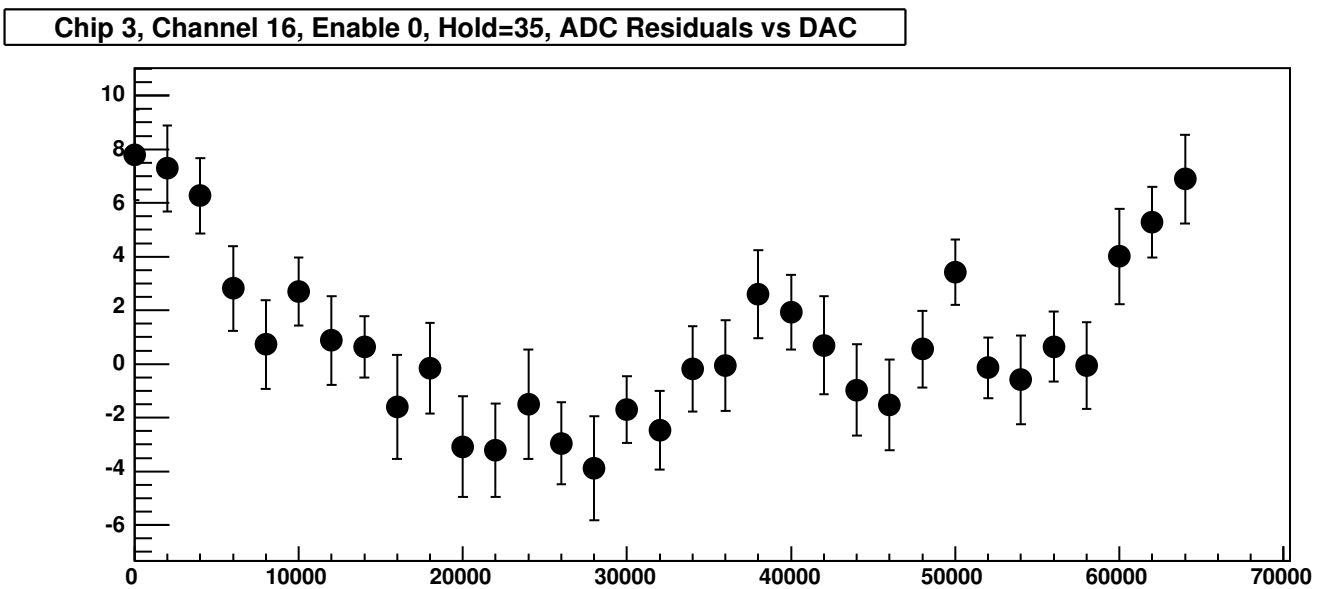

Chip 3, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 4659 / 23
p0 199 ± 0.977
p1 0.4547 ± 4.031e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

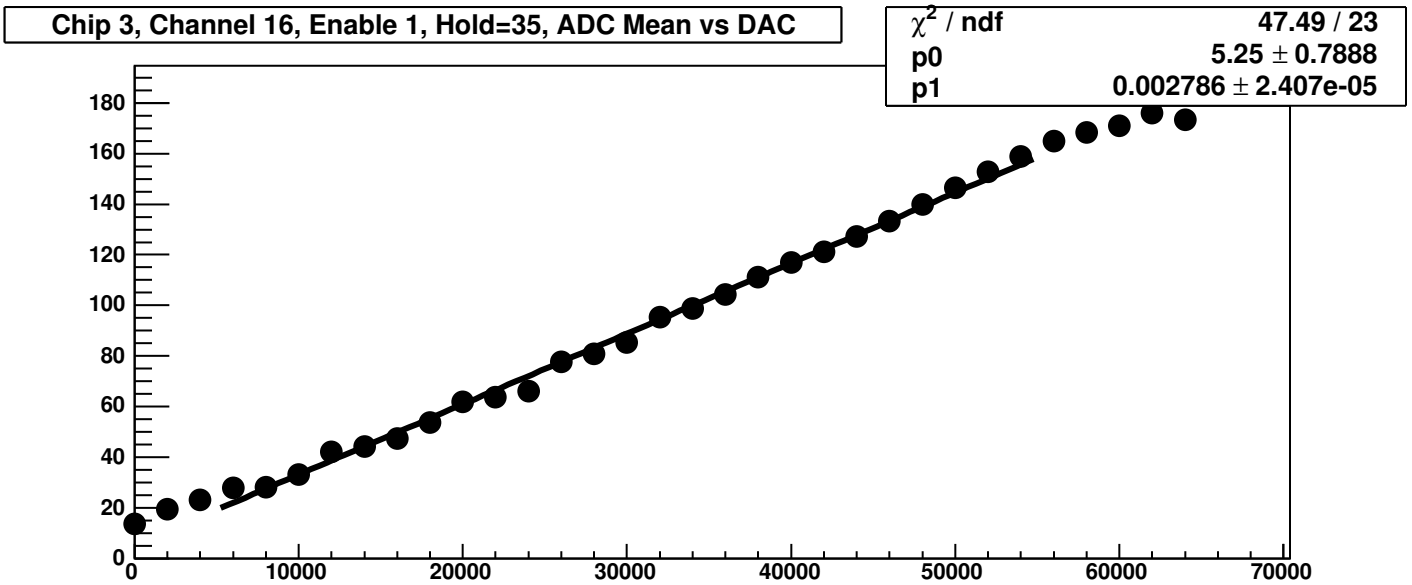

Chip 3, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000

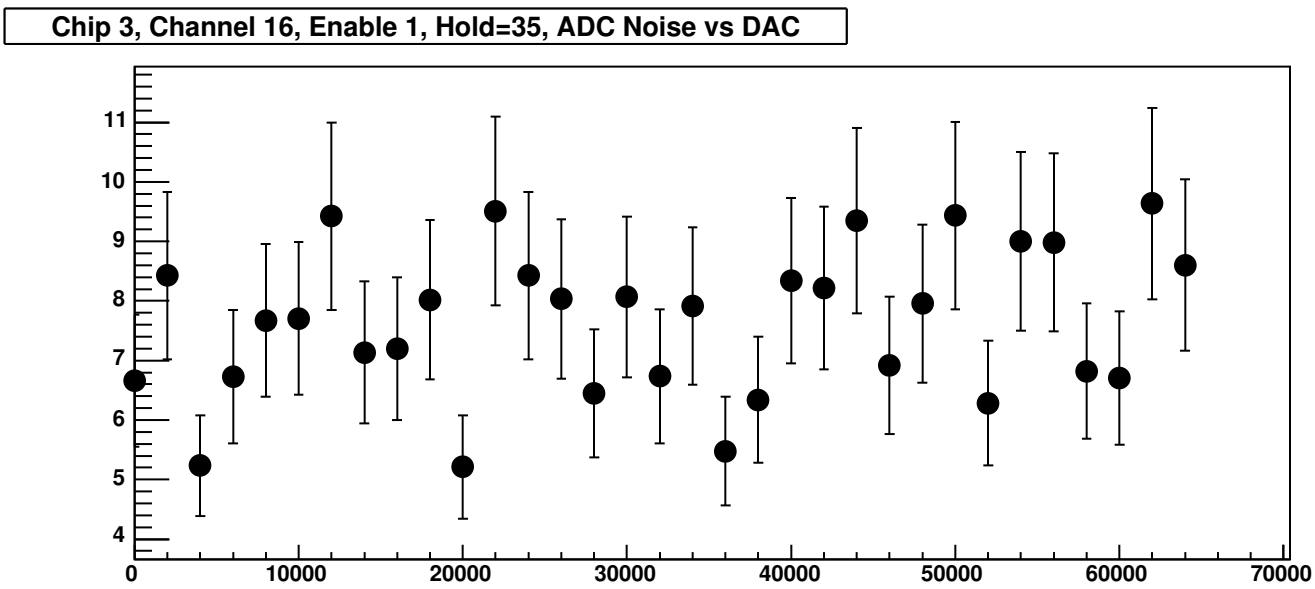

Chip 3, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
100
0
-100
-200
-300
0
10000
20000
30000
40000
50000
60000

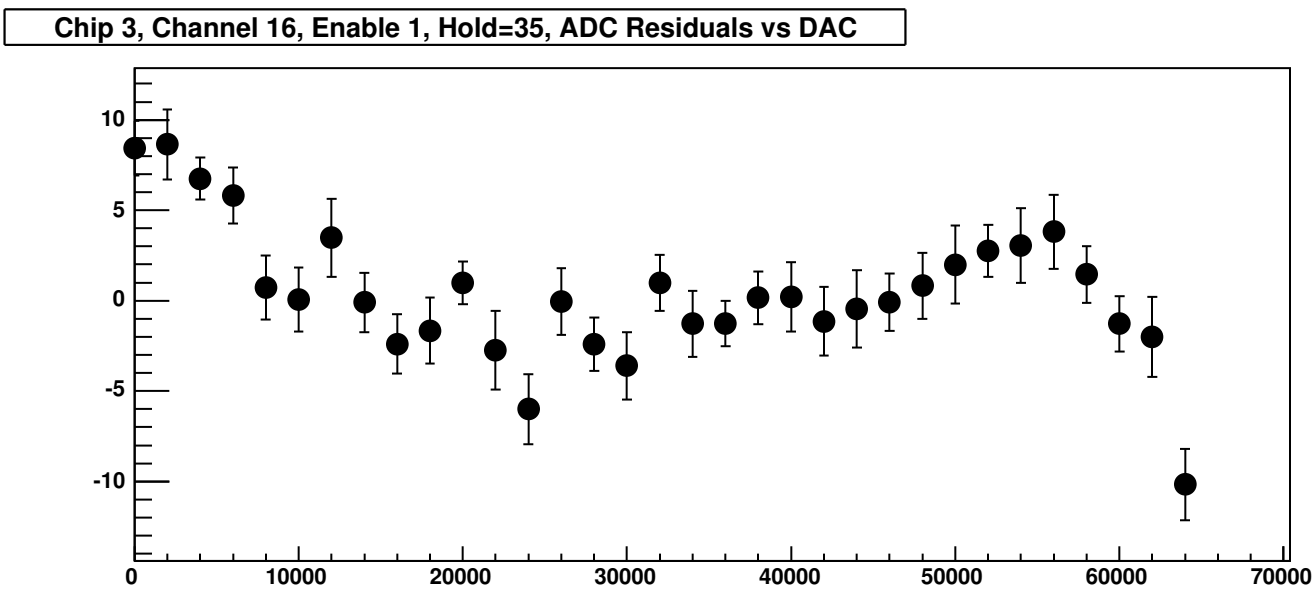

Chip 3, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 42.08 / 23
p0 9.57 ± 0.6948
p1 0.002725 ± 2.024e-05
200
180
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000
70000

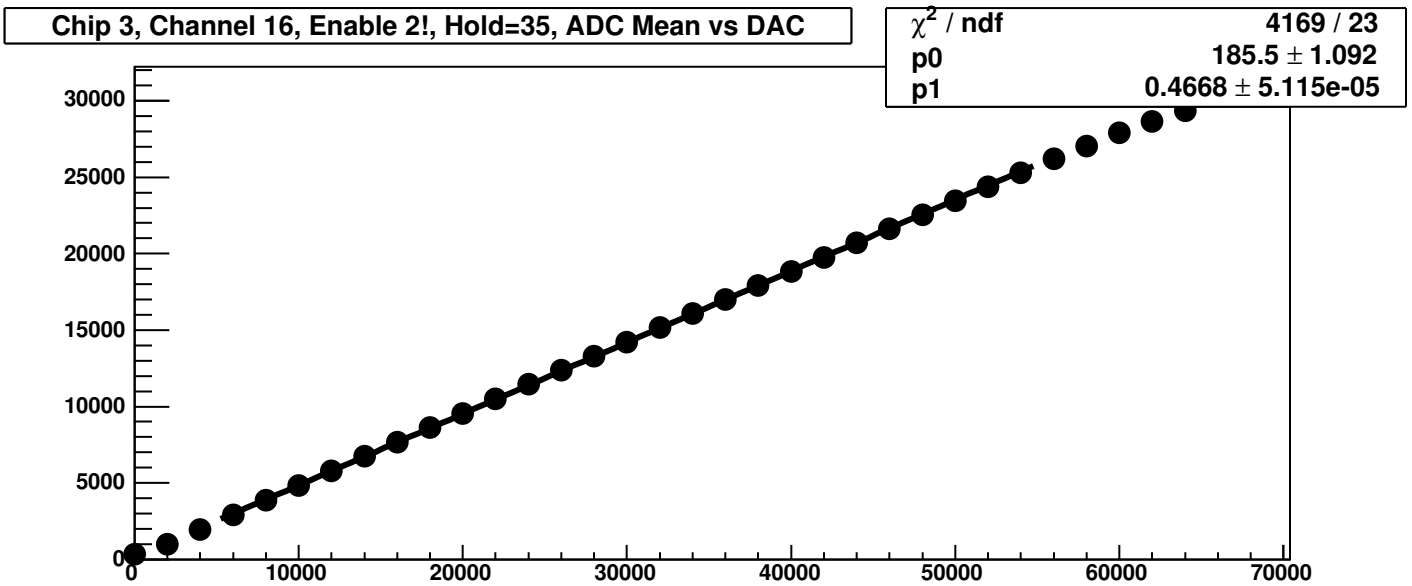

Chip 3, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

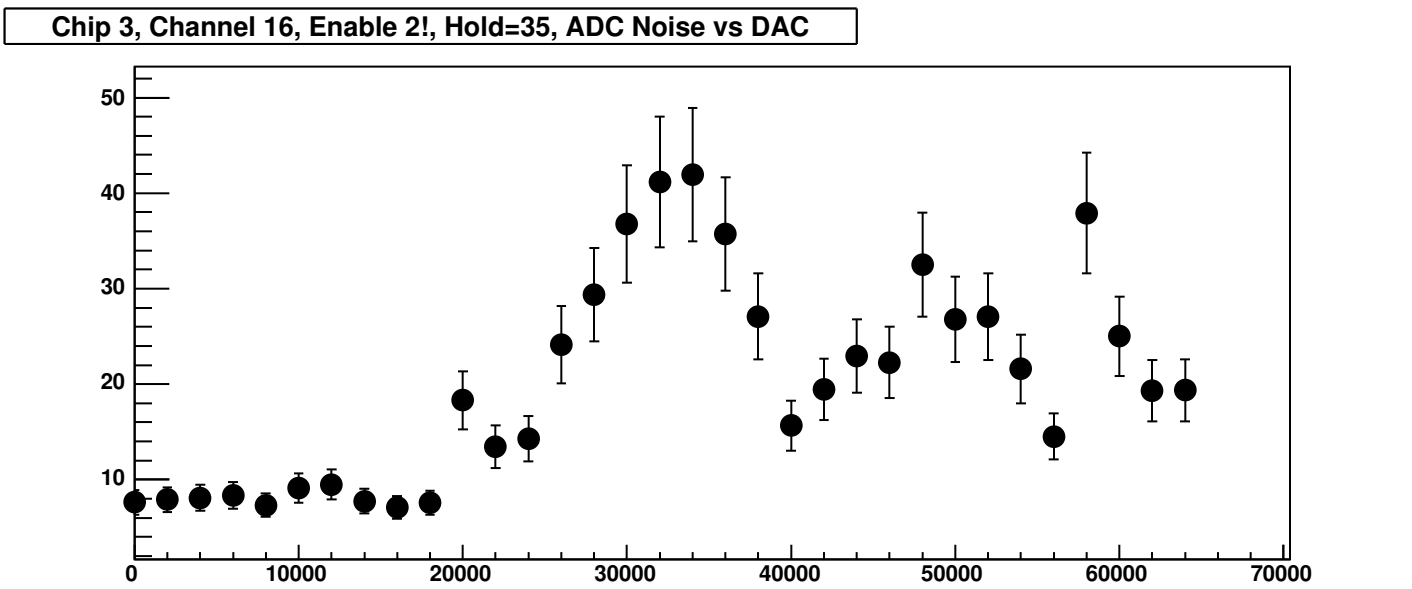

Chip 3, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

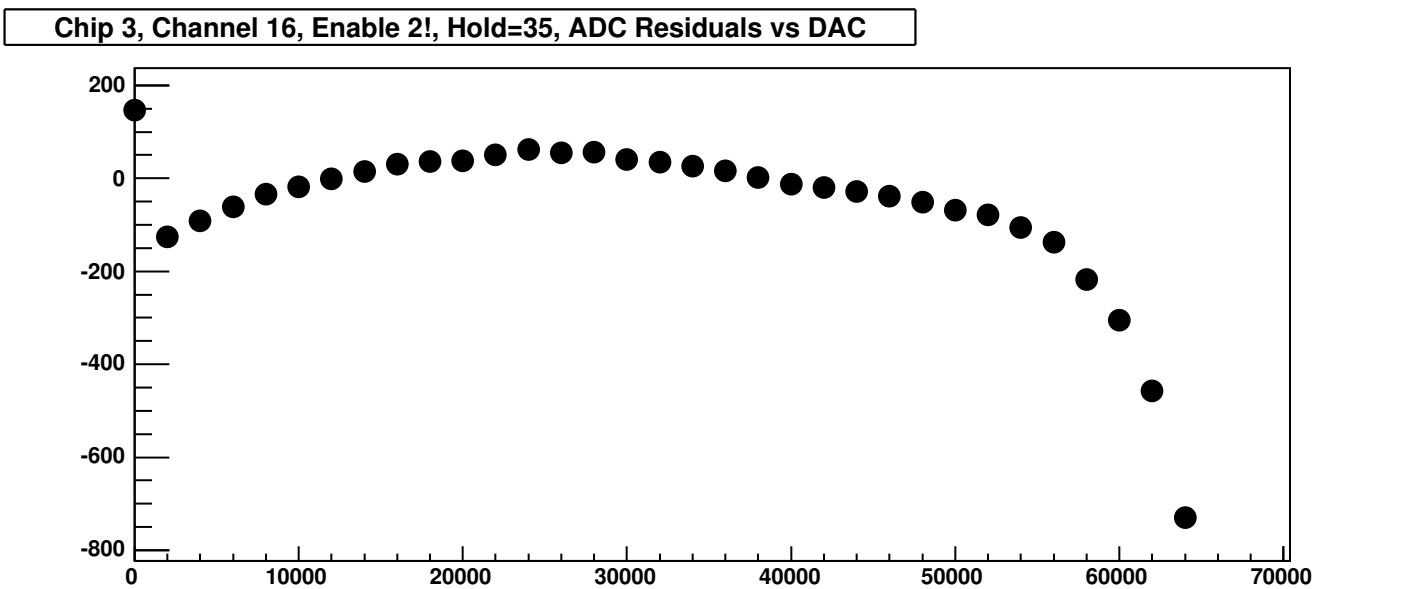

Chip 3, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 47.49 / 23
p0 5.25 ± 0.7888
p1 0.002786 ± 2.407e-05
180
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000
70000

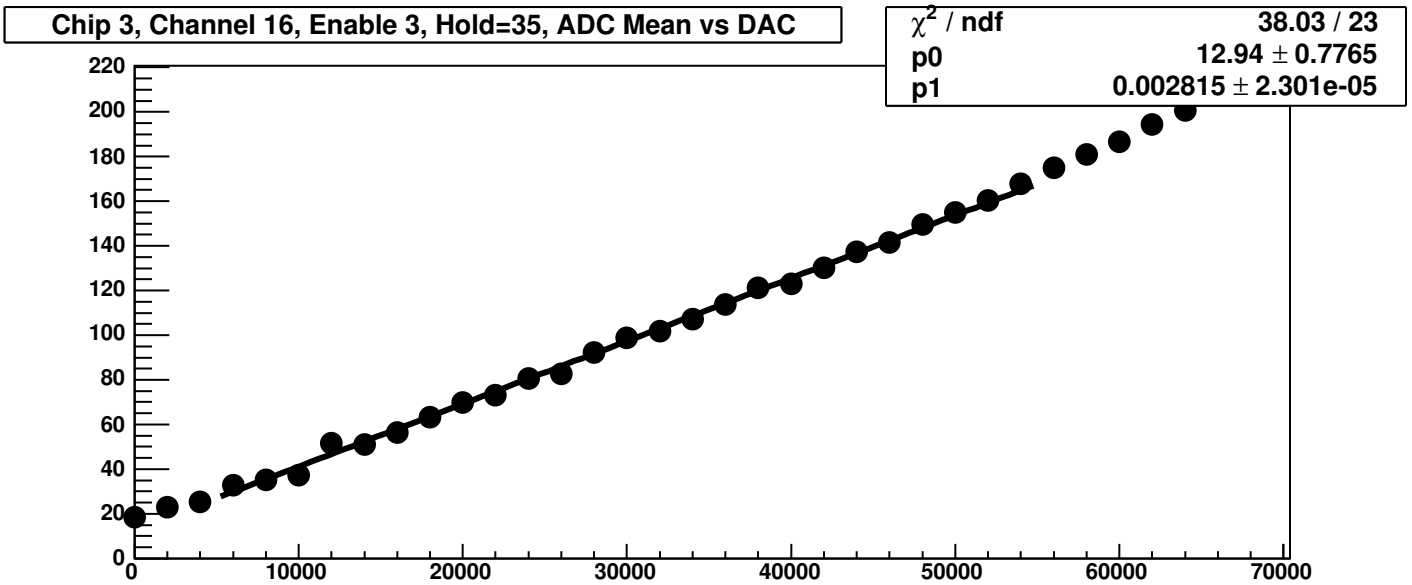

Chip 3, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

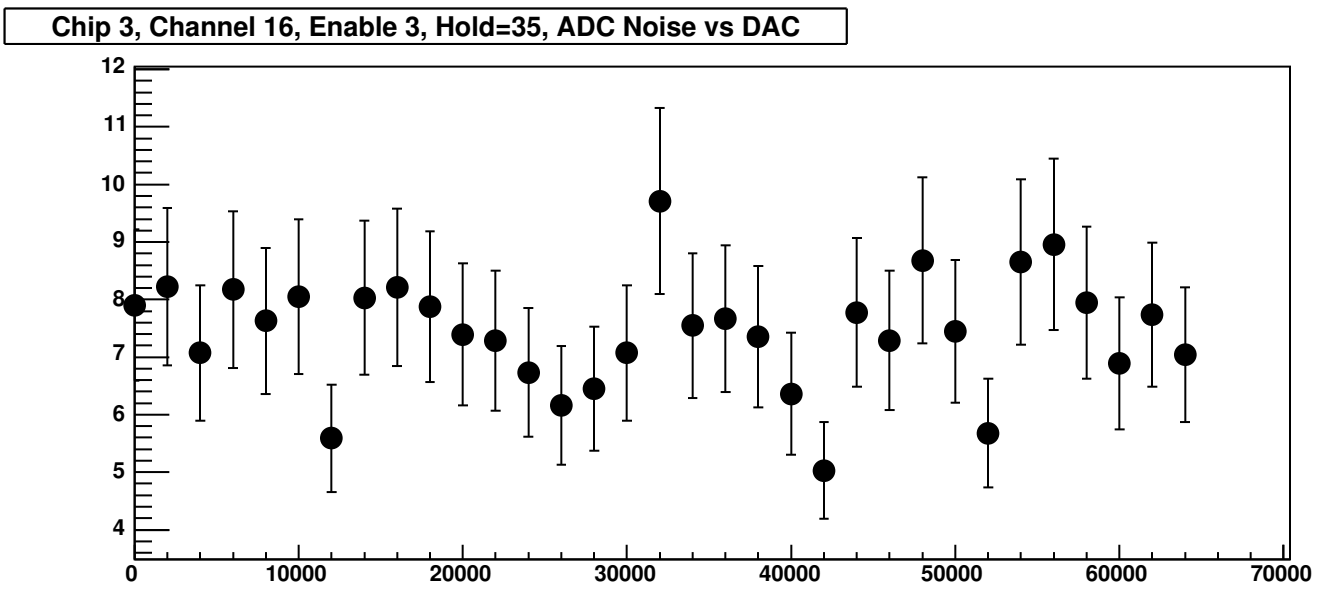

Chip 3, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

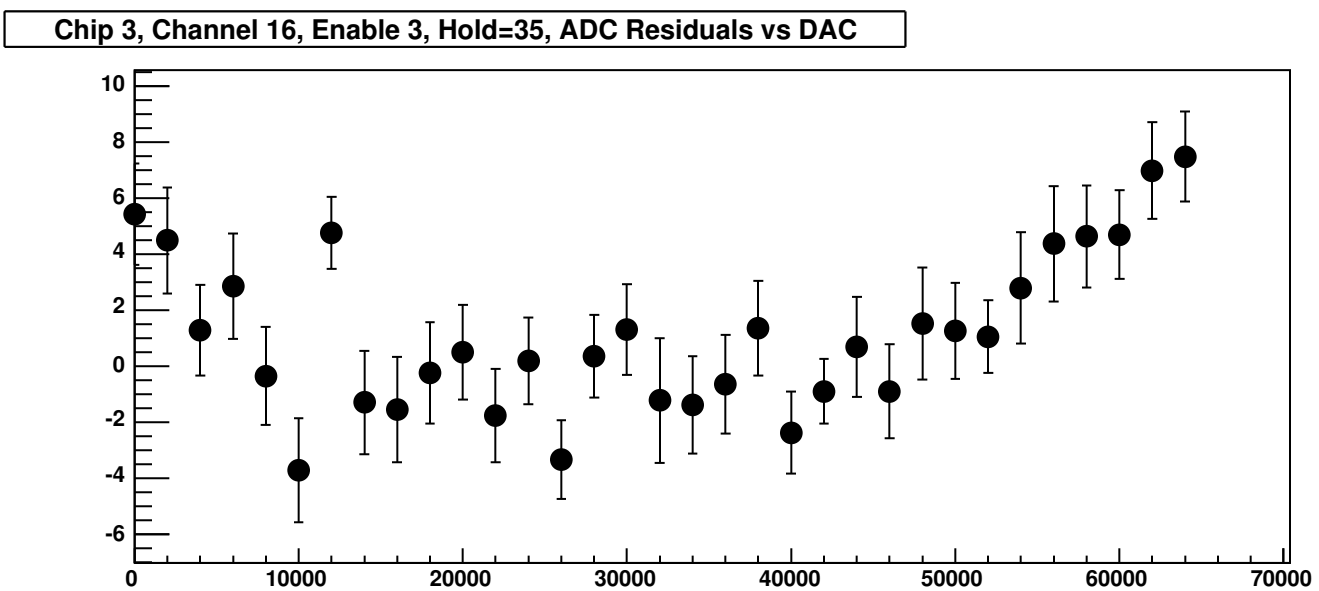

Chip 3, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 4169 / 23
p0 185.5 ± 1.092
p1 0.4668 ± 5.115e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000

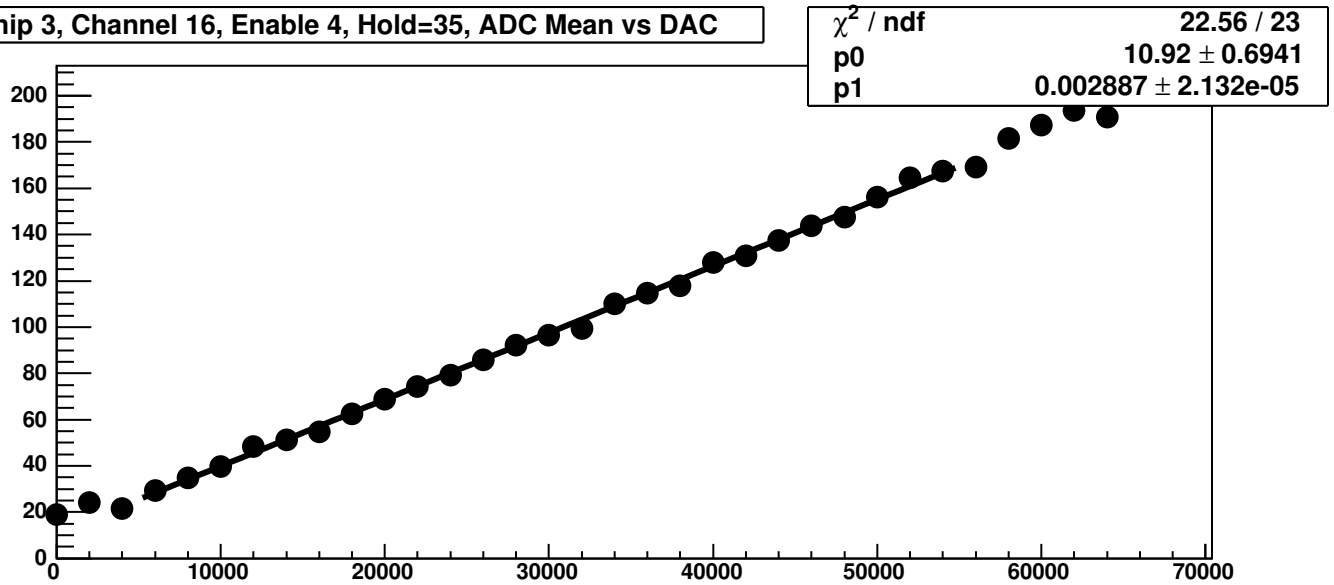

Chip 3, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 38.03 / 23
p0 12.94 ± 0.7765
p1 0.002815 ± 2.301e-05
220
200
180
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000
70000

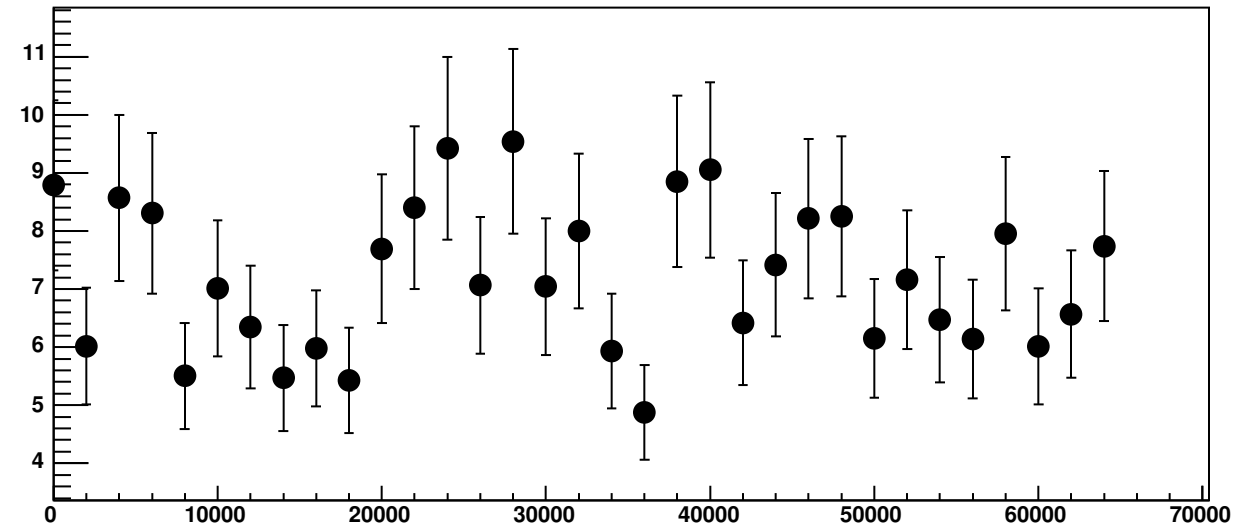

Chip 3, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 3, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

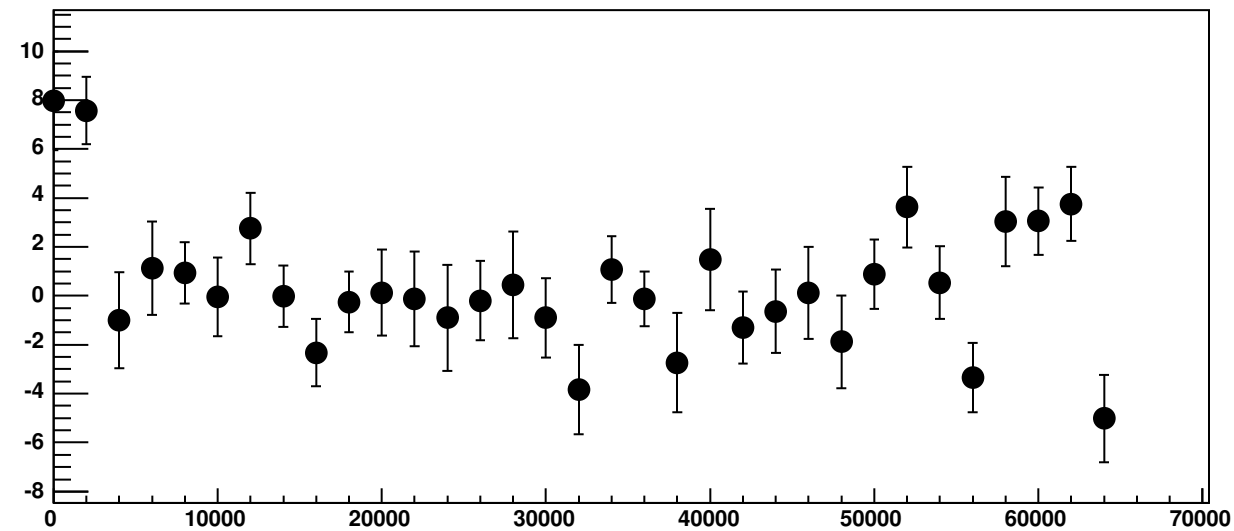
Chip 3, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC



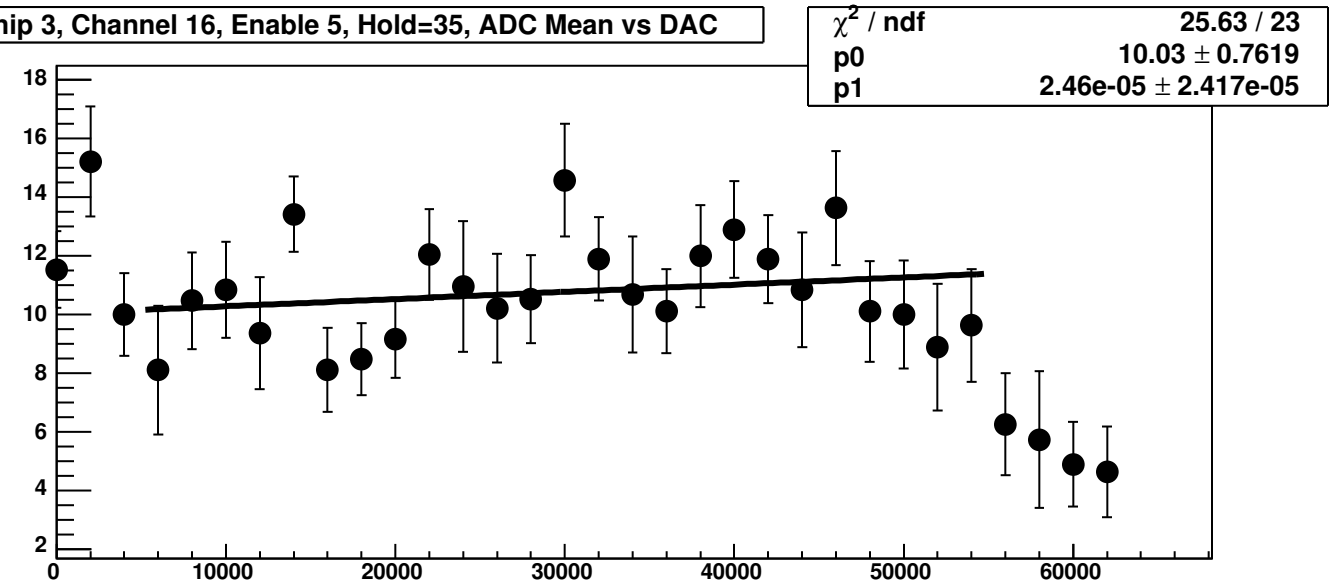
Chip 3, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC



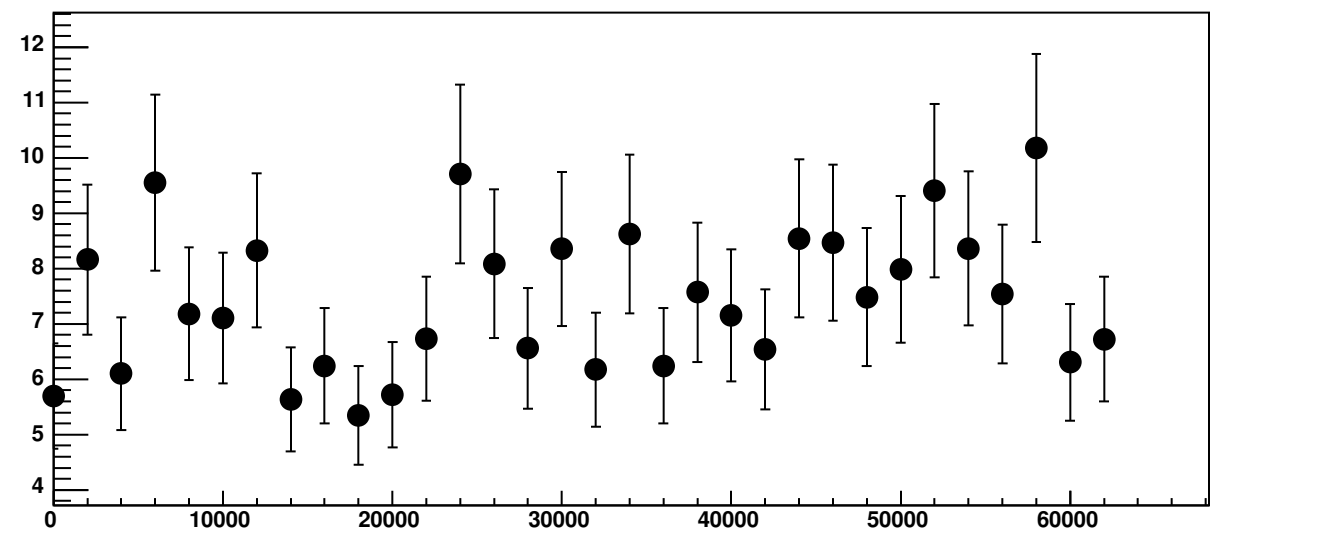
Chip 3, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC



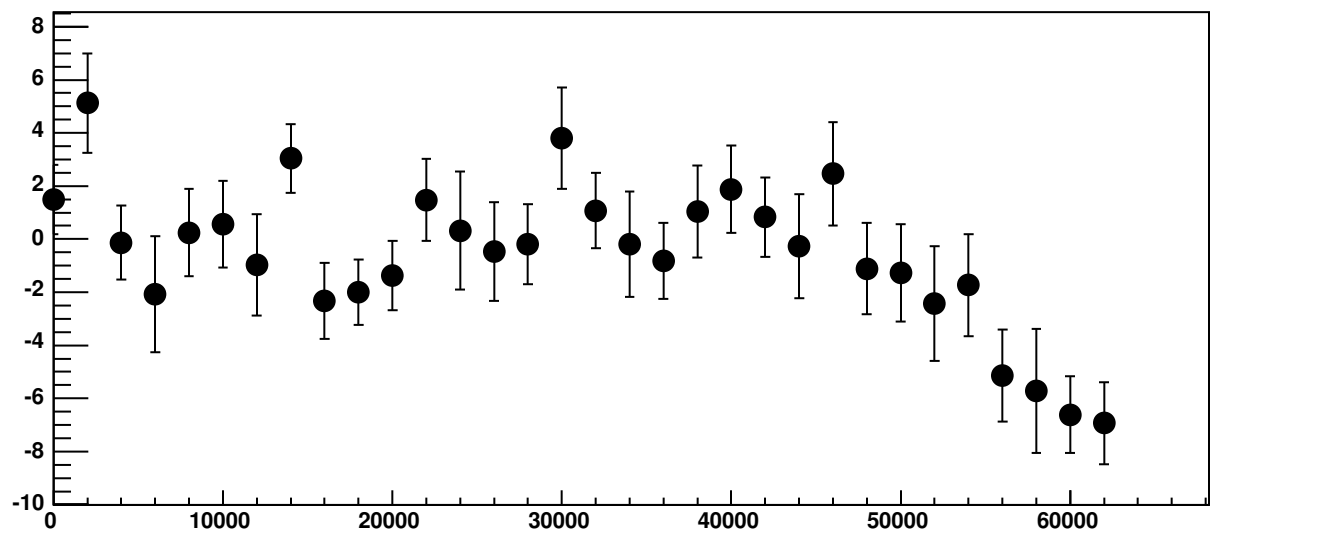
Chip 3, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC

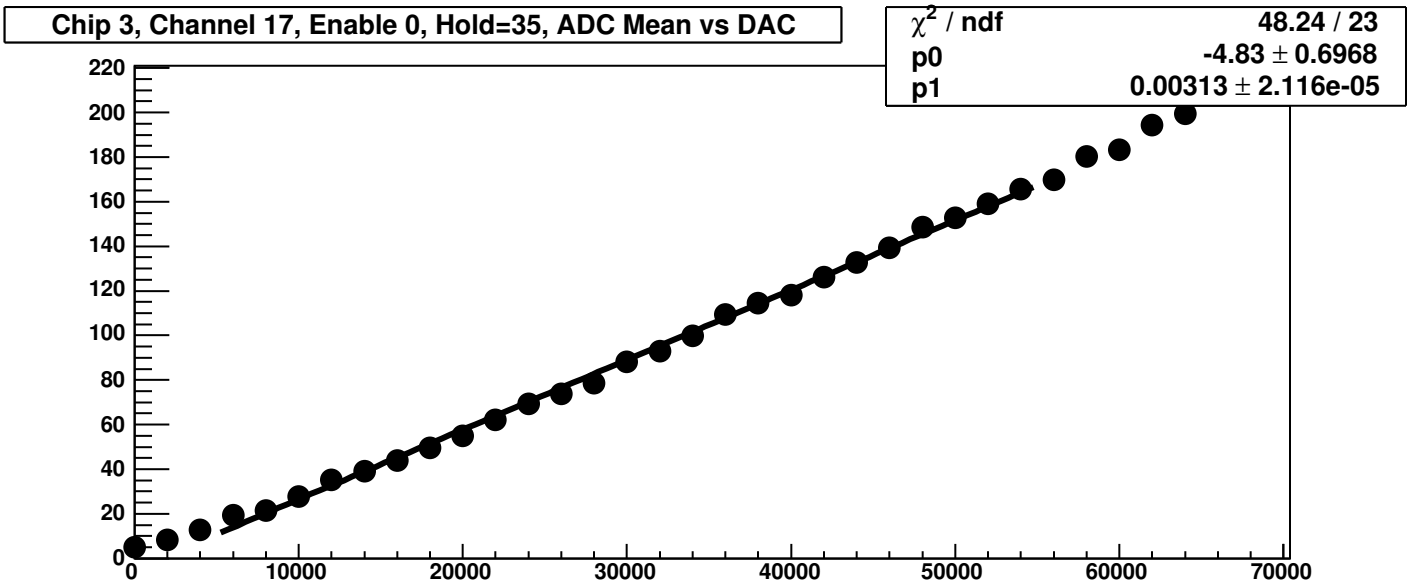


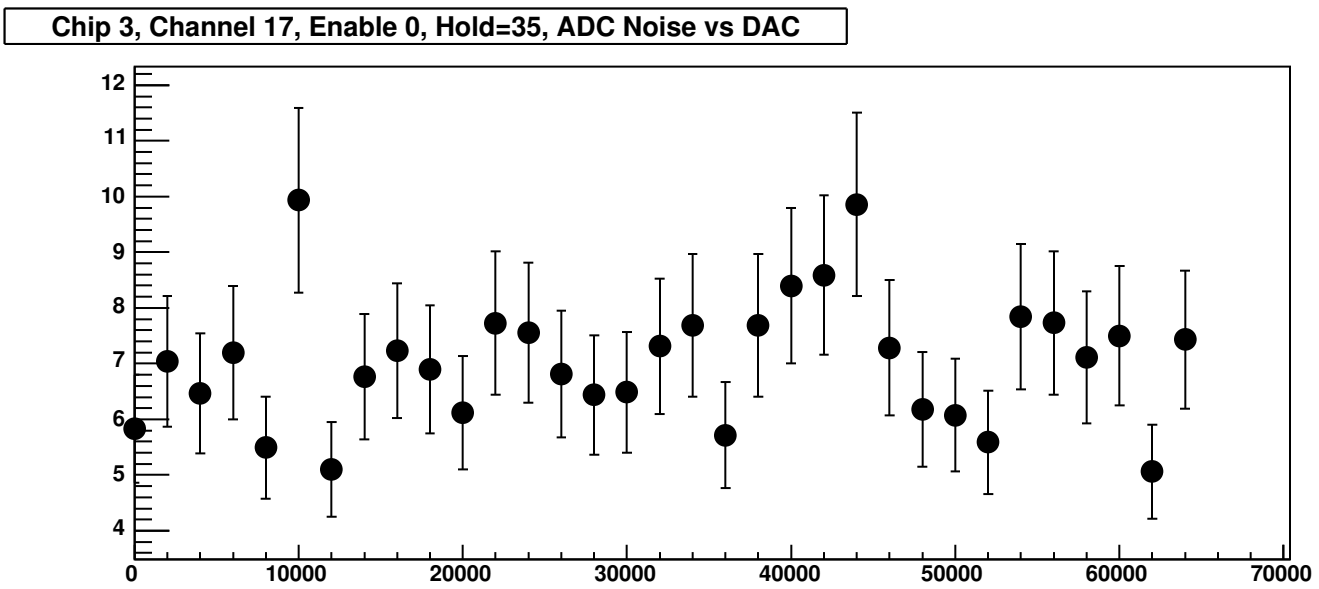
Chip 3, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC

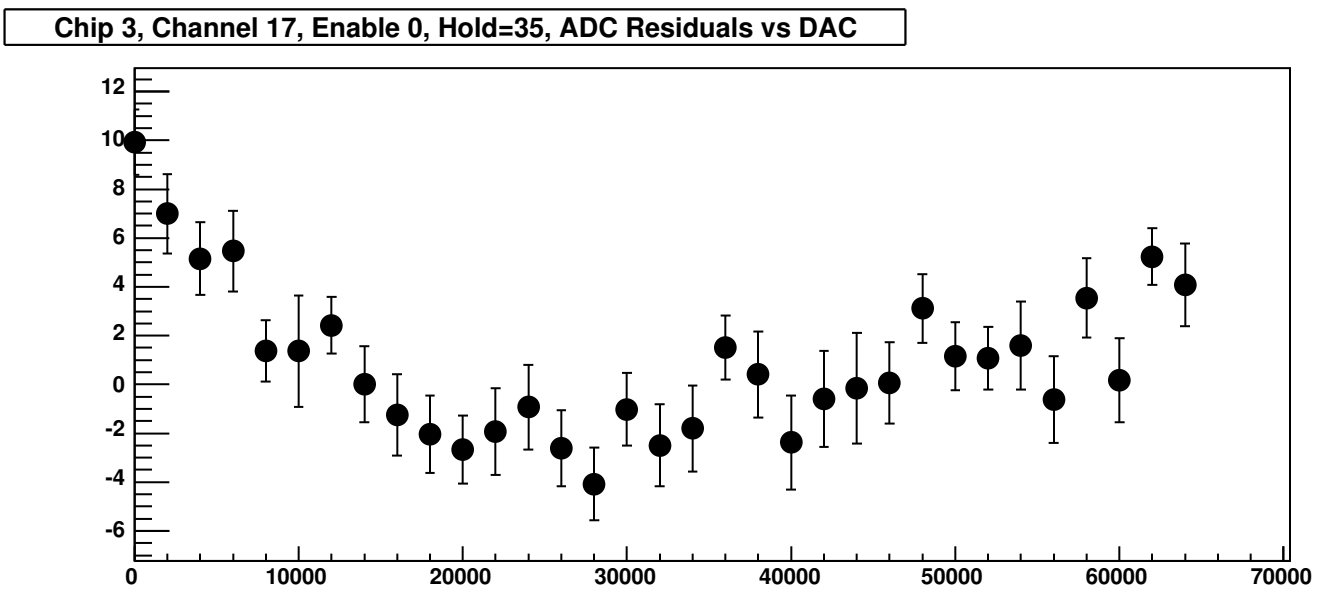


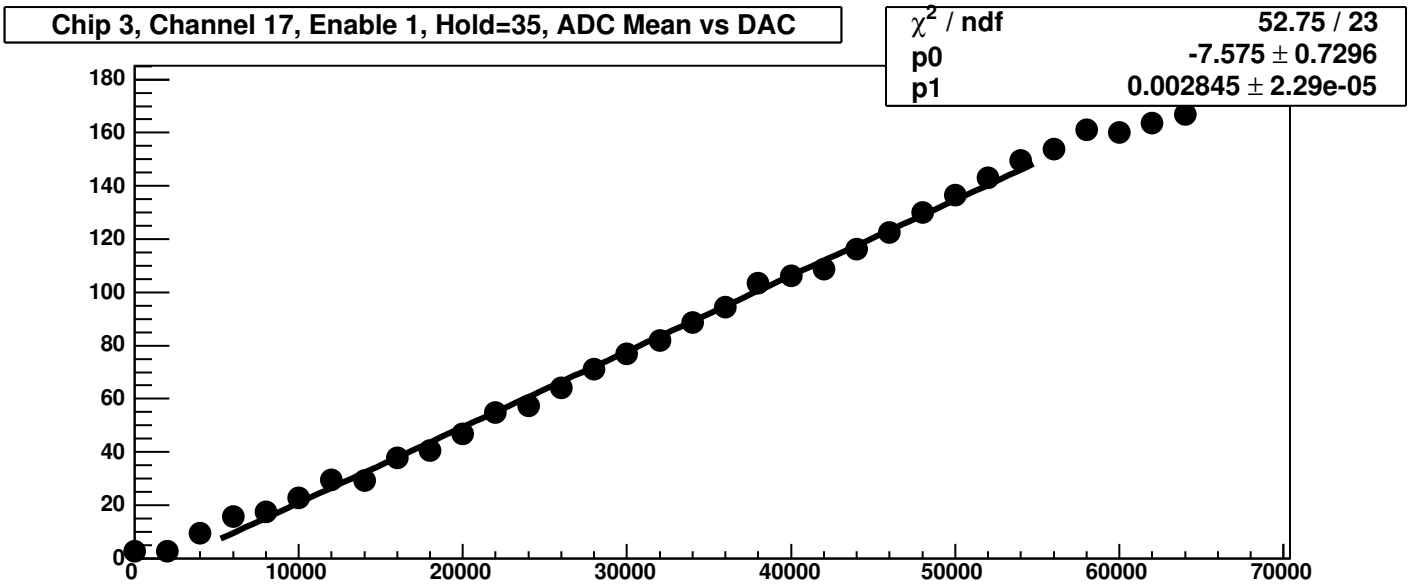
Chip 3, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC

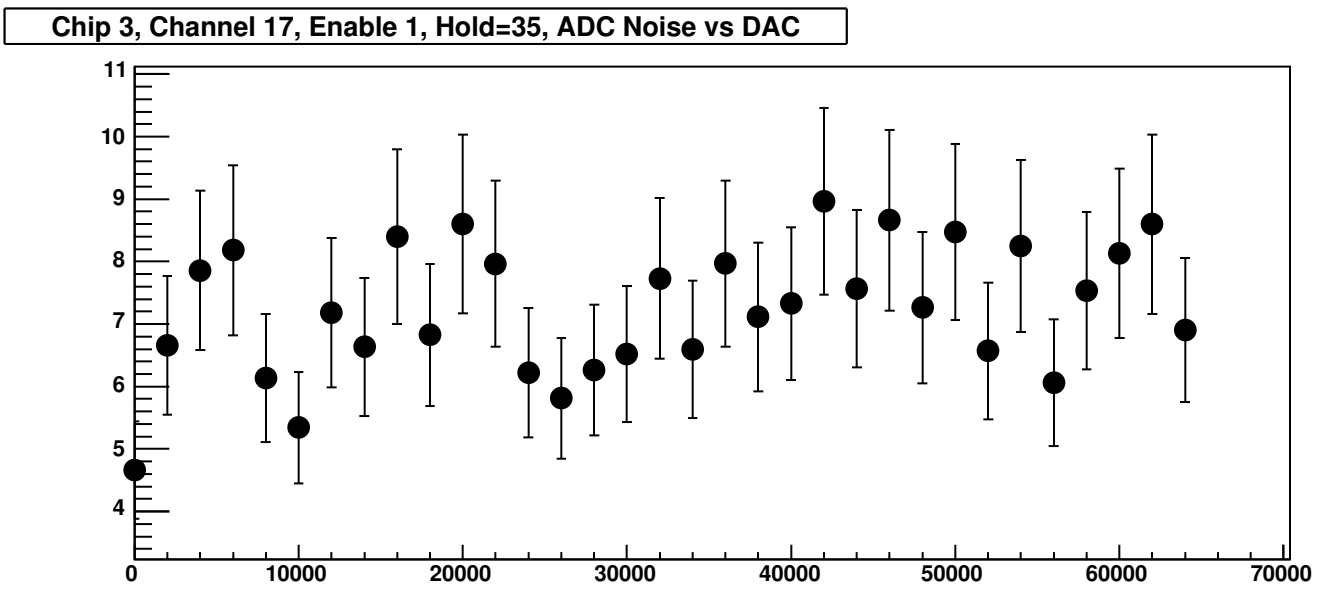


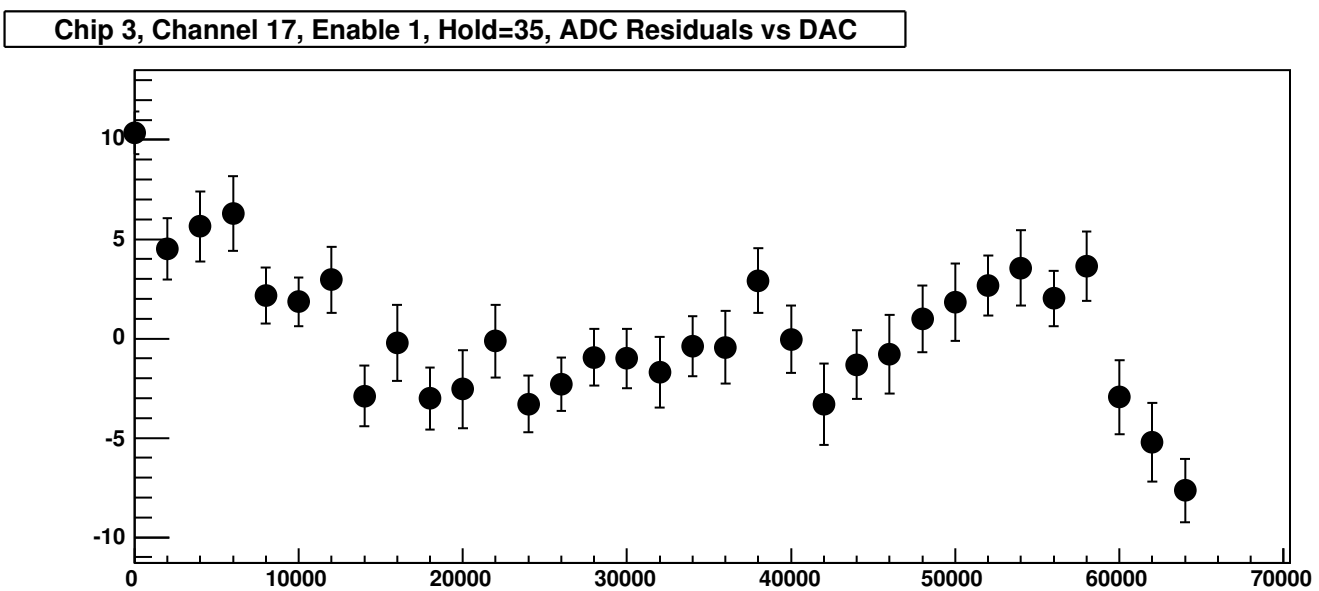

Chip 3, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 48.24 / 23
p0 -4.83 ± 0.6968
p1 0.00313 ± 2.116e-05
220
200
180
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000
70000

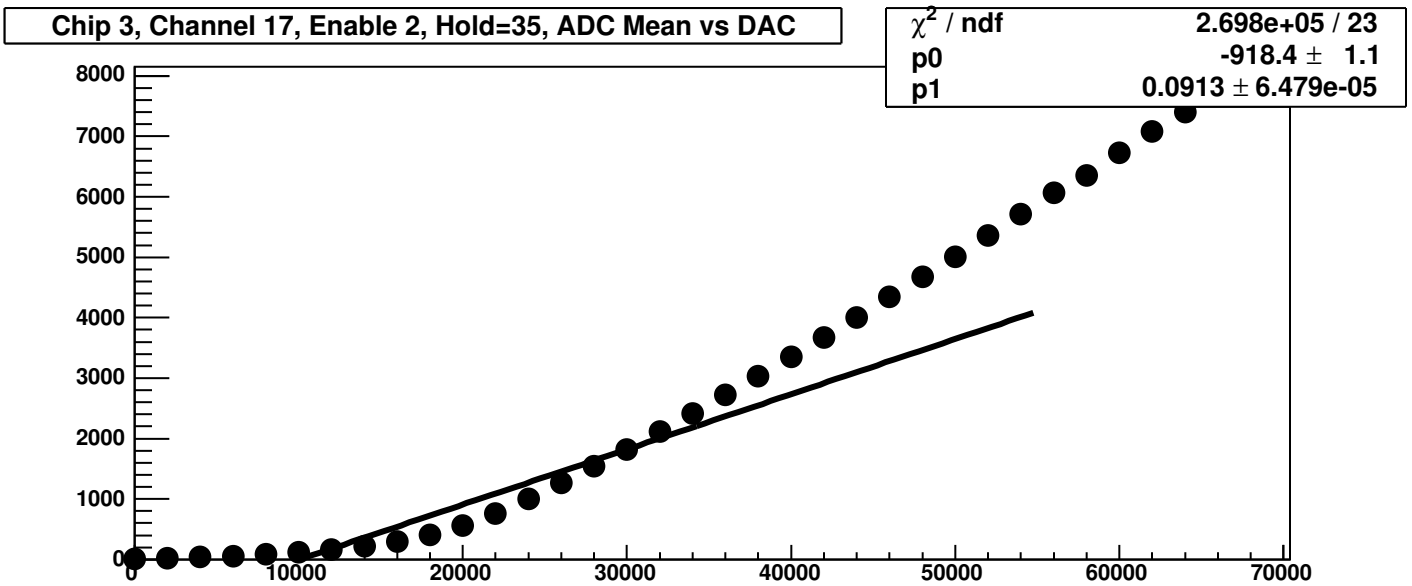

Chip 3, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

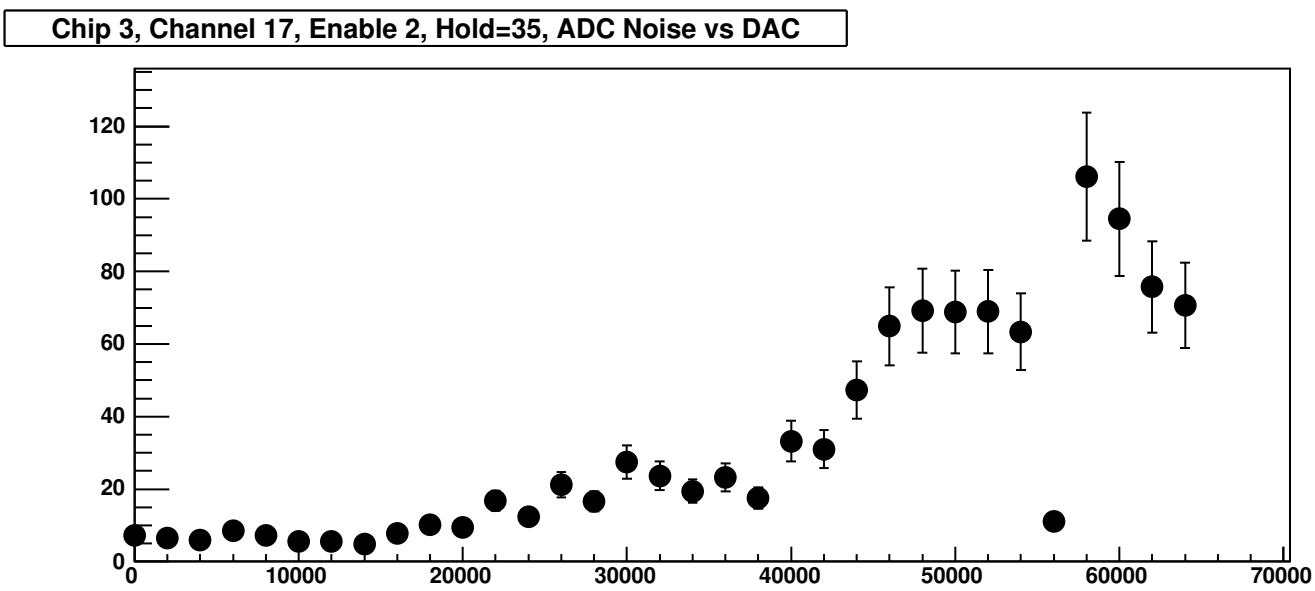

Chip 3, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

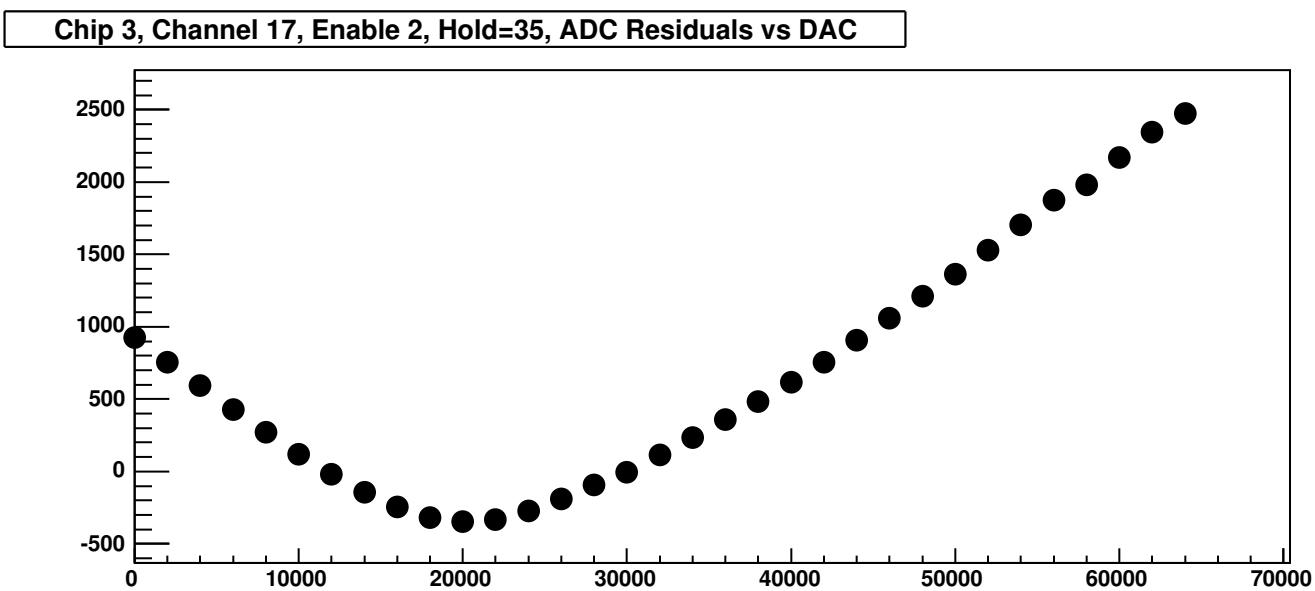

Chip 3, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 52.75 / 23
p0 -7.575 ± 0.7296
p1 0.002845 ± 2.29e-05
180
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000
70000

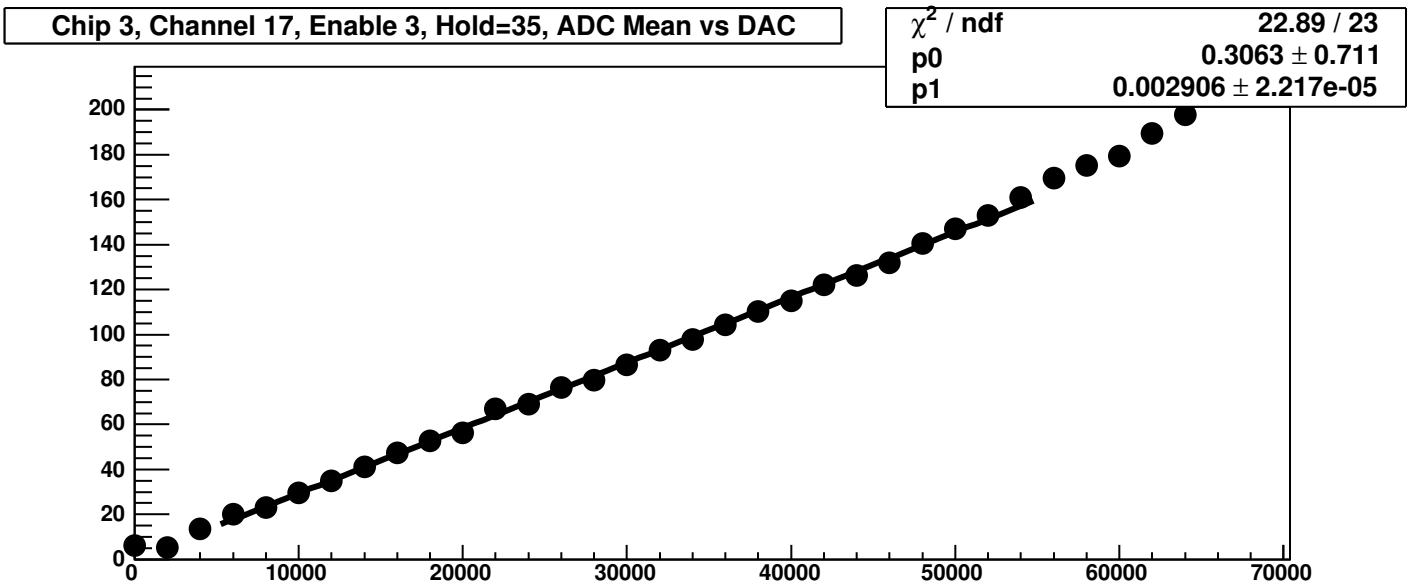

Chip 3, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

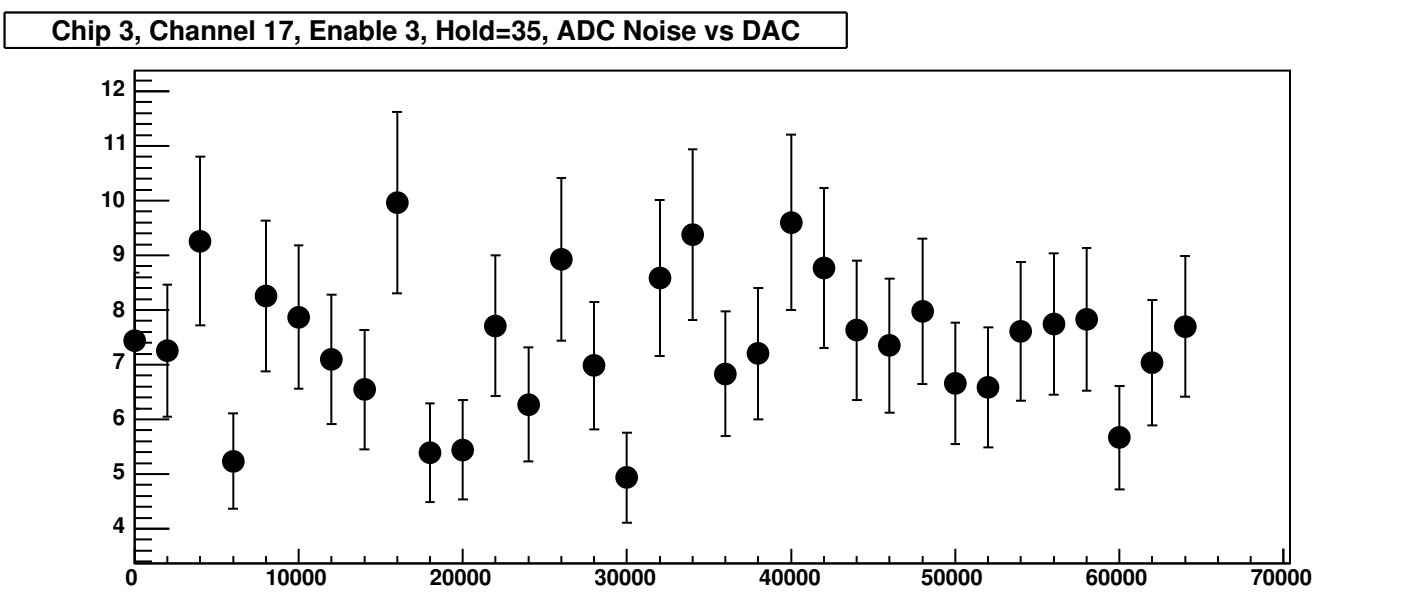

Chip 3, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

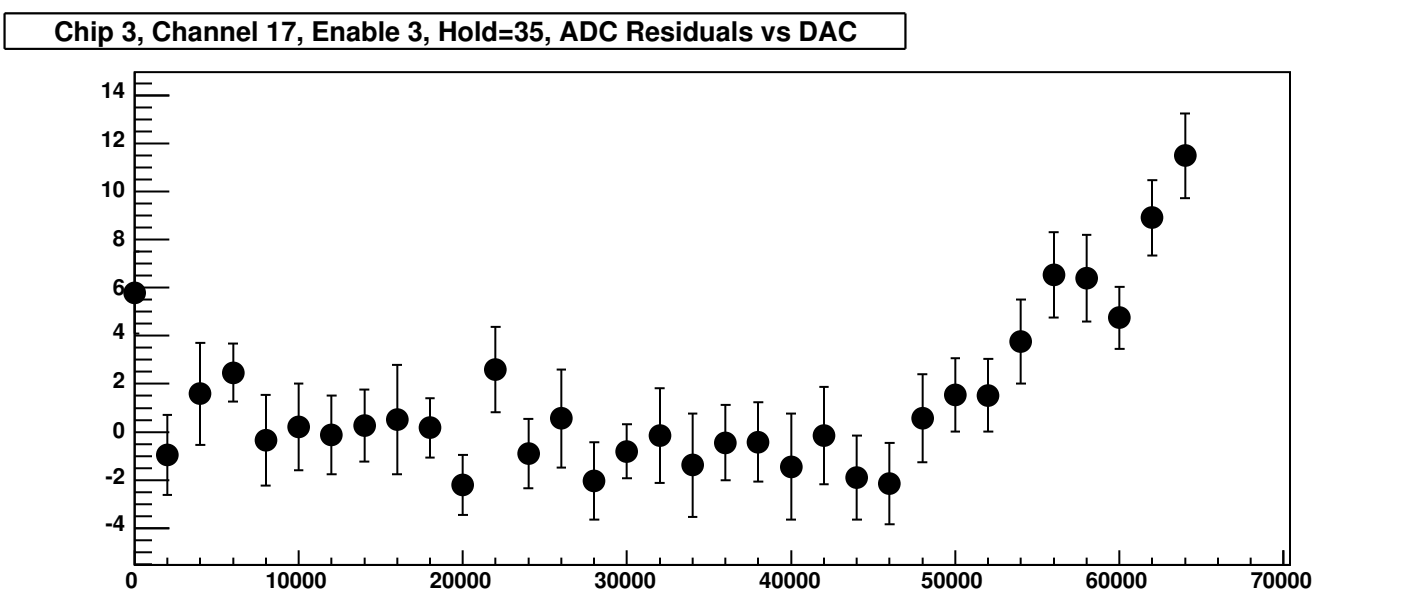

Chip 3, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 2.698e+05 / 23
p0 -918.4 ± 1.1
p1 0.0913 ± 6.479e-05
8000
7000
6000
5000
4000
3000
2000
1000
0
0
10000
20000
30000
40000
50000
60000
70000

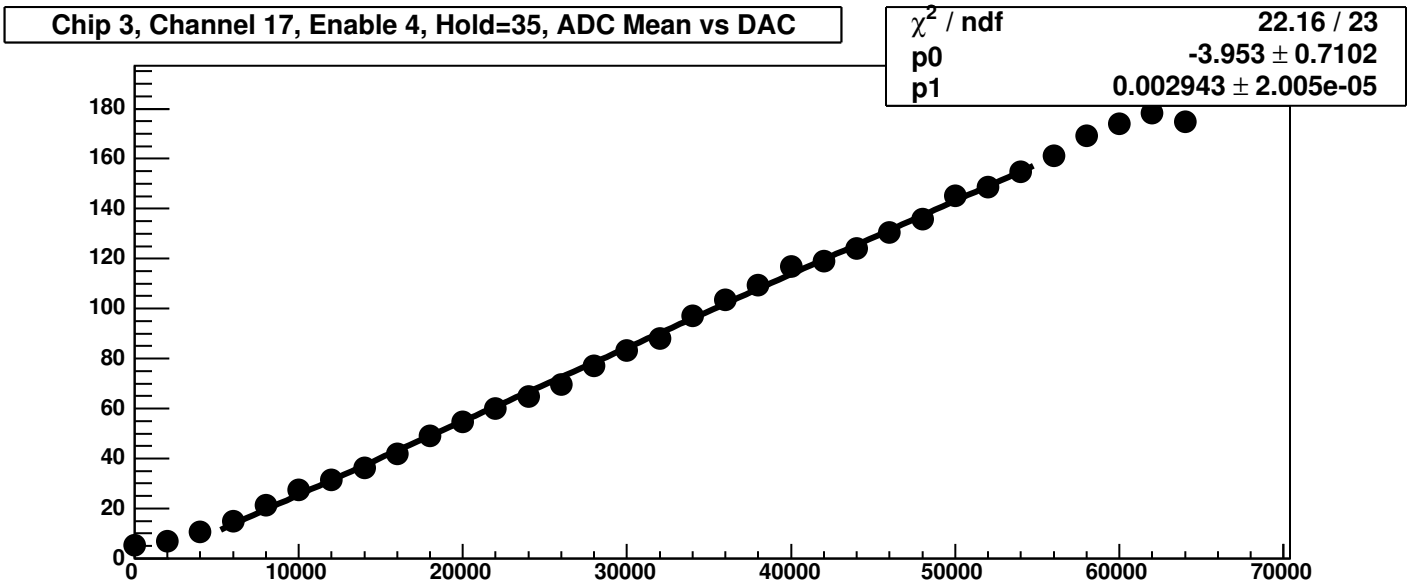

Chip 3, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000
70000

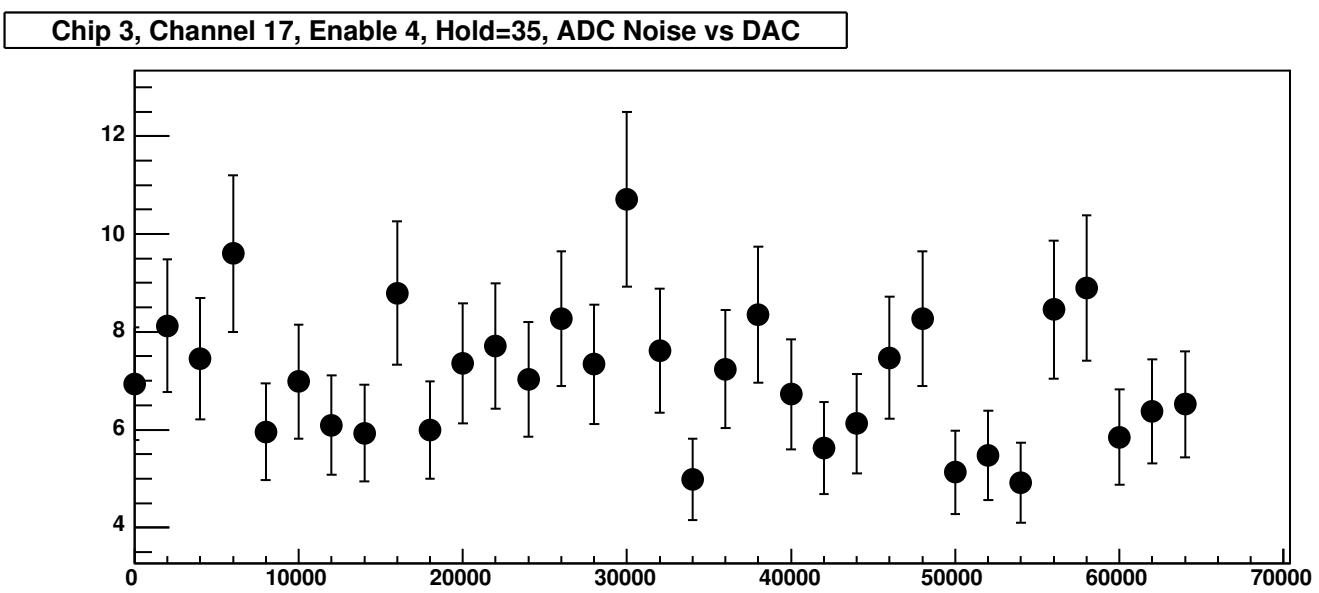

Chip 3, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
2500
2000
1500
1000
500
0
-500
0
10000
20000
30000
40000
50000
60000
70000

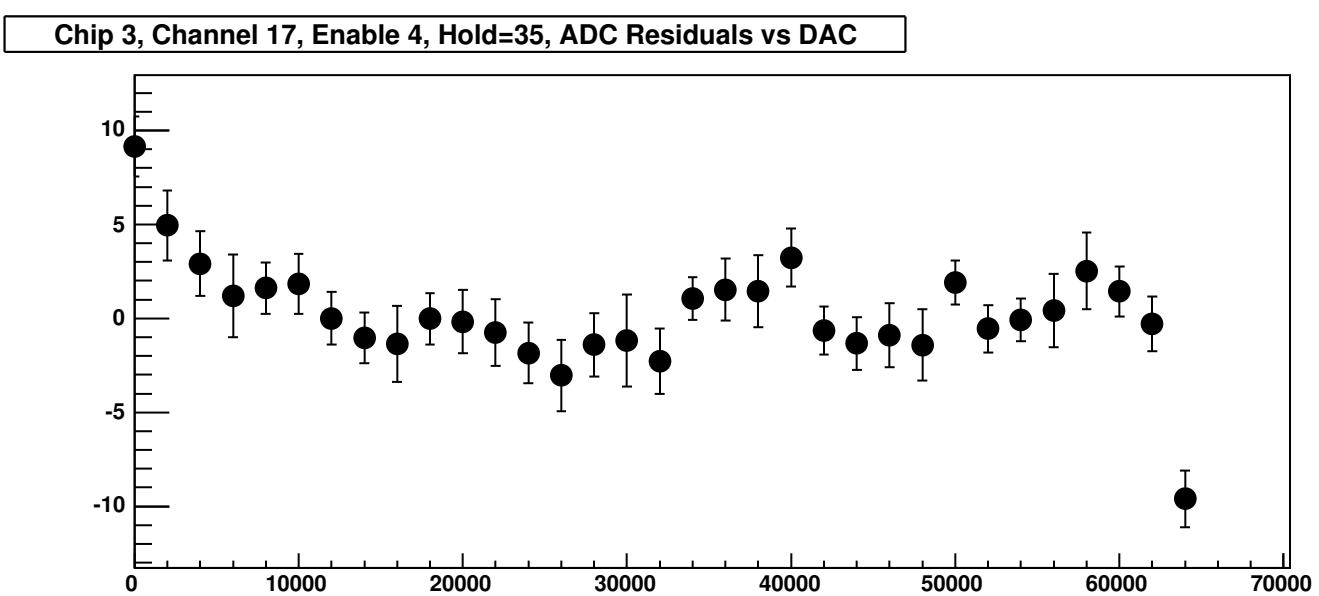

Chip 3, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 22.89 / 23
p0 0.3063 ± 0.711
p1 0.002906 ± 2.217e-05
0 20 40 60 80 100 120 140 160 180 200
0 10000 20000 30000 40000 50000 60000 70000

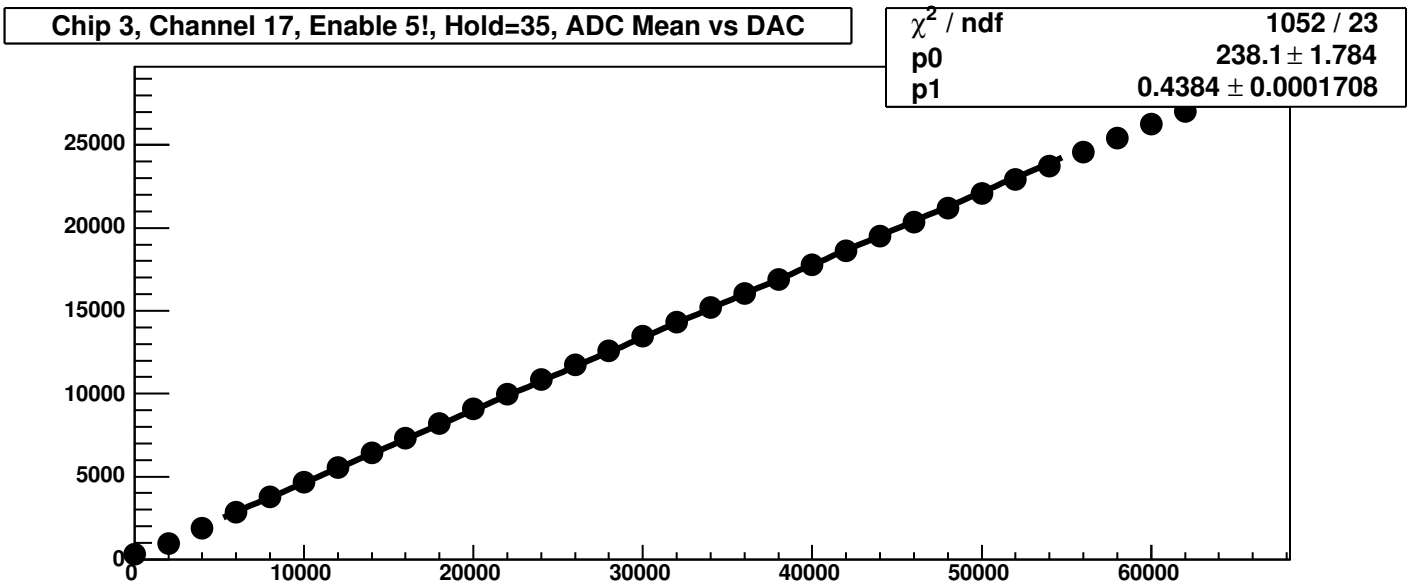

Chip 3, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
4 5 6 7 8 9 10 11 12
0 10000 20000 30000 40000 50000 60000 70000

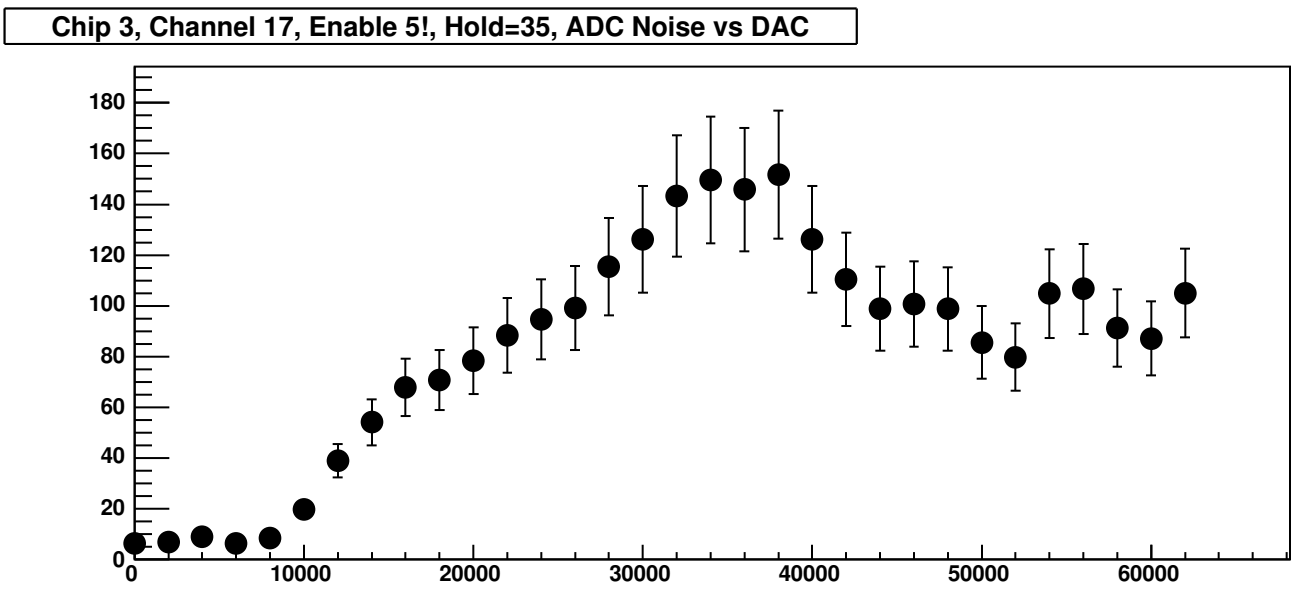

Chip 3, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
-4 -2 0 2 4 6 8 10 12 14
0 10000 20000 30000 40000 50000 60000 70000

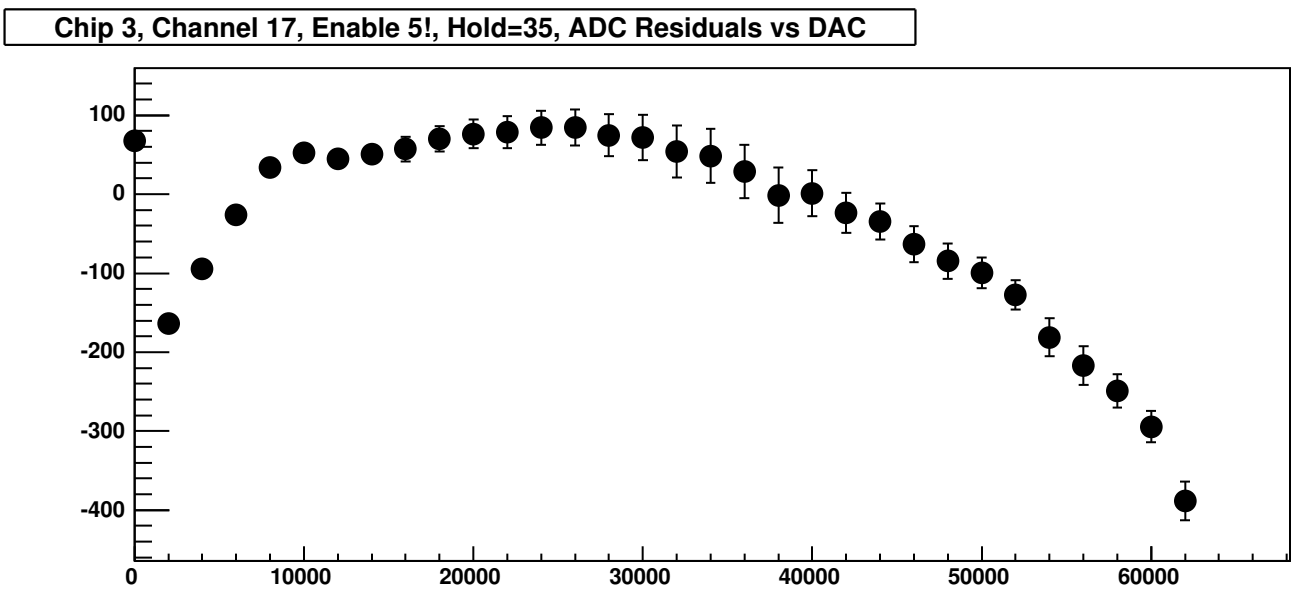

Chip 3, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 22.16 / 23
p0 -3.953 ± 0.7102
p1 0.002943 ± 2.005e-05
180
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000
70000

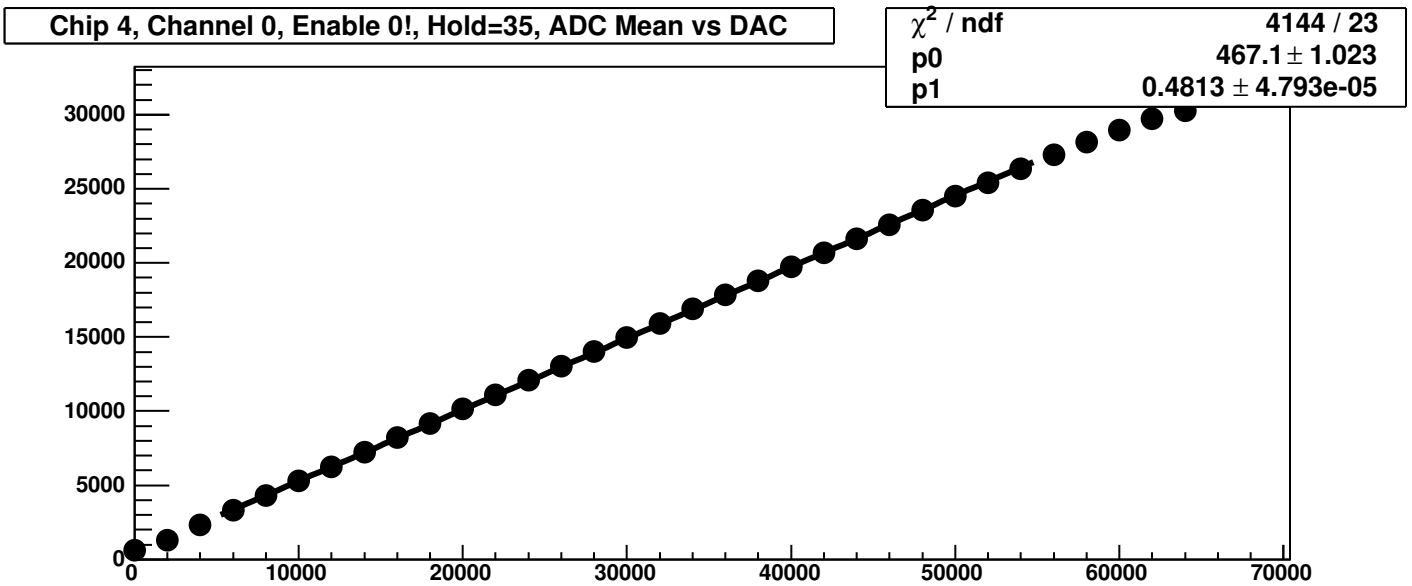

Chip 3, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

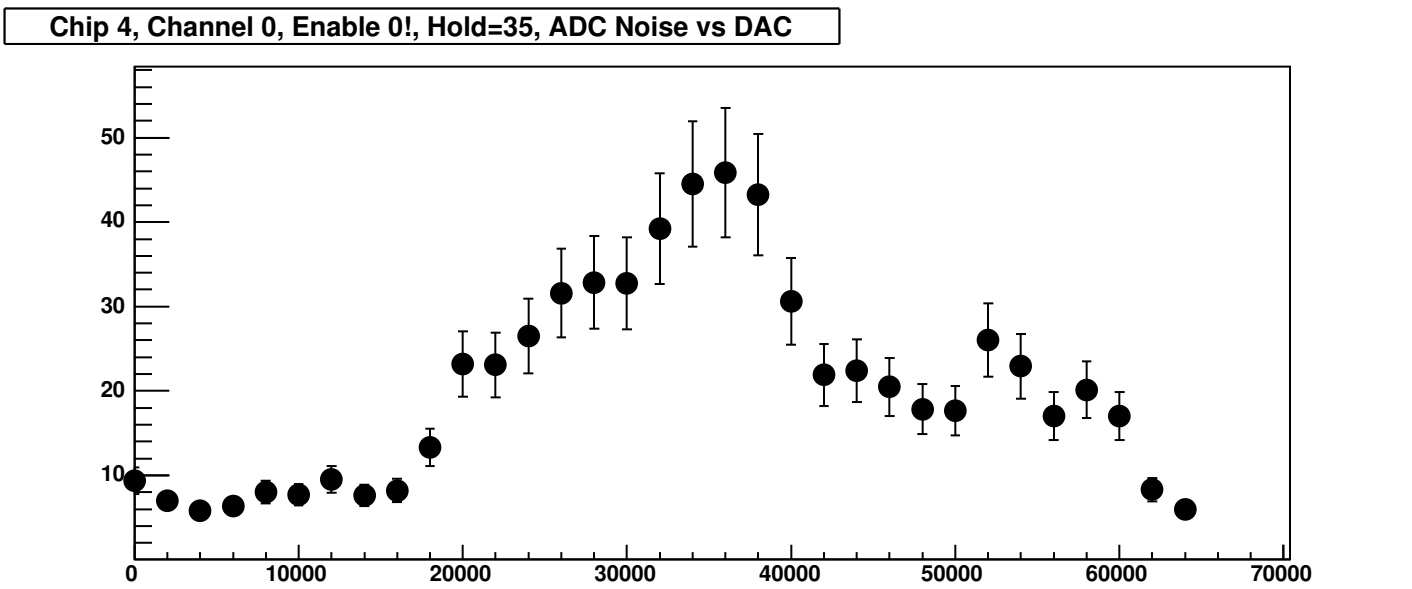

Chip 3, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

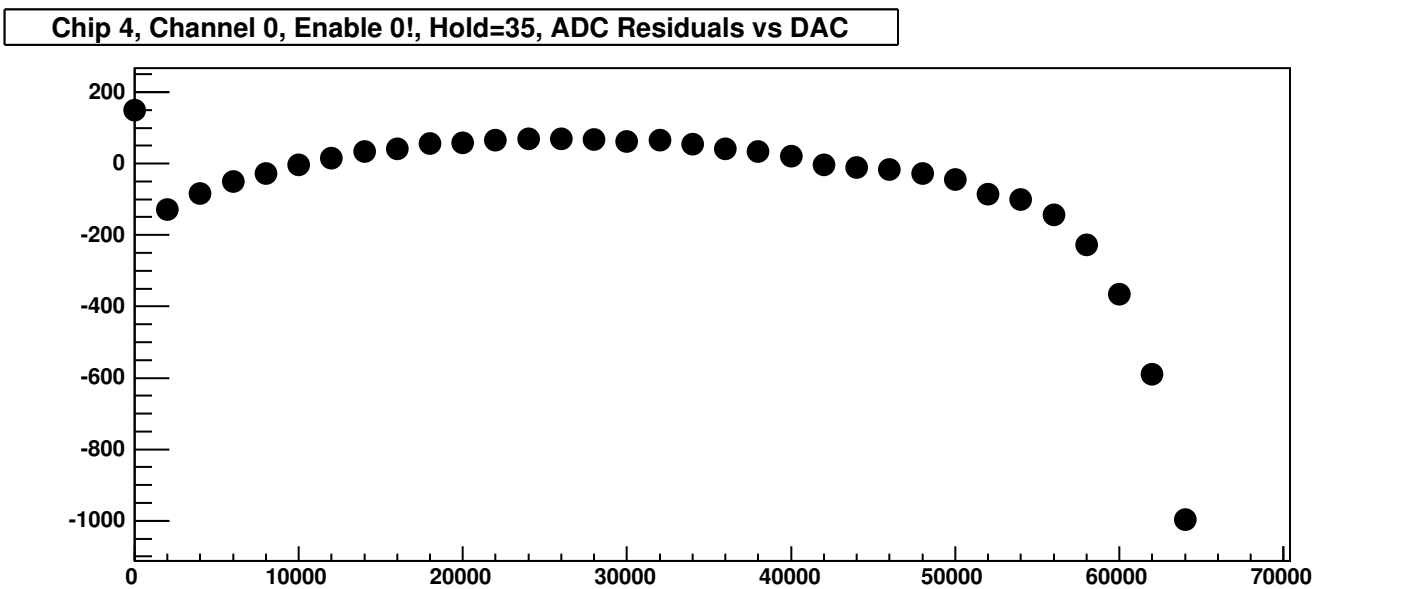

Chip 3, Channel 17, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 1052 / 23
p0 238.1 ± 1.784
p1 0.4384 ± 0.0001708
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

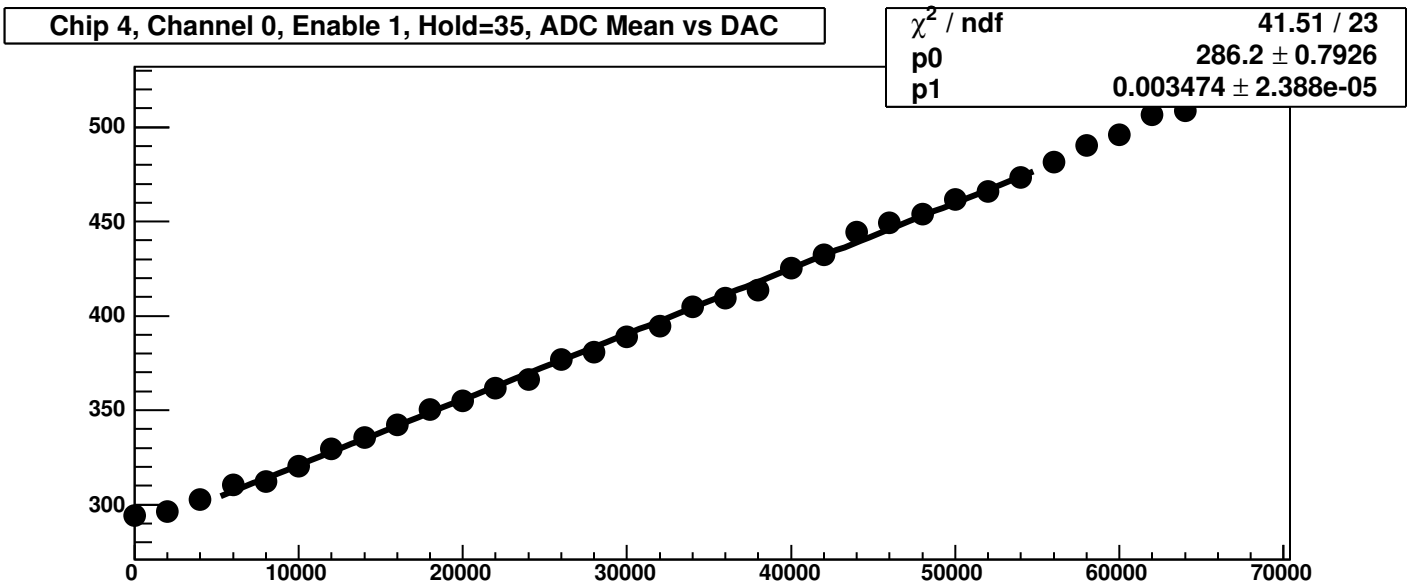

Chip 3, Channel 17, Enable 5!, Hold=35, ADC Noise vs DAC
180
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

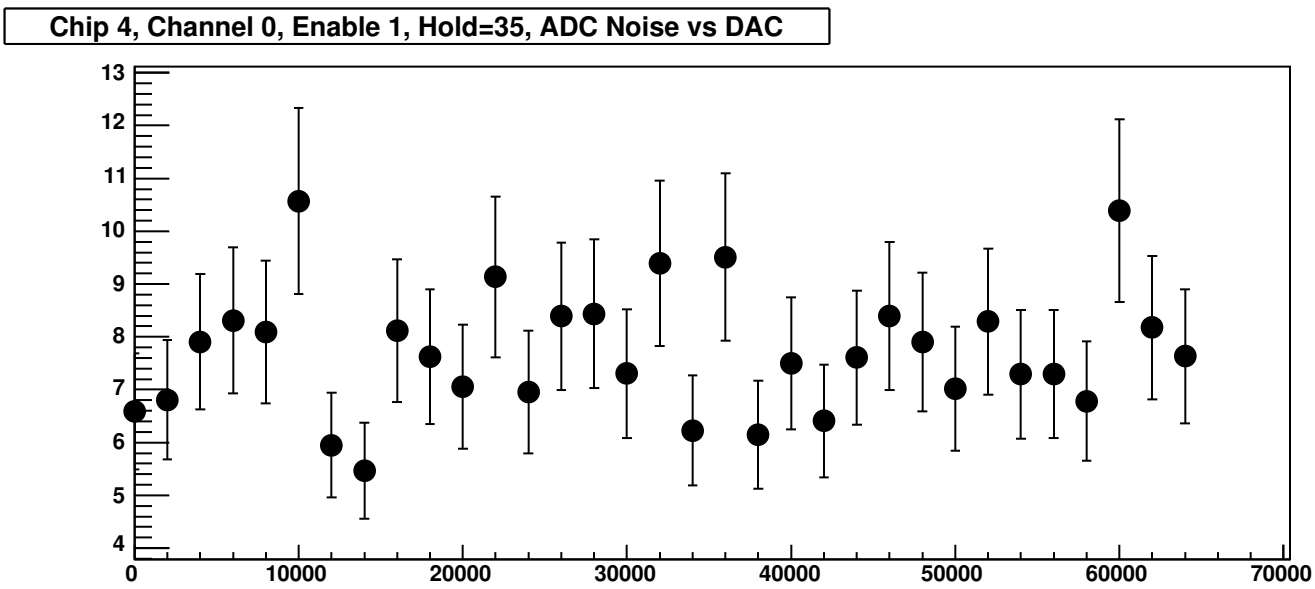

Chip 3, Channel 17, Enable 5!, Hold=35, ADC Residuals vs DAC
100
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000

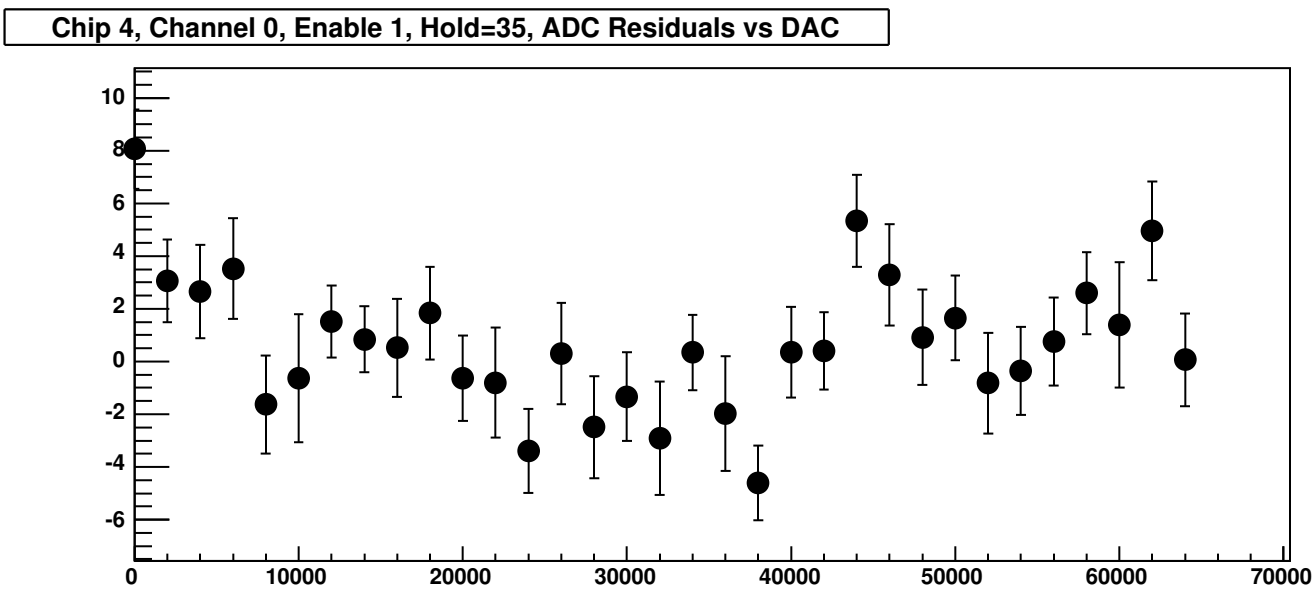

Chip 4, Channel 0, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 4144 / 23
p0 467.1 ± 1.023
p1 0.4813 ± 4.793e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 0, Enable 0!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000

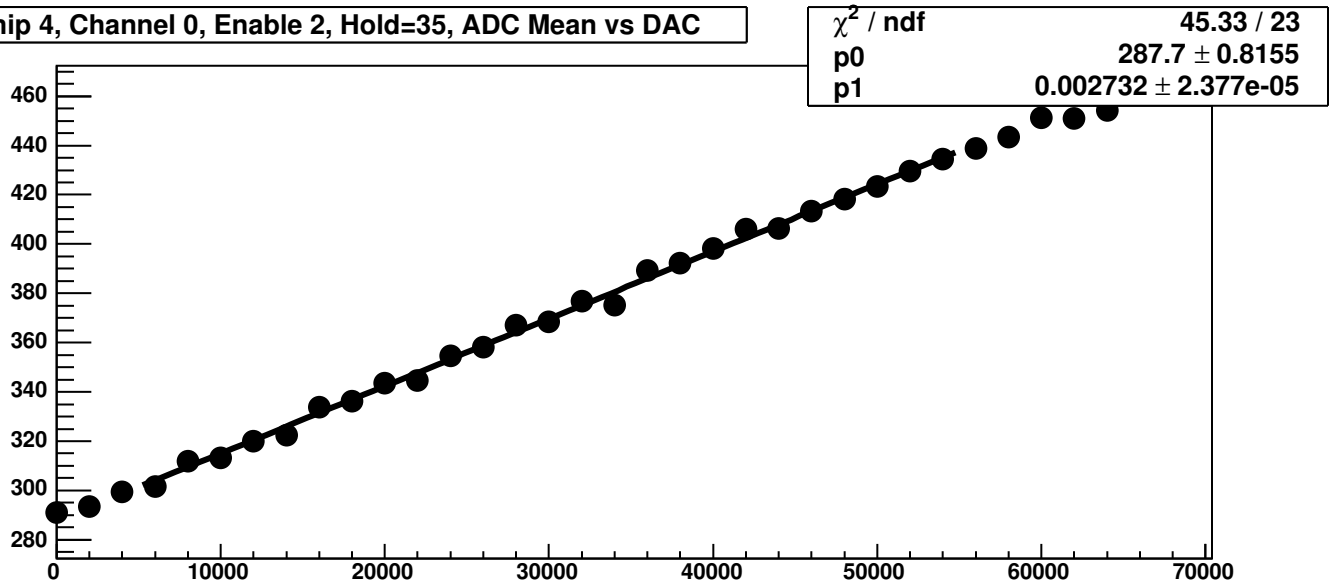

Chip 4, Channel 0, Enable 0!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 41.51 / 23
p0 286.2 ± 0.7926
p1 0.003474 ± 2.388e-05
500
450
400
350
300
0
10000
20000
30000
40000
50000
60000
70000

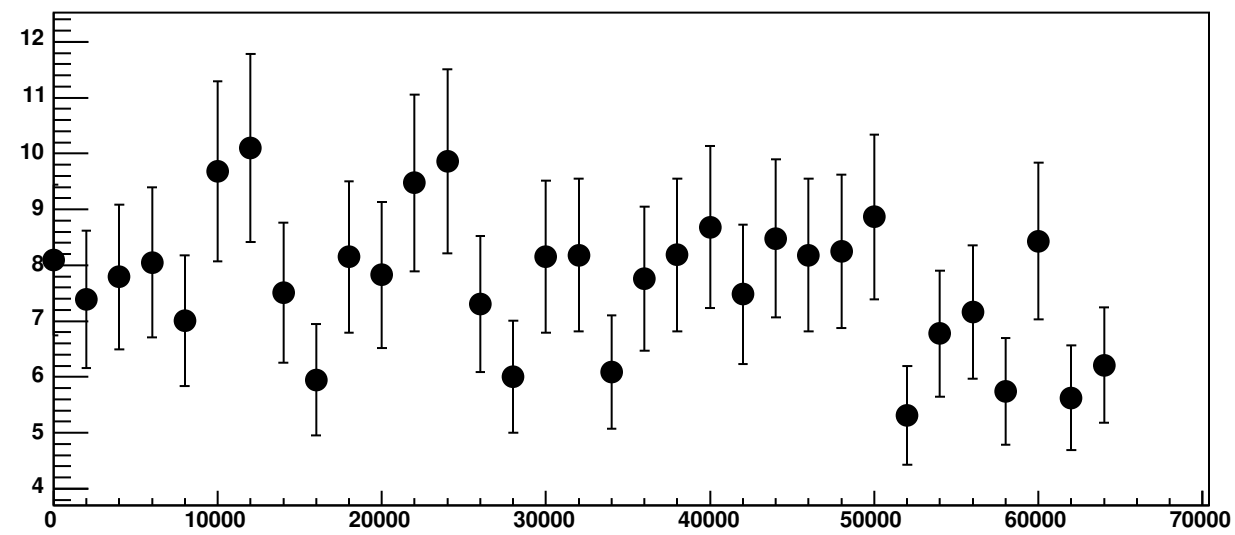

Chip 4, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

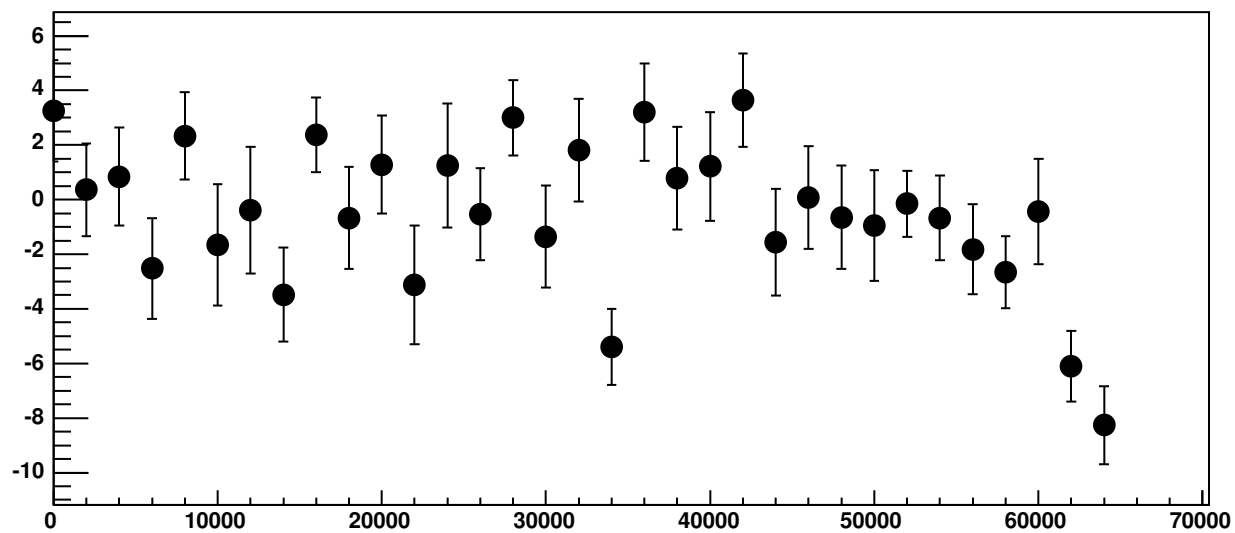
Chip 4, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC

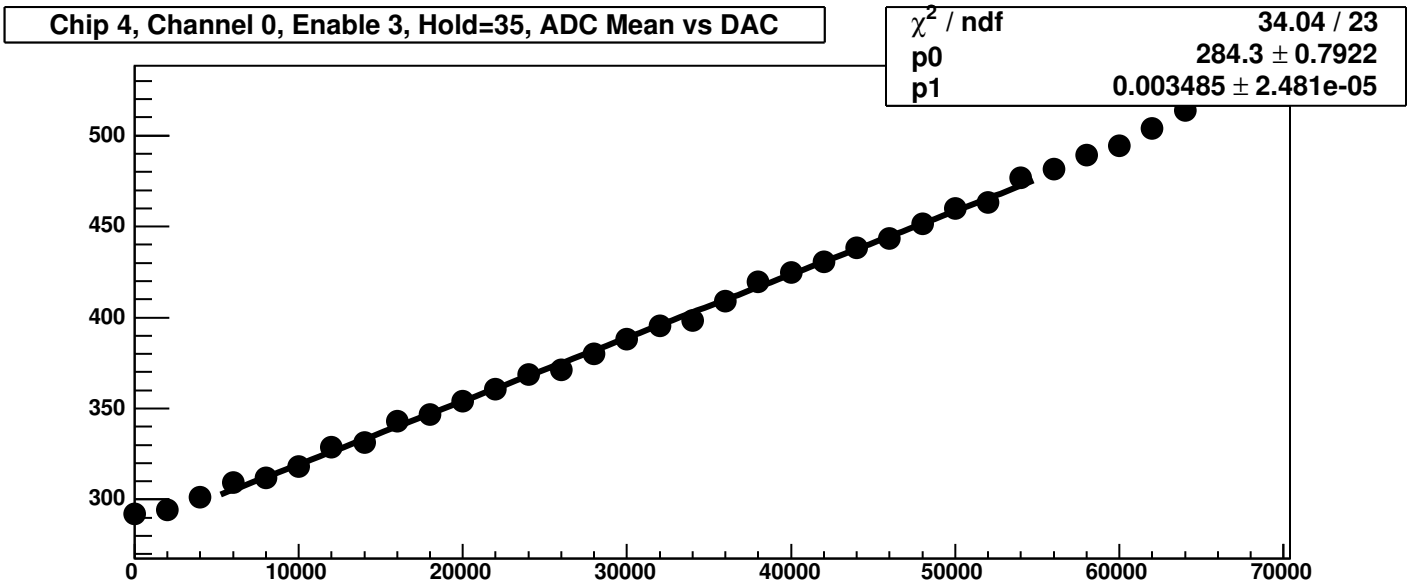


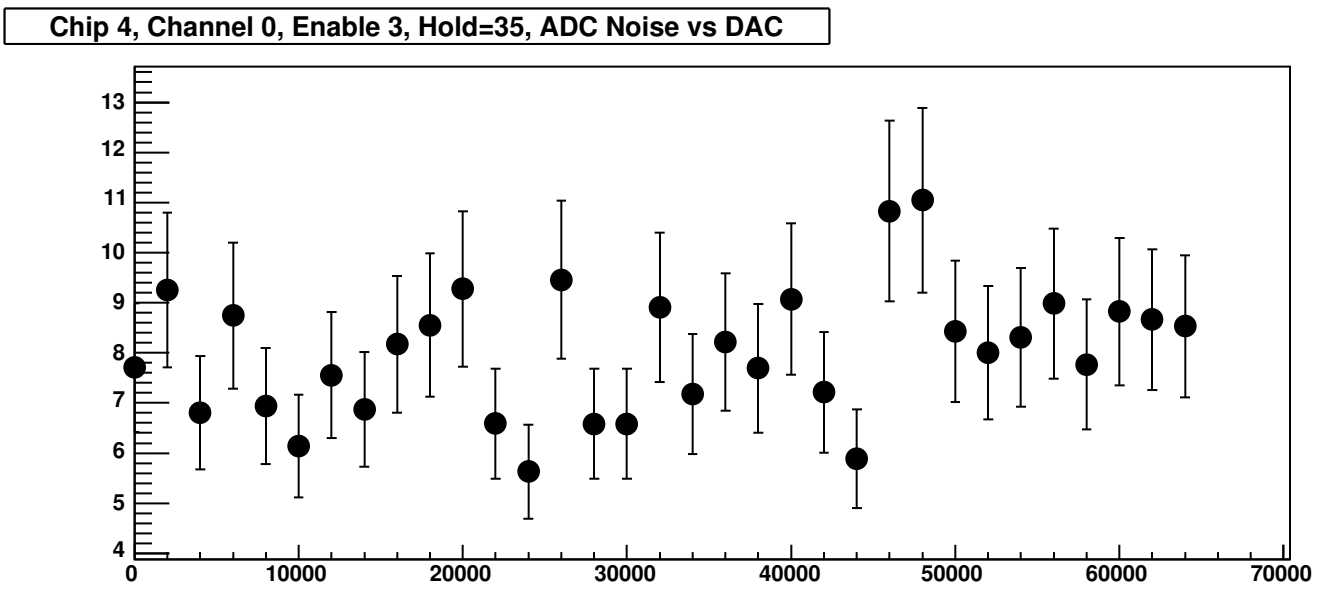
Chip 4, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC

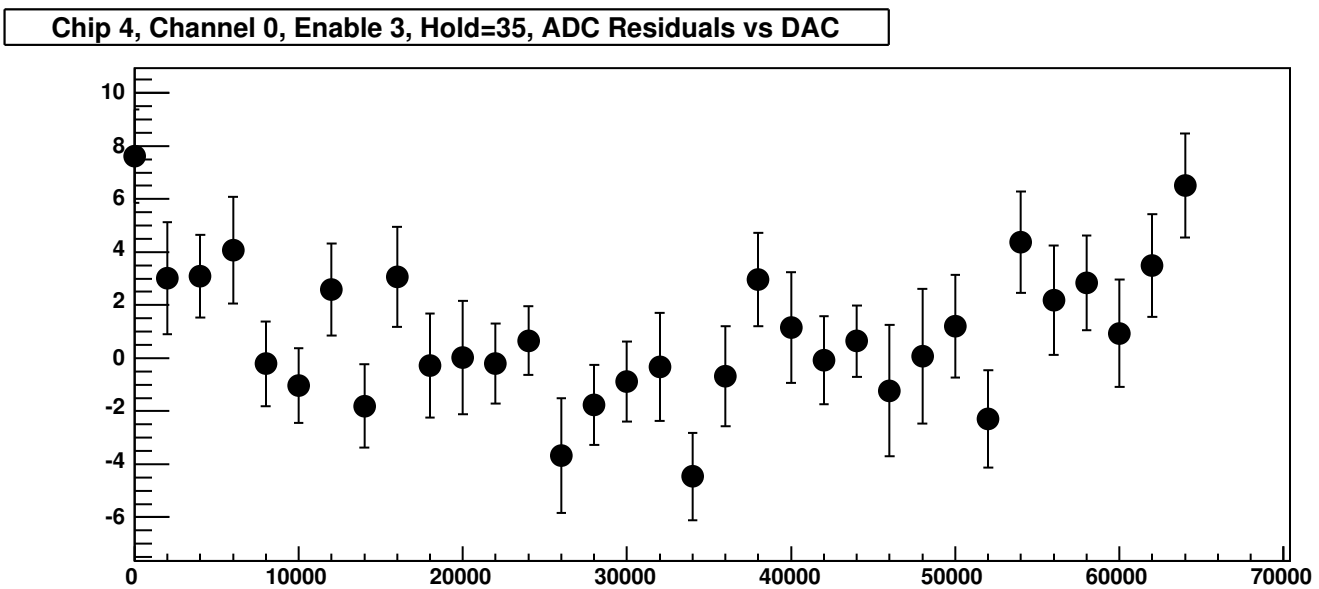


Chip 4, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC

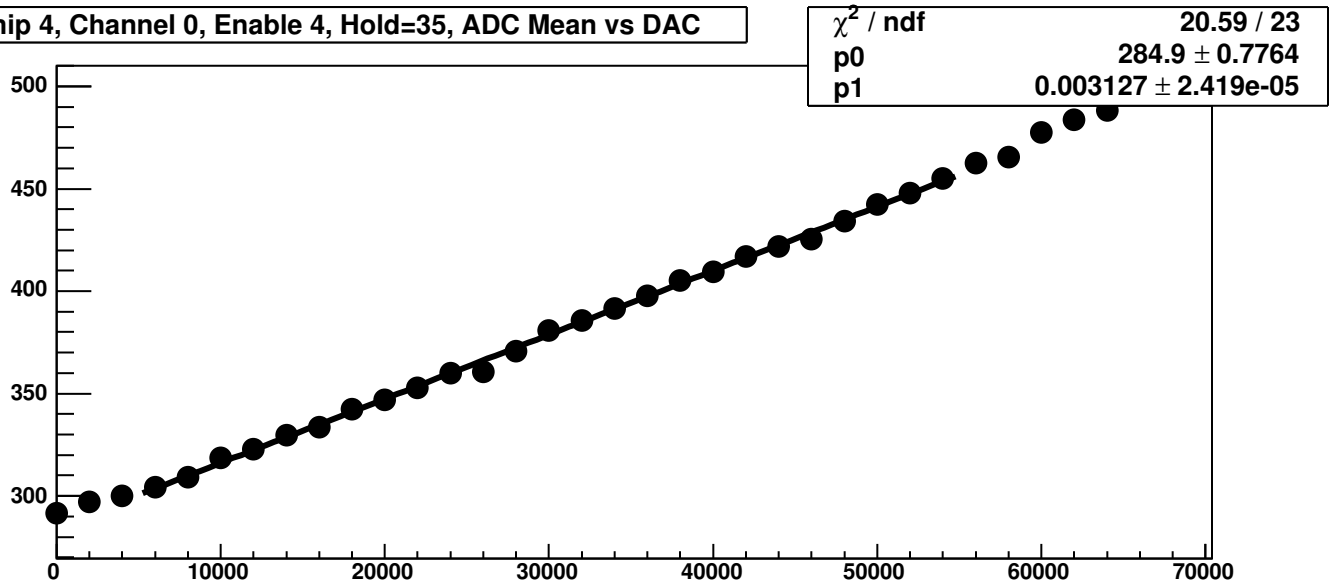



Chip 4, Channel 0, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 34.04 / 23
p0 284.3 ± 0.7922
p1 0.003485 ± 2.481e-05
500
450
400
350
300
0
10000
20000
30000
40000
50000
60000
70000

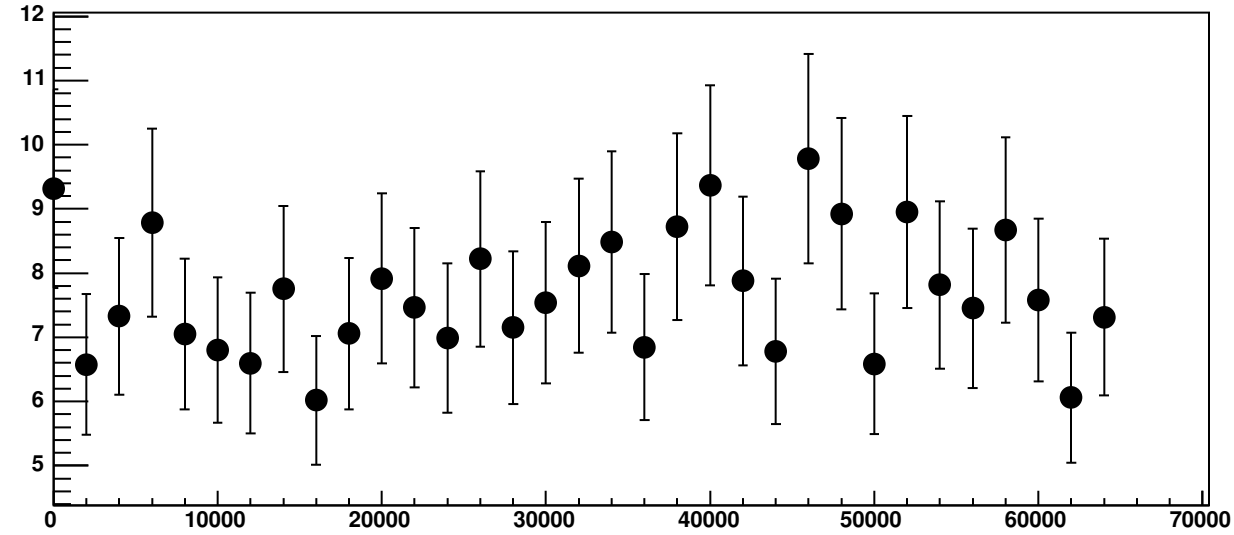

Chip 4, Channel 0, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 0, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

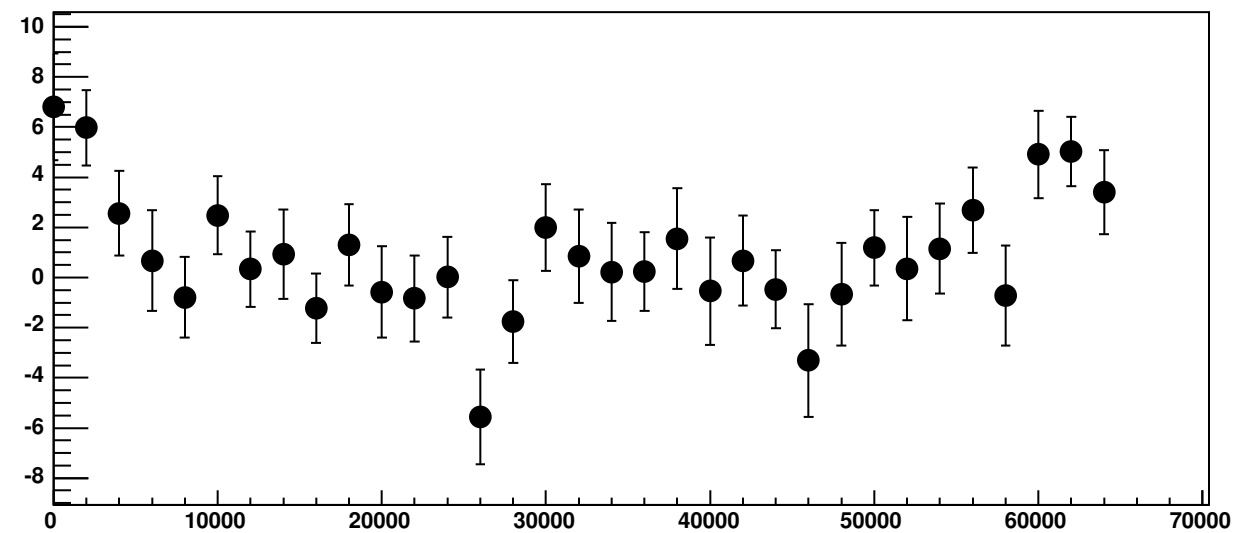
Chip 4, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

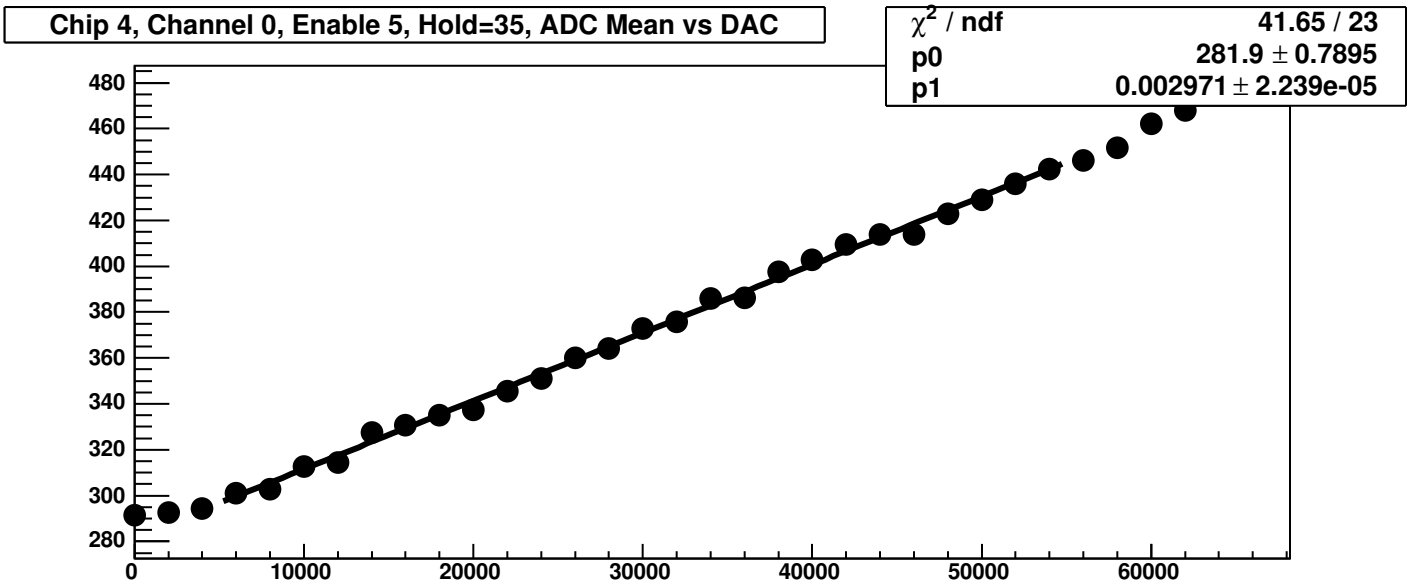


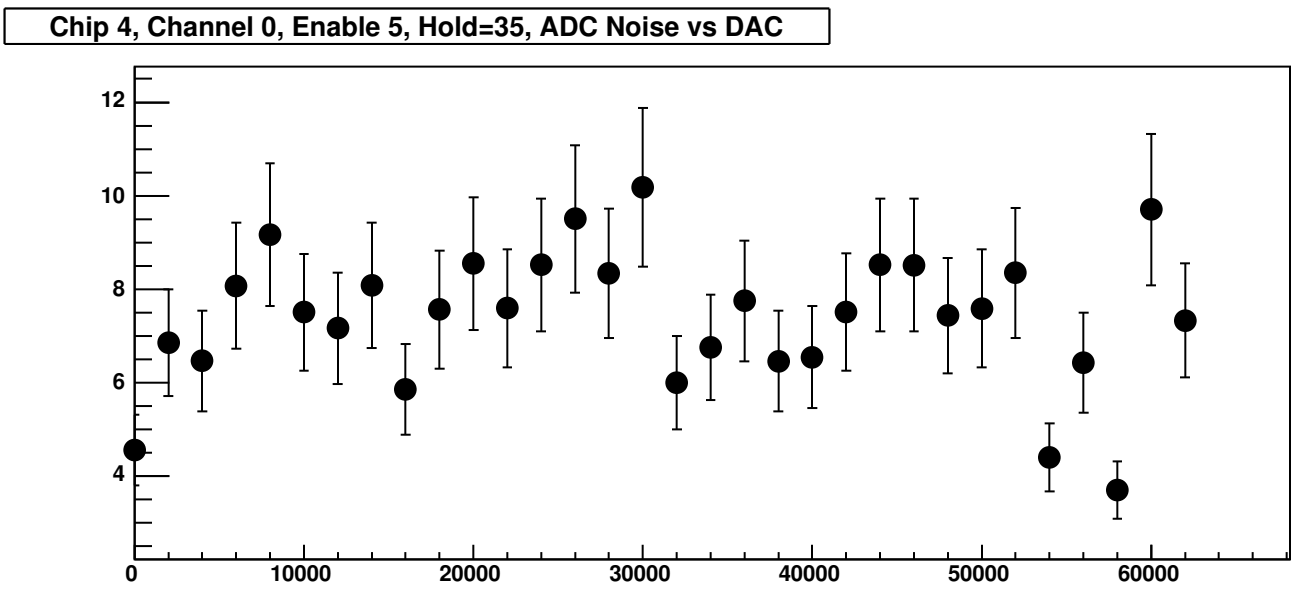
Chip 4, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

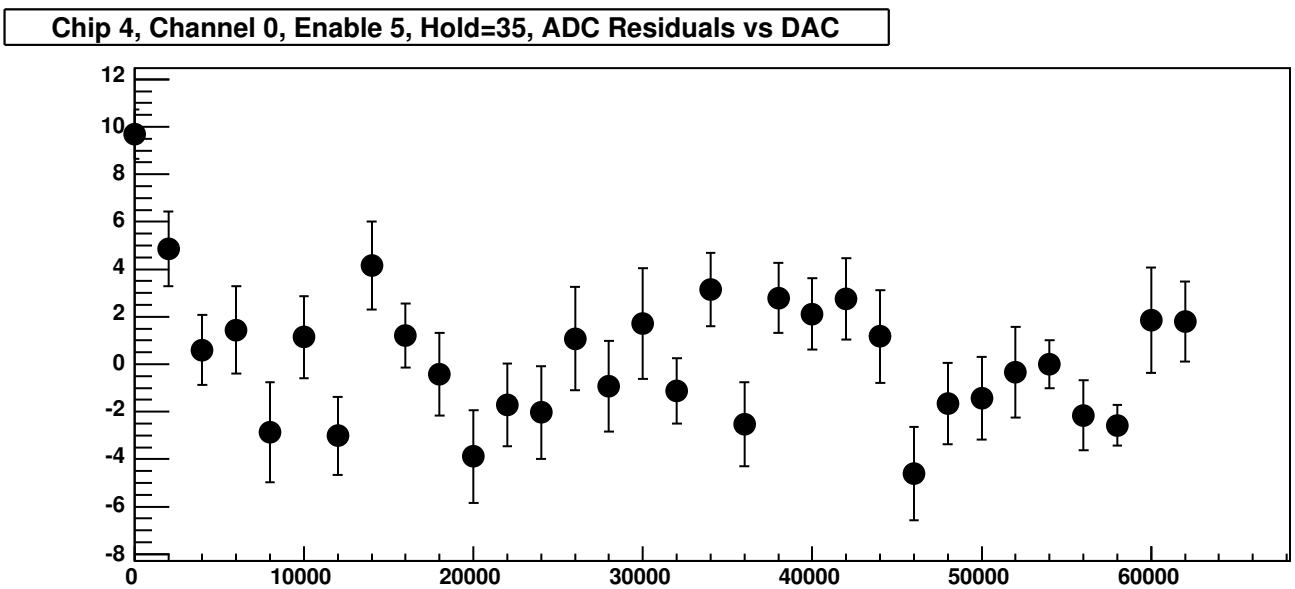


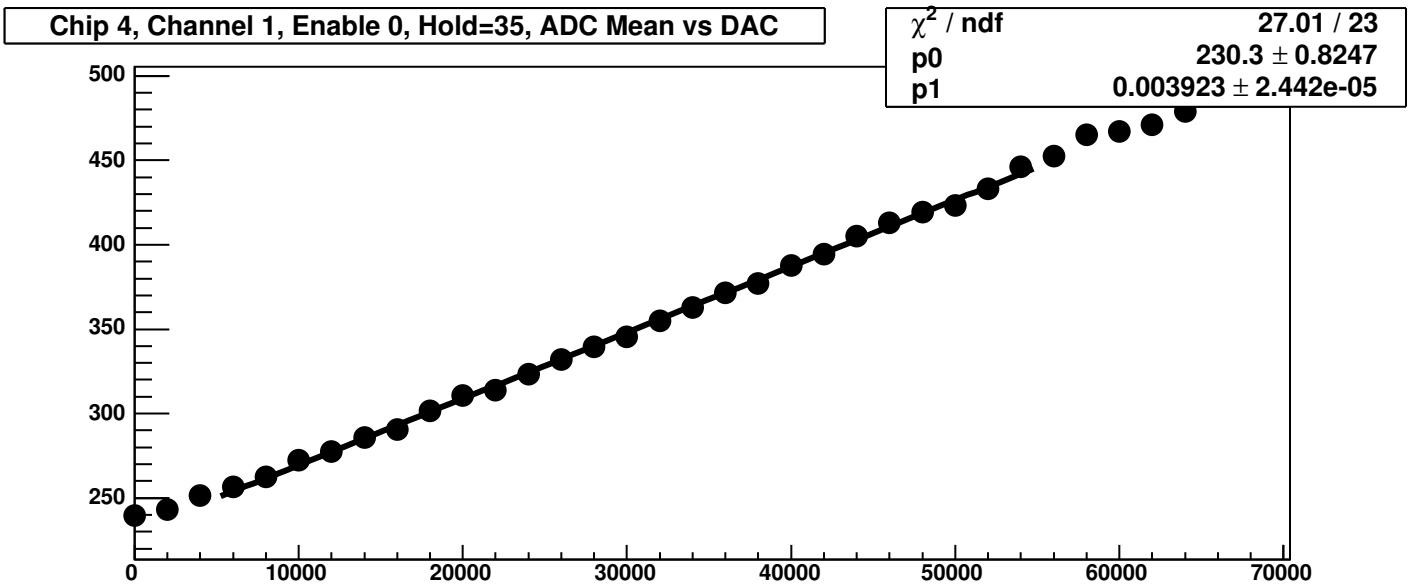
Chip 4, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

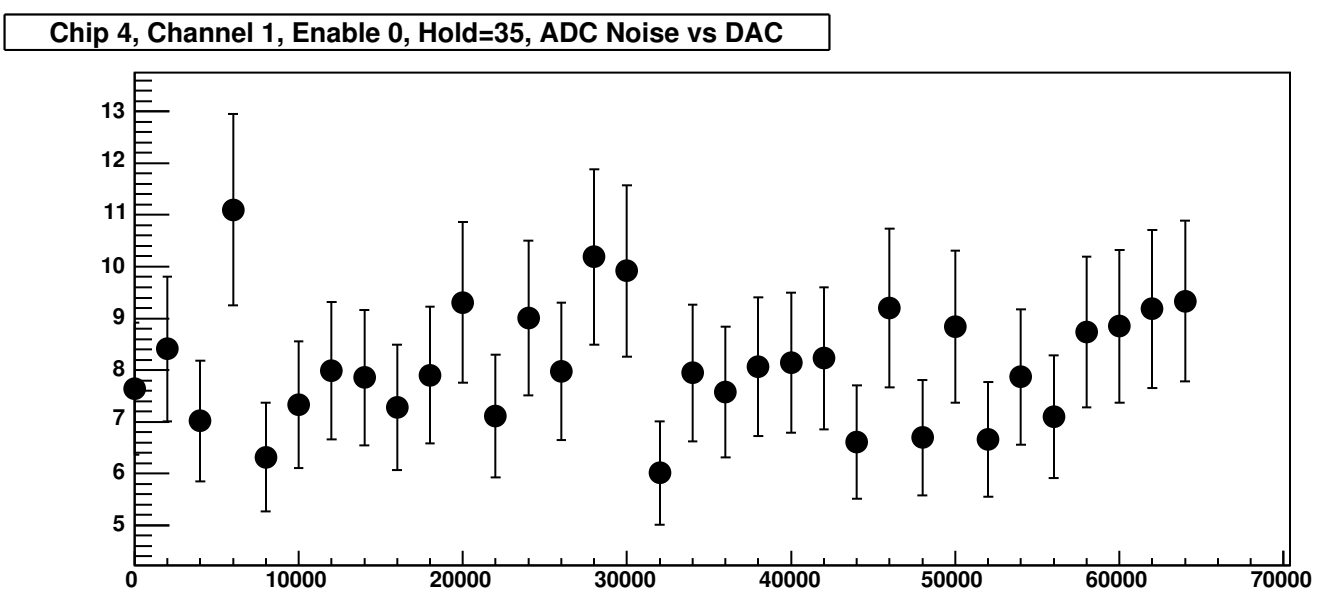


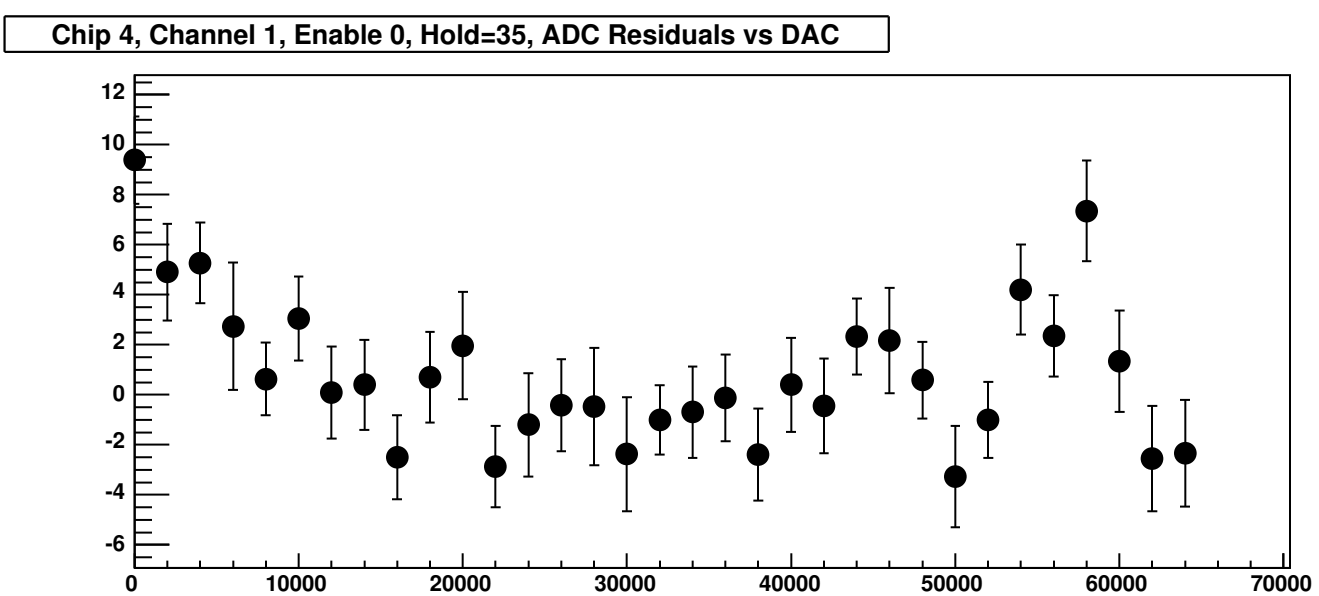

Chip 4, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 41.65 / 23
p0 281.9 ± 0.7895
p1 0.002971 ± 2.239e-05
480
460
440
420
400
380
360
340
320
300
280
0
10000
20000
30000
40000
50000
60000

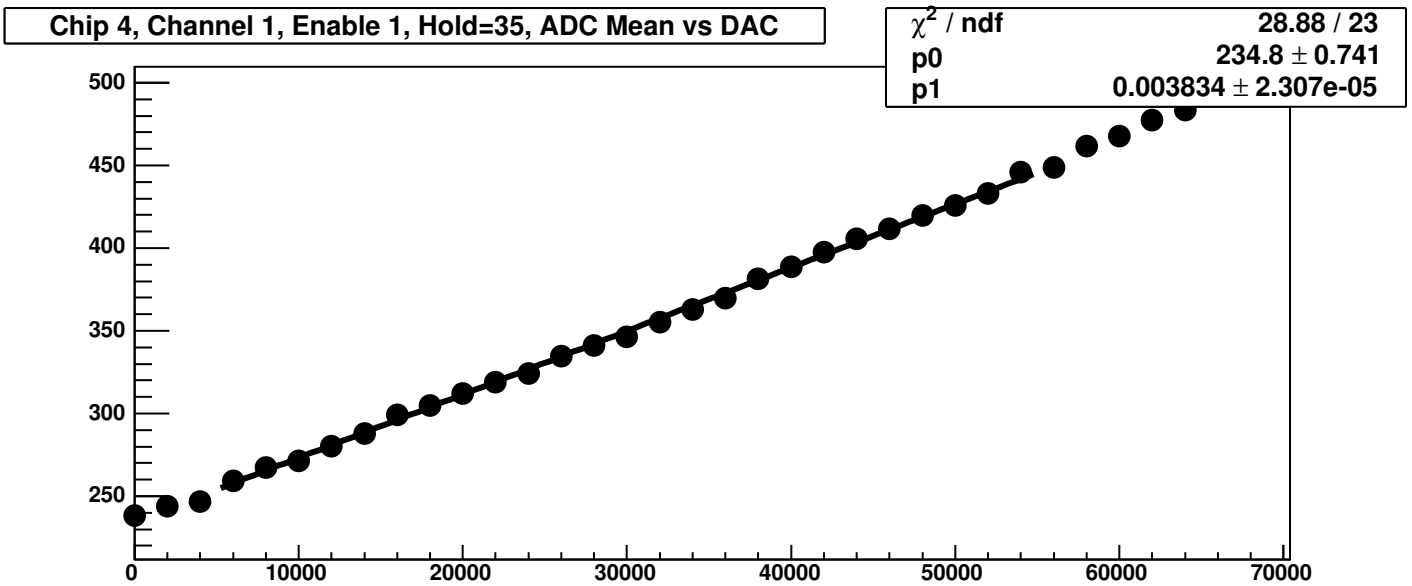

Chip 4, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000

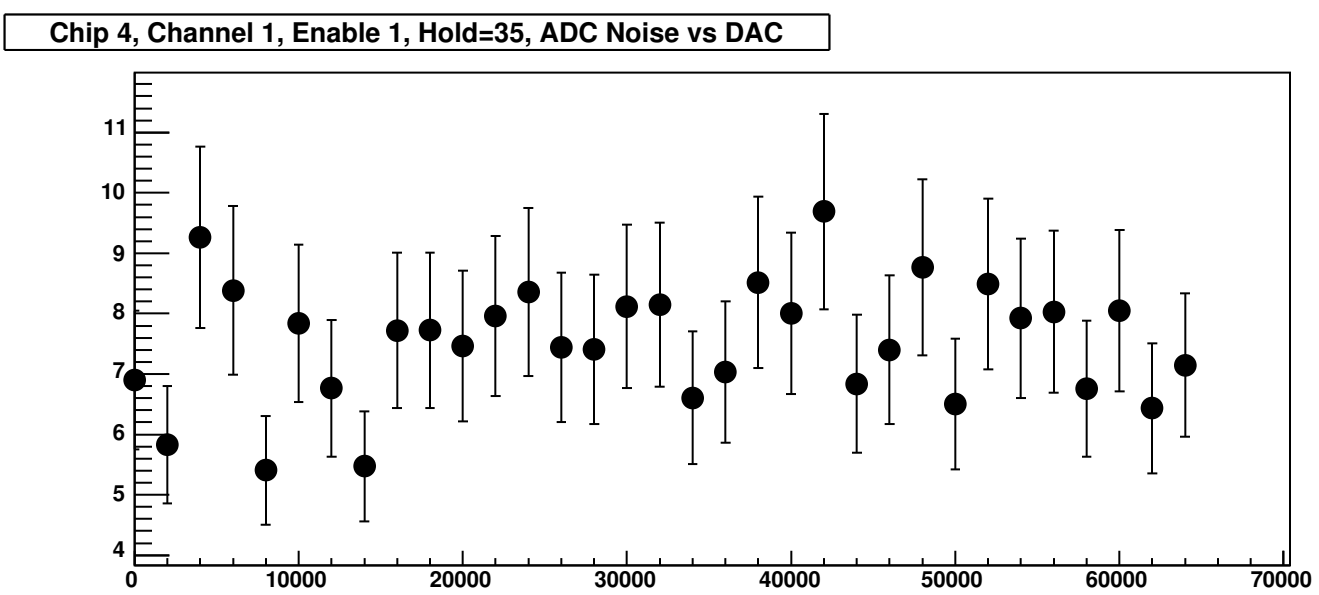

Chip 4, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000

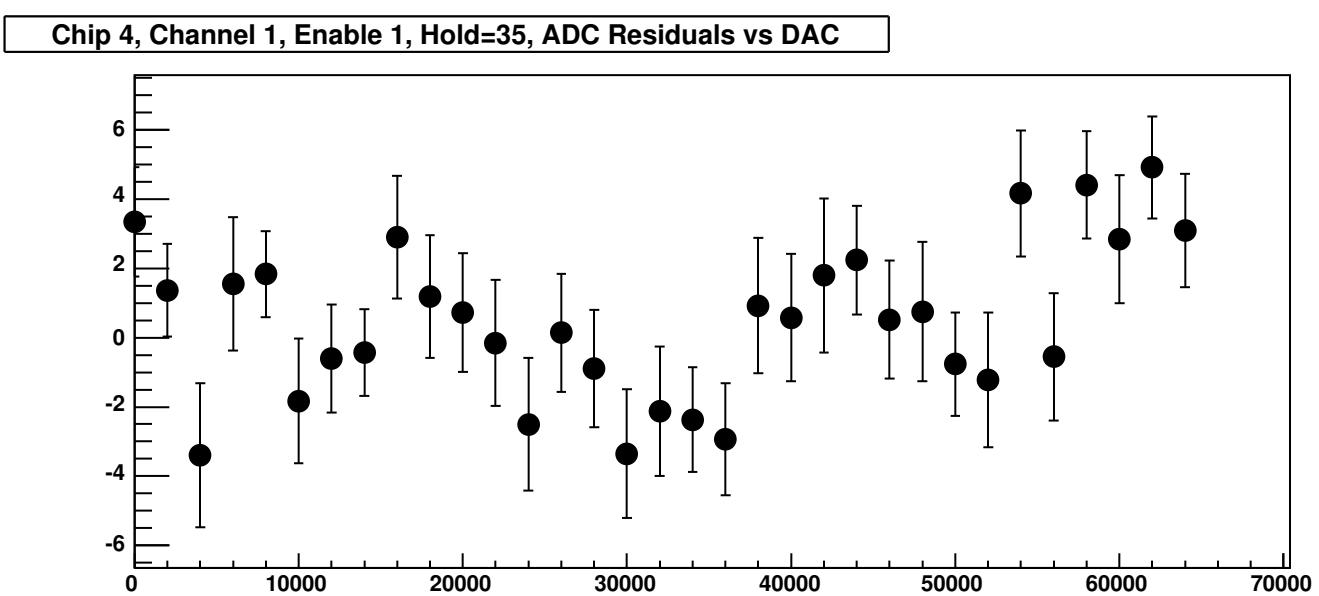

Chip 4, Channel 1, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 27.01 / 23
p0 230.3 ± 0.8247
p1 0.003923 ± 2.442e-05
500
450
400
350
300
250
0
10000
20000
30000
40000
50000
60000
70000

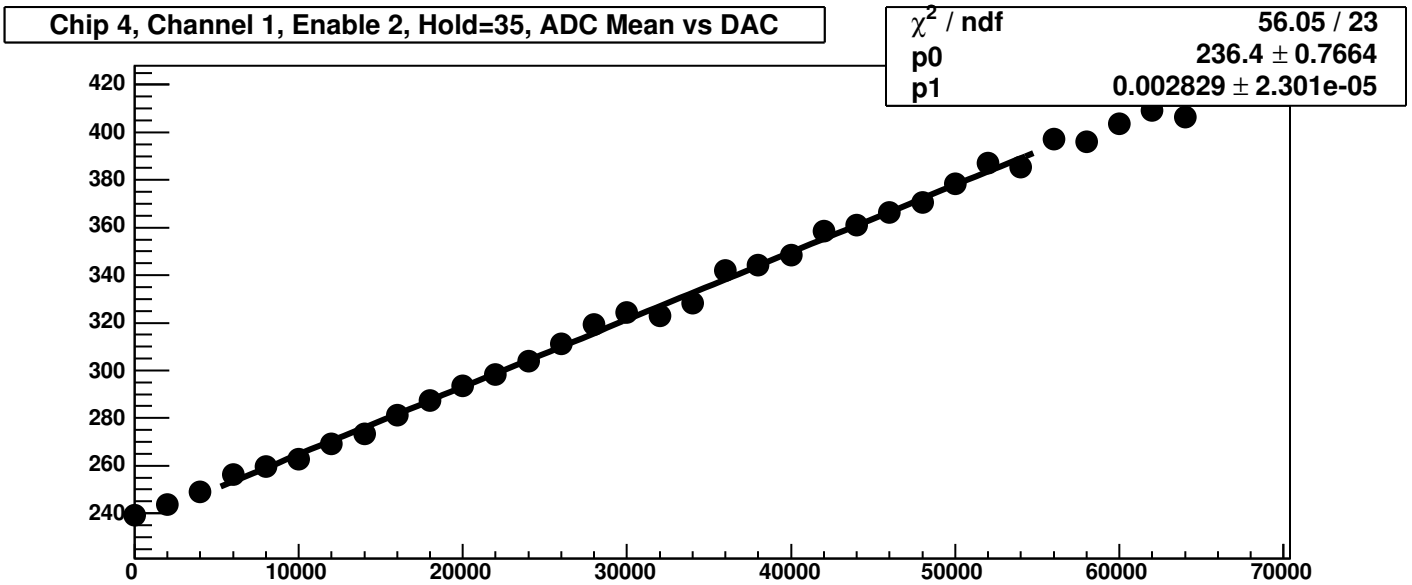

Chip 4, Channel 1, Enable 0, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000

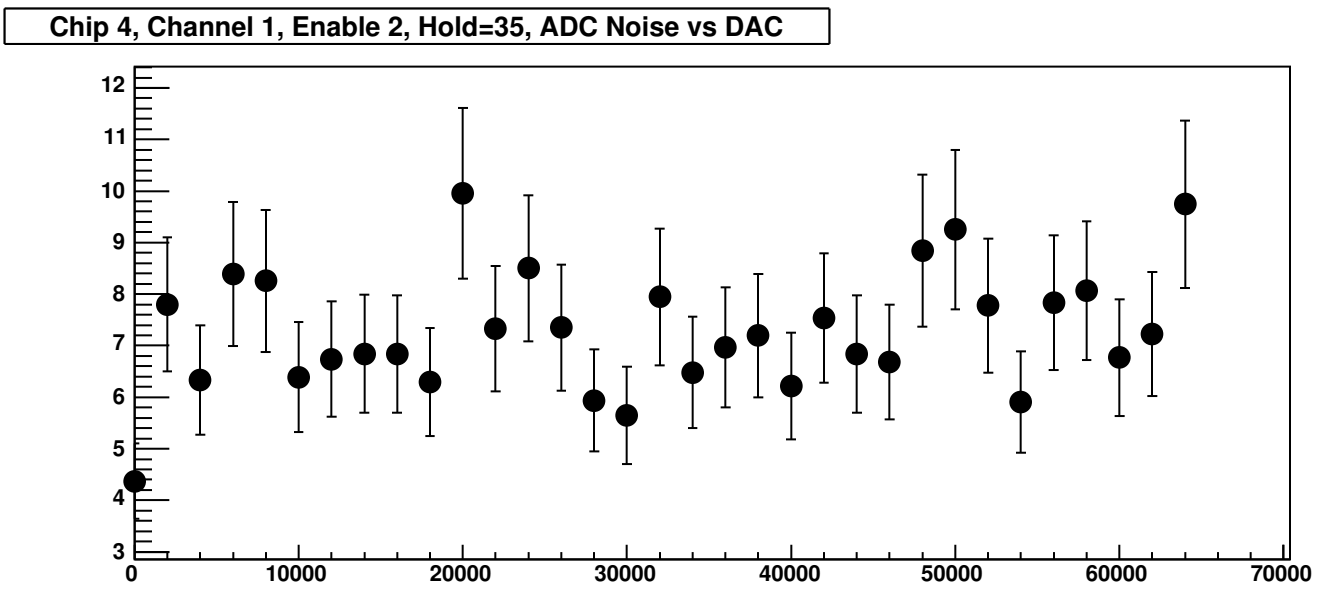

Chip 4, Channel 1, Enable 0, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

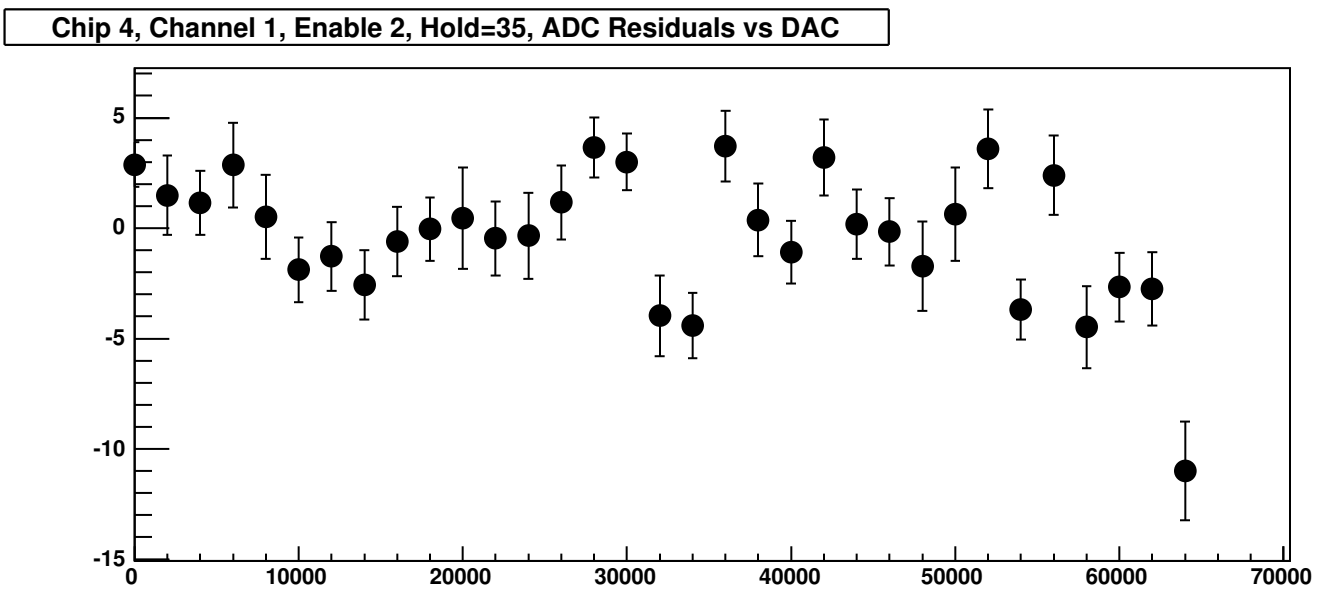

Chip 4, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 28.88 / 23
p0 234.8 ± 0.741
p1 0.003834 ± 2.307e-05
500
450
400
350
300
250
0
10000
20000
30000
40000
50000
60000
70000

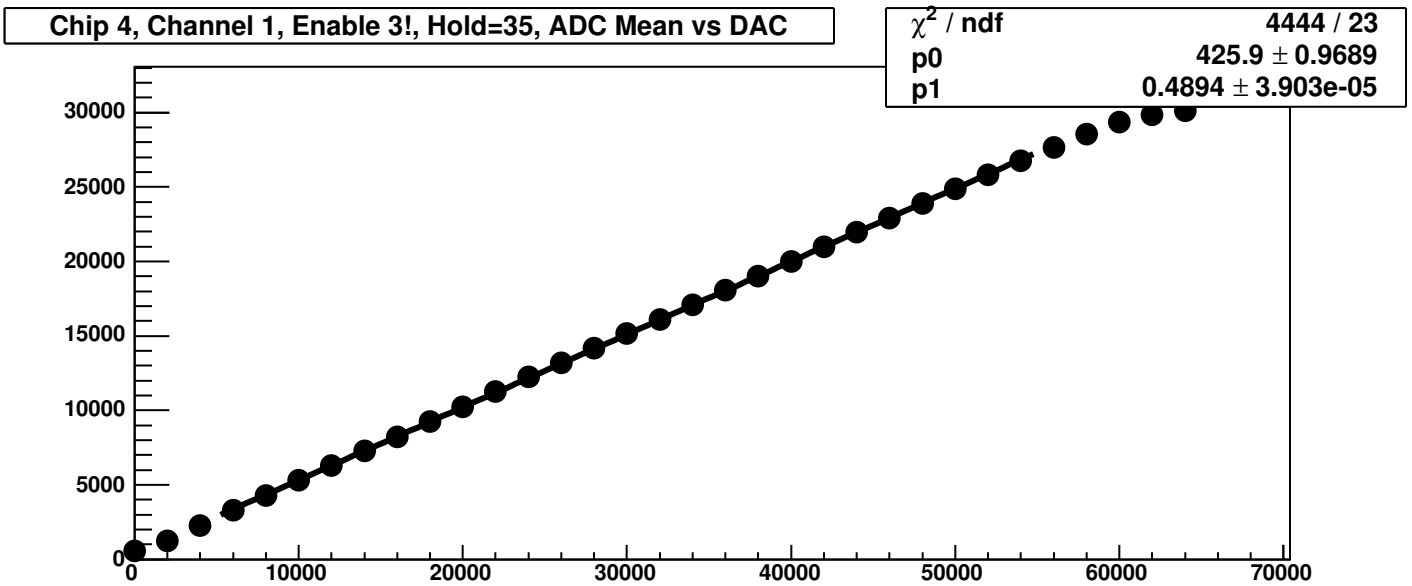

Chip 4, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

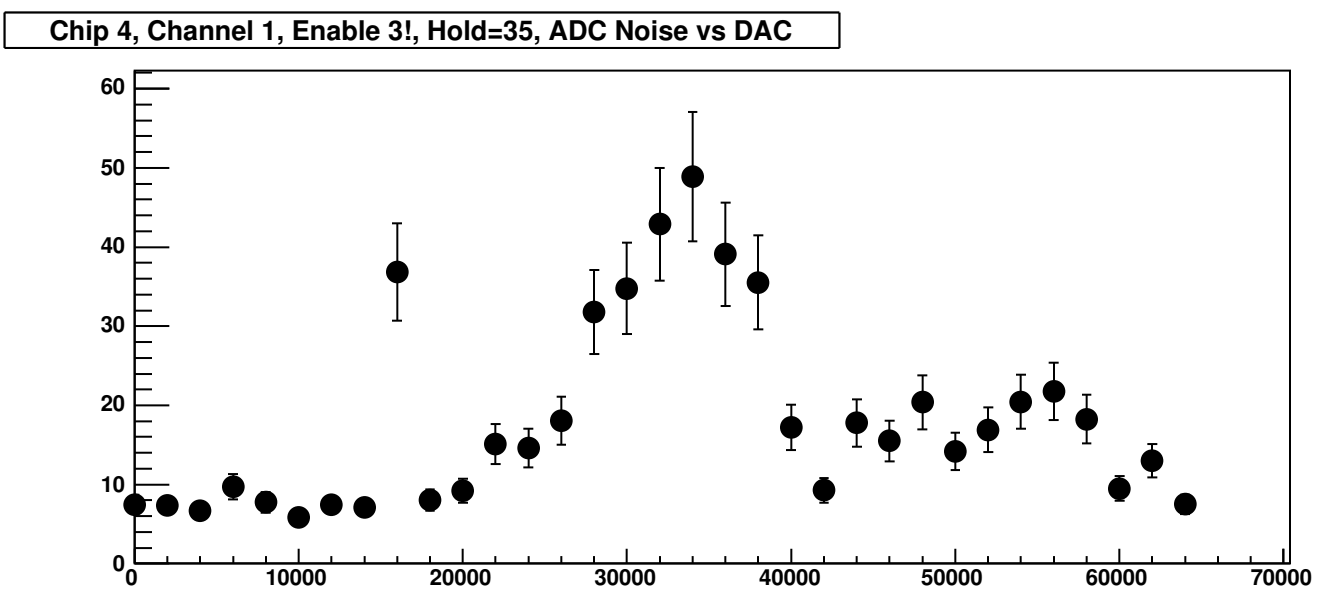

Chip 4, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

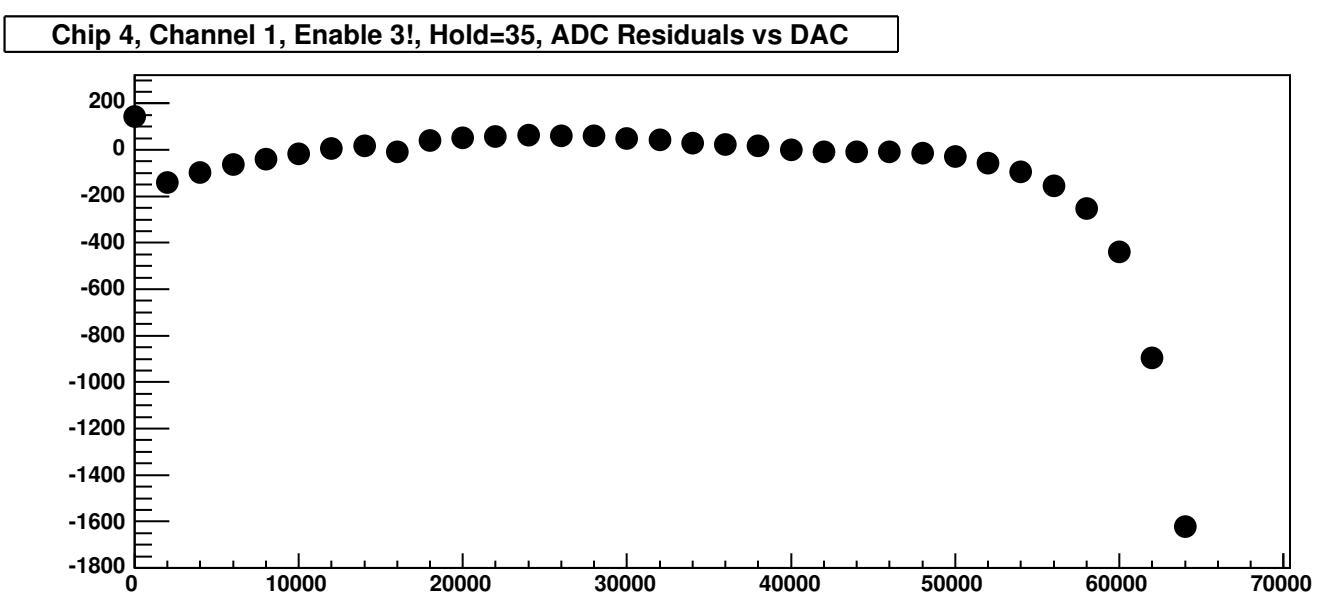

Chip 4, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 56.05 / 23
p0 236.4 ± 0.7664
p1 0.002829 ± 2.301e-05
420
400
380
360
340
320
300
280
260
240
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

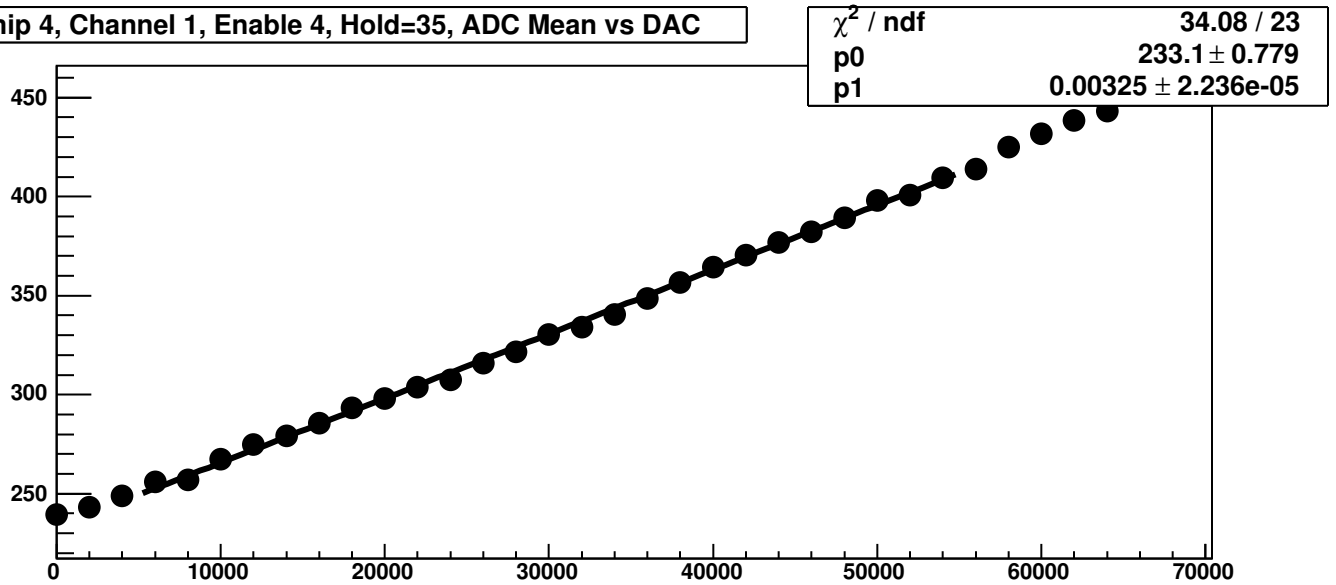

Chip 4, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 1, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 4444 / 23
p0 425.9 ± 0.9689
p1 0.4894 ± 3.903e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

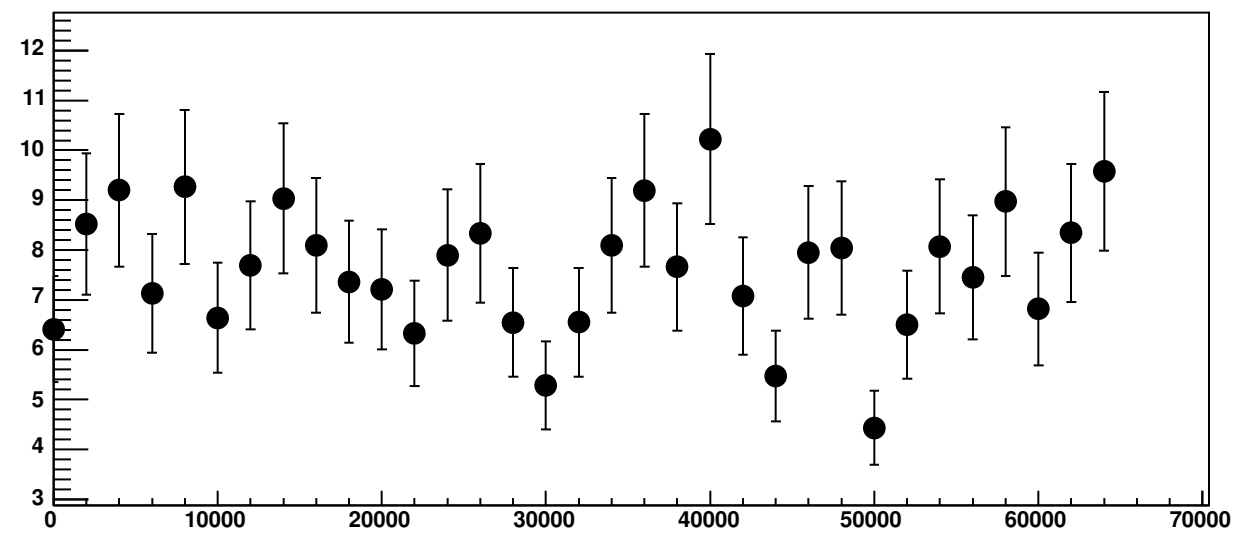

Chip 4, Channel 1, Enable 3!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 1, Enable 3!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
10000
20000
30000
40000
50000
60000
70000

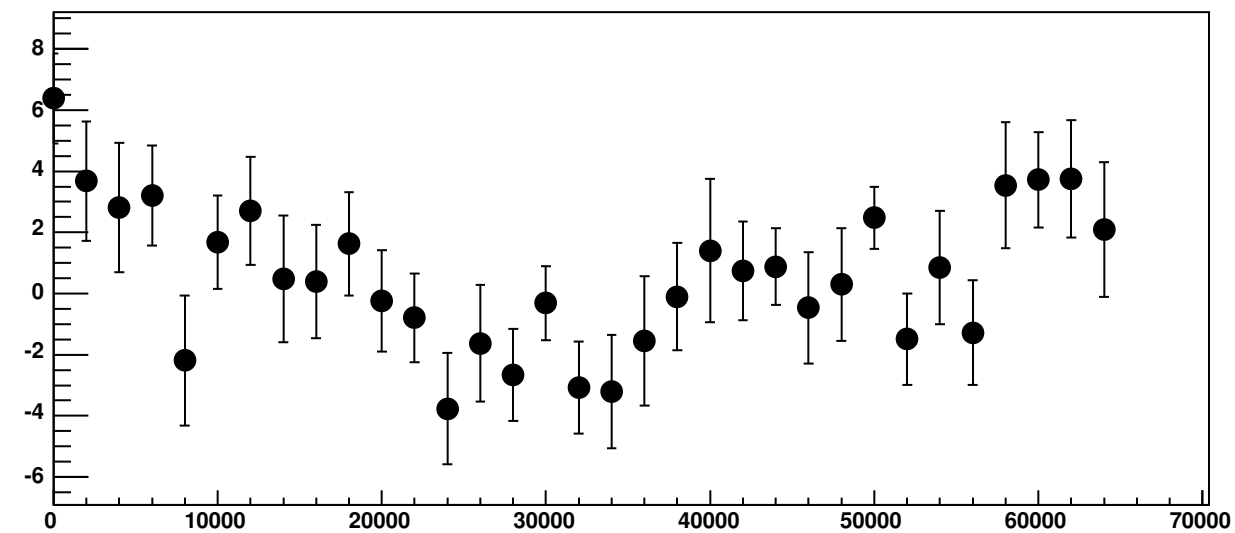
Chip 4, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC

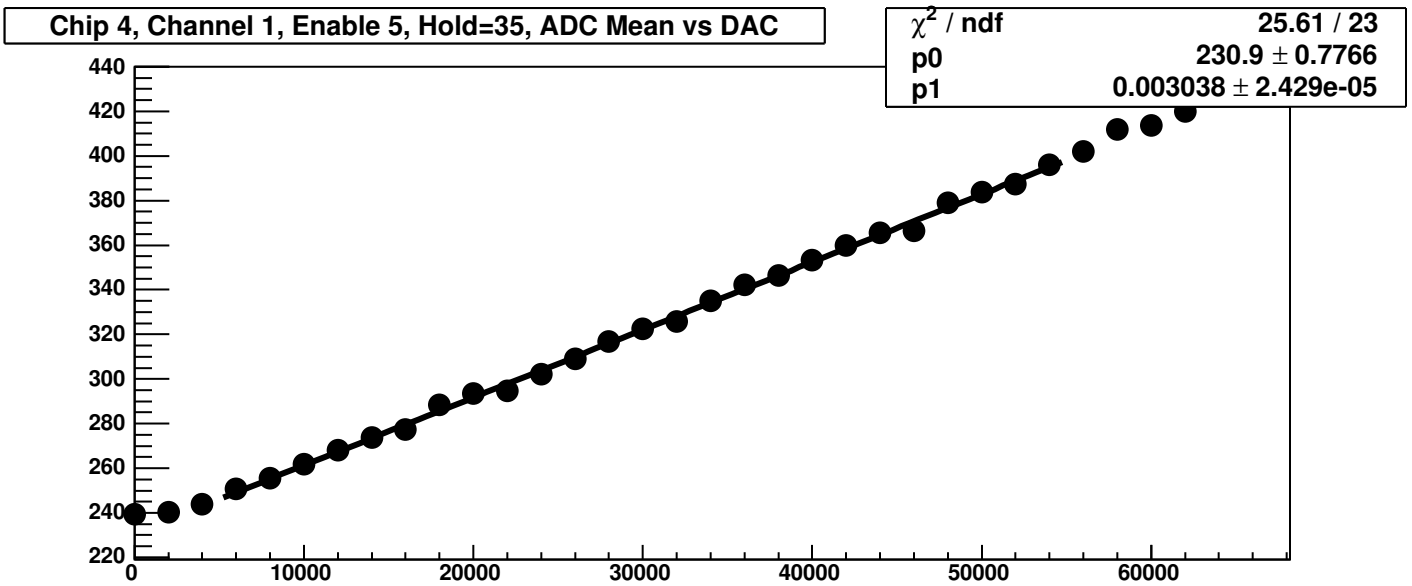


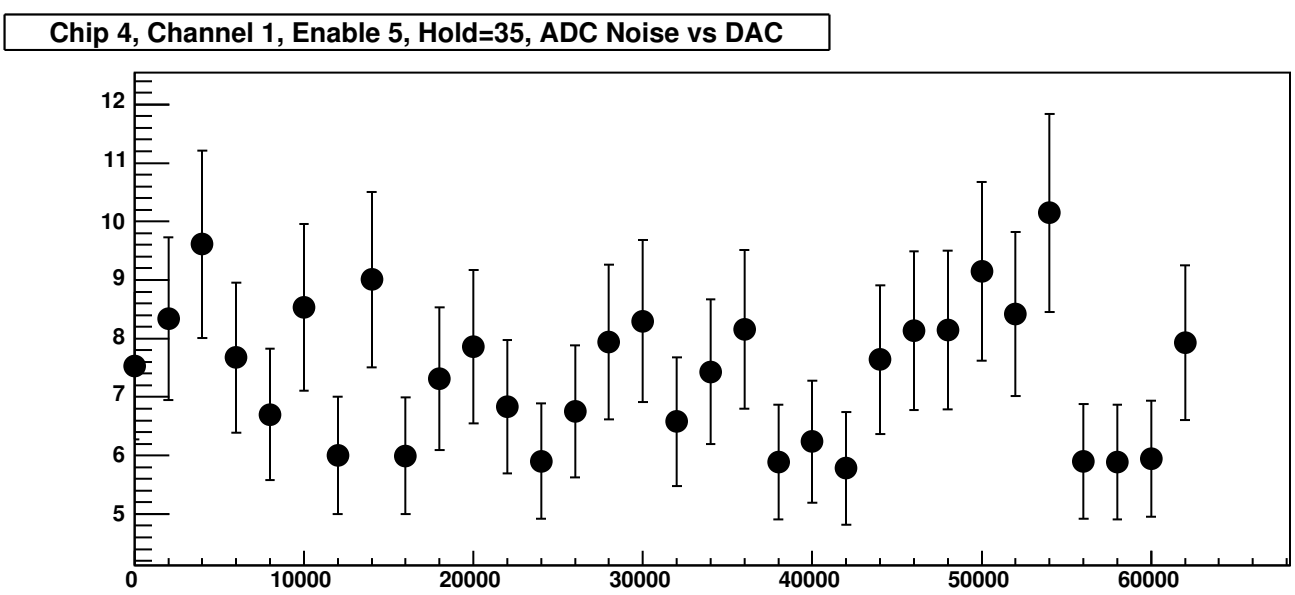
Chip 4, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC

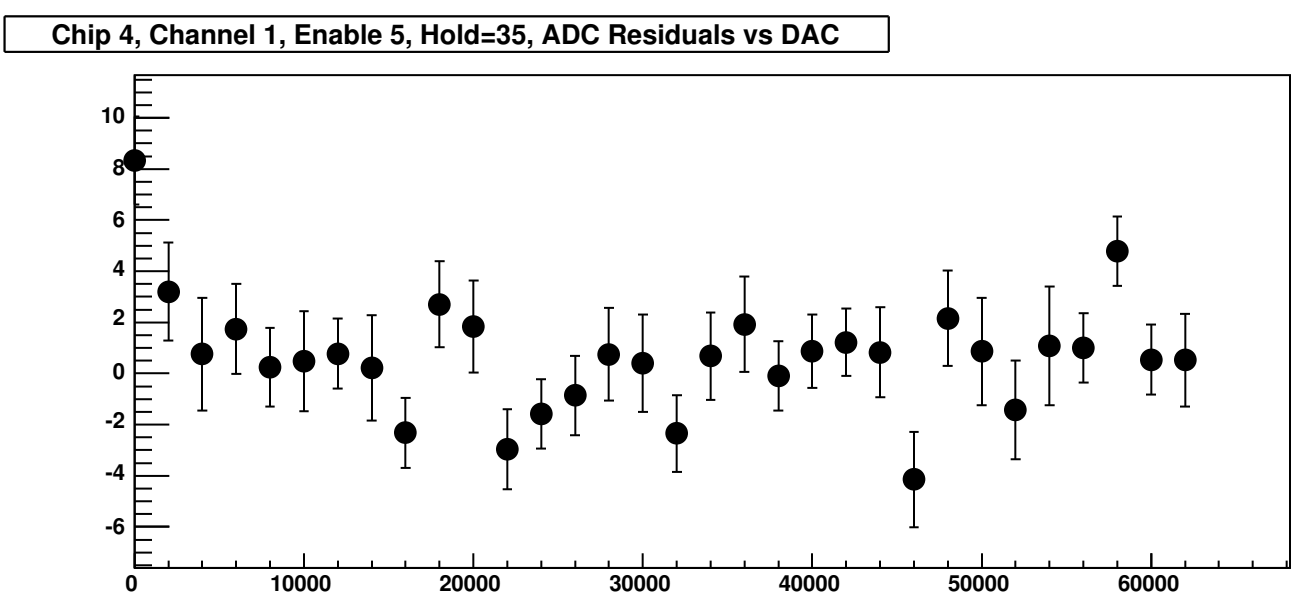


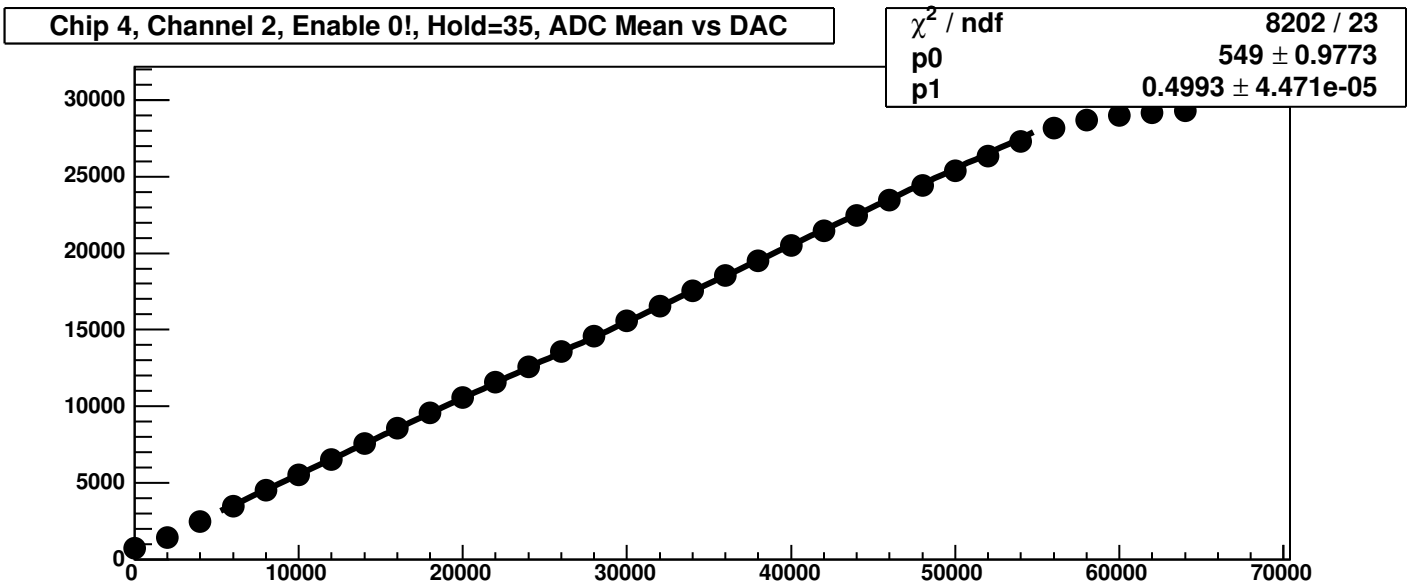
Chip 4, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC

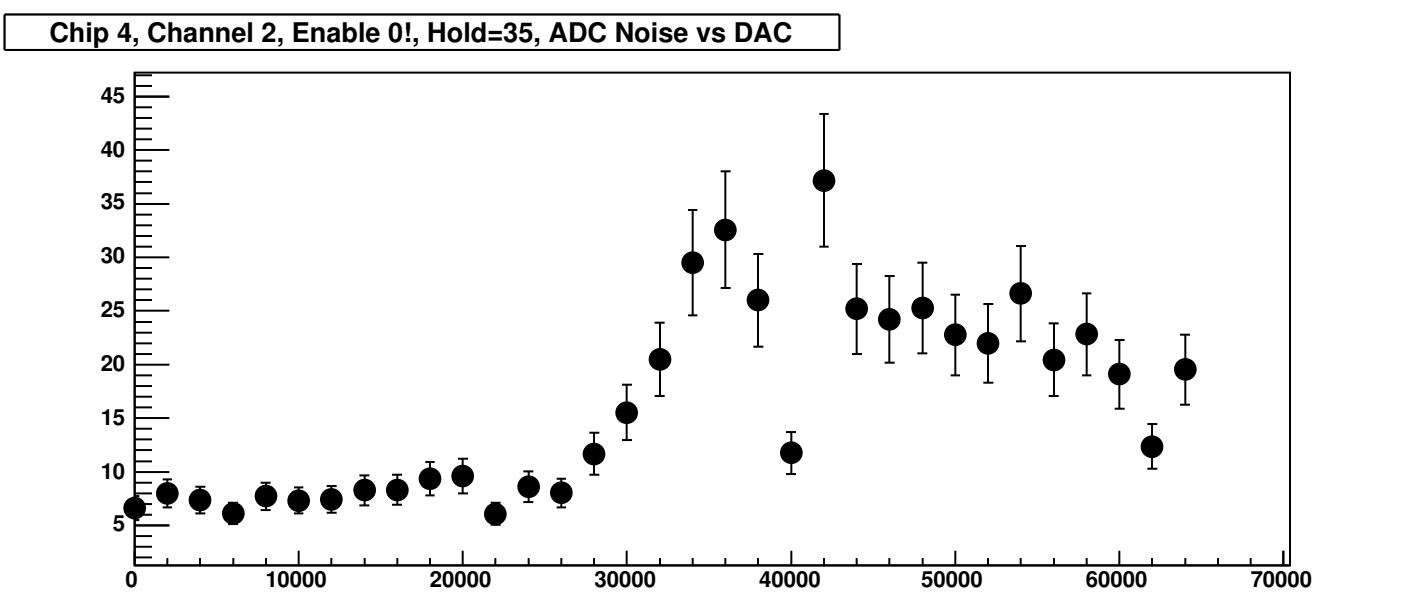


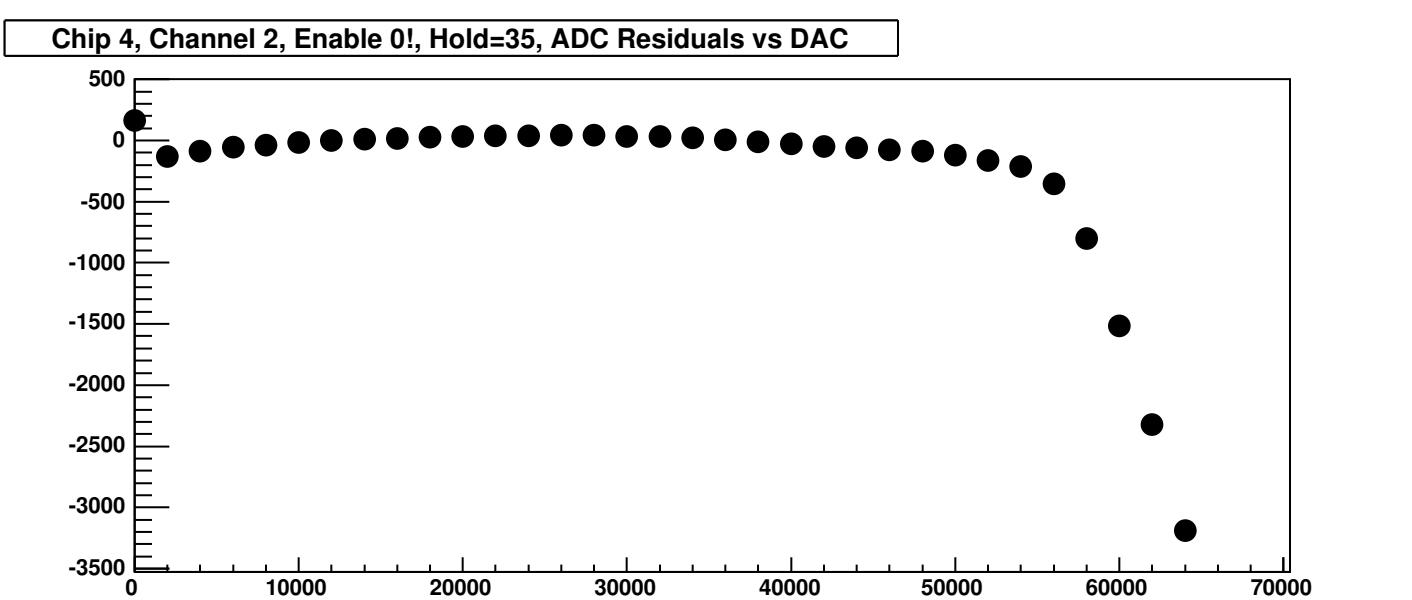

Chip 4, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 25.61 / 23
p0 230.9 ± 0.7766
p1 0.003038 ± 2.429e-05
440
420
400
380
360
340
320
300
280
260
240
220
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000

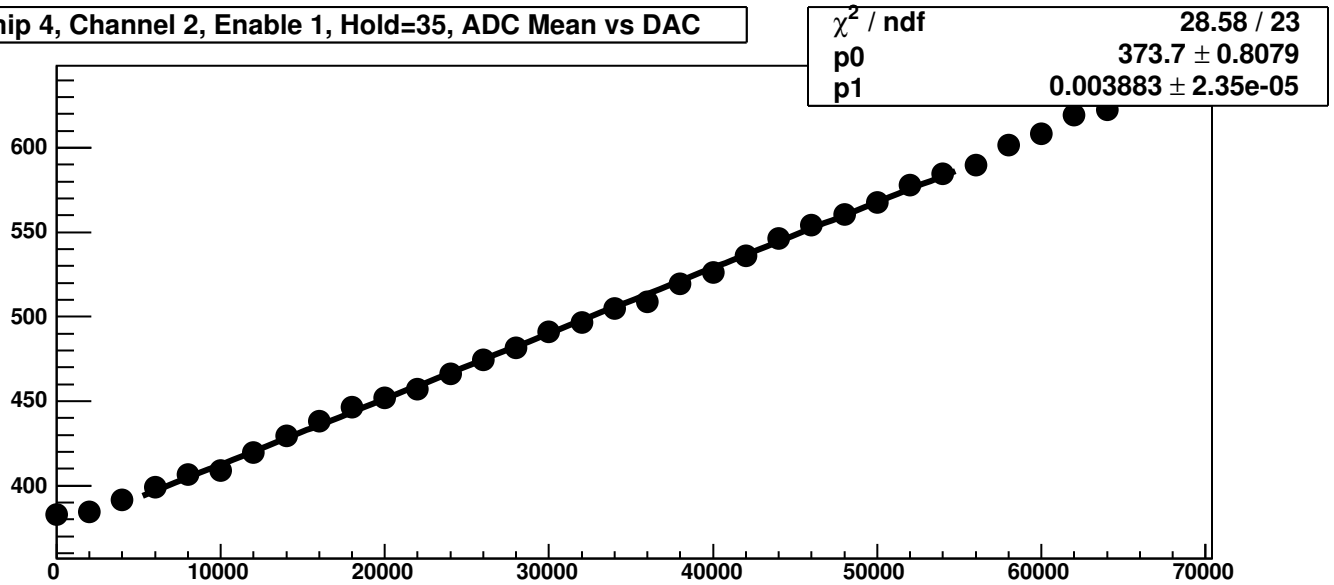

Chip 4, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 2, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 8202 / 23
p0 549 ± 0.9773
p1 0.4993 ± 4.471e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

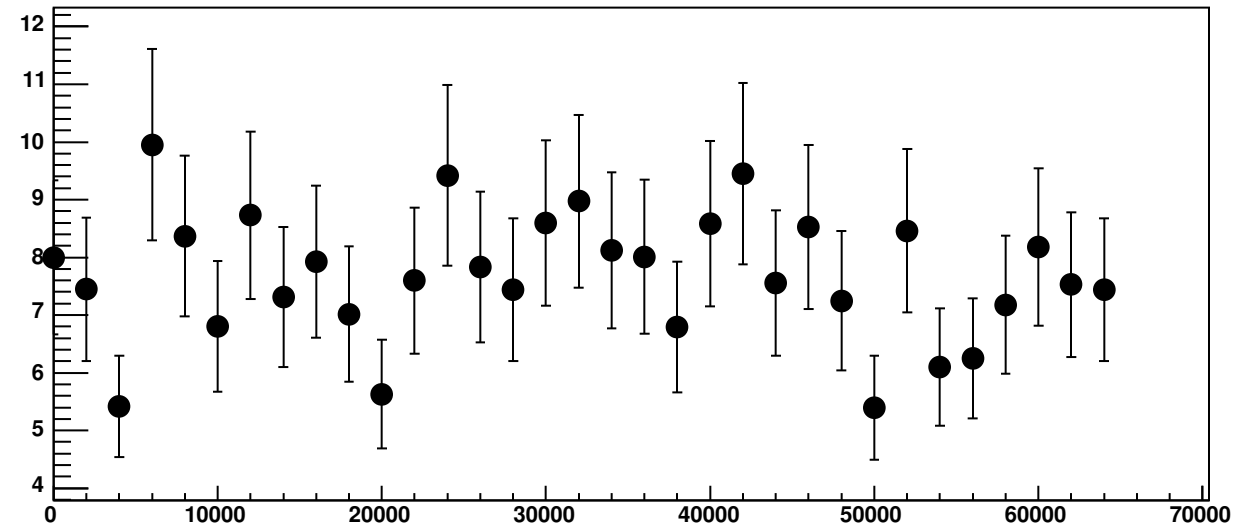

Chip 4, Channel 2, Enable 0!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 2, Enable 0!, Hold=35, ADC Residuals vs DAC
500
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
0
10000
20000
30000
40000
50000
60000
70000

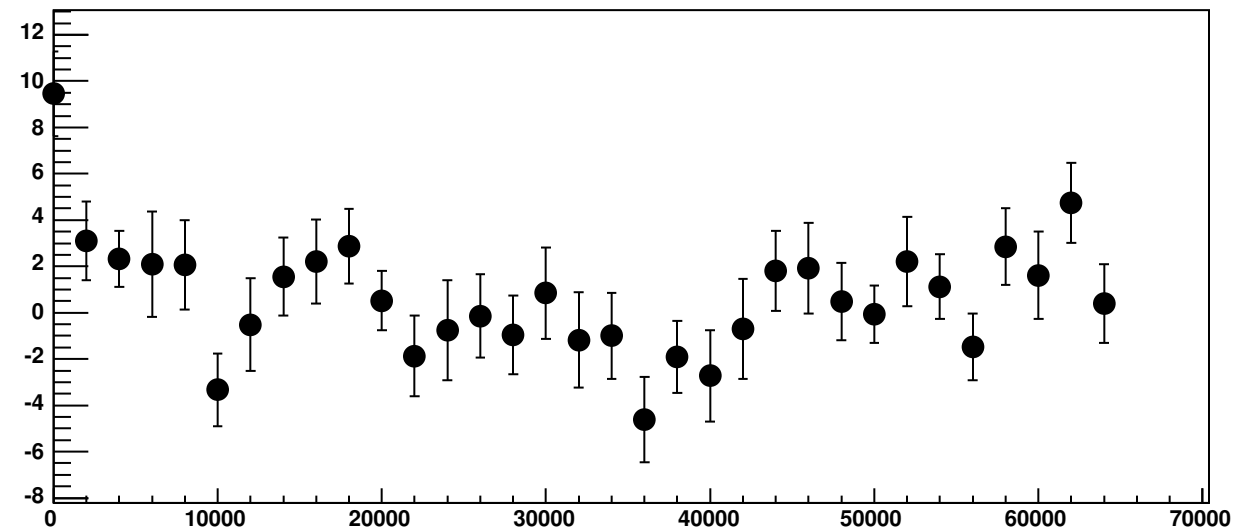
Chip 4, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

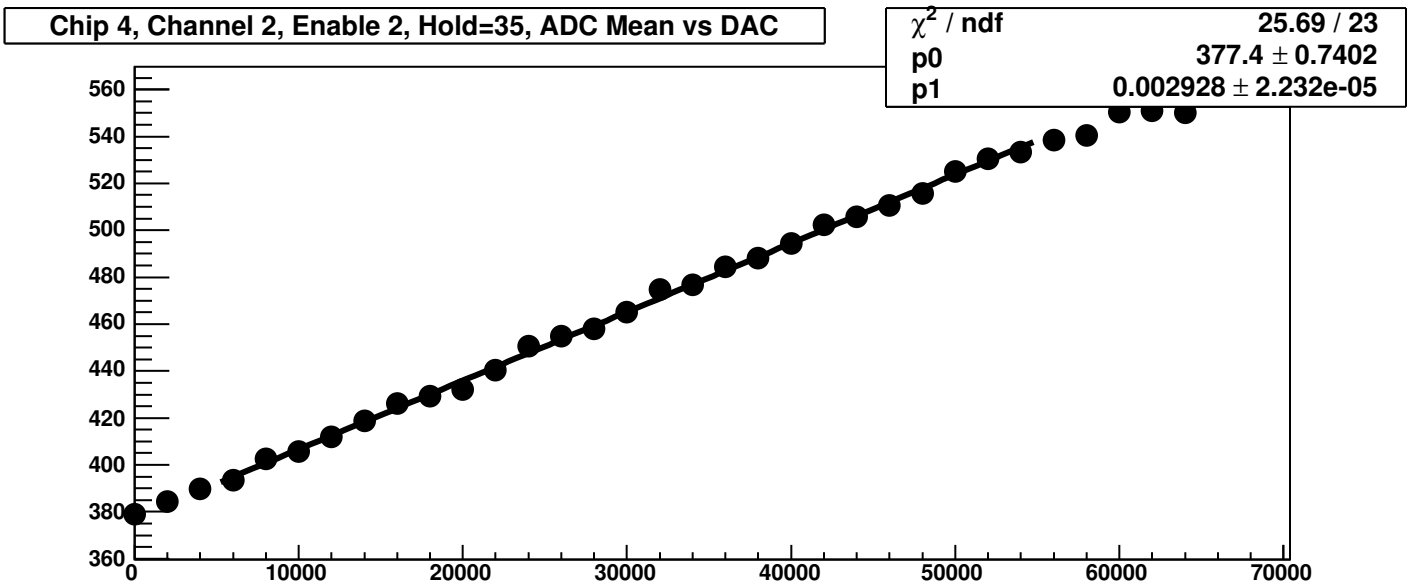


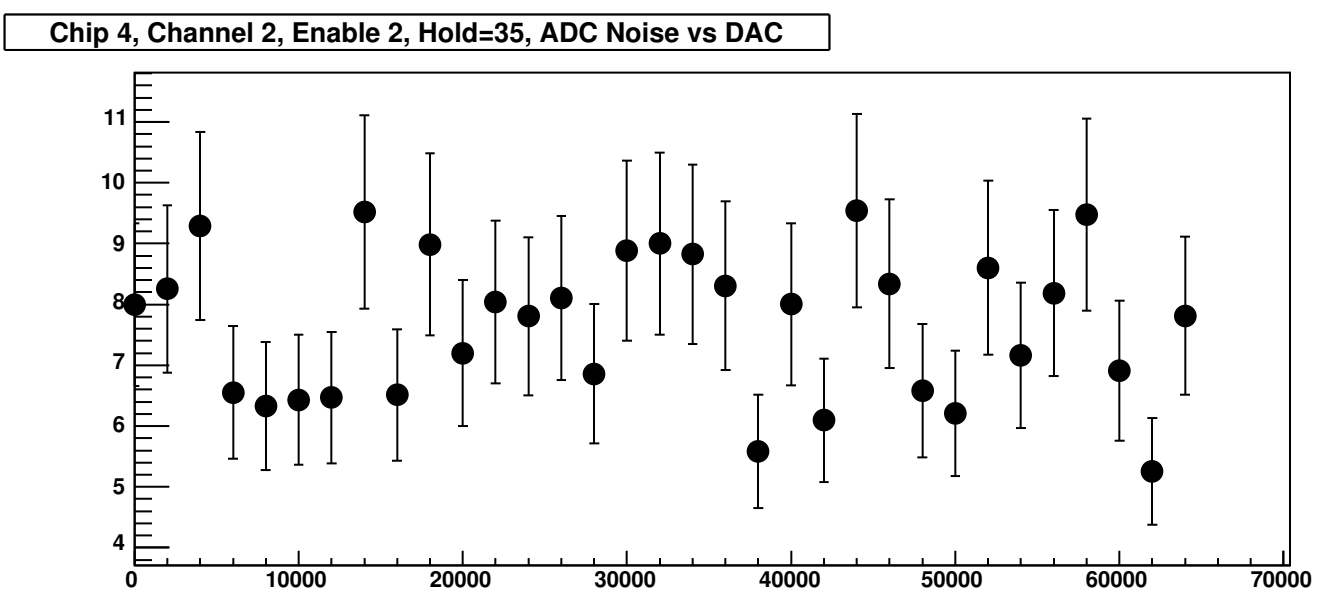
Chip 4, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC

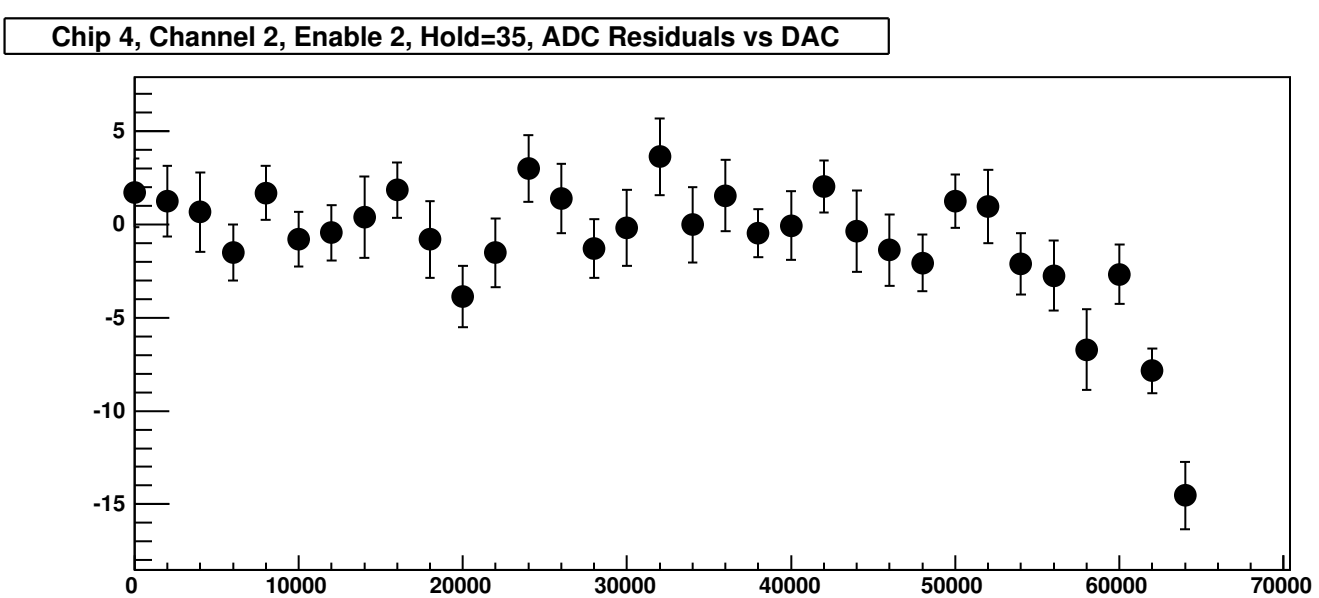


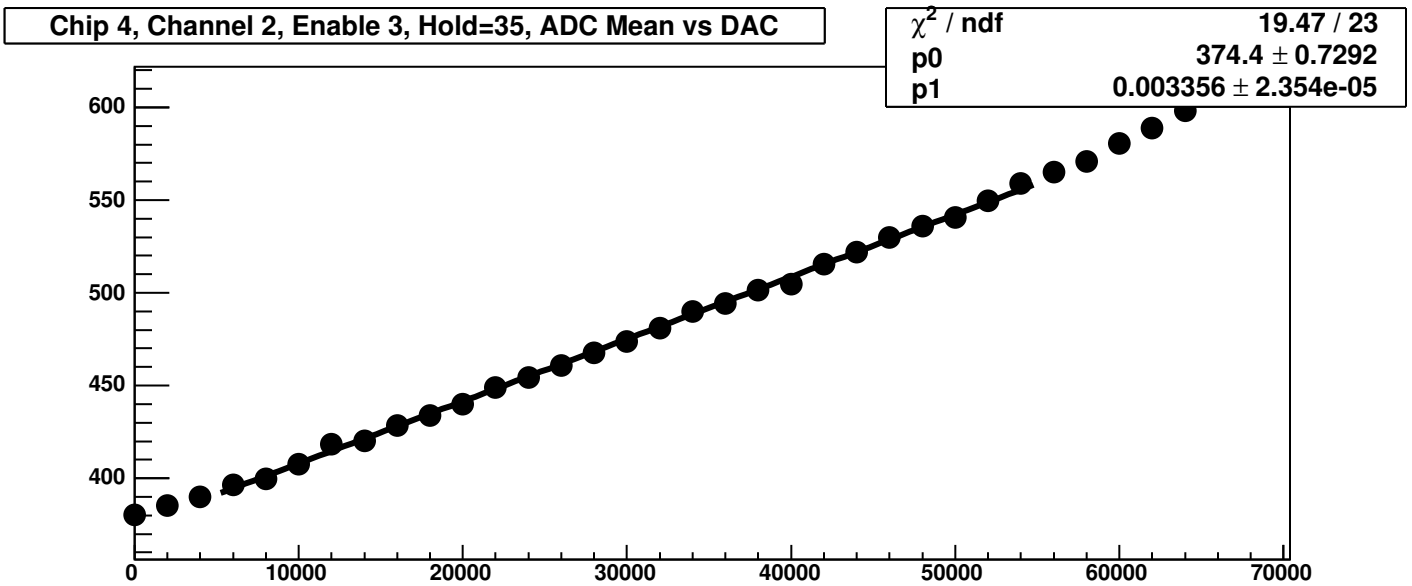
Chip 4, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

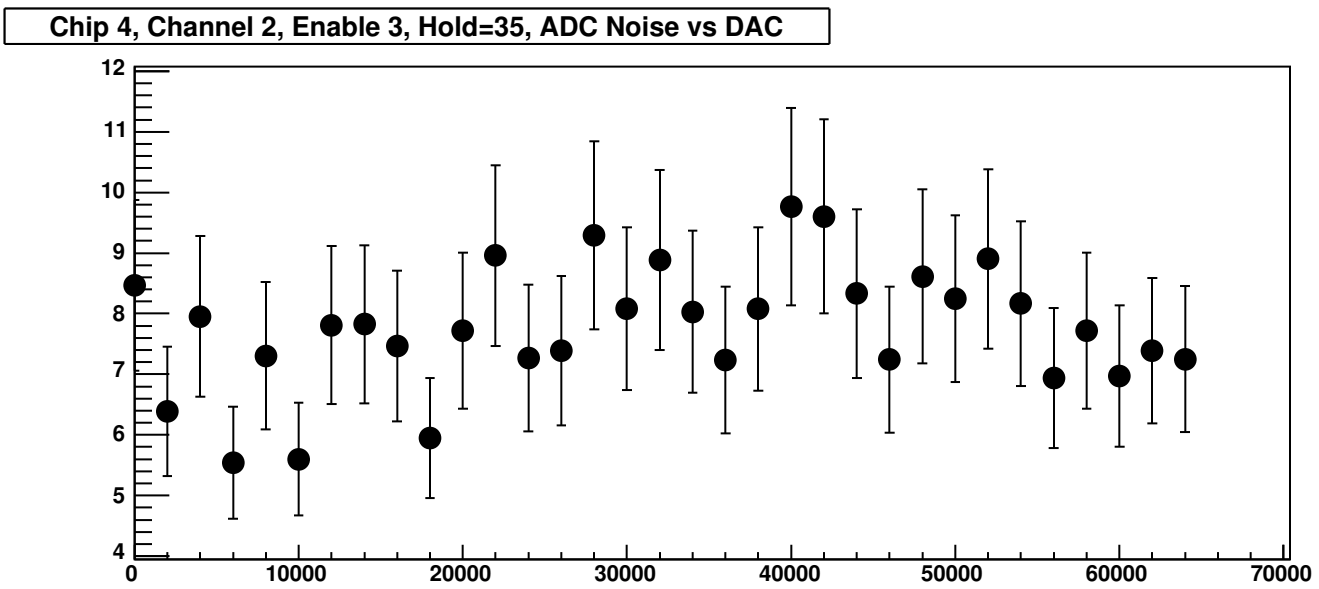


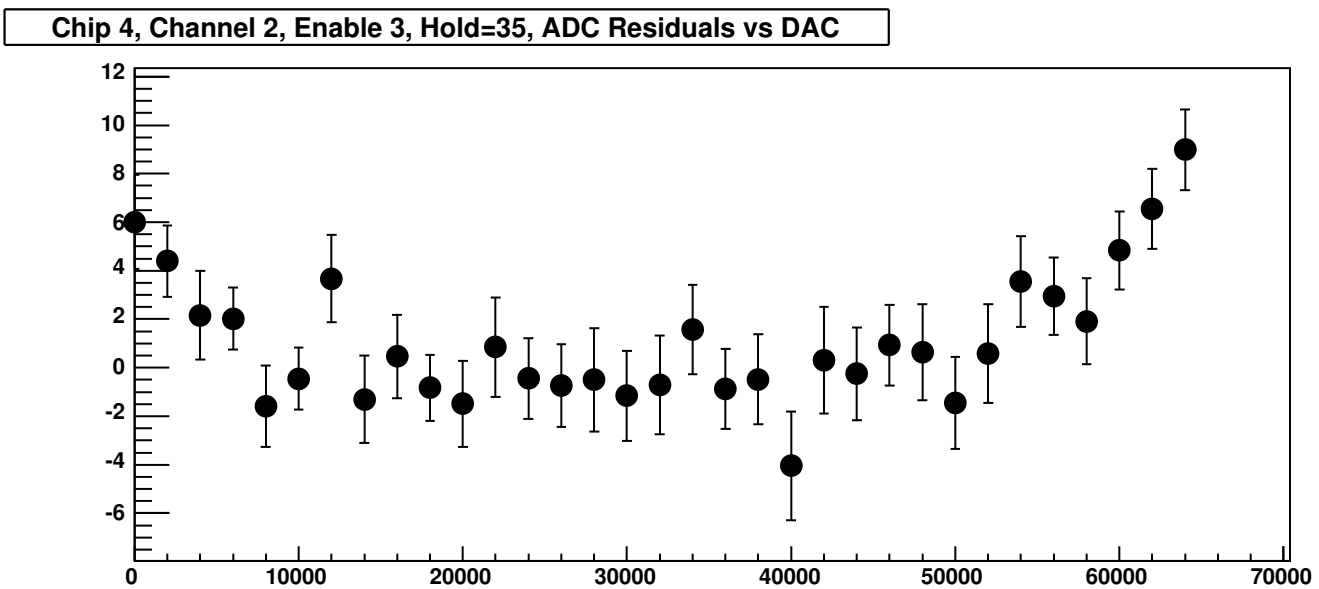

Chip 4, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 25.69 / 23
p0 377.4 ± 0.7402
p1 0.002928 ± 2.232e-05
560
540
520
500
480
460
440
420
400
380
360
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

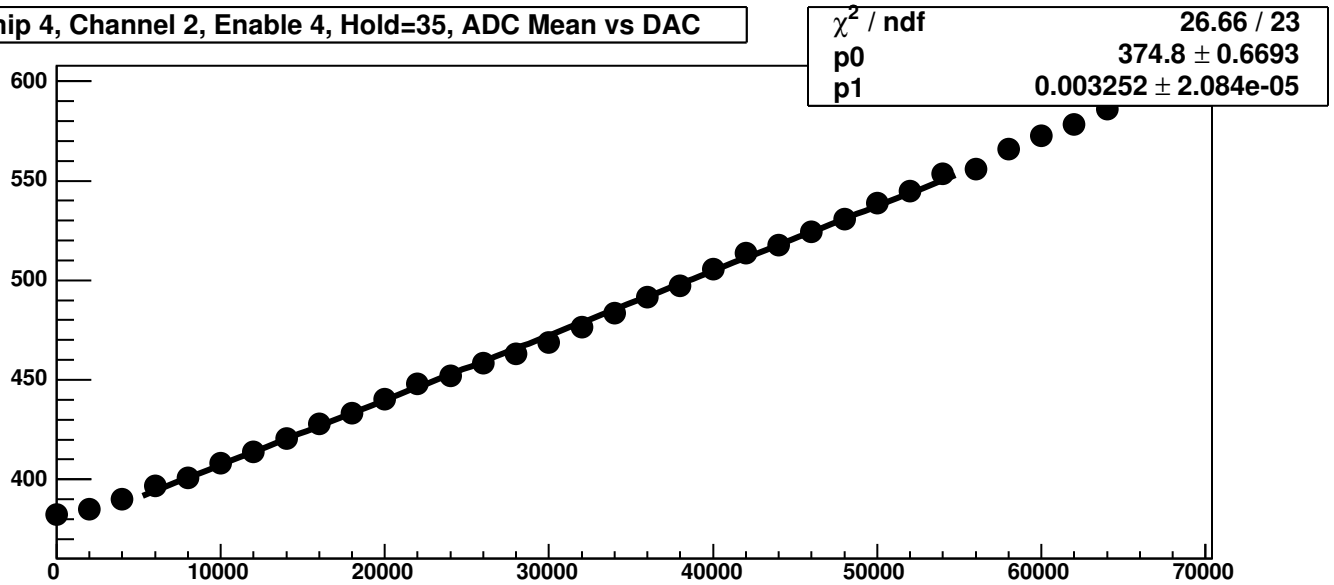

Chip 4, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 2, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 19.47 / 23
p0 374.4 ± 0.7292
p1 0.003356 ± 2.354e-05
600
550
500
450
400
0
10000
20000
30000
40000
50000
60000
70000

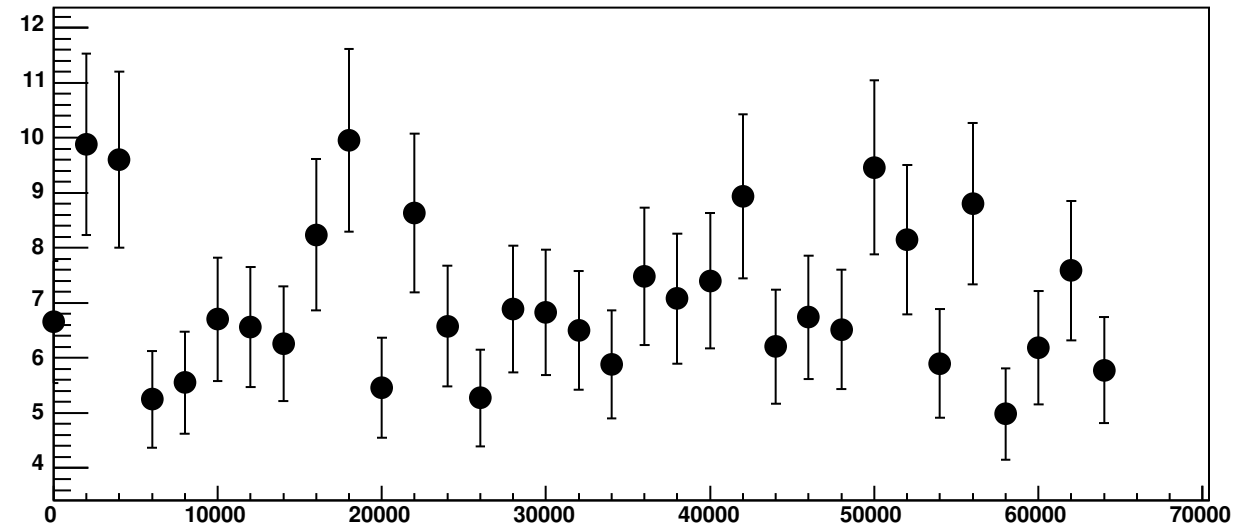

Chip 4, Channel 2, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 2, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

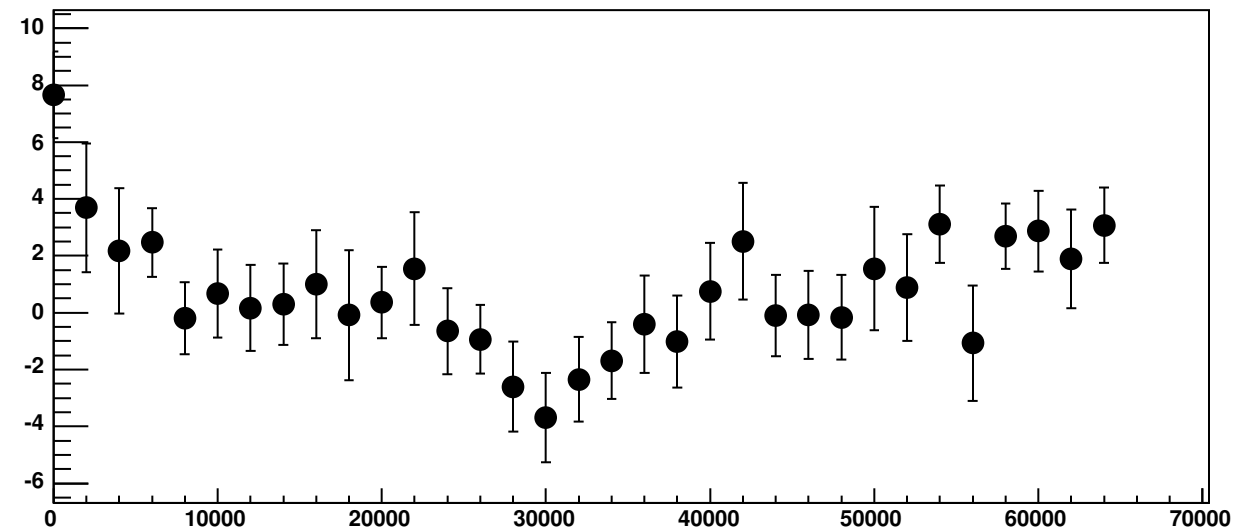
Chip 4, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

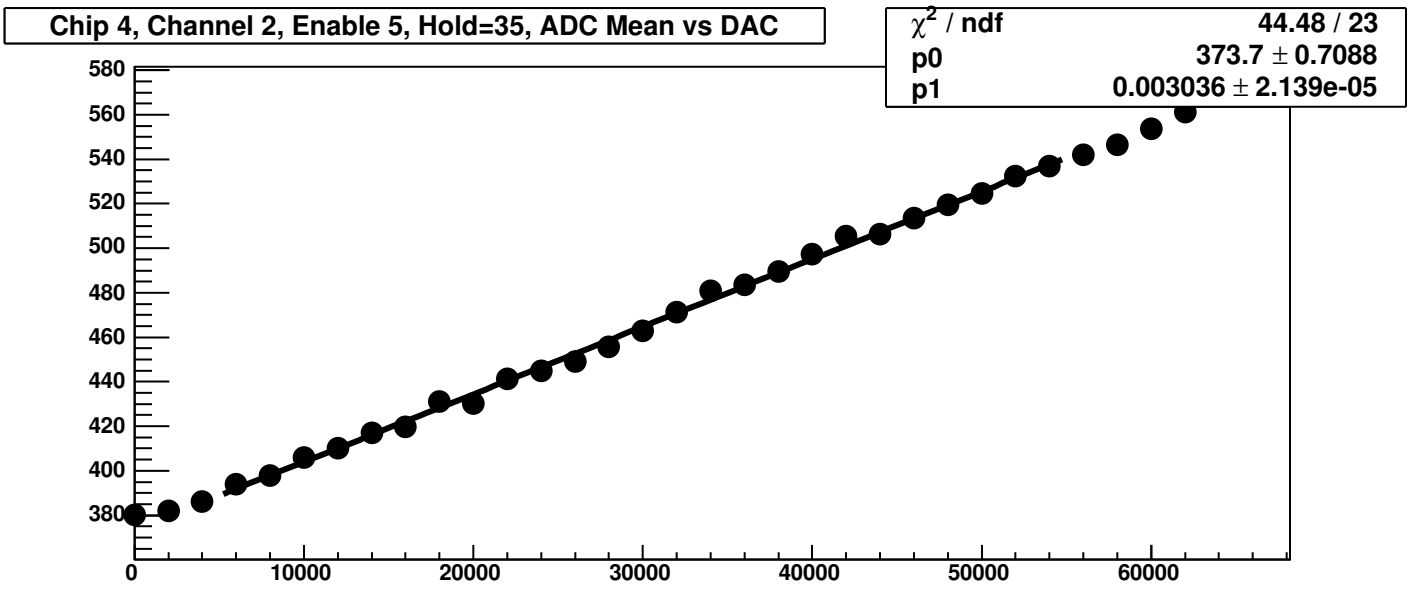


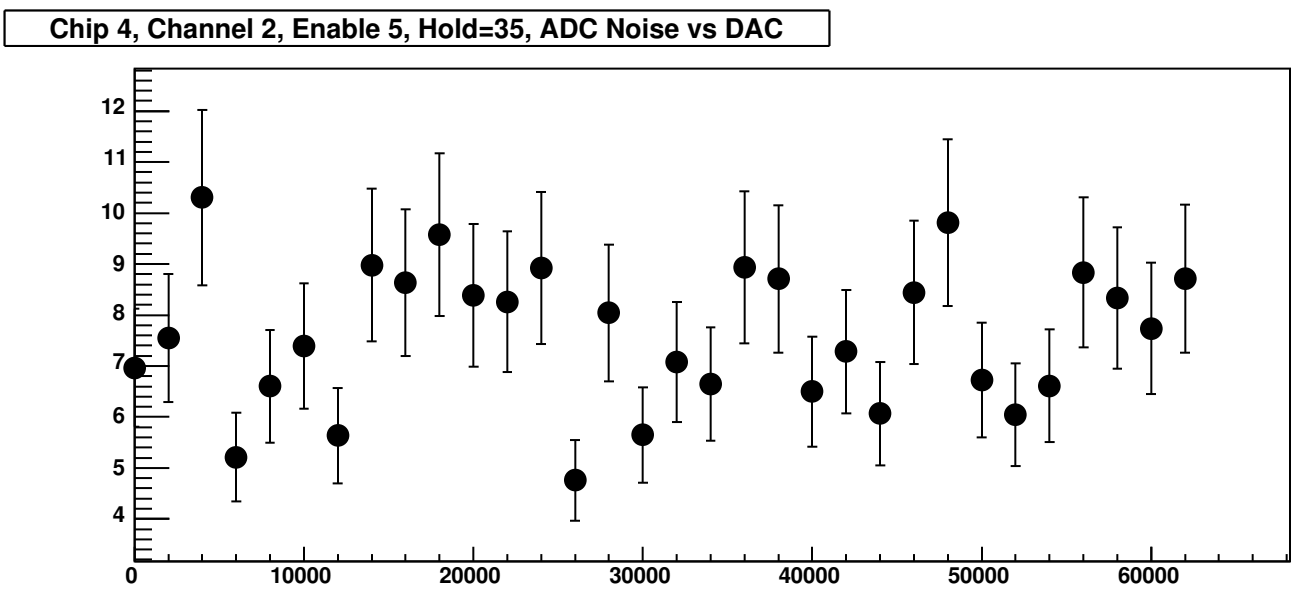
Chip 4, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

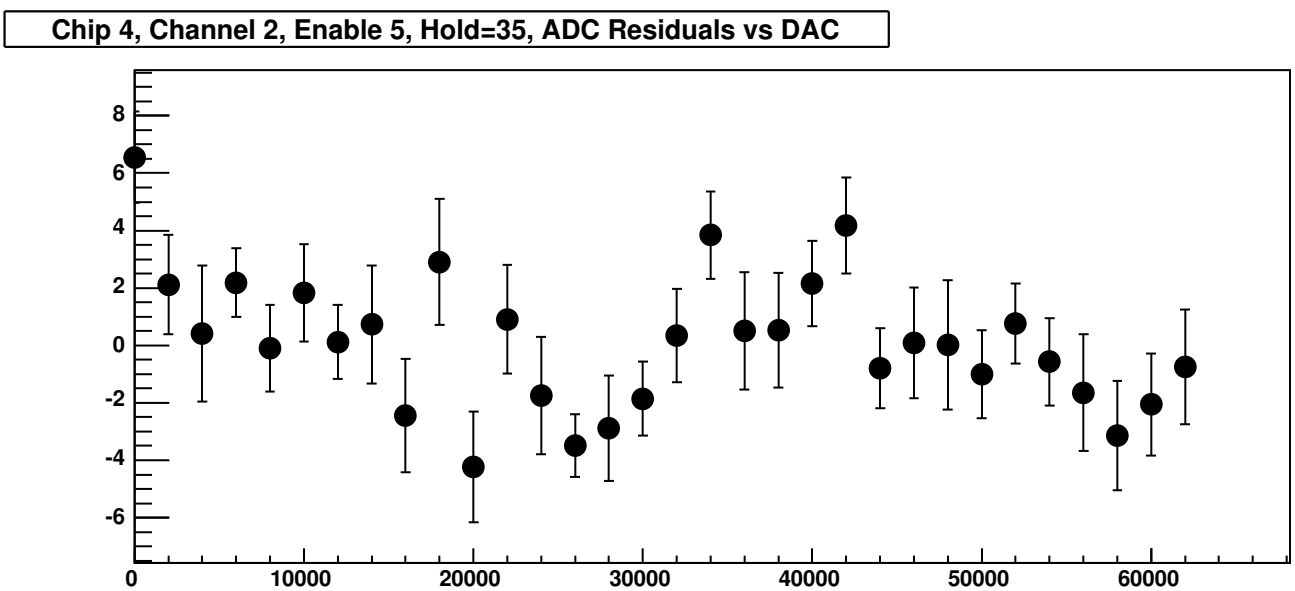


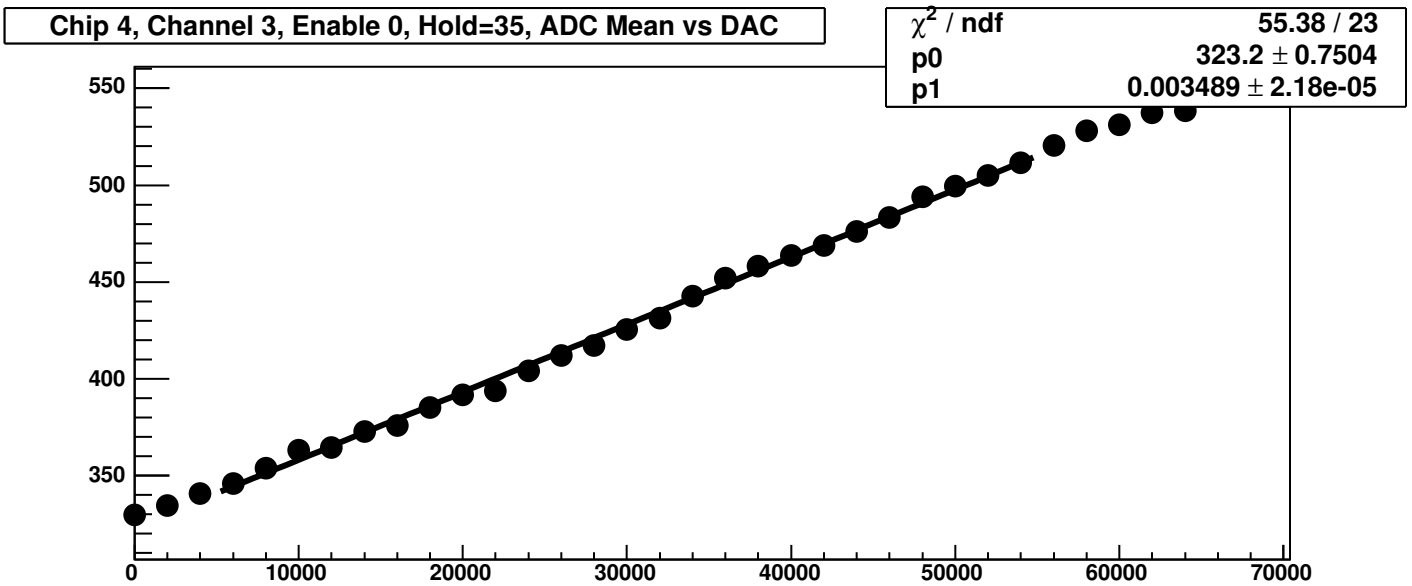
Chip 4, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

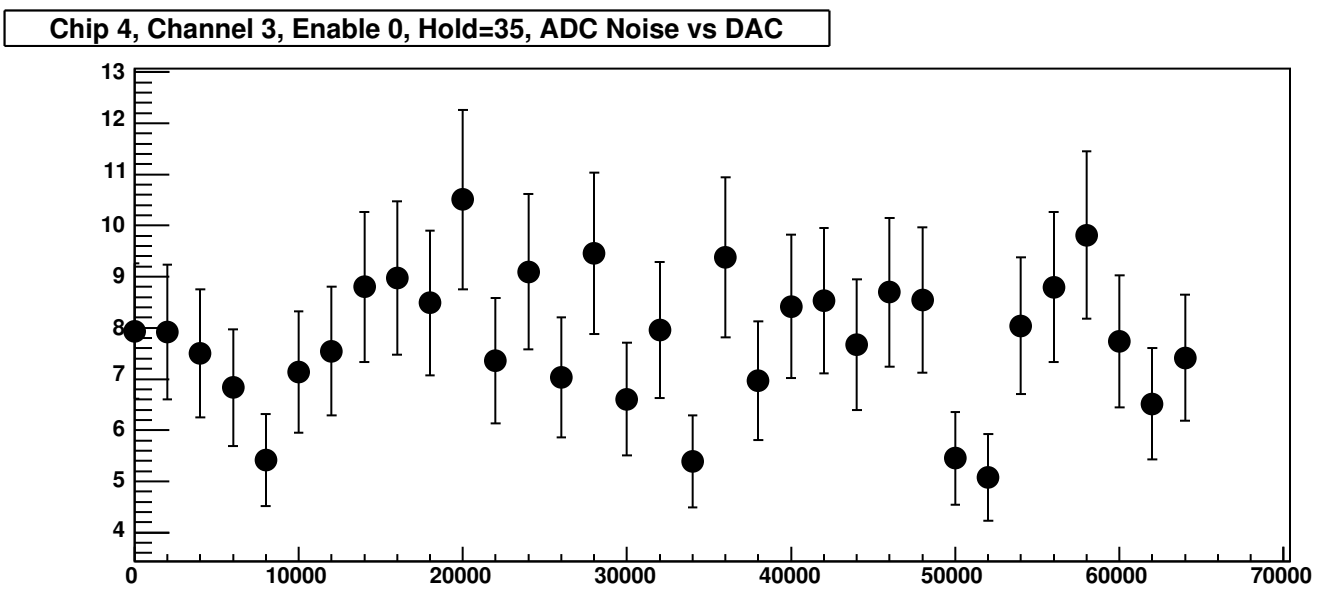


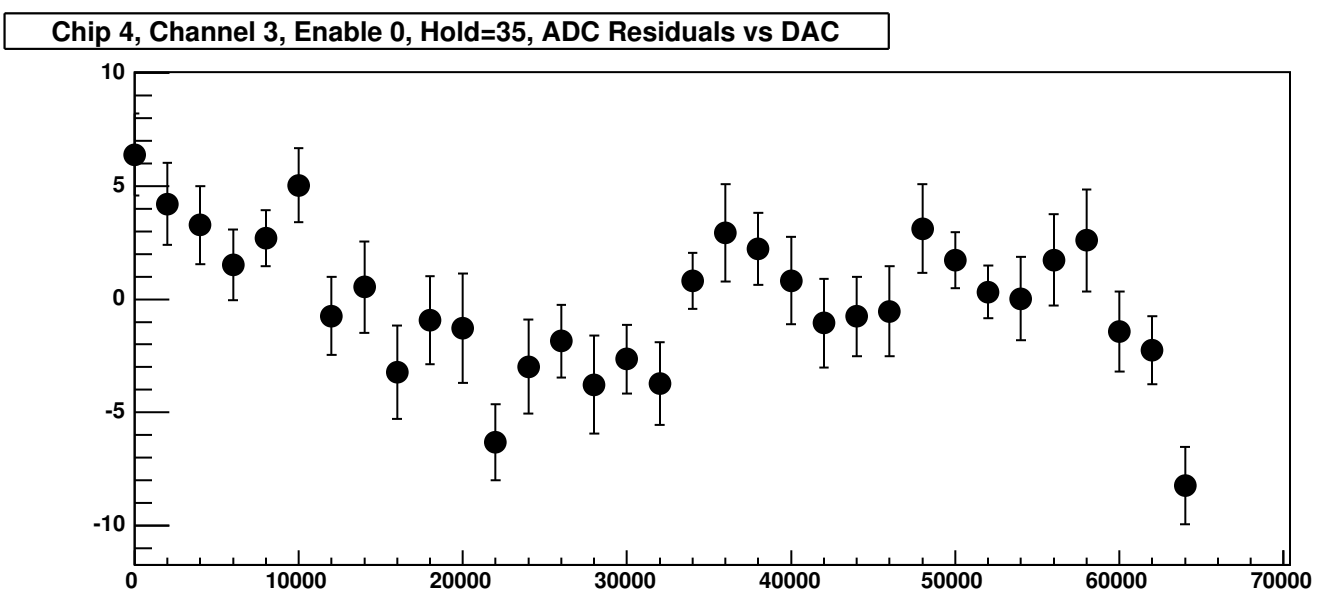

Chip 4, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 44.48 / 23
p0 373.7 ± 0.7088
p1 0.003036 ± 2.139e-05
580
560
540
520
500
480
460
440
420
400
380
0
10000
20000
30000
40000
50000
60000

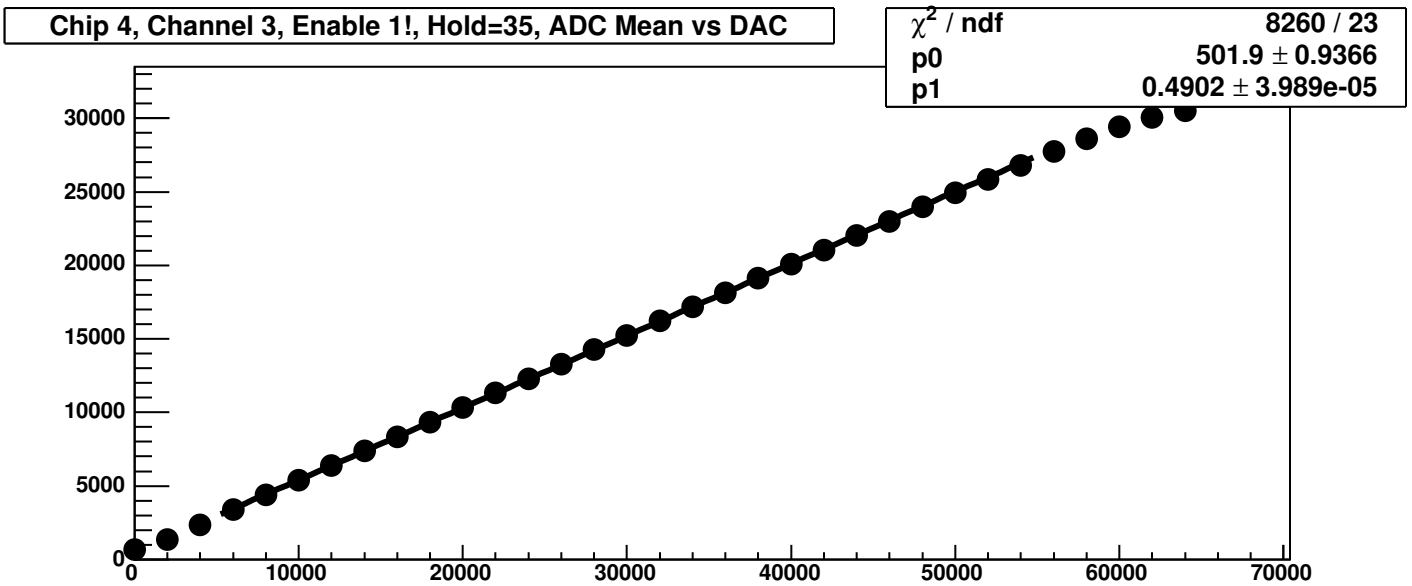

Chip 4, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

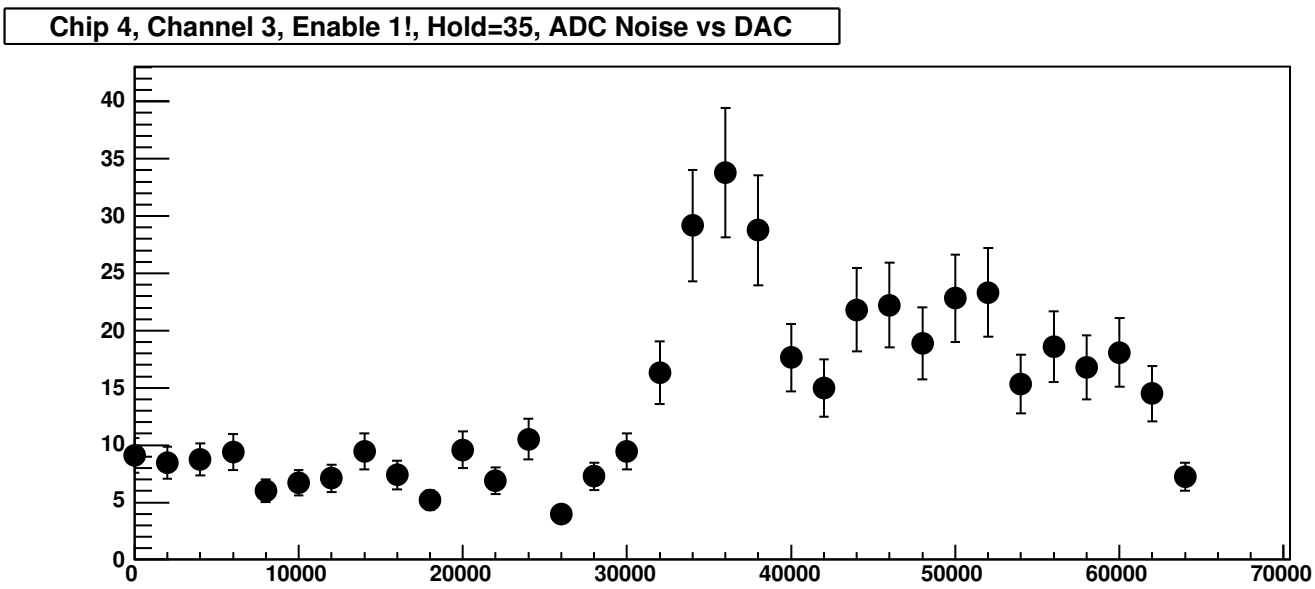

Chip 4, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000

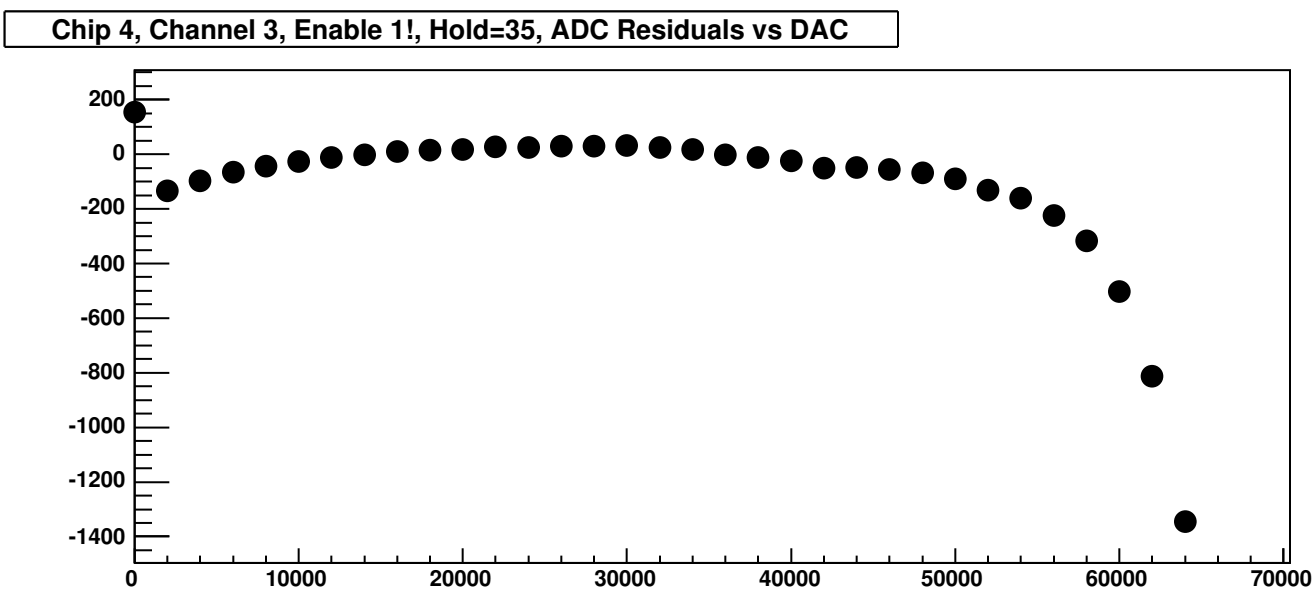

Chip 4, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 55.38 / 23
p0 323.2 ± 0.7504
p1 0.003489 ± 2.18e-05
550
500
450
400
350
0
10000
20000
30000
40000
50000
60000
70000

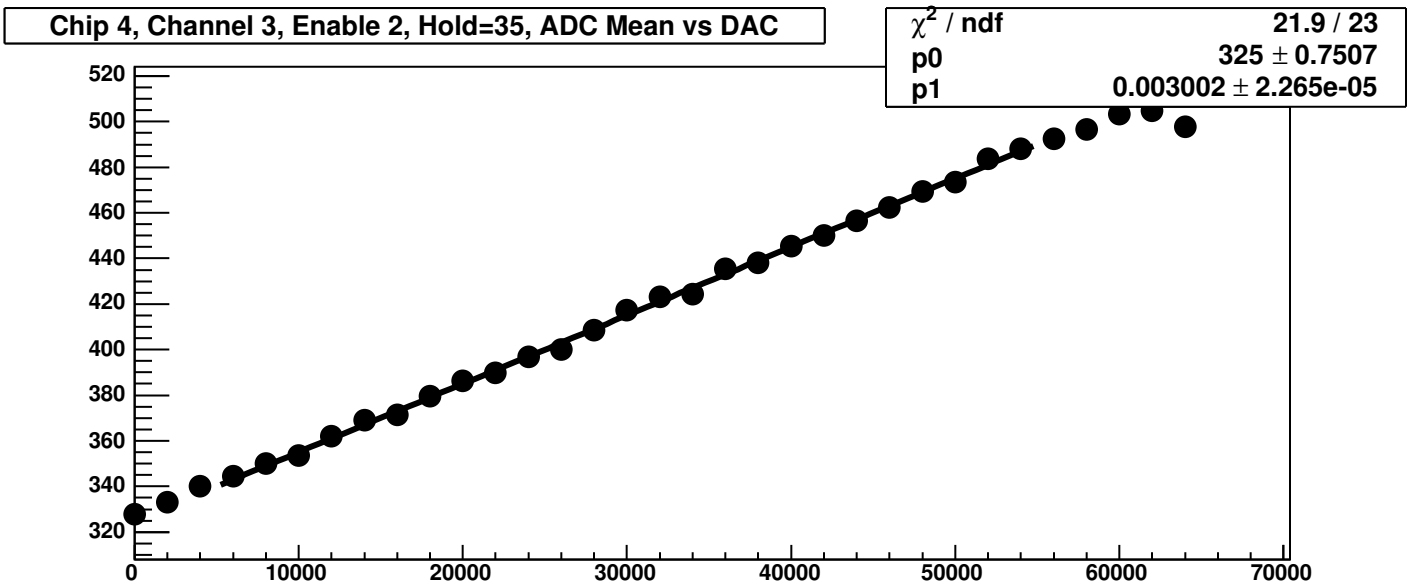

Chip 4, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

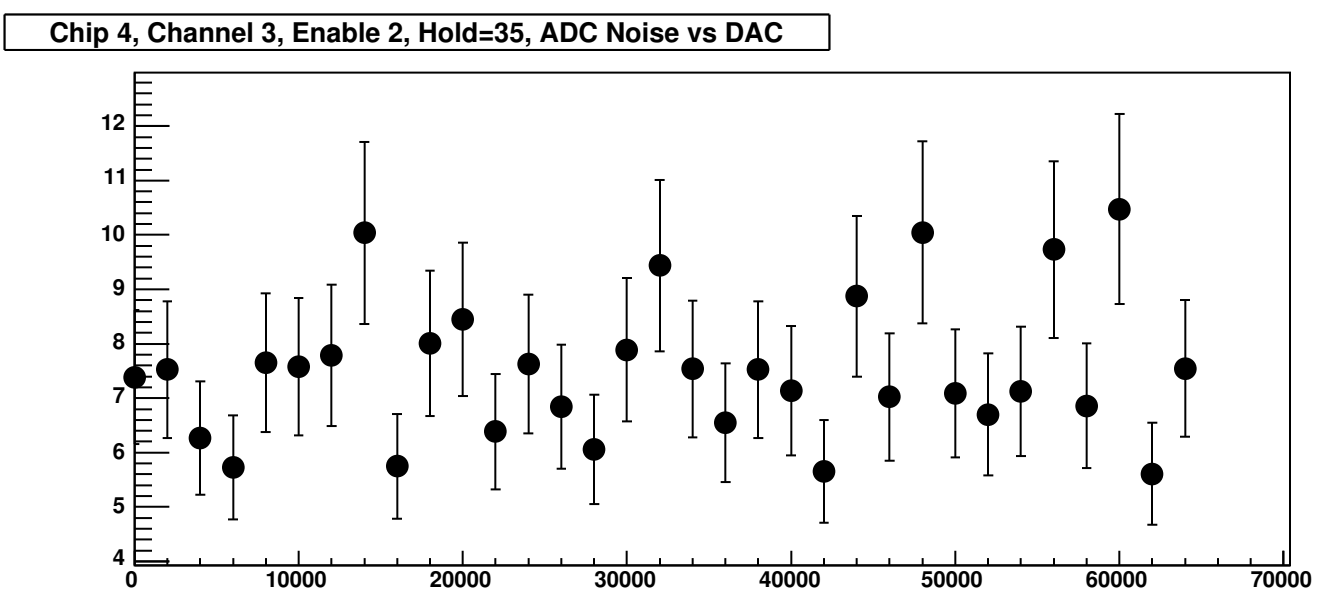

Chip 4, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

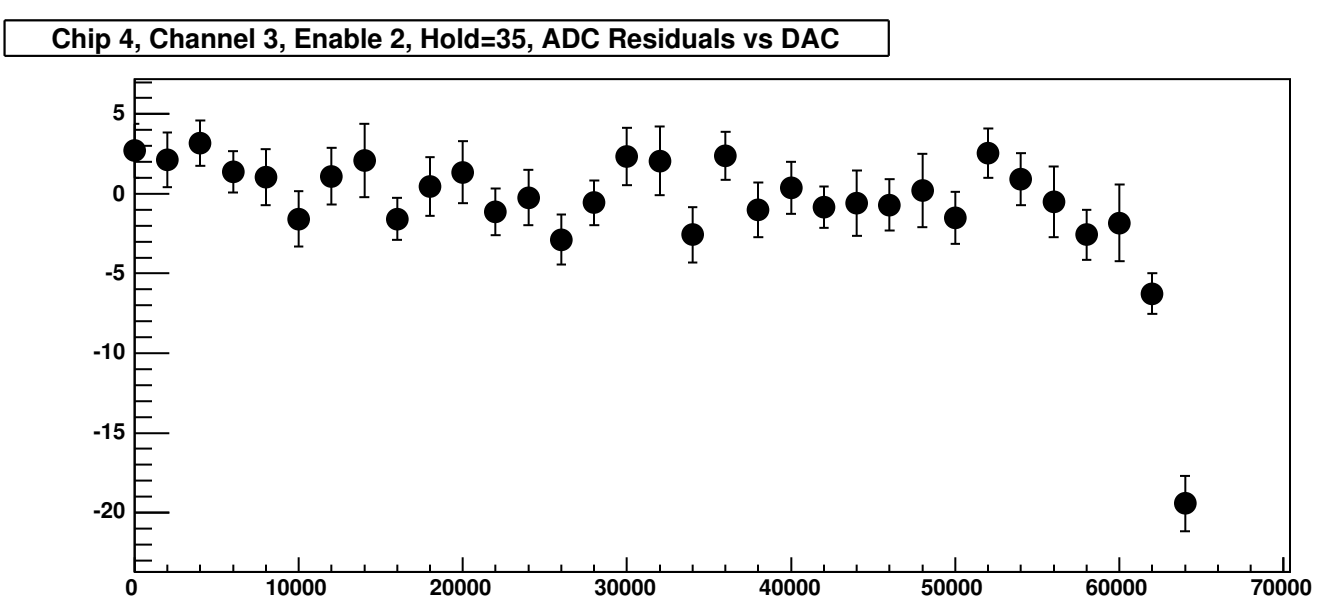

Chip 4, Channel 3, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 8260 / 23
p0 501.9 ± 0.9366
p1 0.4902 ± 3.989e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 3, Enable 1!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
0
10000
20000
30000
40000
50000
60000
70000

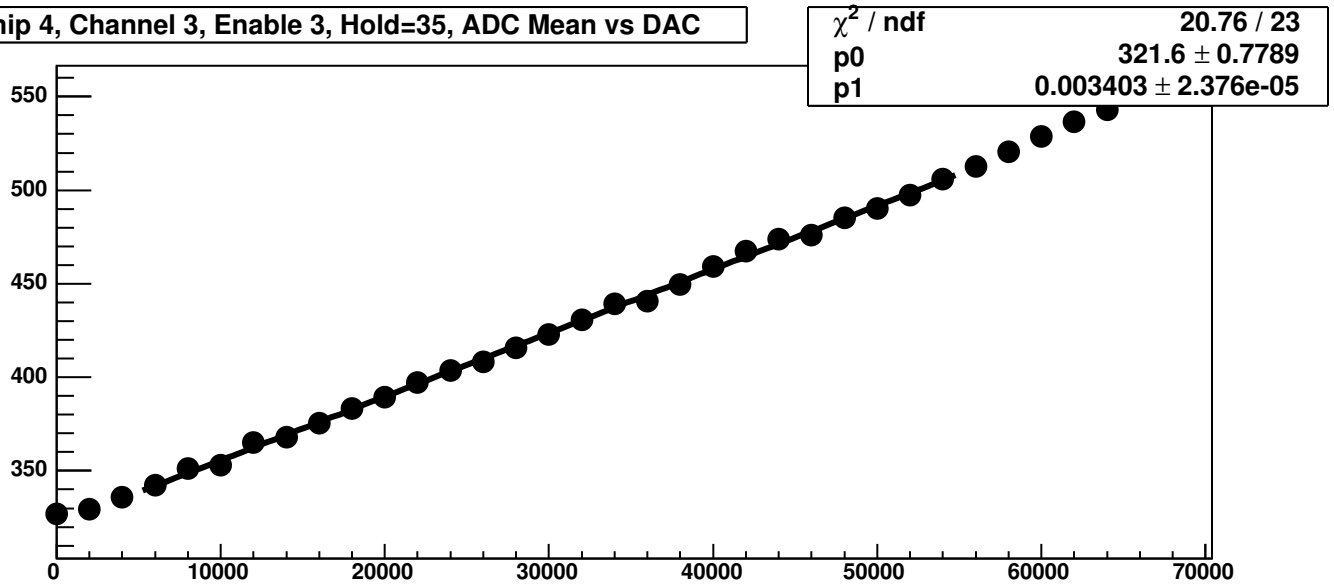

Chip 4, Channel 3, Enable 1!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ^2 / ndf 21.9 / 23
p0 325 ± 0.7507
p1 0.003002 ± 2.265e-05
520
500
480
460
440
420
400
380
360
340
320
0
10000
20000
30000
40000
50000
60000
70000

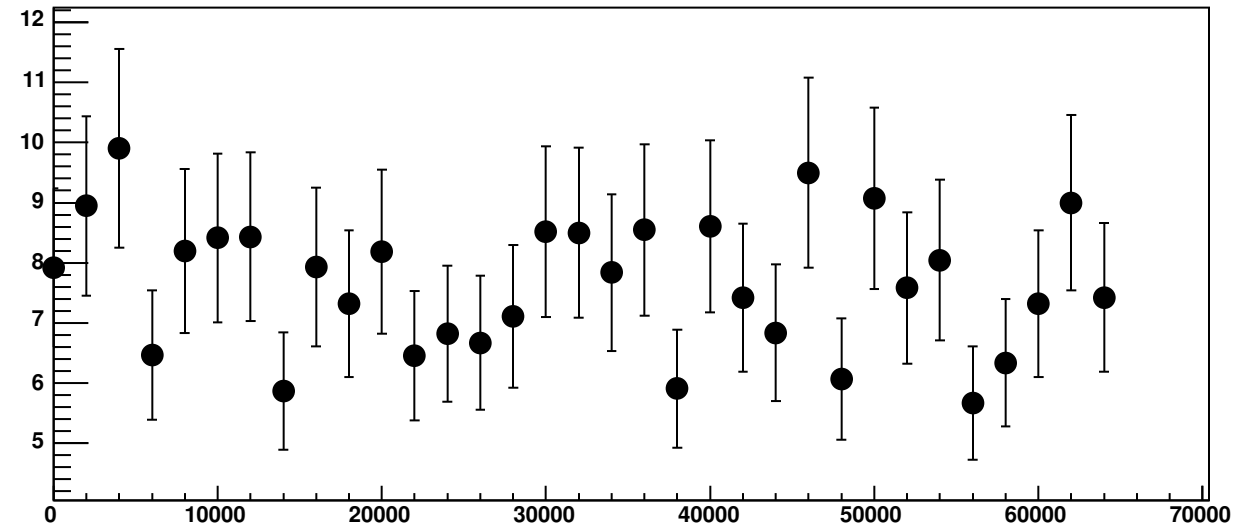

Chip 4, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
0
10000
20000
30000
40000
50000
60000
70000

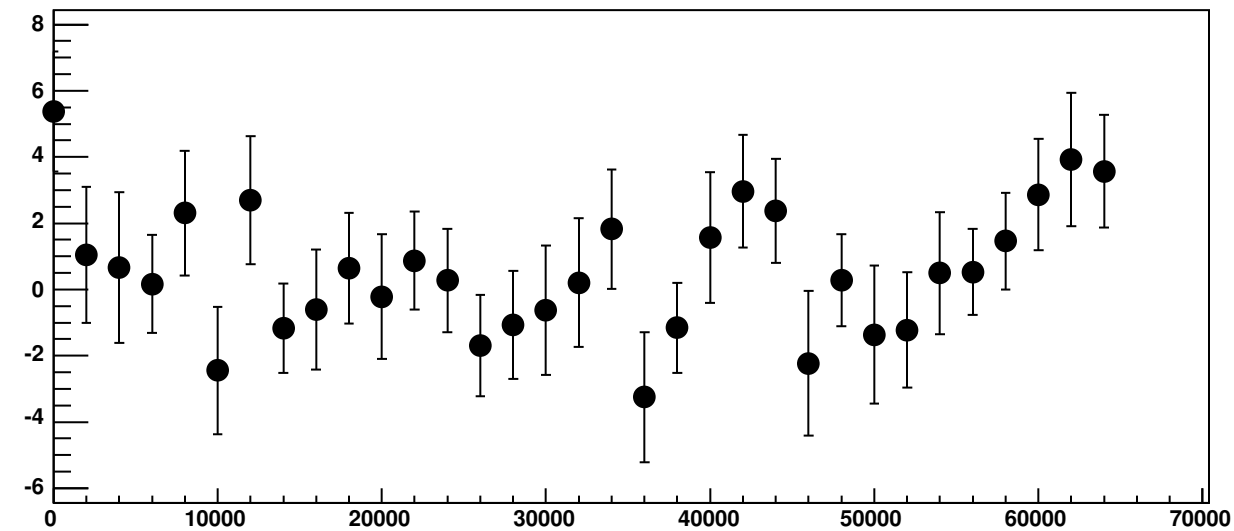
Chip 4, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC



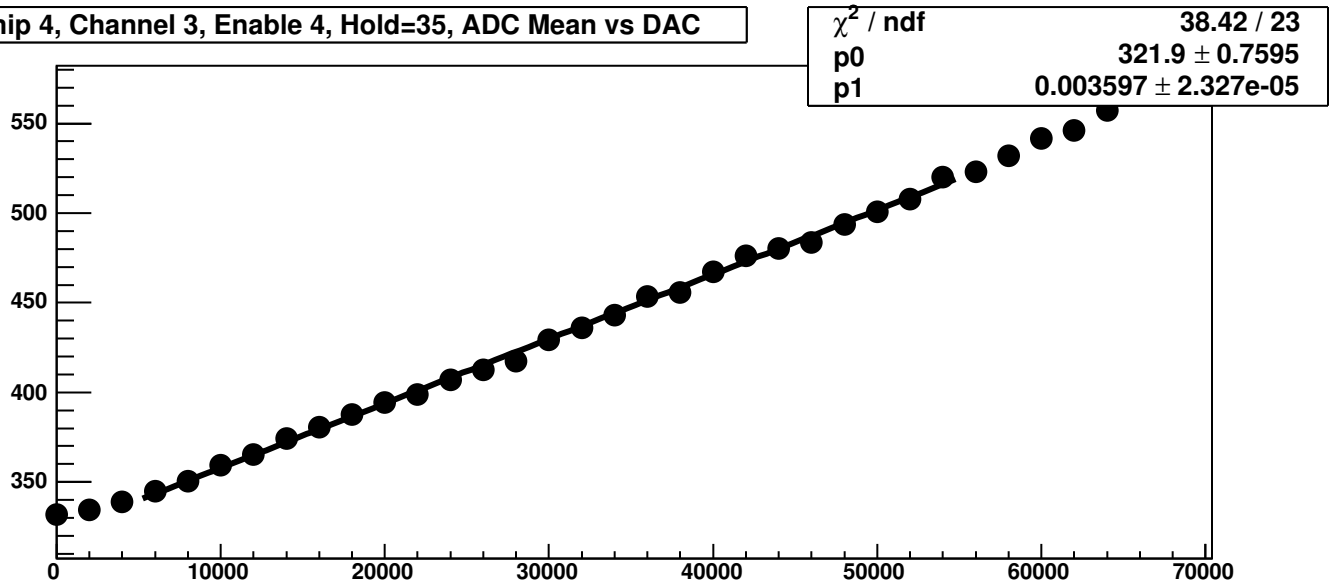
Chip 4, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC



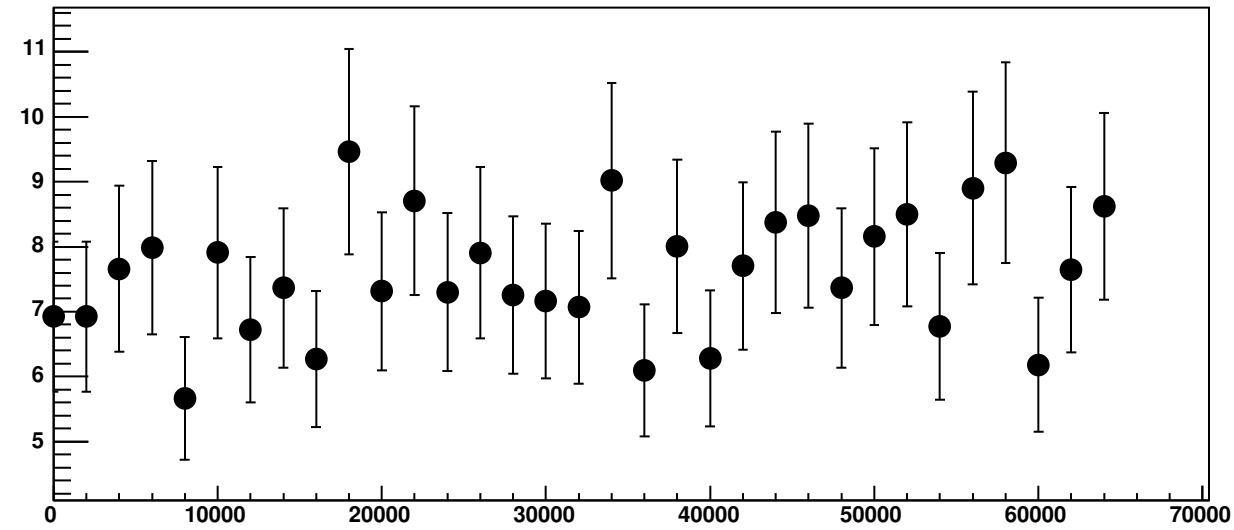
Chip 4, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC



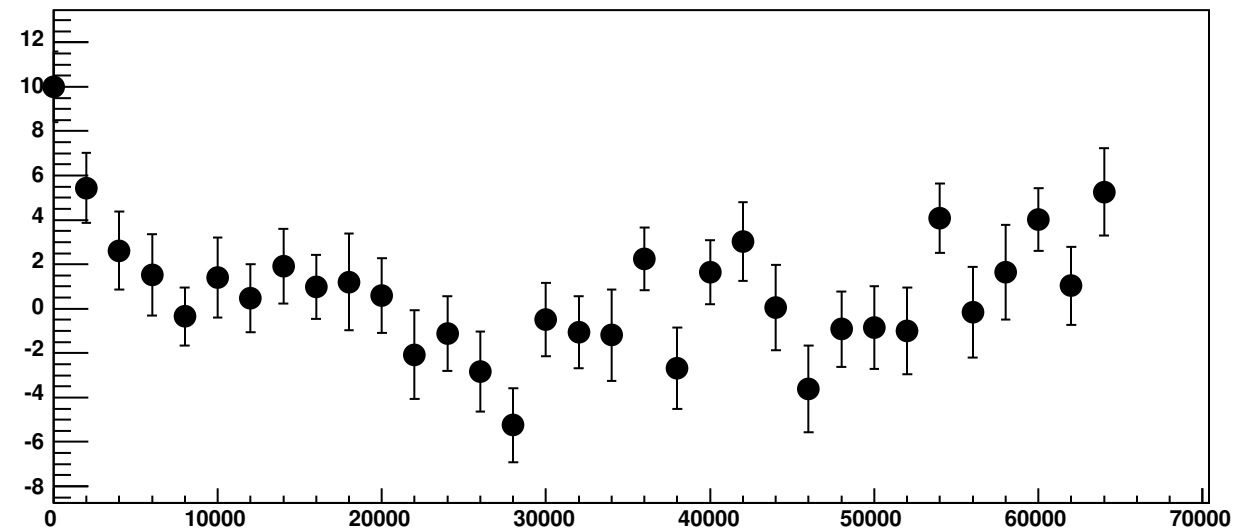
Chip 4, Channel 3, Enable 4, Hold=35, ADC Mean vs DAC

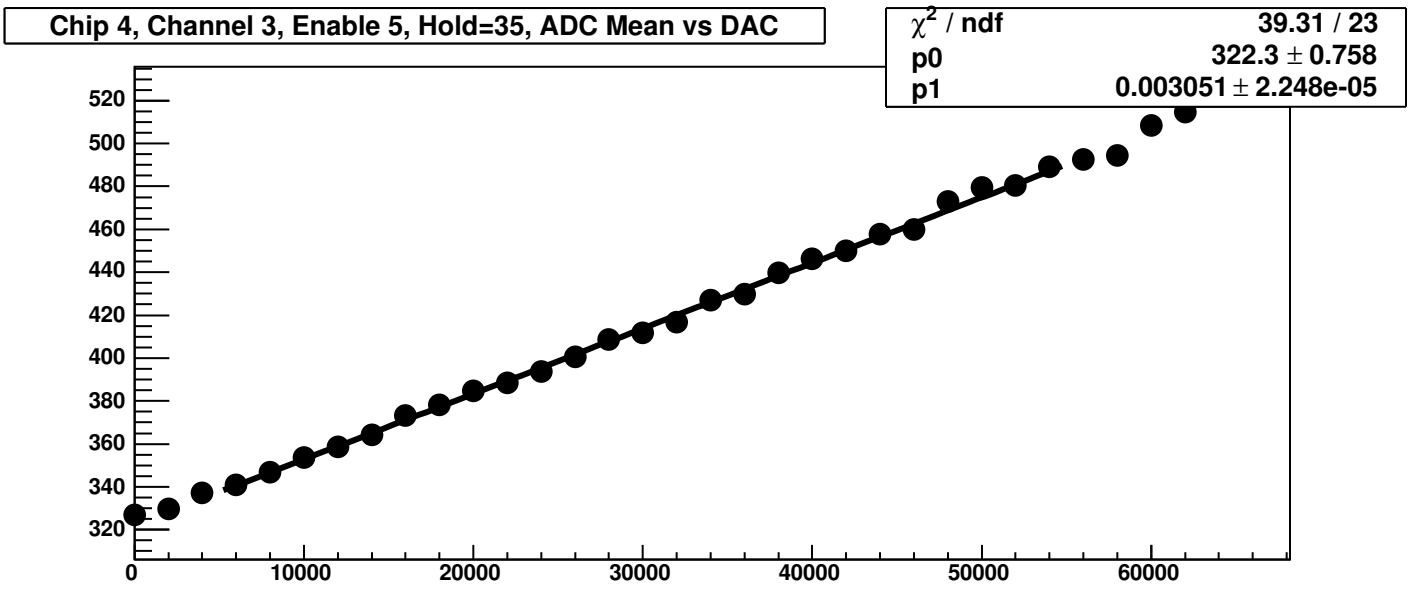


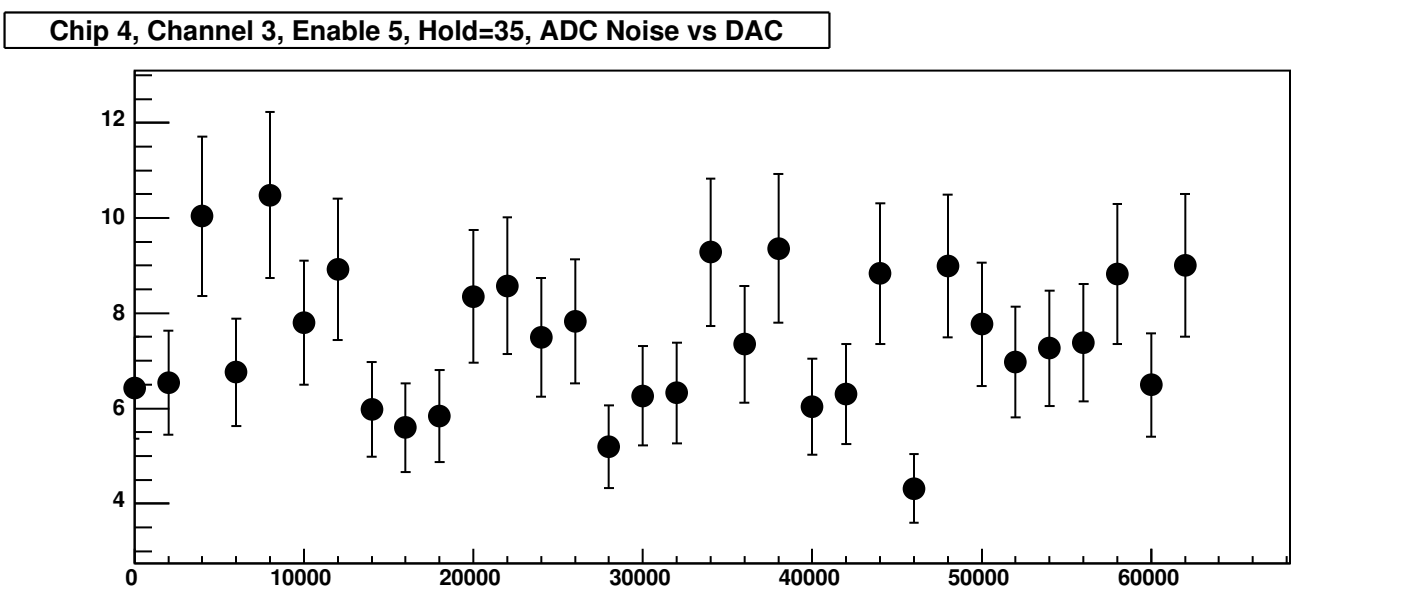
Chip 4, Channel 3, Enable 4, Hold=35, ADC Noise vs DAC

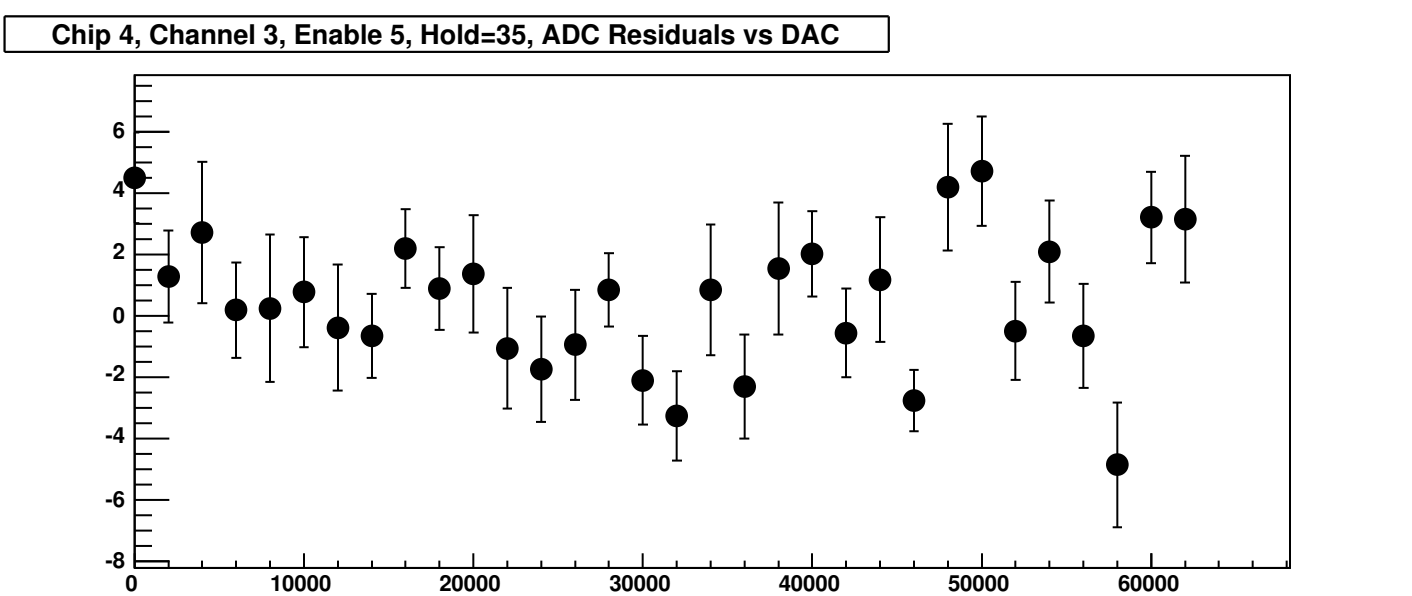


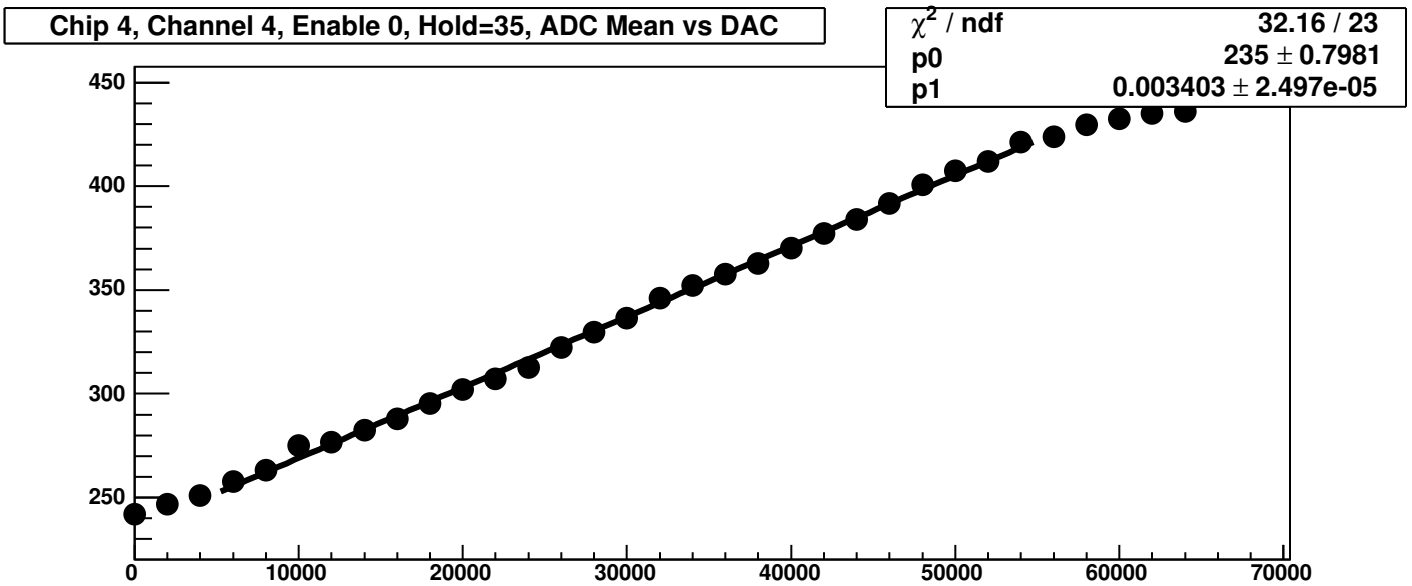
Chip 4, Channel 3, Enable 4, Hold=35, ADC Residuals vs DAC

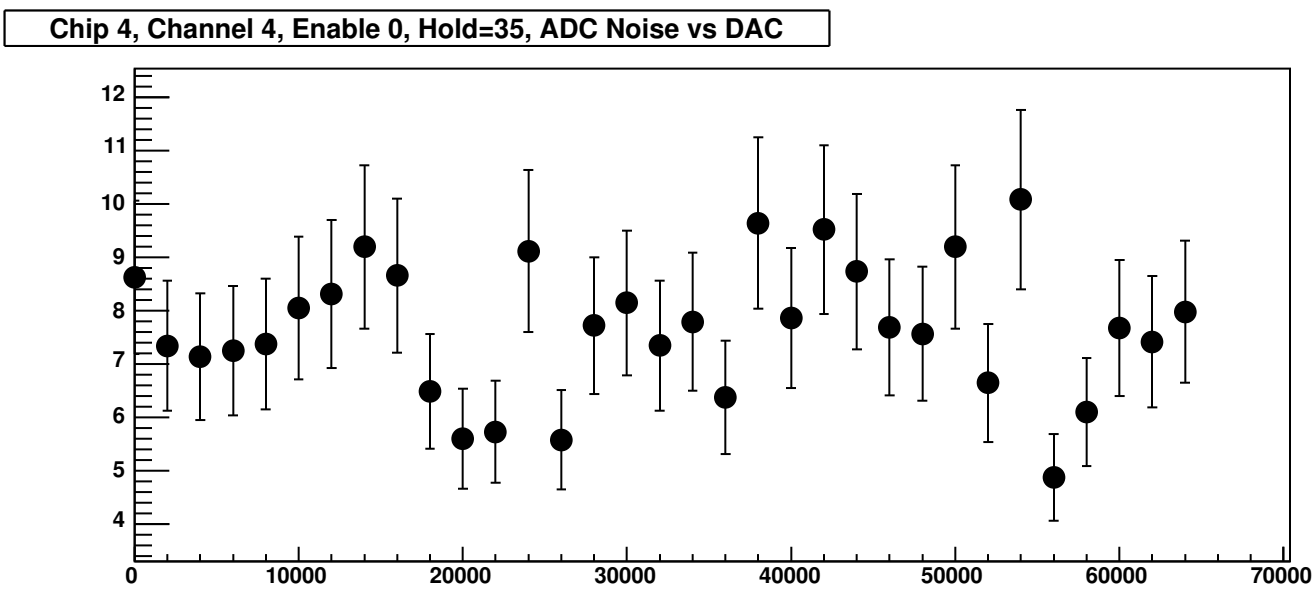


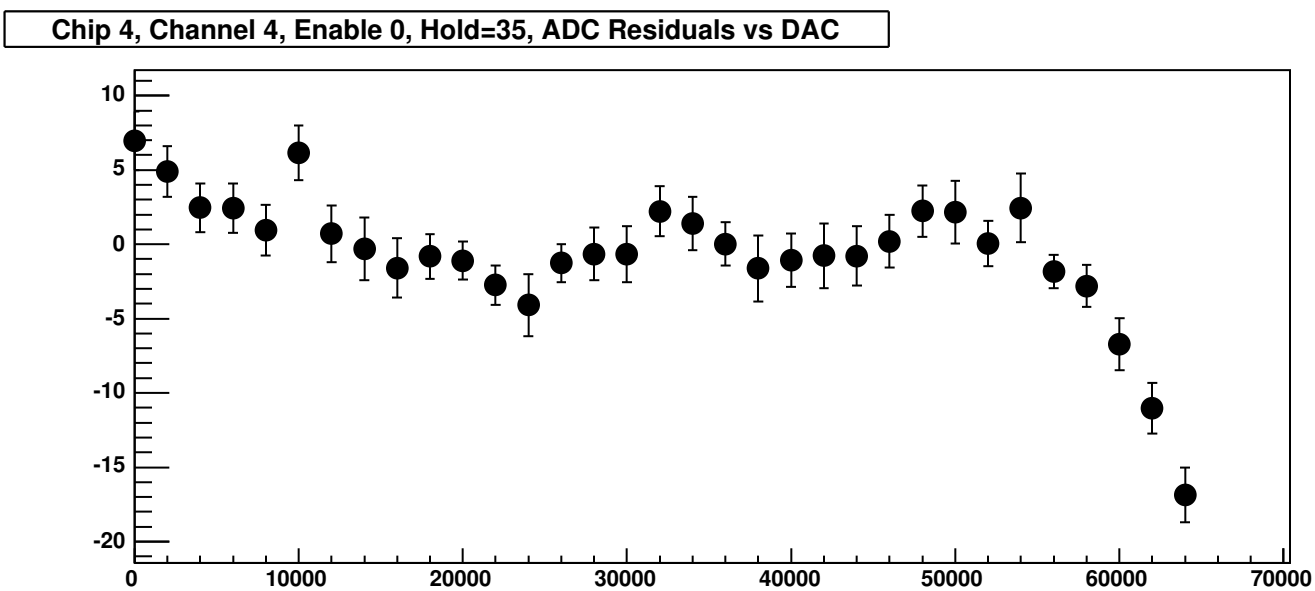

Chip 4, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 39.31 / 23
p0 322.3 ± 0.758
p1 0.003051 ± 2.248e-05
520
500
480
460
440
420
400
380
360
340
320
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000

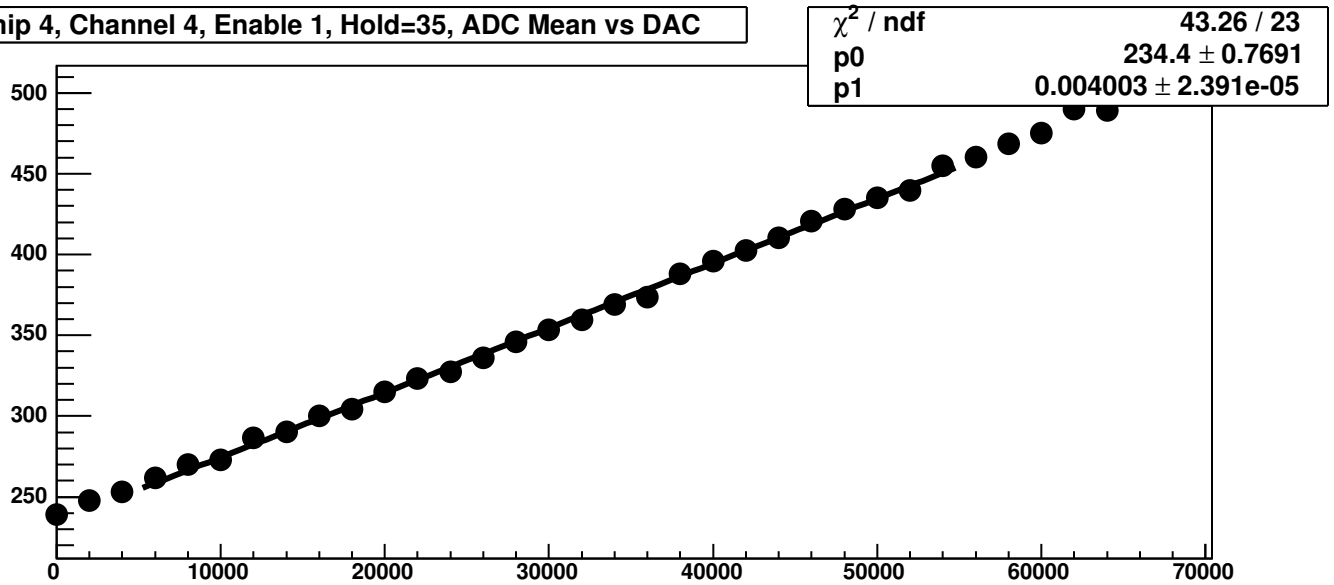

Chip 4, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 32.16 / 23
p0 235 ± 0.7981
p1 0.003403 ± 2.497e-05
450
400
350
300
250
0
10000
20000
30000
40000
50000
60000
70000

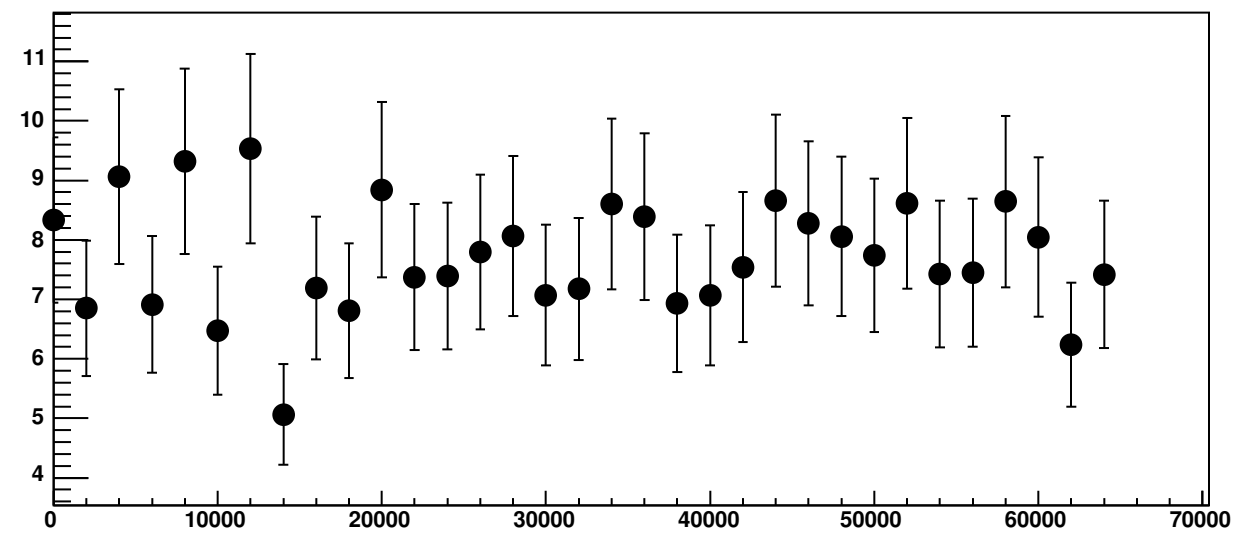

Chip 4, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
0
10000
20000
30000
40000
50000
60000
70000

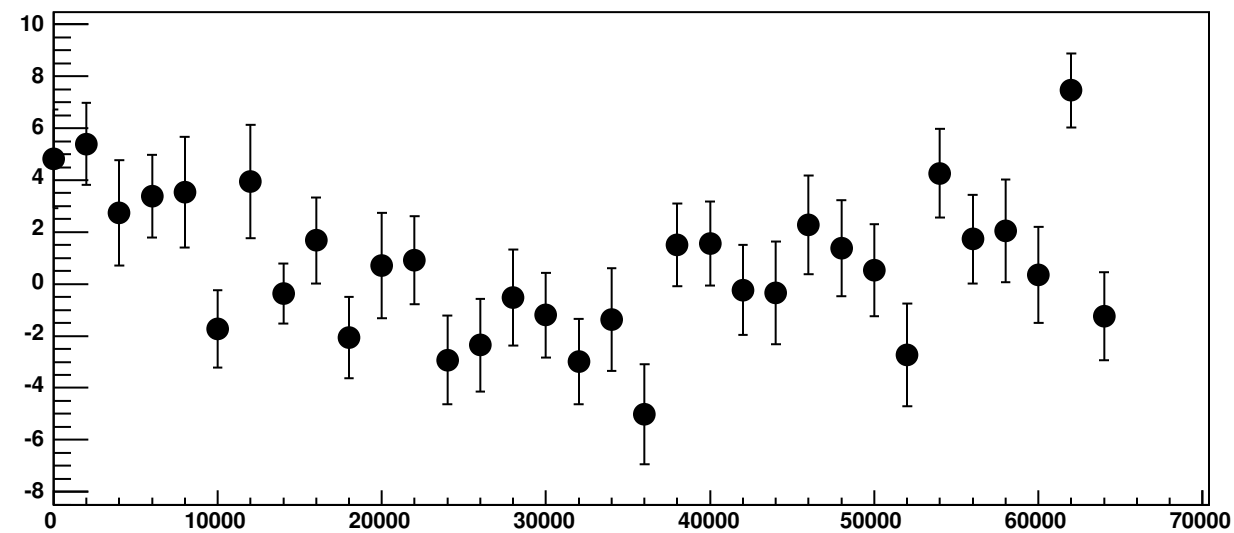
Chip 4, Channel 4, Enable 1, Hold=35, ADC Mean vs DAC

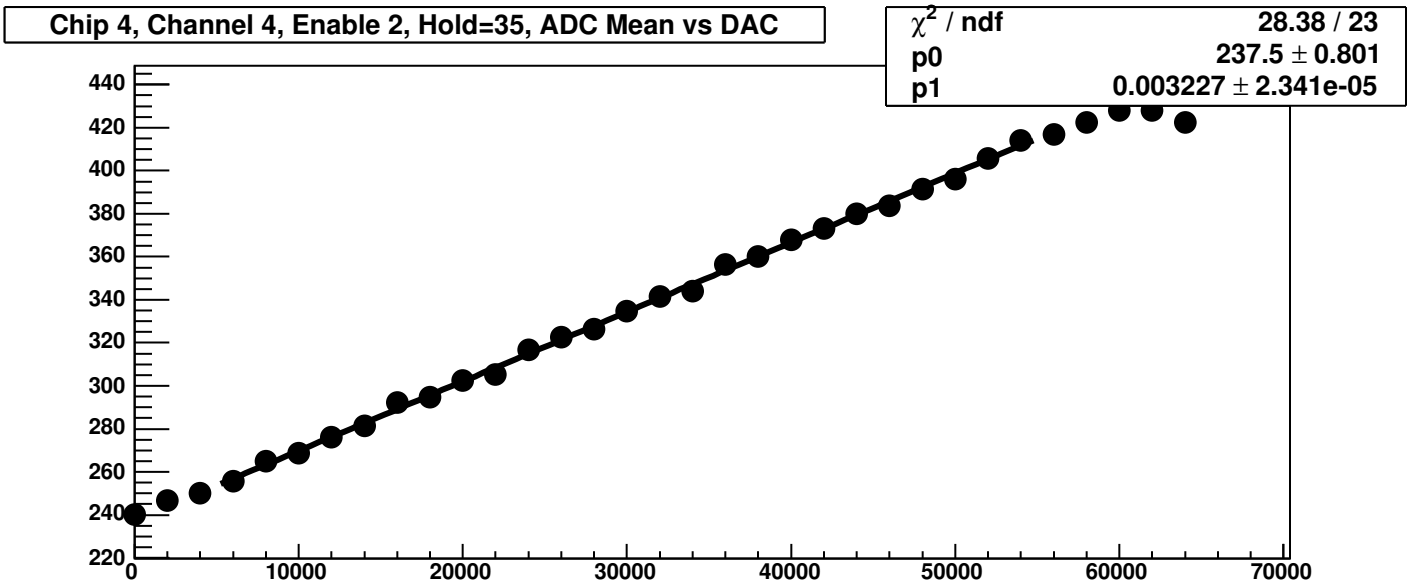


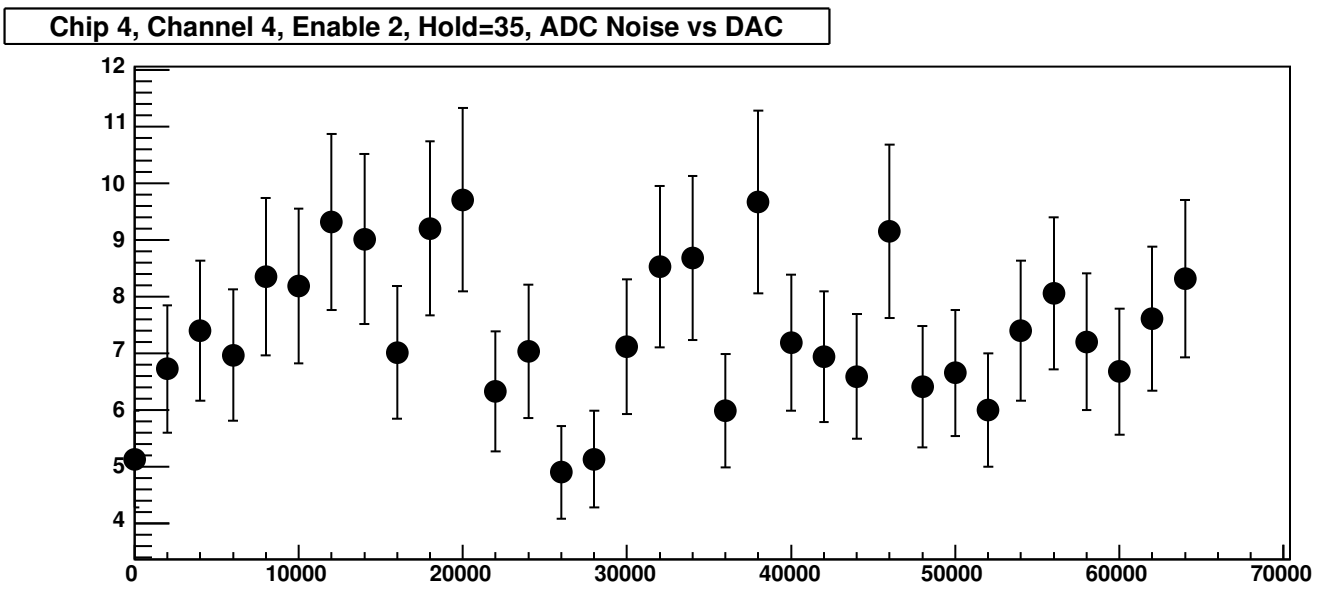
Chip 4, Channel 4, Enable 1, Hold=35, ADC Noise vs DAC

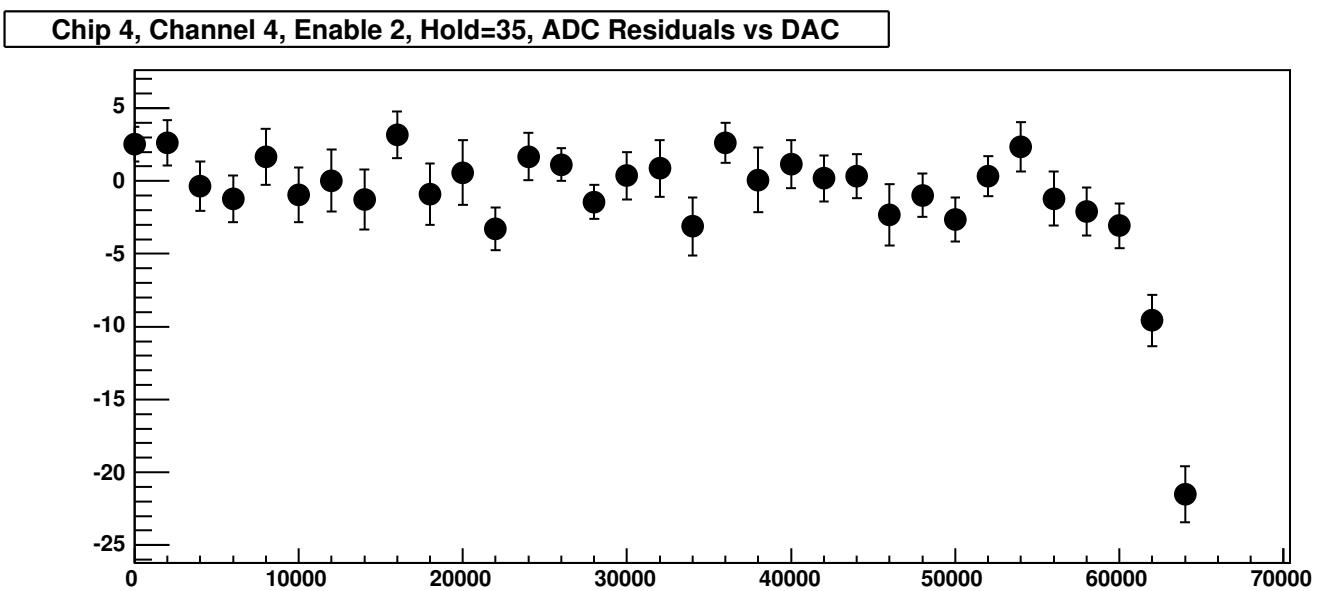


Chip 4, Channel 4, Enable 1, Hold=35, ADC Residuals vs DAC

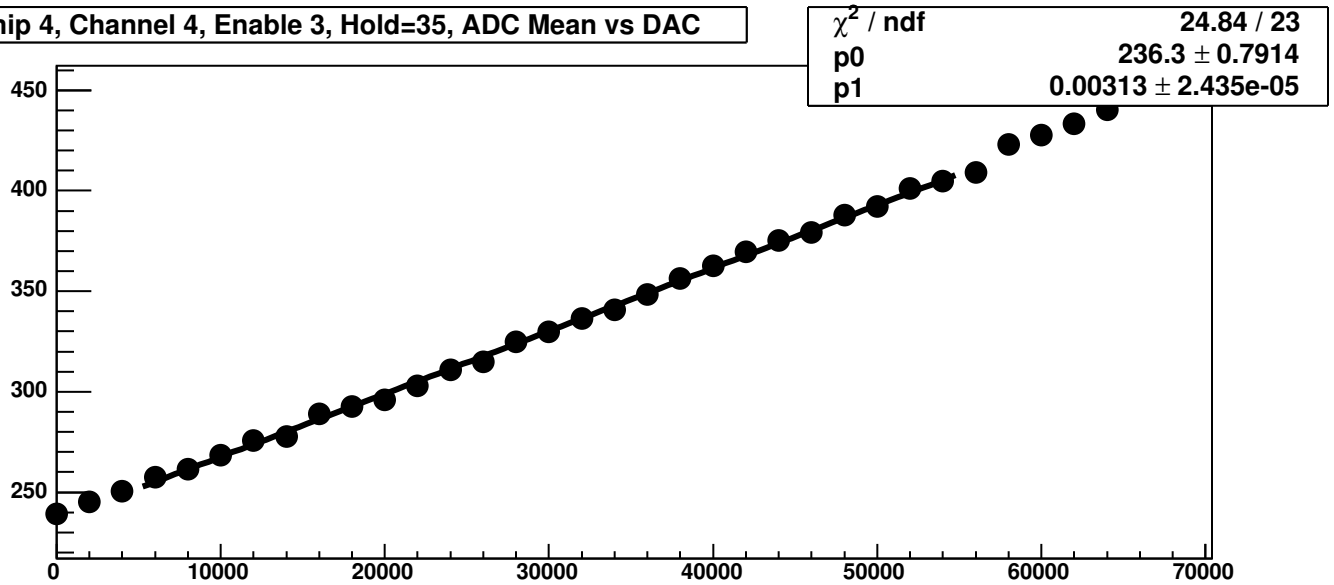



Chip 4, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ^2 / ndf 28.38 / 23
p0 237.5 ± 0.801
p1 0.003227 ± 2.341e-05
440
420
400
380
360
340
320
300
280
260
240
220
0
10000
20000
30000
40000
50000
60000
70000

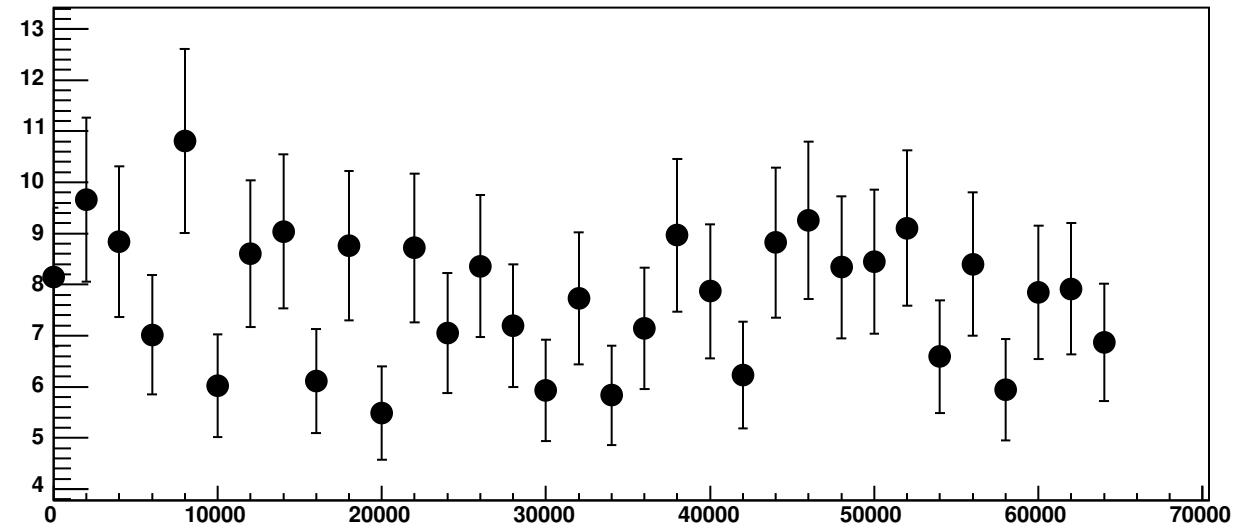

Chip 4, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
0
10000
20000
30000
40000
50000
60000
70000

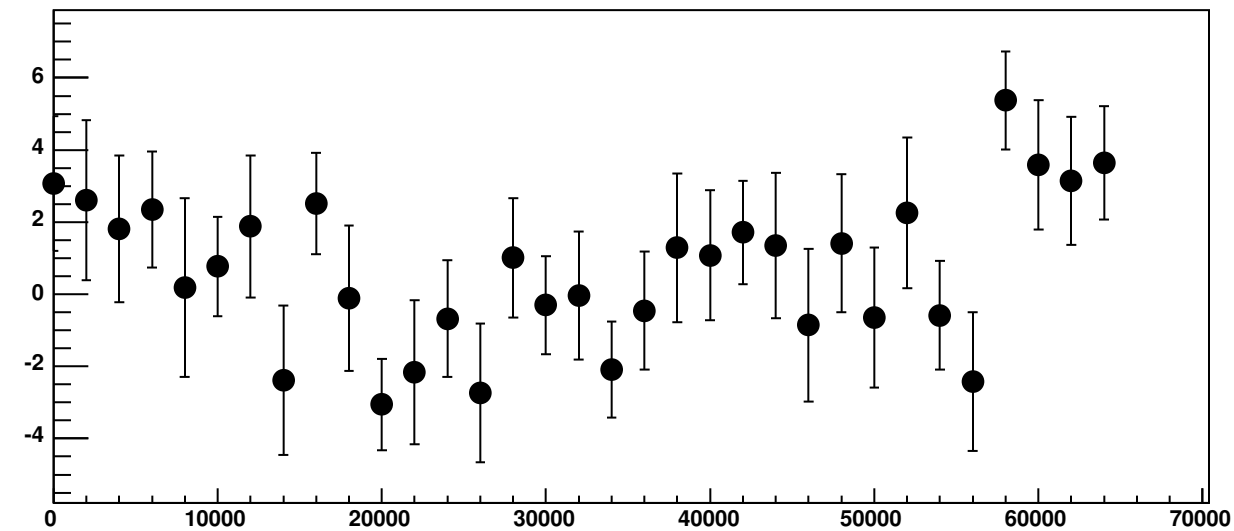
Chip 4, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC

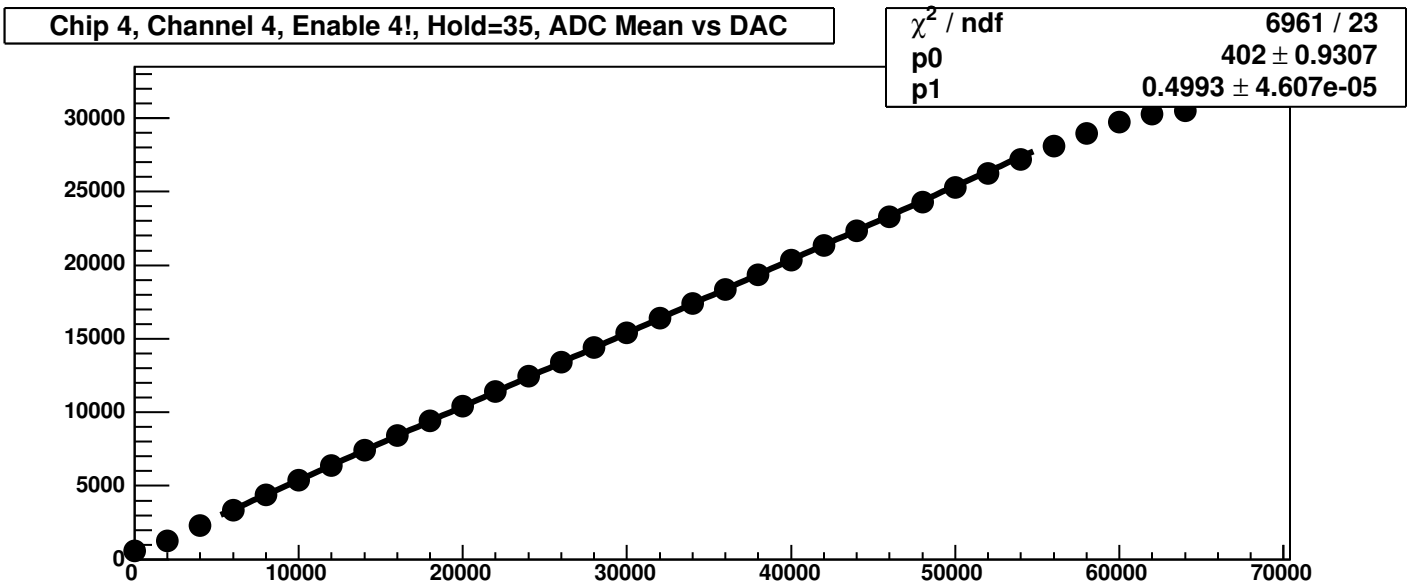


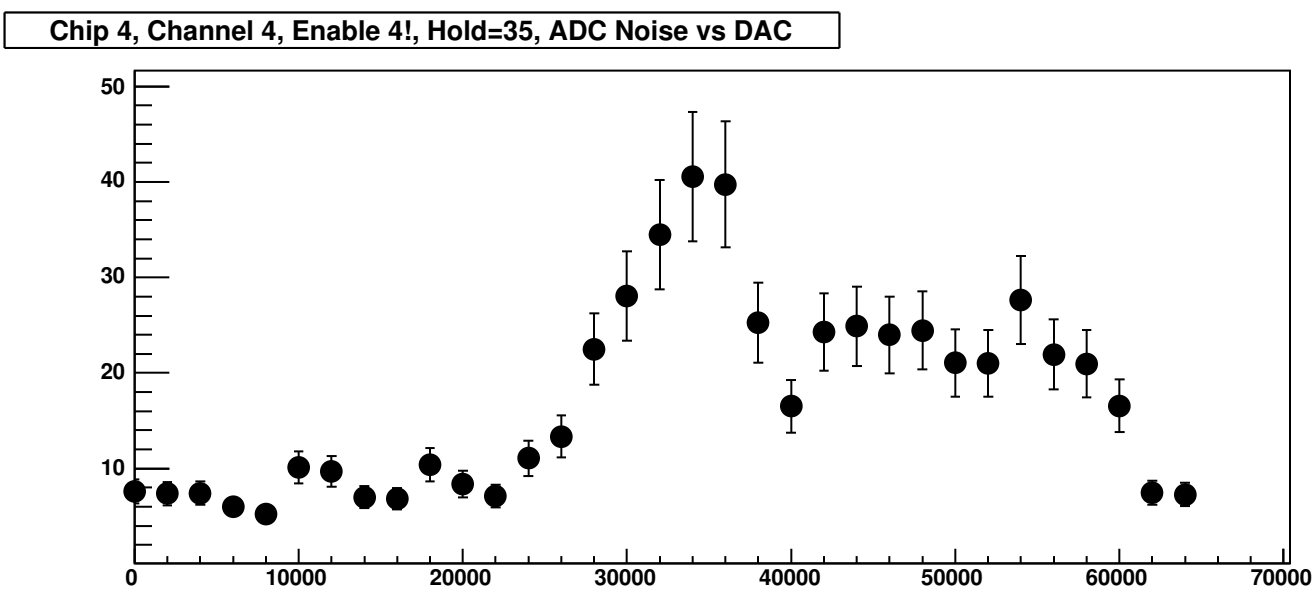
Chip 4, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC

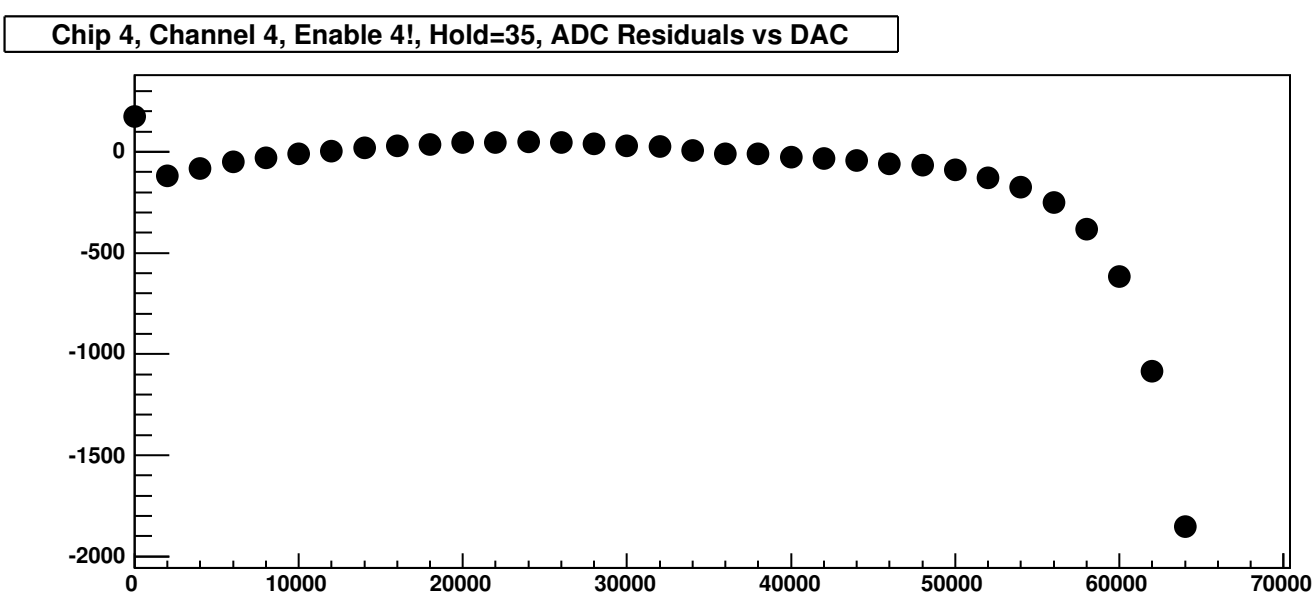


Chip 4, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC

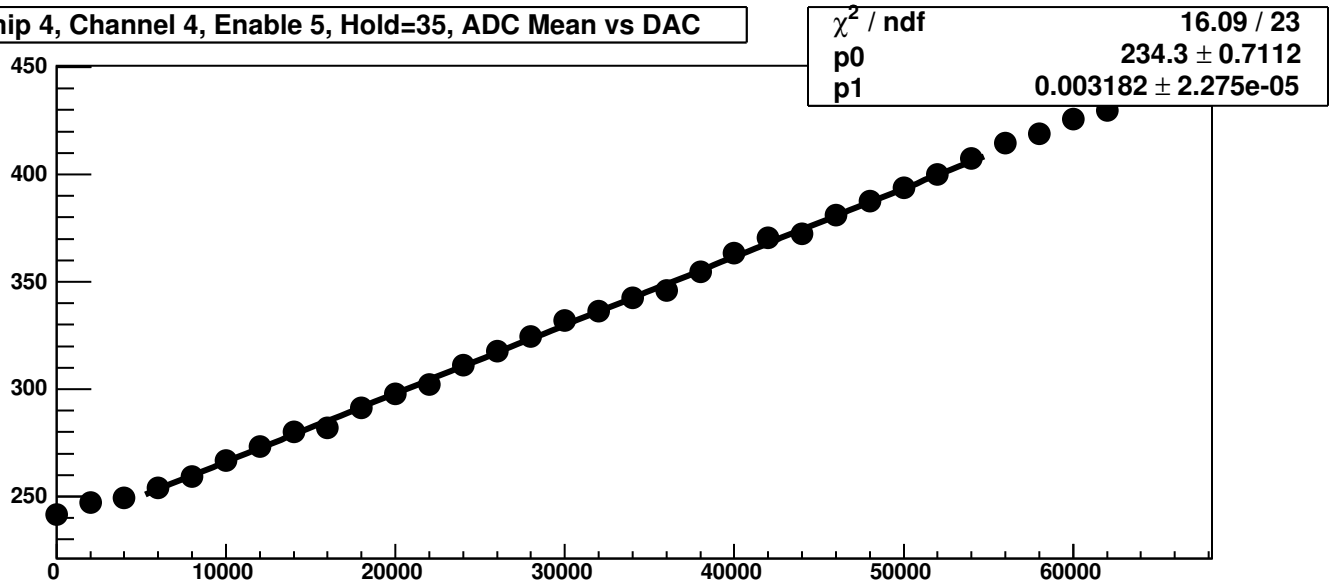



Chip 4, Channel 4, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 6961 / 23
p0 402 ± 0.9307
p1 0.4993 ± 4.607e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

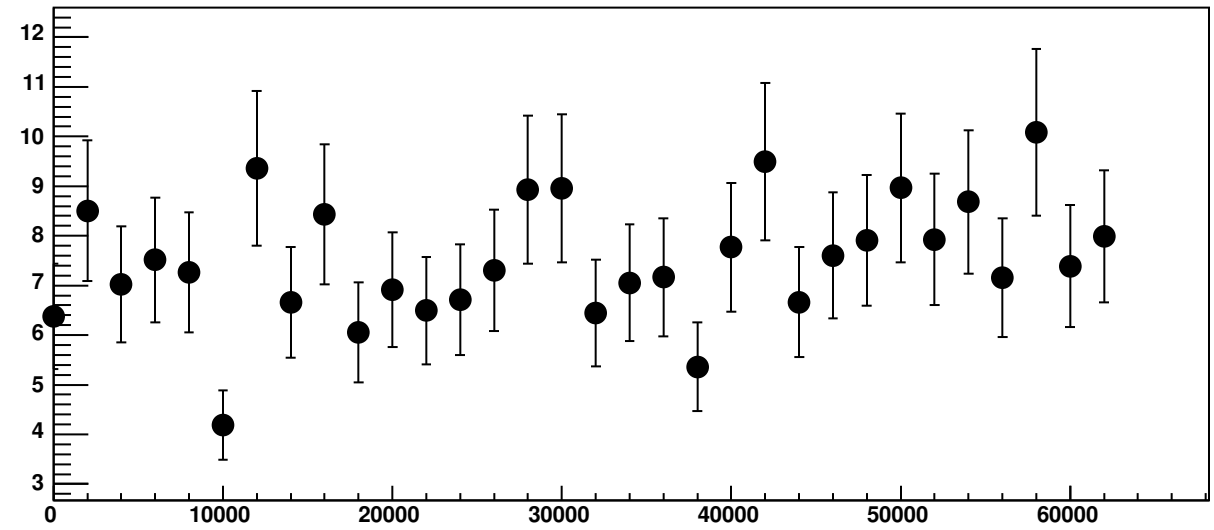

Chip 4, Channel 4, Enable 4!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 4, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000

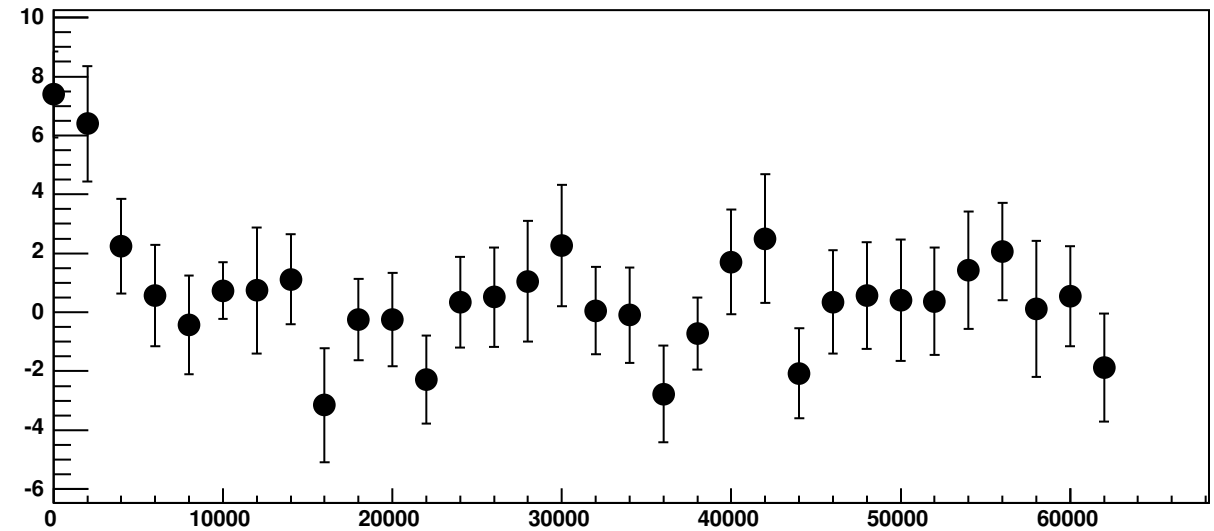
Chip 4, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC

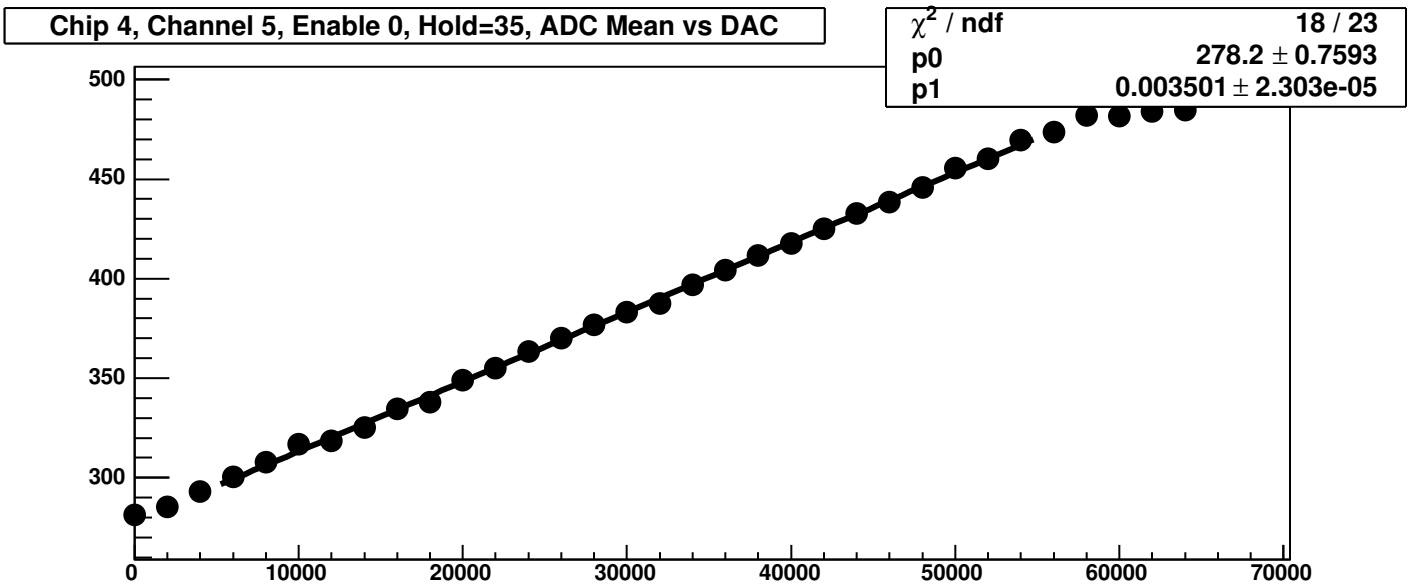


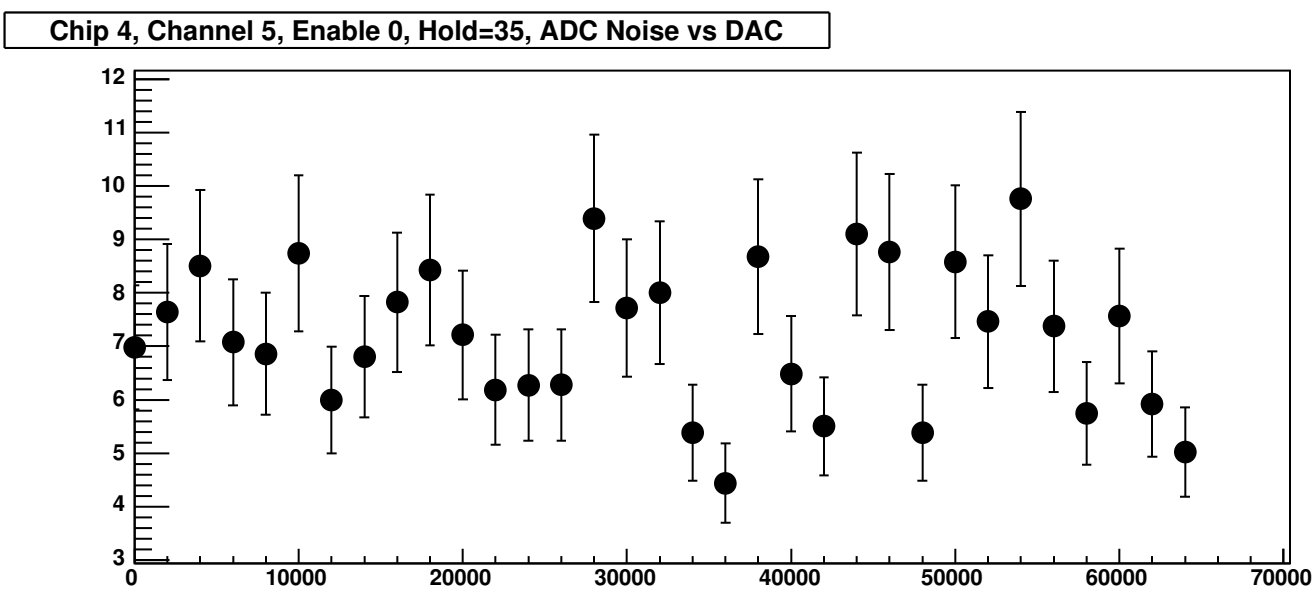
Chip 4, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC

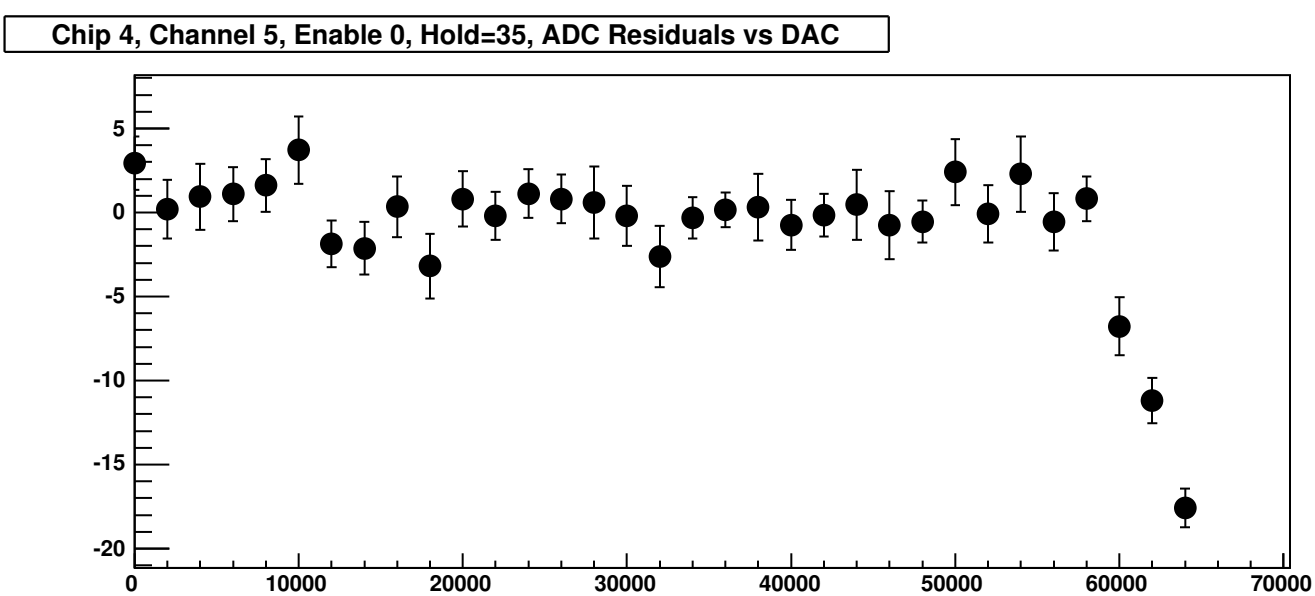


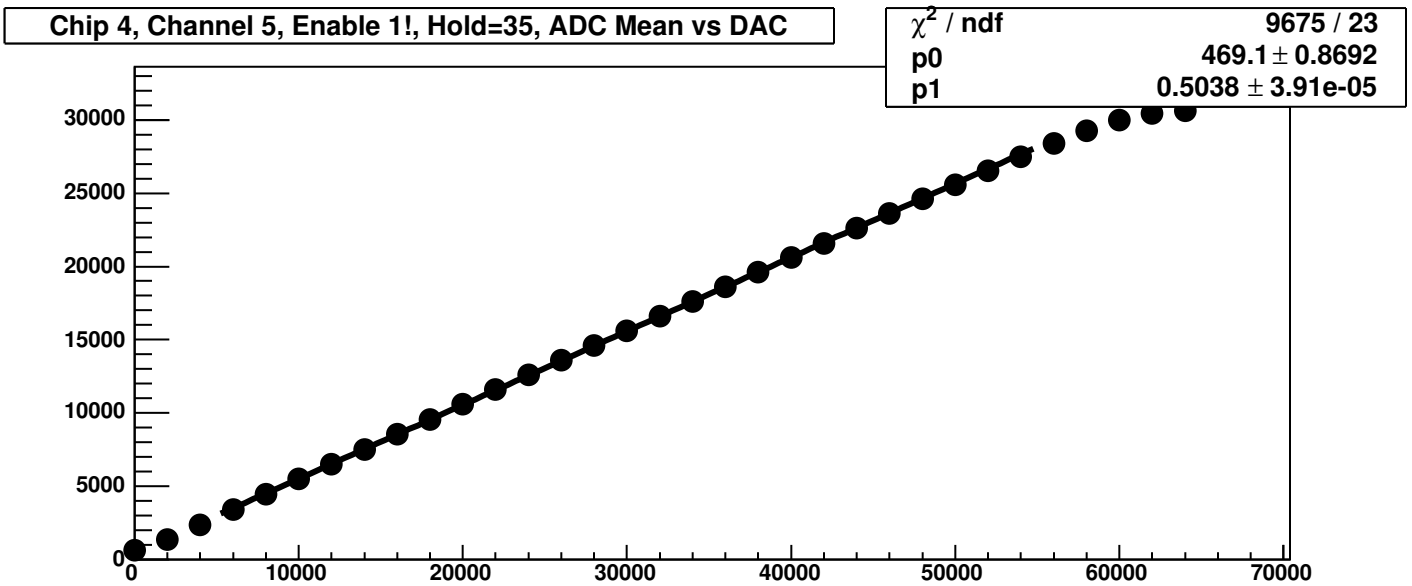
Chip 4, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC

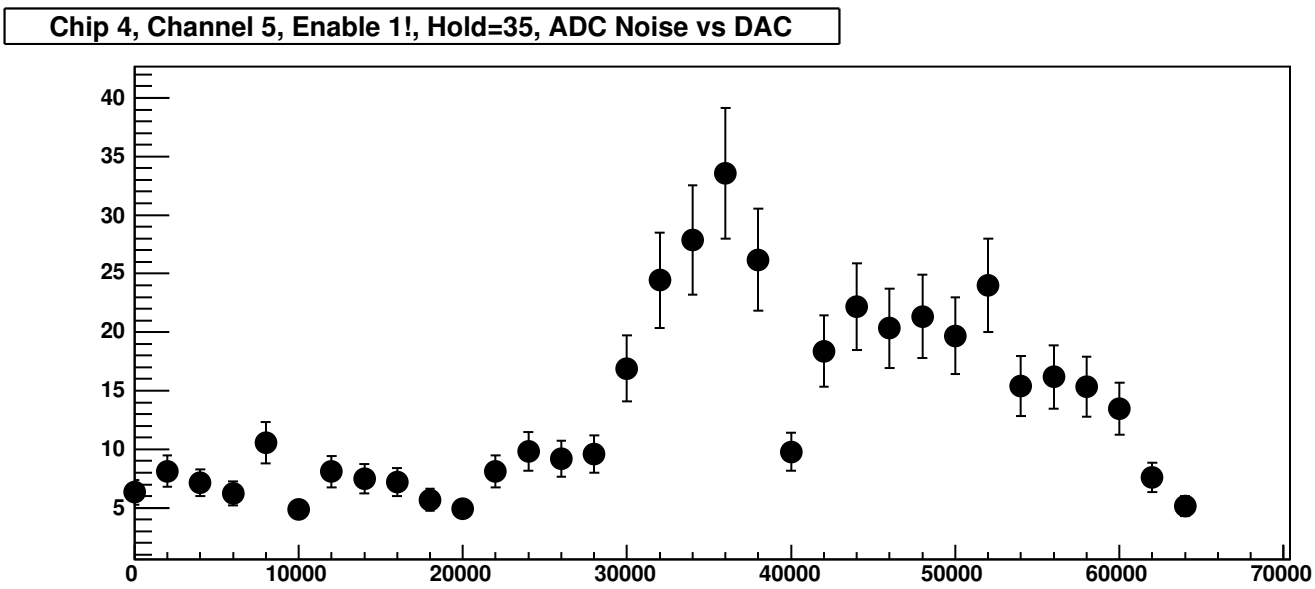


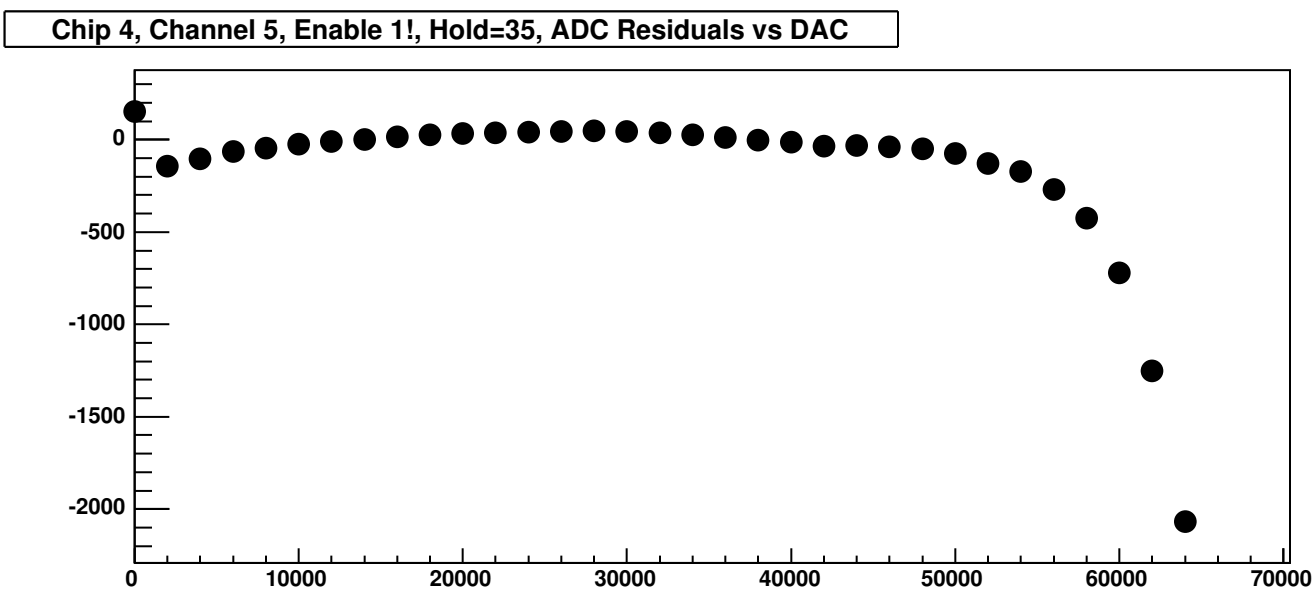

Chip 4, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 18 / 23
p0 278.2 ± 0.7593
p1 0.003501 ± 2.303e-05
500
450
400
350
300
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

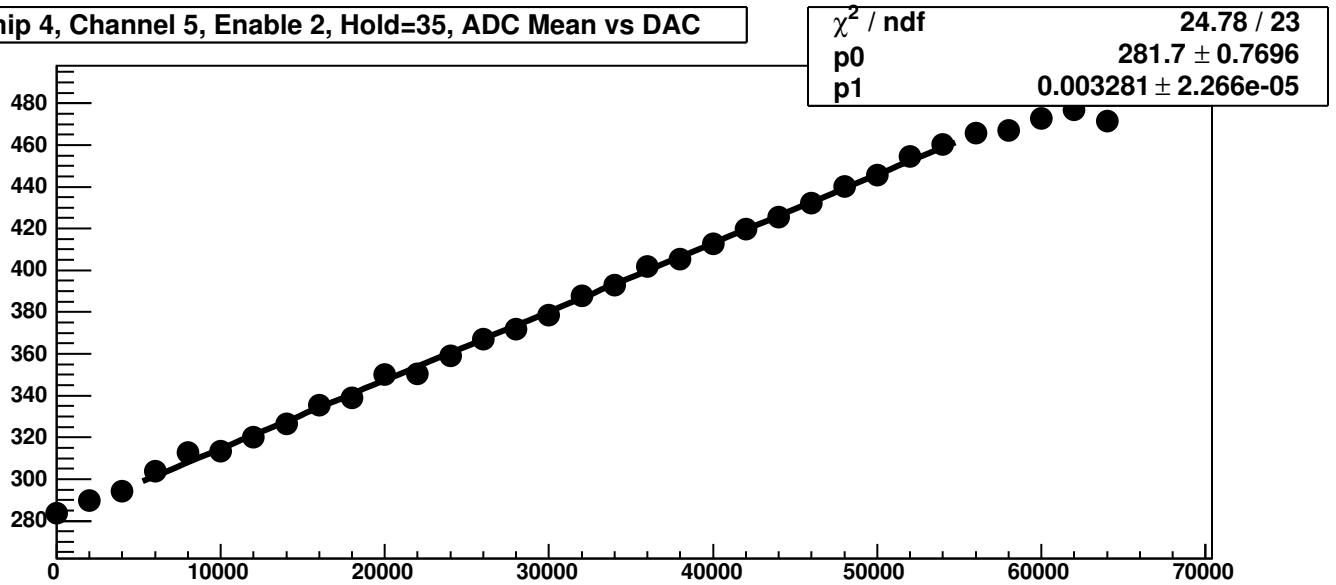

Chip 4, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 5, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 9675 / 23
p0 469.1 ± 0.8692
p1 0.5038 ± 3.91e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

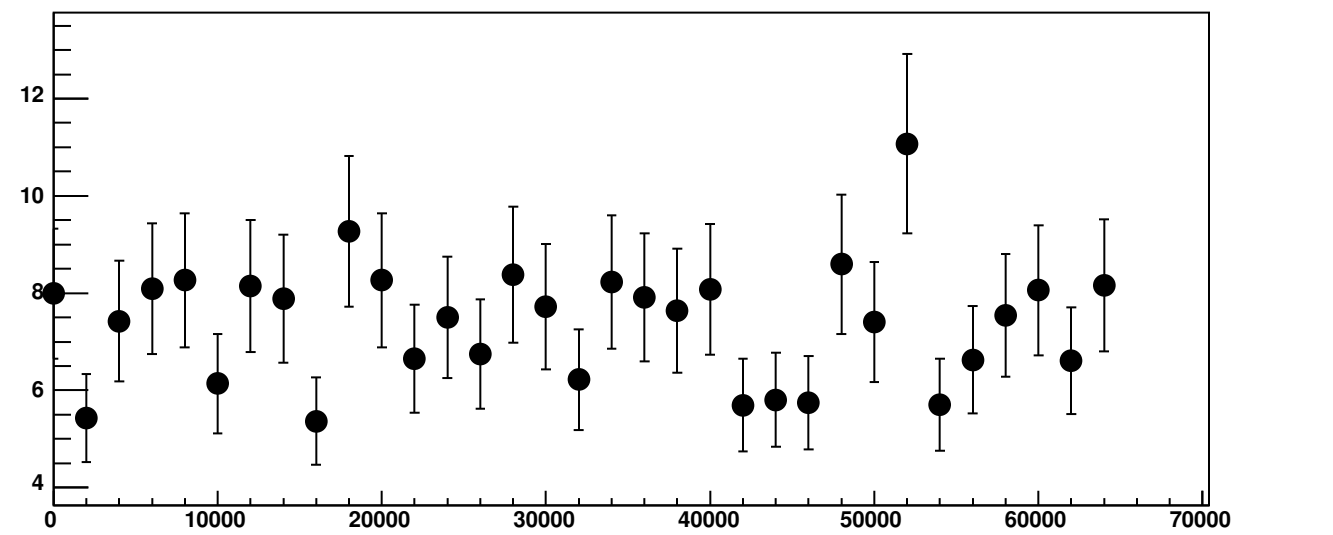

Chip 4, Channel 5, Enable 1!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 5, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000

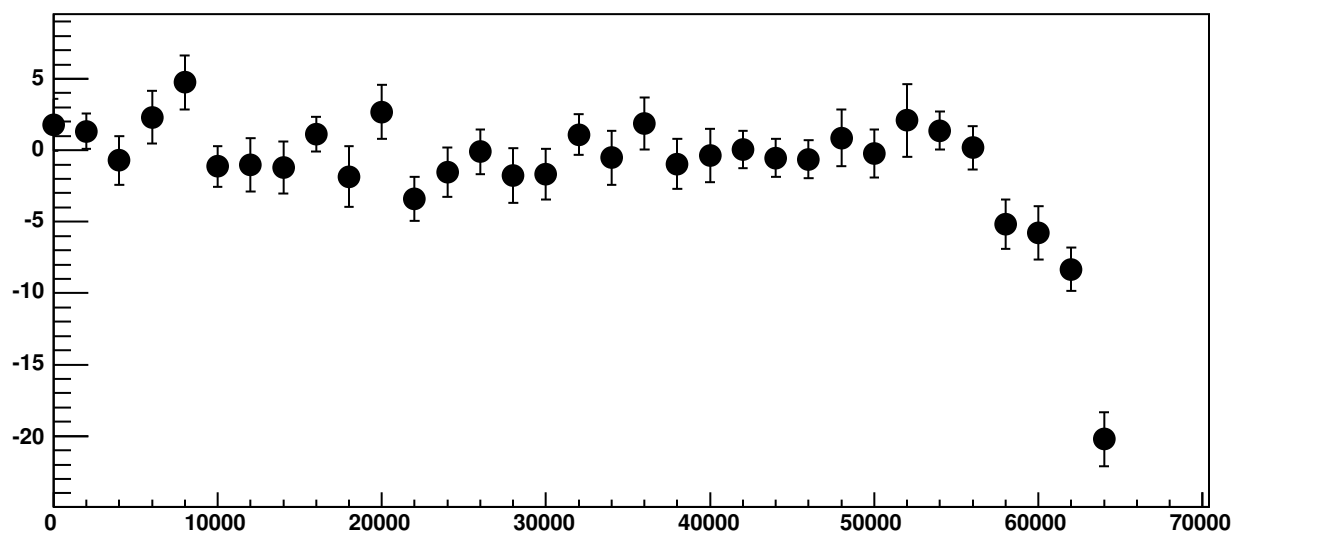
Chip 4, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC



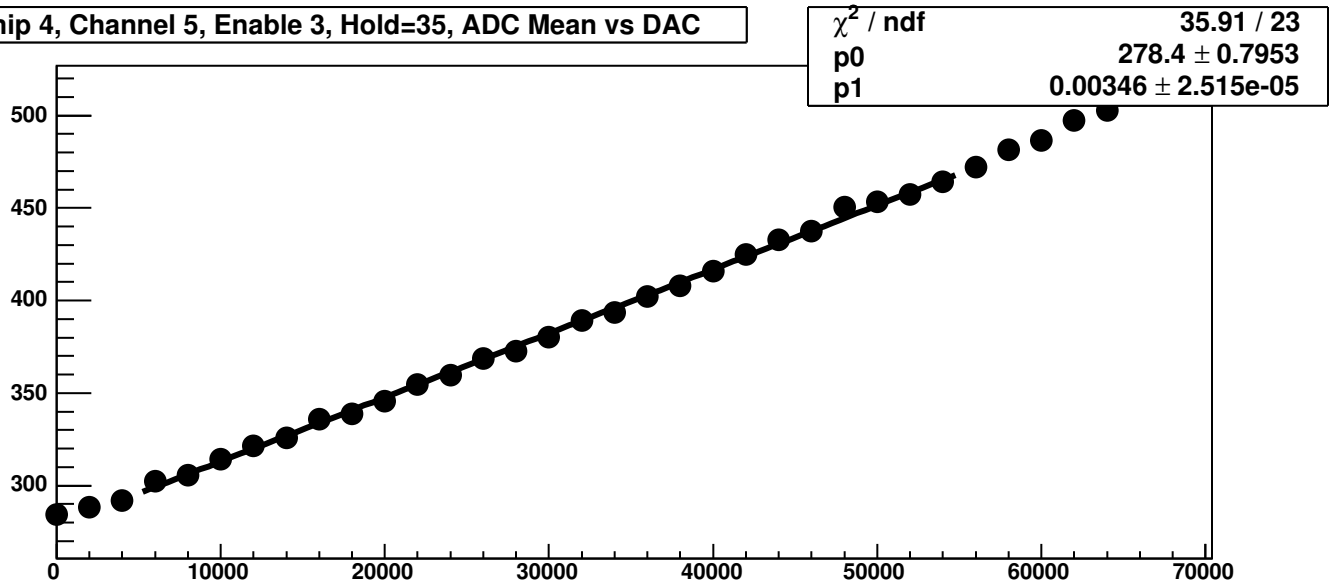
Chip 4, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC



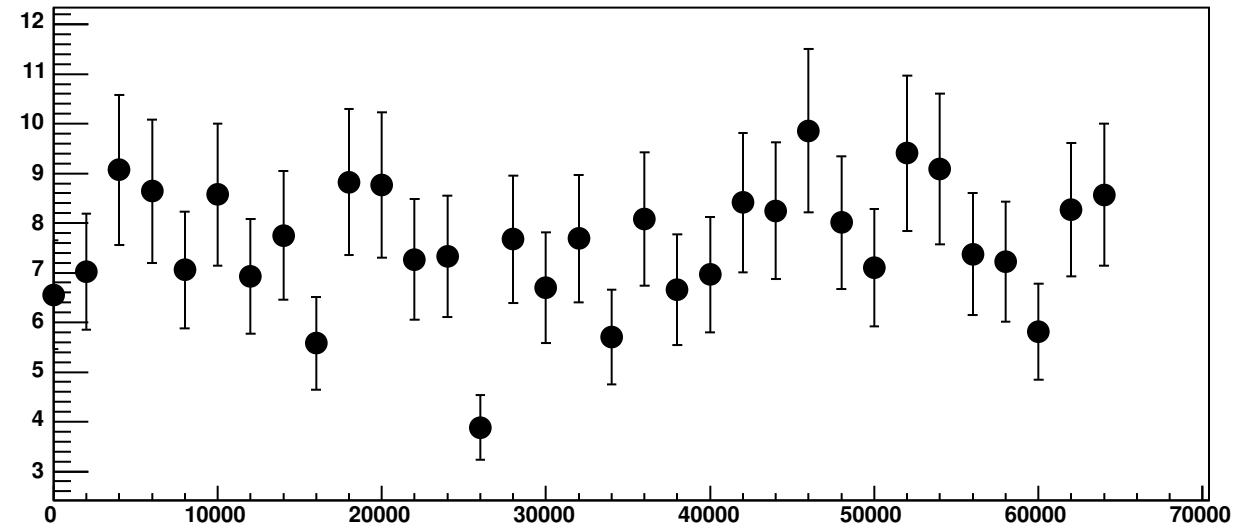
Chip 4, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC



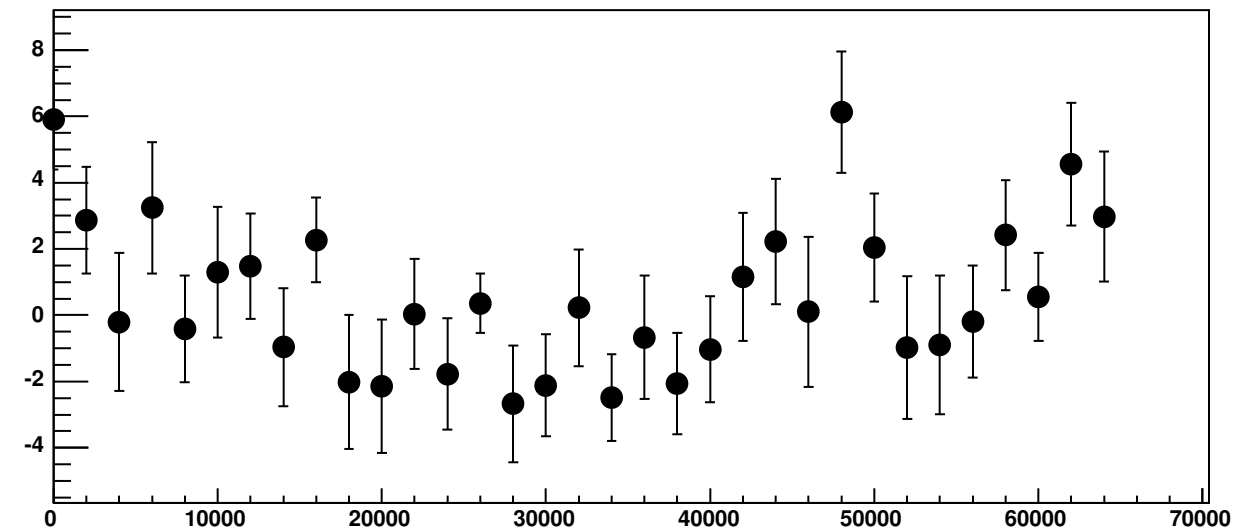
Chip 4, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC



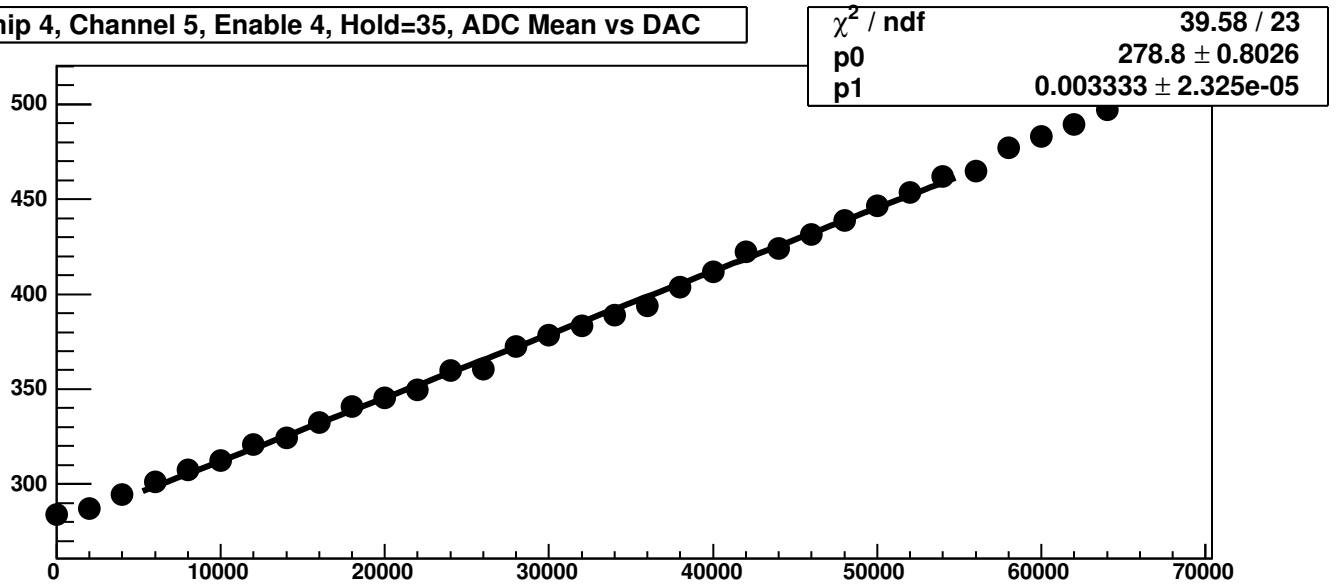
Chip 4, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC



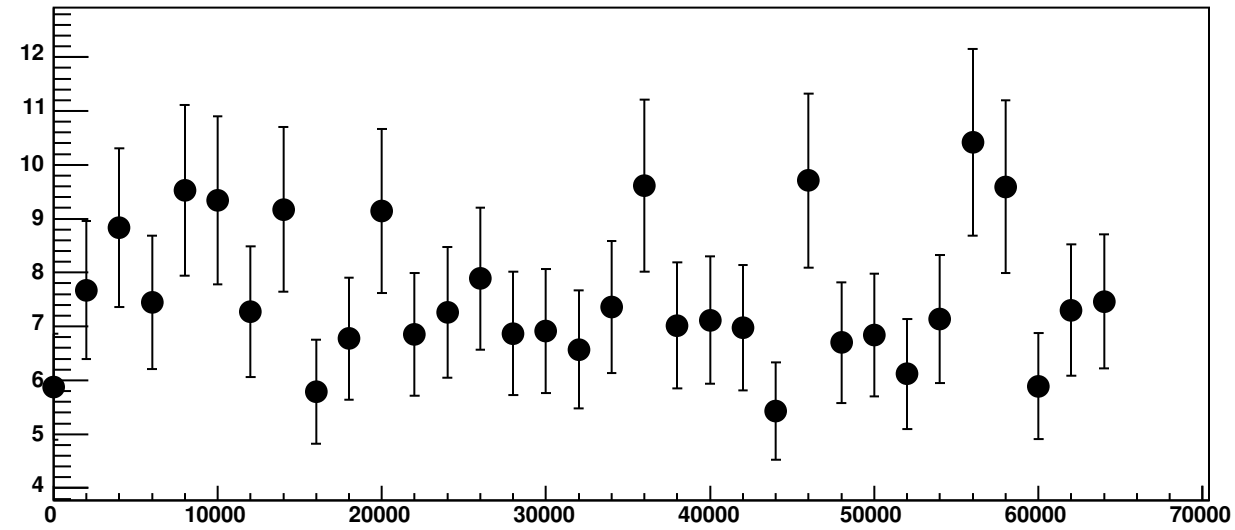
Chip 4, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC



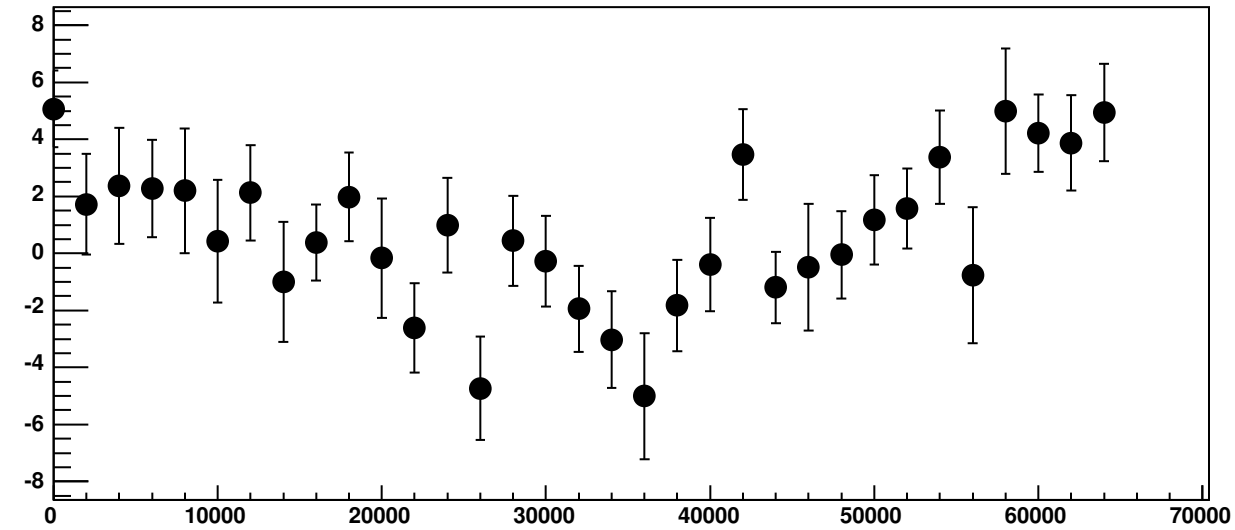
Chip 4, Channel 5, Enable 4, Hold=35, ADC Mean vs DAC



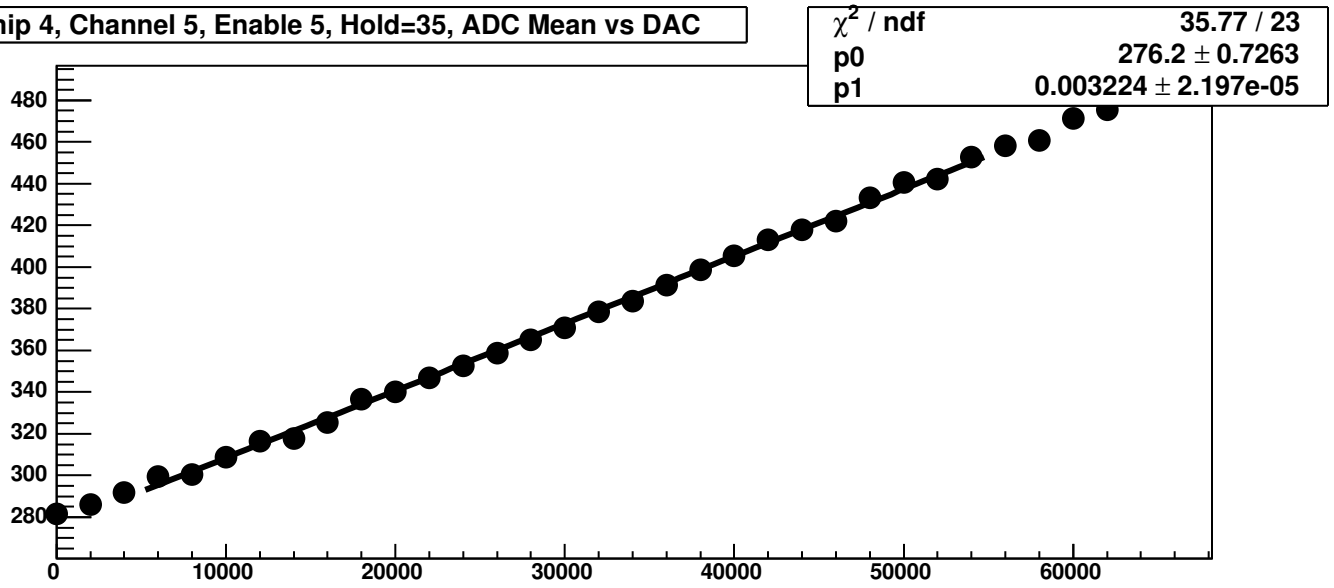
Chip 4, Channel 5, Enable 4, Hold=35, ADC Noise vs DAC



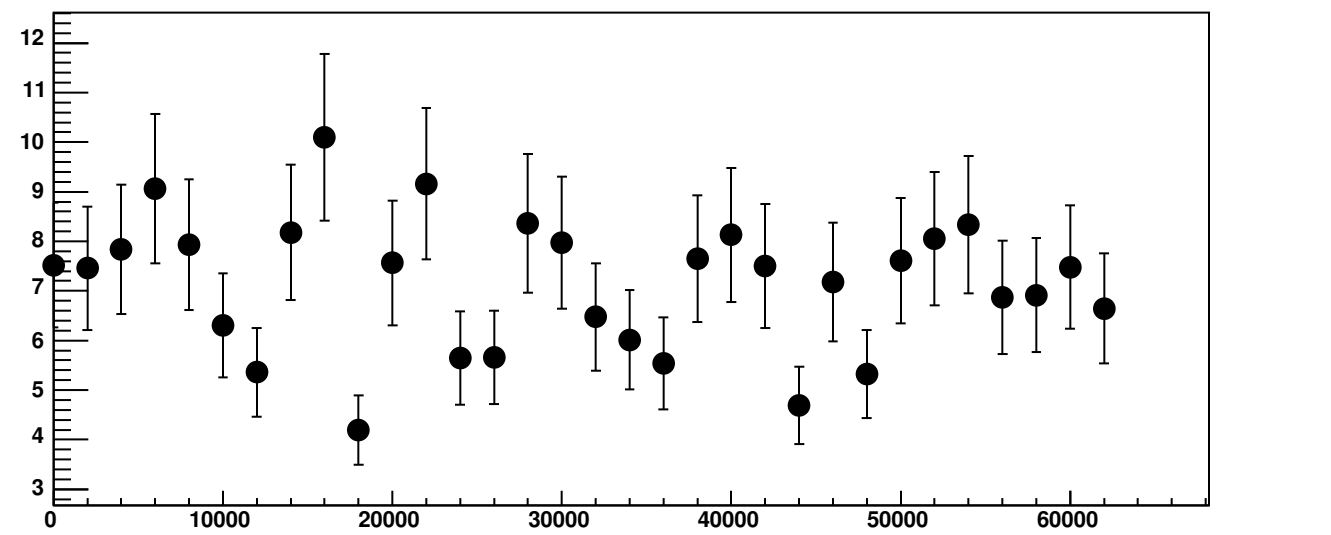
Chip 4, Channel 5, Enable 4, Hold=35, ADC Residuals vs DAC



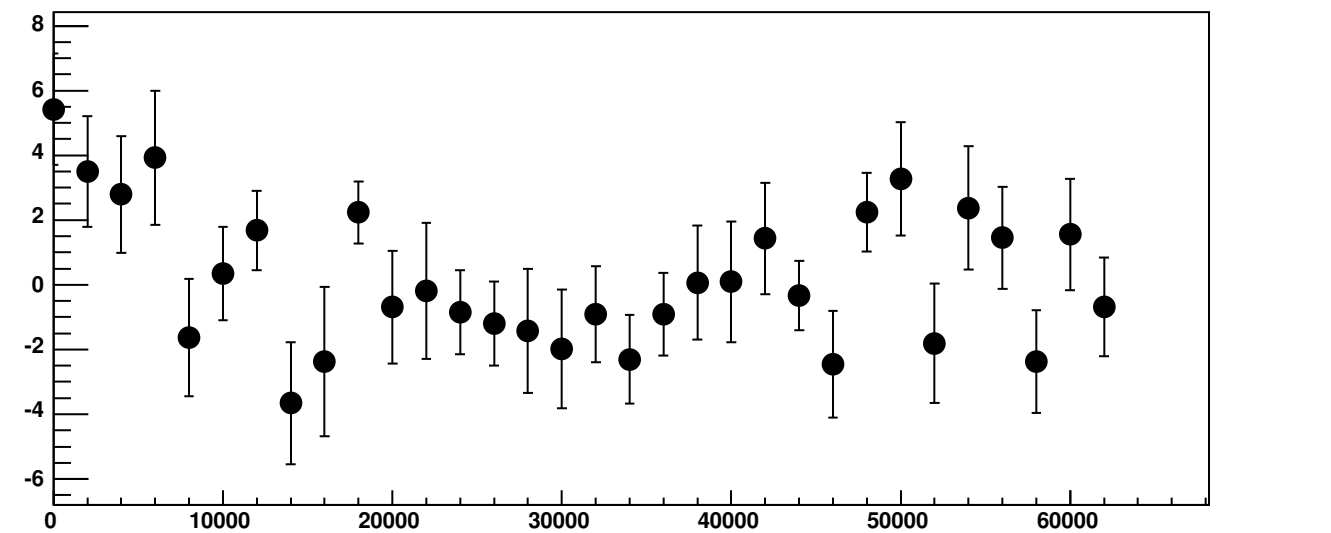
Chip 4, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC

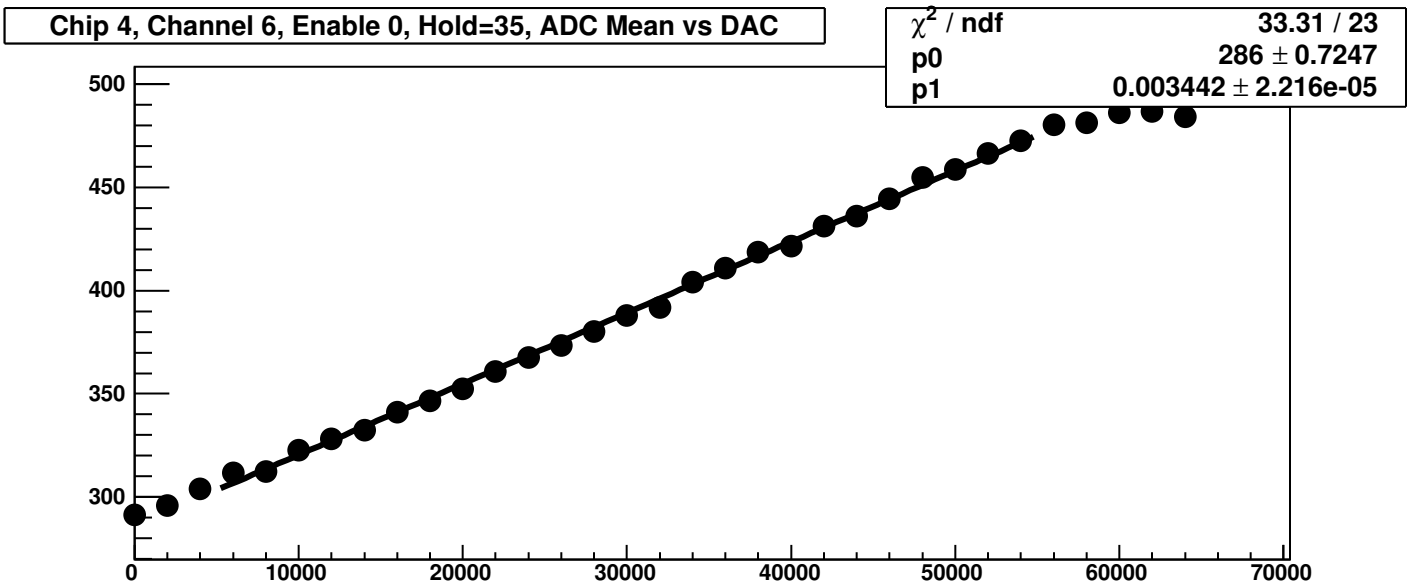


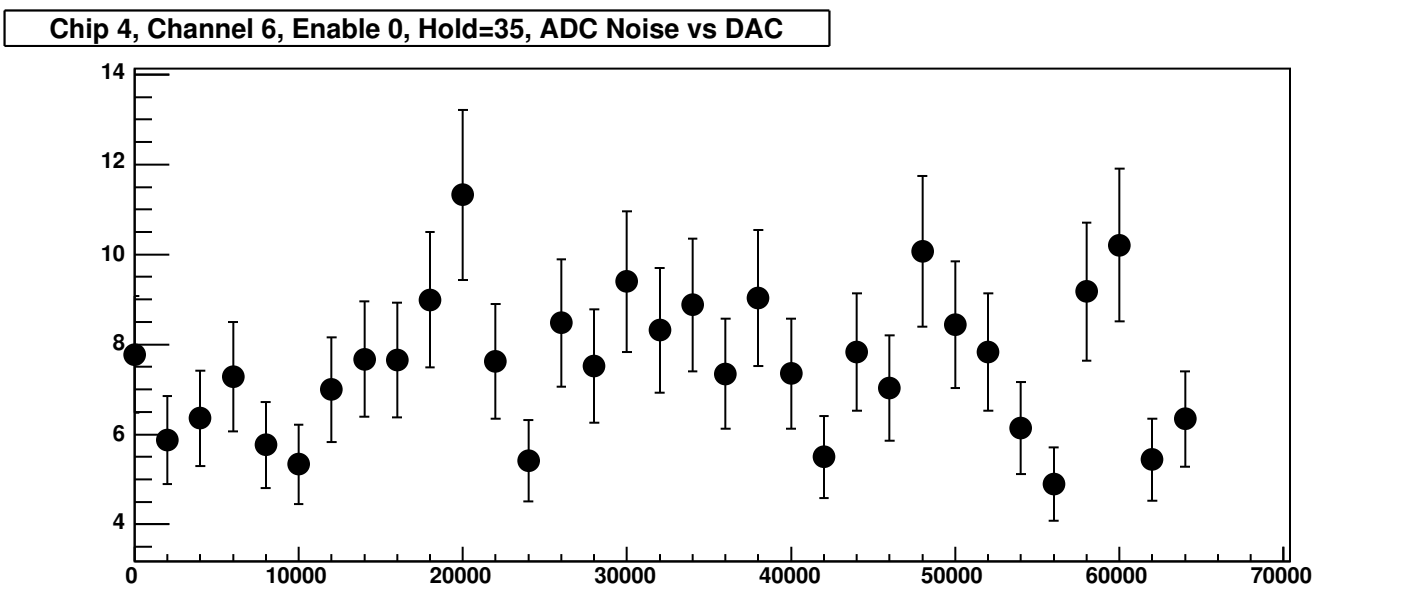
Chip 4, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC

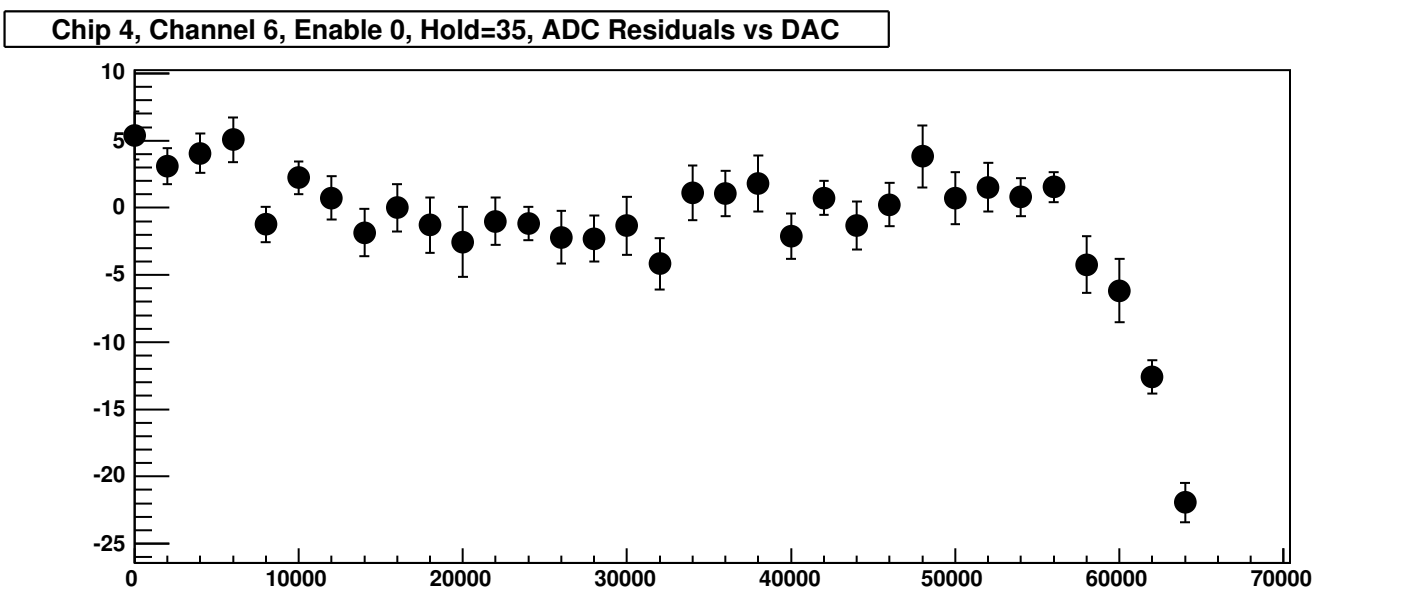


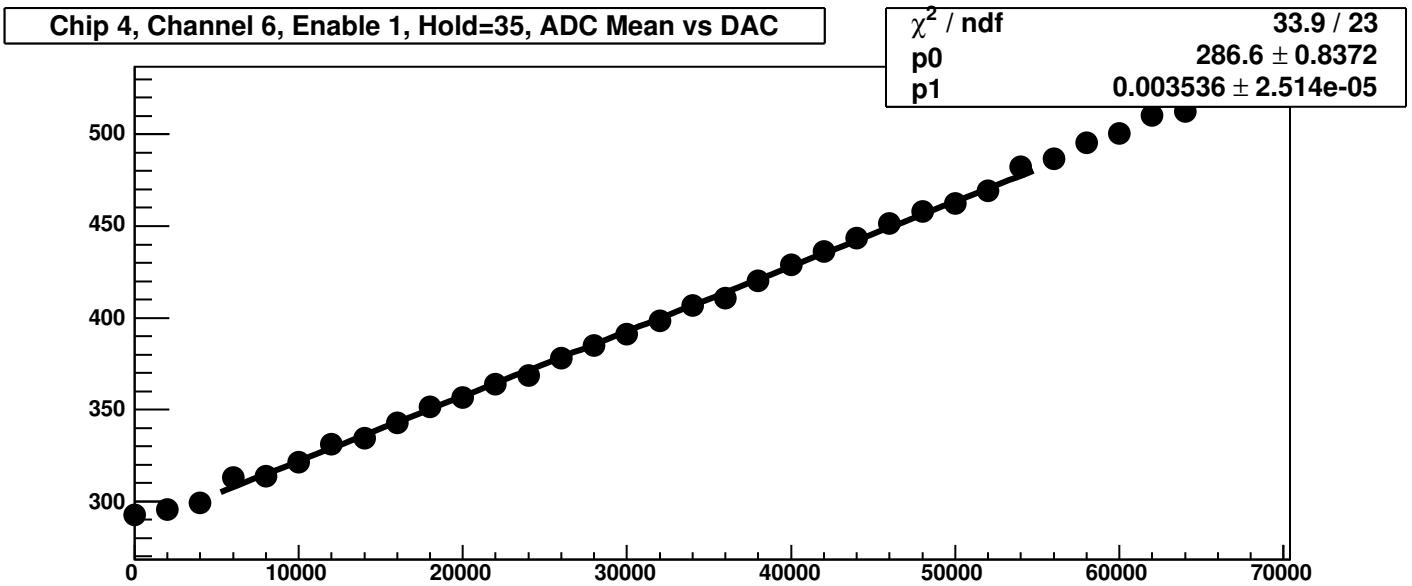
Chip 4, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC

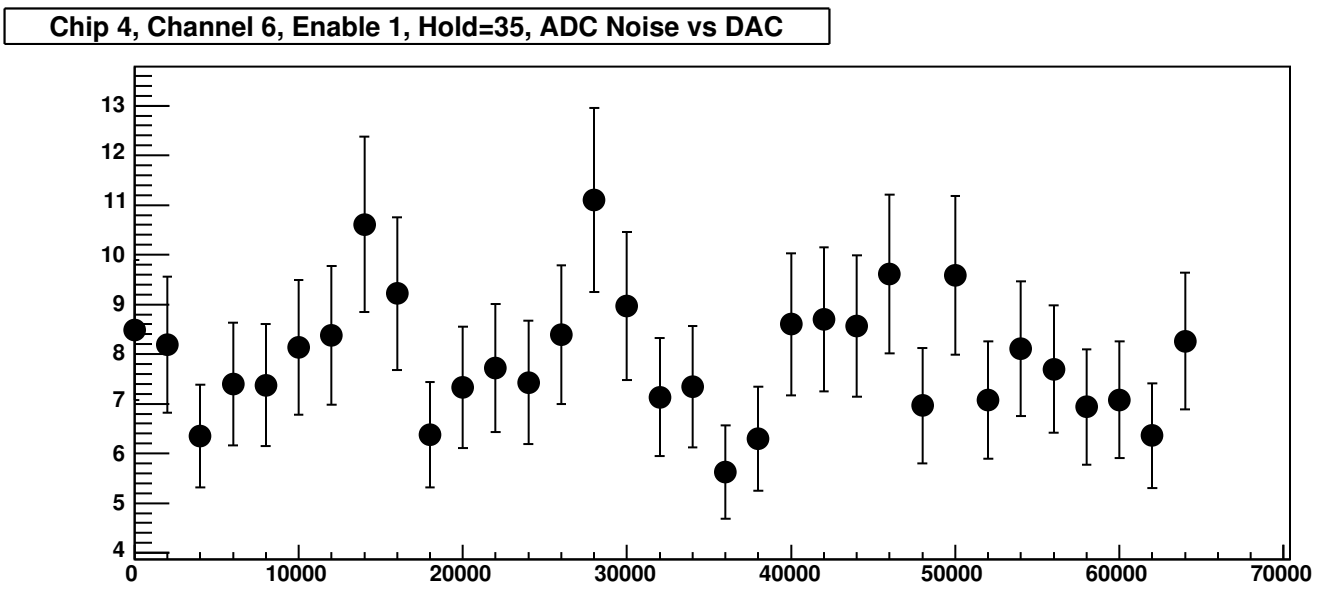


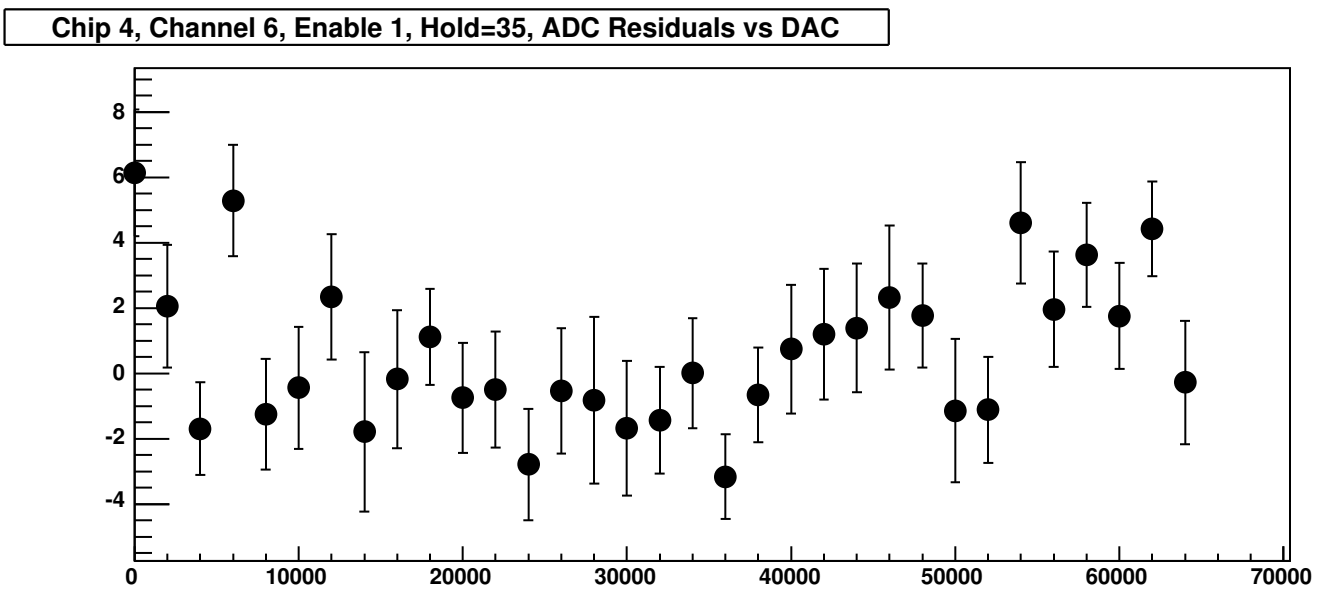

Chip 4, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 33.31 / 23
p0 286 ± 0.7247
p1 0.003442 ± 2.216e-05
500
450
400
350
300
0
10000
20000
30000
40000
50000
60000
70000

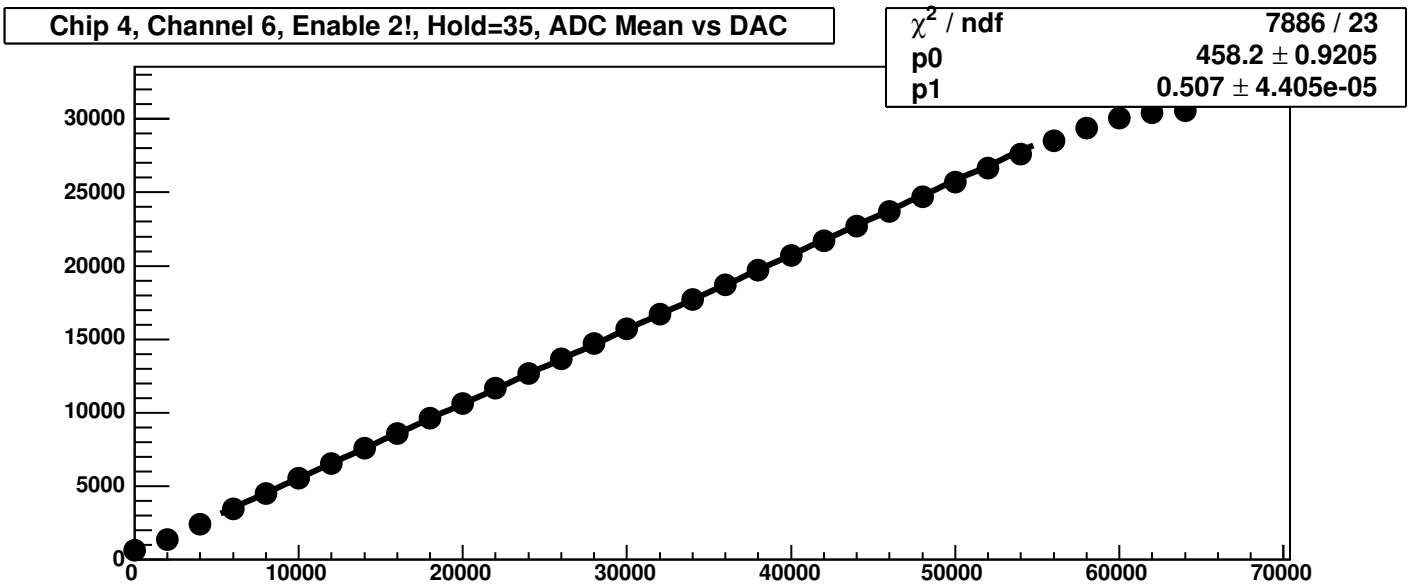

Chip 4, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

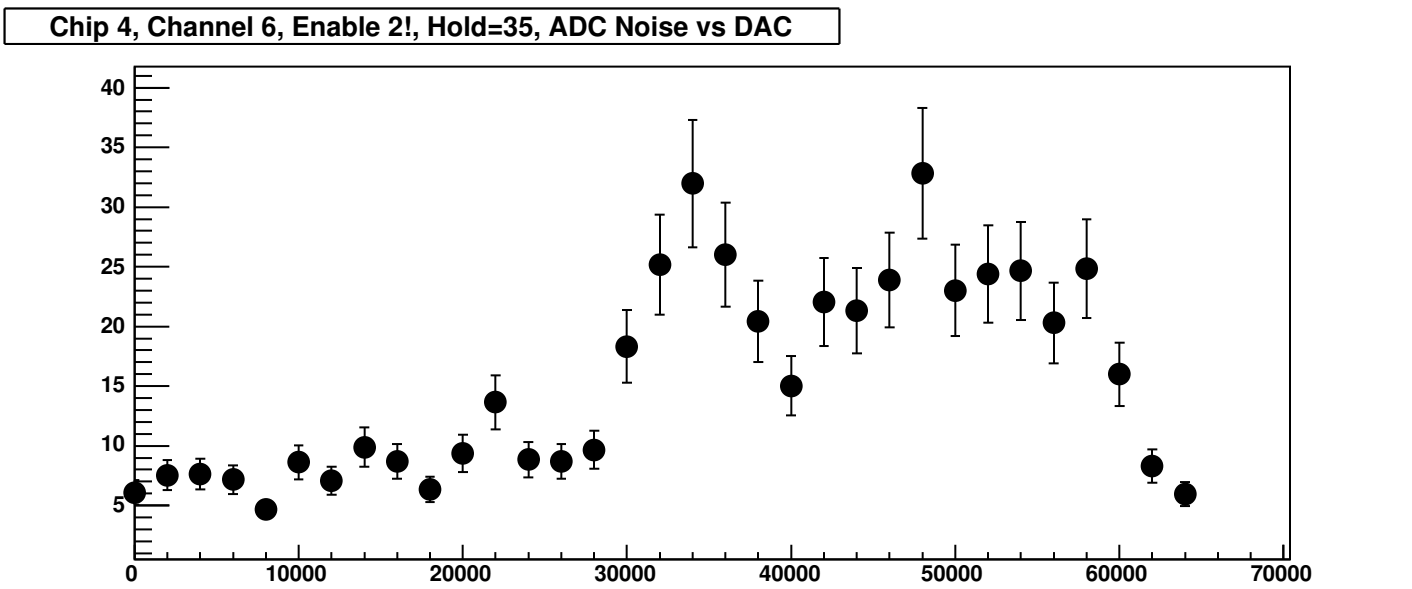

Chip 4, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
-25
0
10000
20000
30000
40000
50000
60000
70000

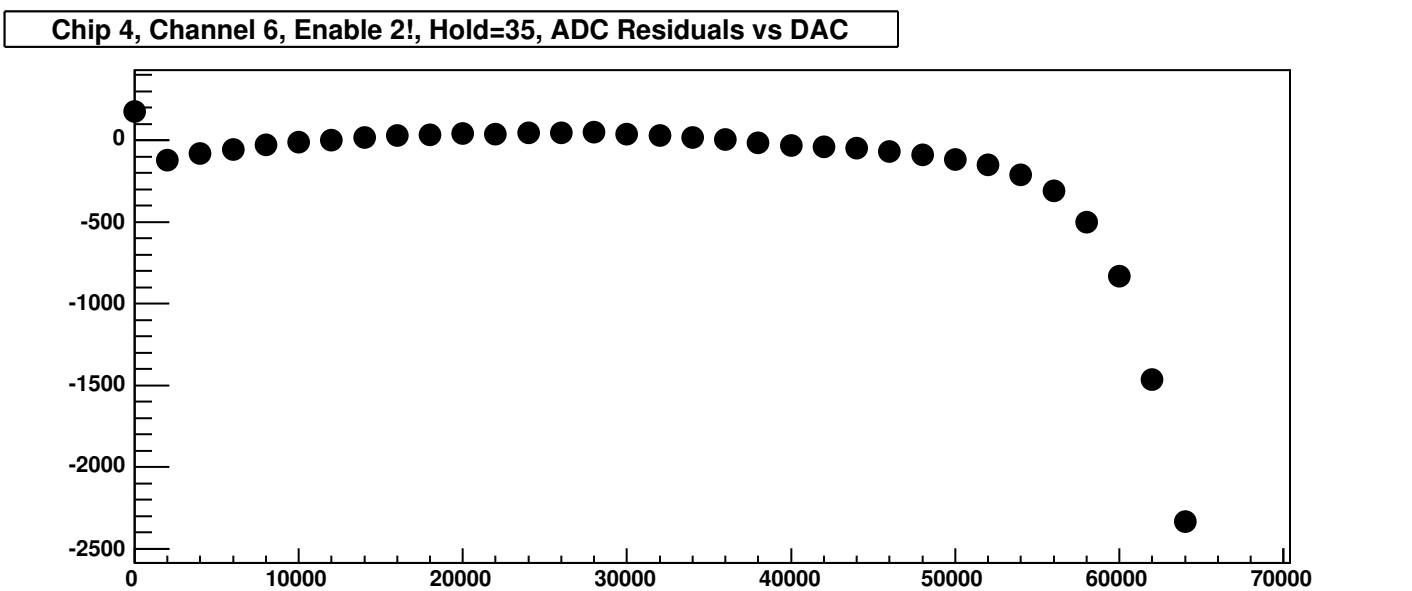

Chip 4, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 33.9 / 23
p0 286.6 ± 0.8372
p1 0.003536 ± 2.514e-05
500
450
400
350
300
0
10000
20000
30000
40000
50000
60000
70000

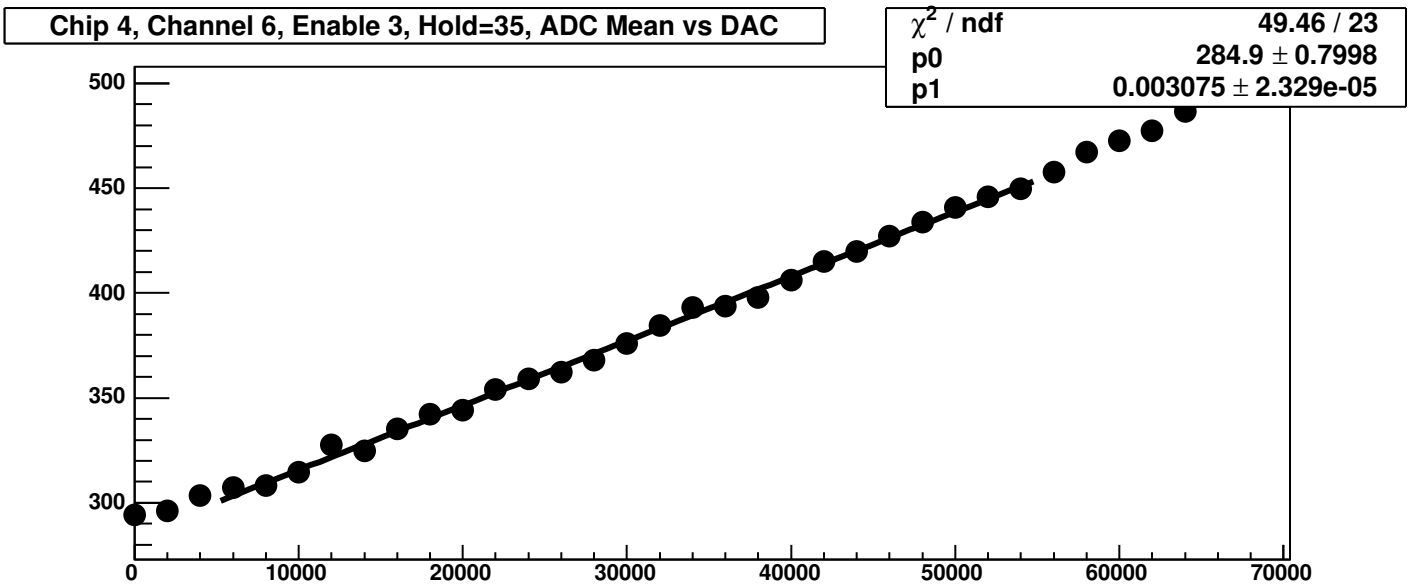

Chip 4, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

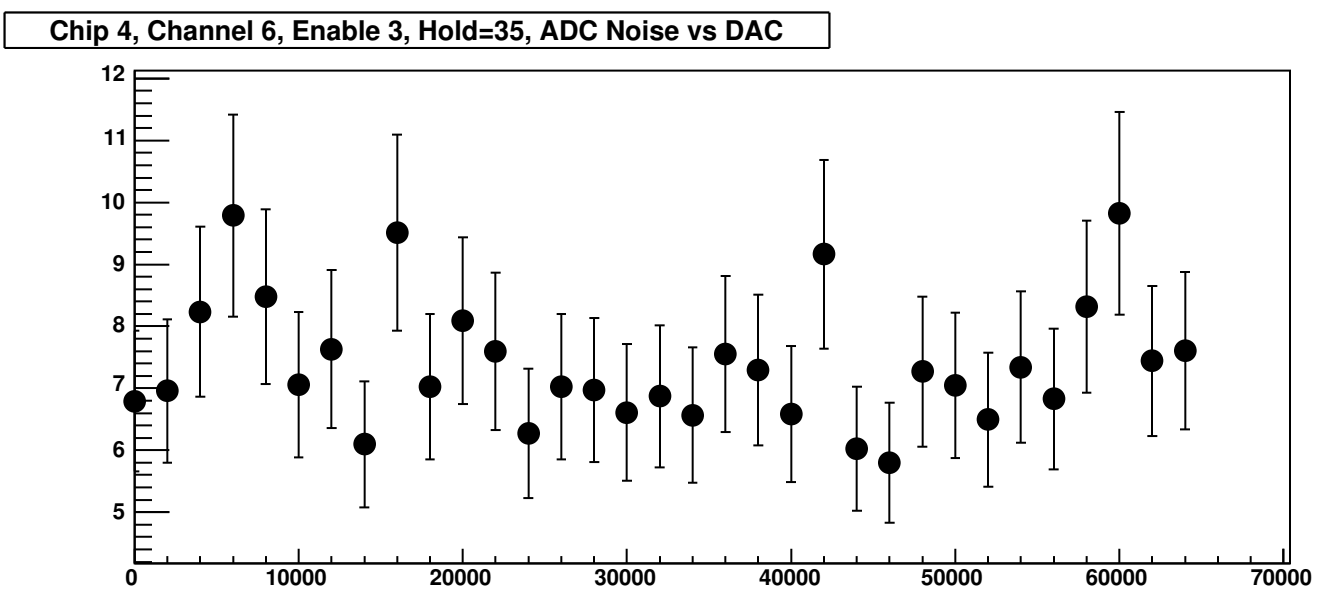

Chip 4, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
0
10000
20000
30000
40000
50000
60000
70000

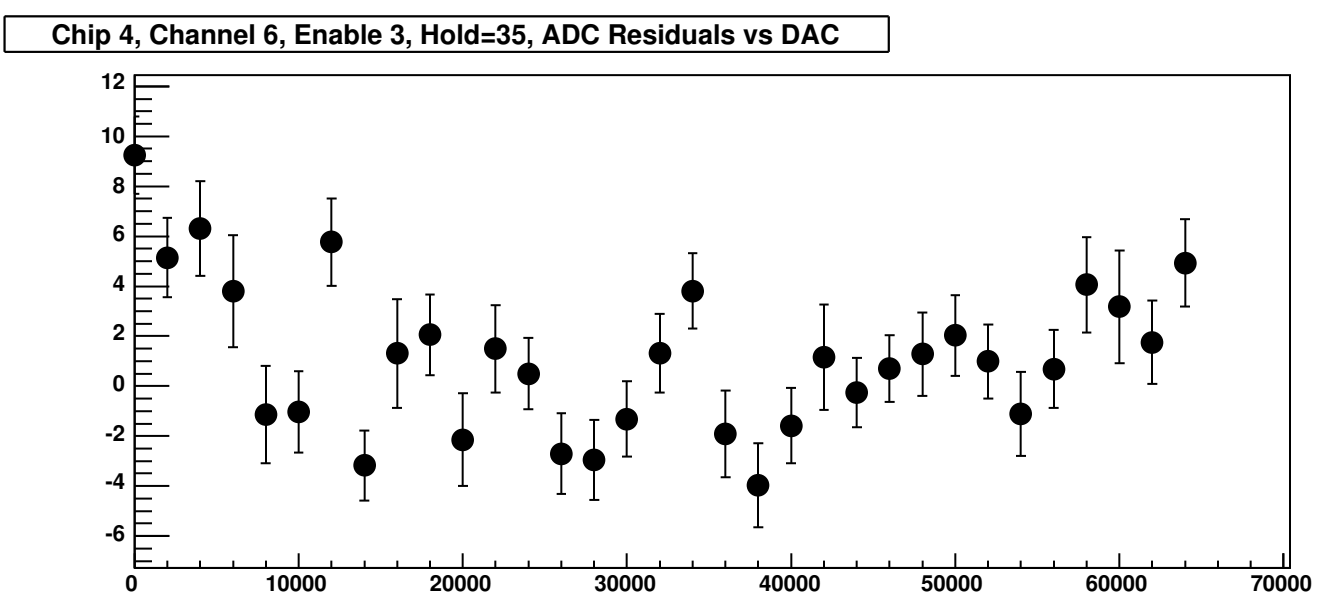

Chip 4, Channel 6, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 7886 / 23
p0 458.2 ± 0.9205
p1 0.507 ± 4.405e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 6, Enable 2!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000

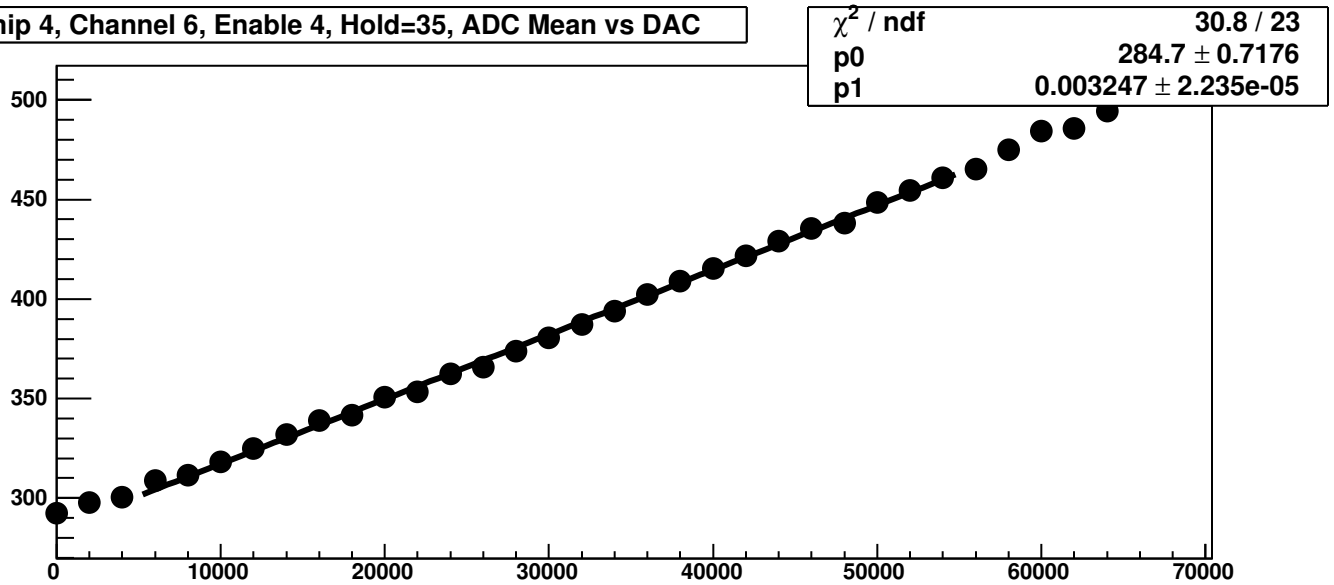

Chip 4, Channel 6, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 49.46 / 23
p0 284.9 ± 0.7998
p1 0.003075 ± 2.329e-05
500
450
400
350
300
0
10000
20000
30000
40000
50000
60000
70000

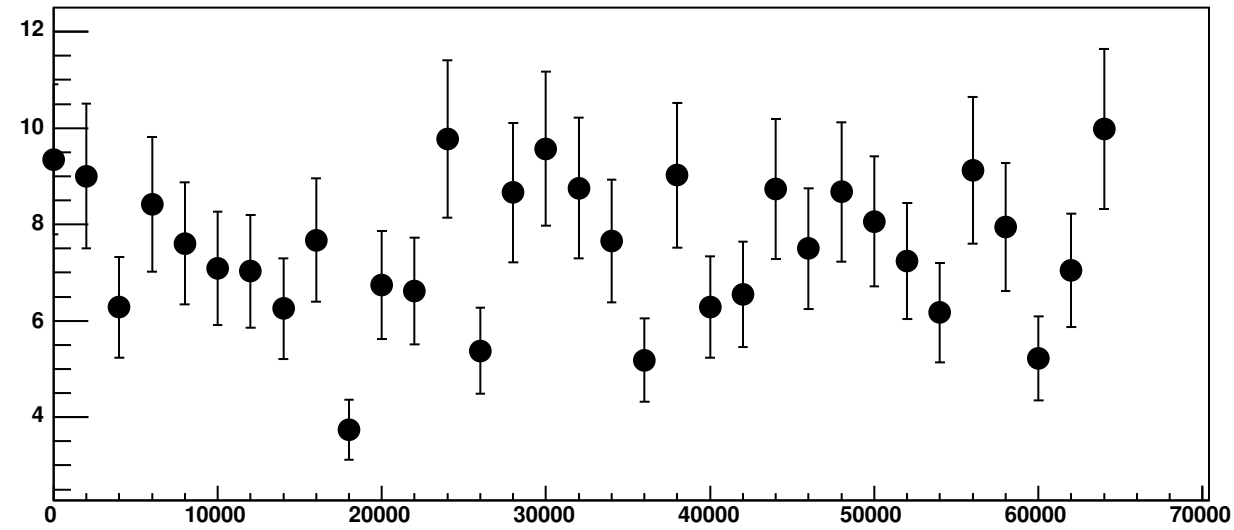

Chip 4, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

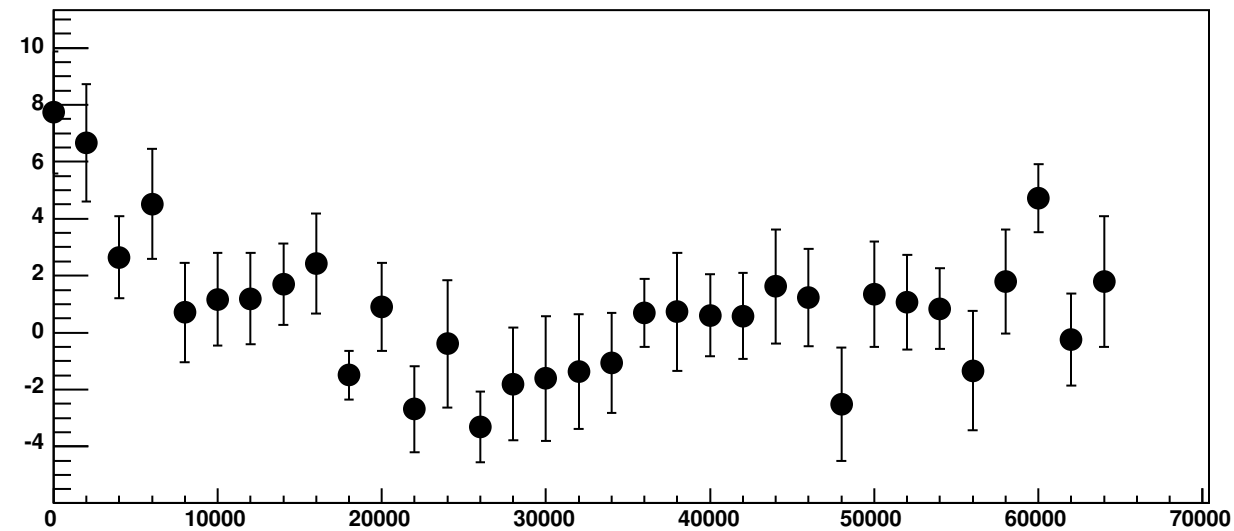
Chip 4, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC



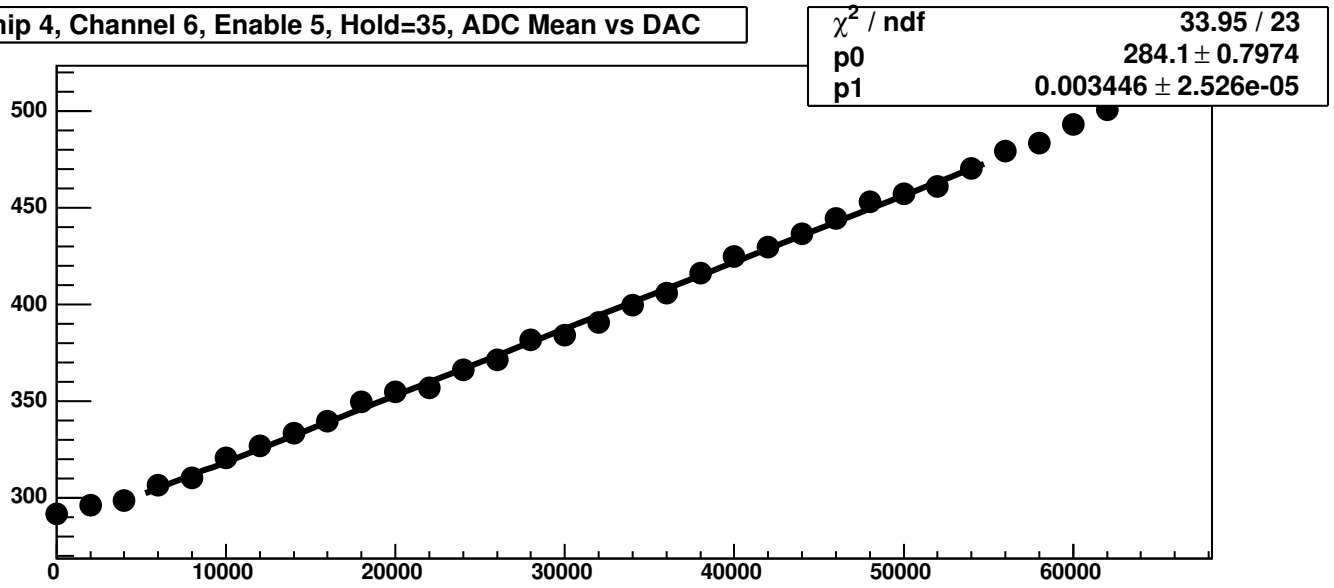
Chip 4, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC



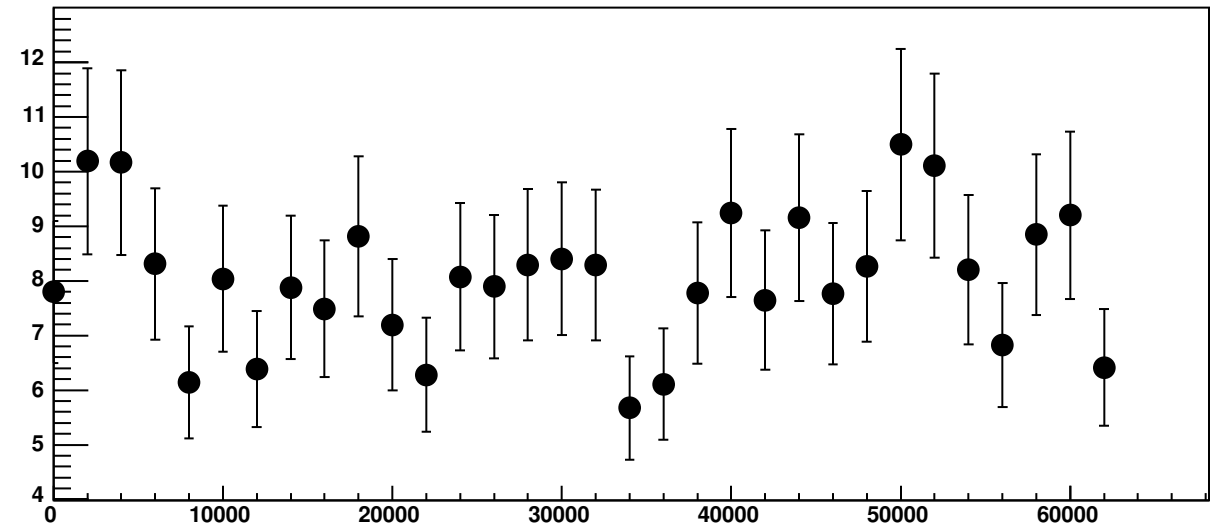
Chip 4, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC



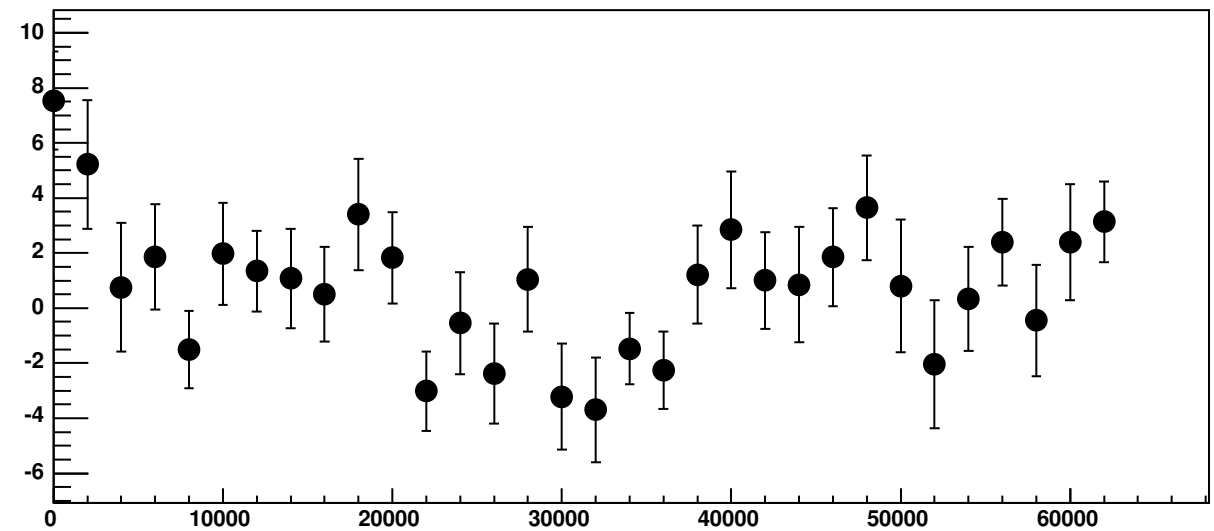
Chip 4, Channel 6, Enable 5, Hold=35, ADC Mean vs DAC



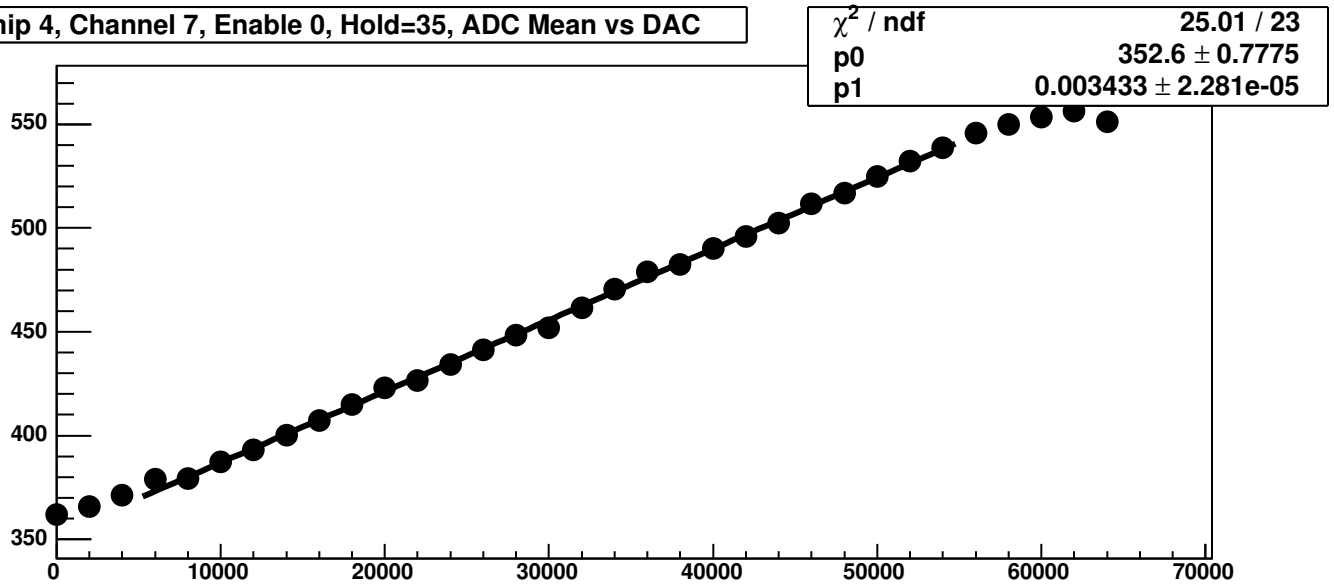
Chip 4, Channel 6, Enable 5, Hold=35, ADC Noise vs DAC



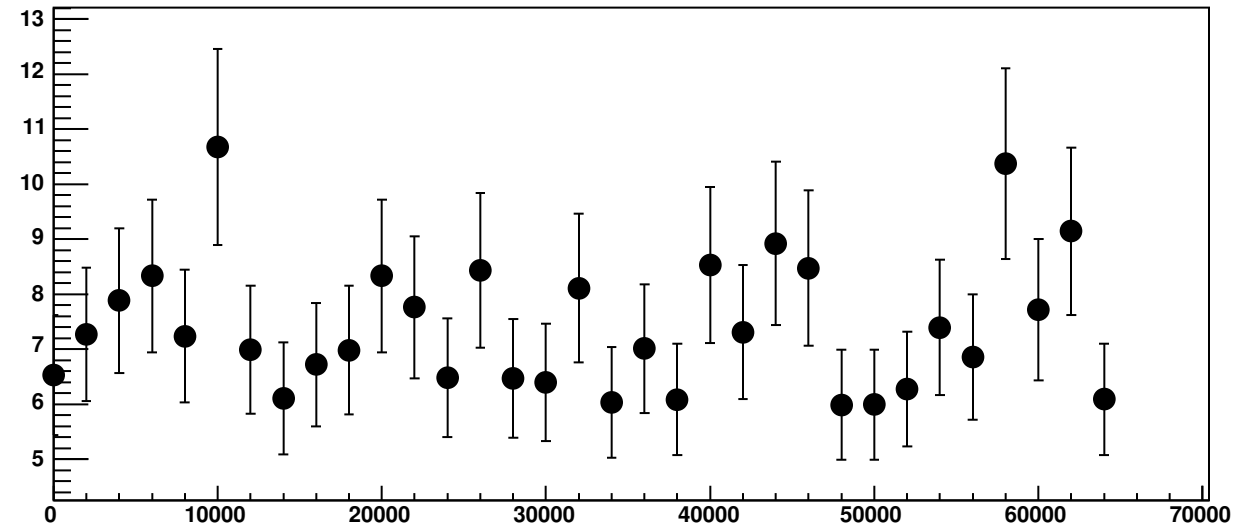
Chip 4, Channel 6, Enable 5, Hold=35, ADC Residuals vs DAC



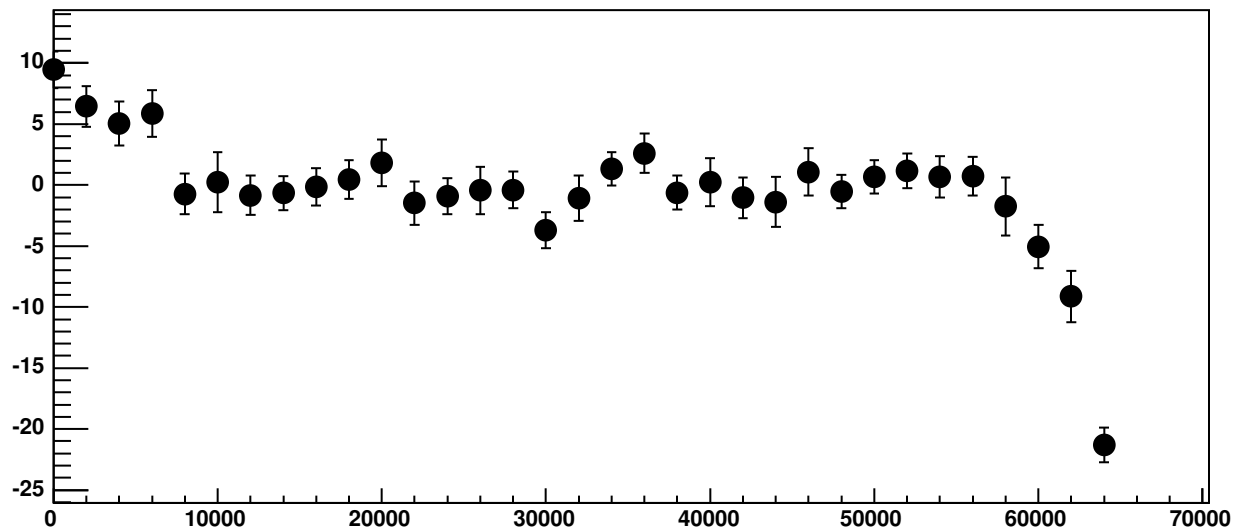
Chip 4, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC

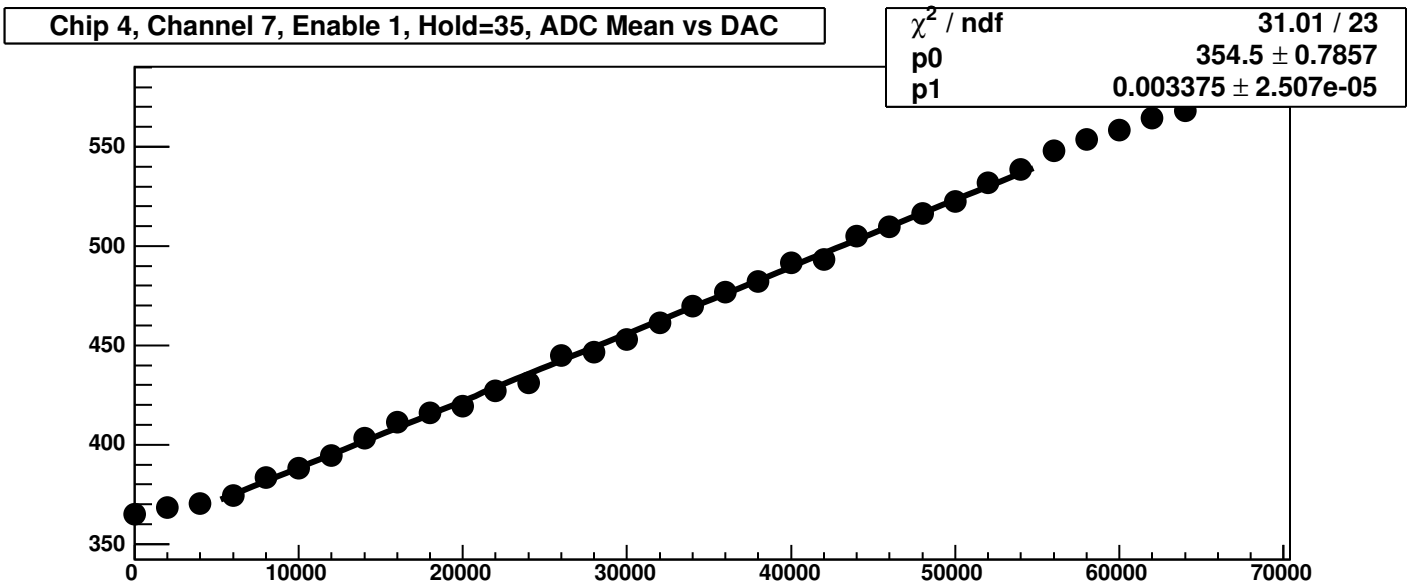


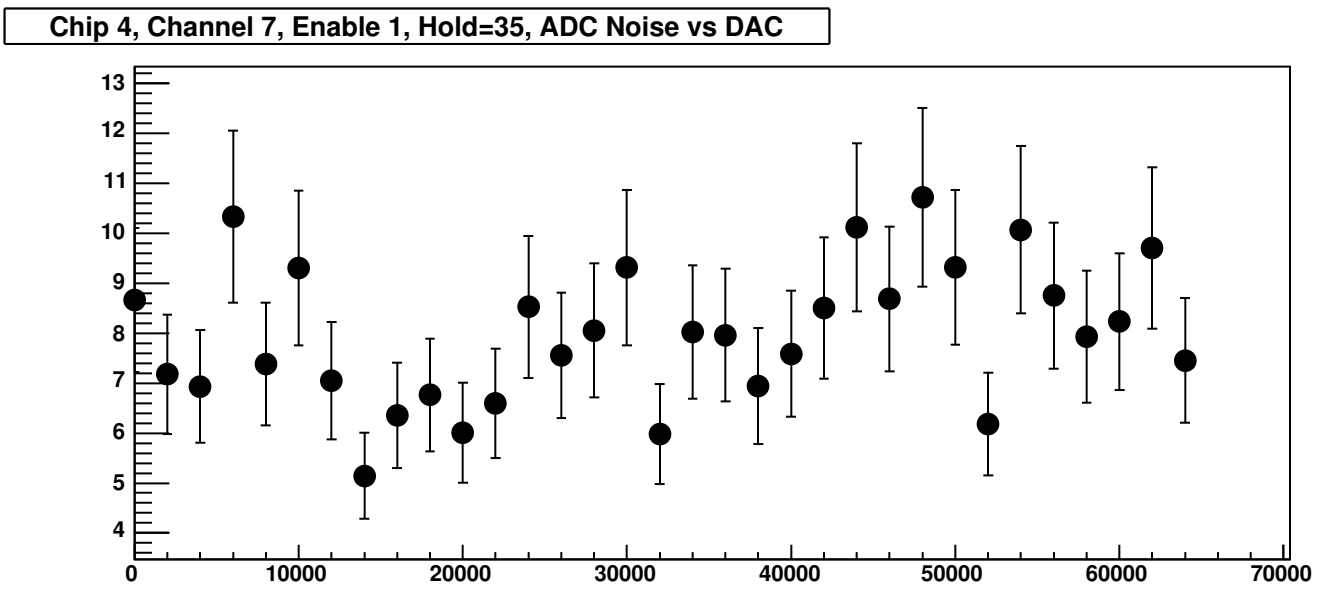
Chip 4, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC

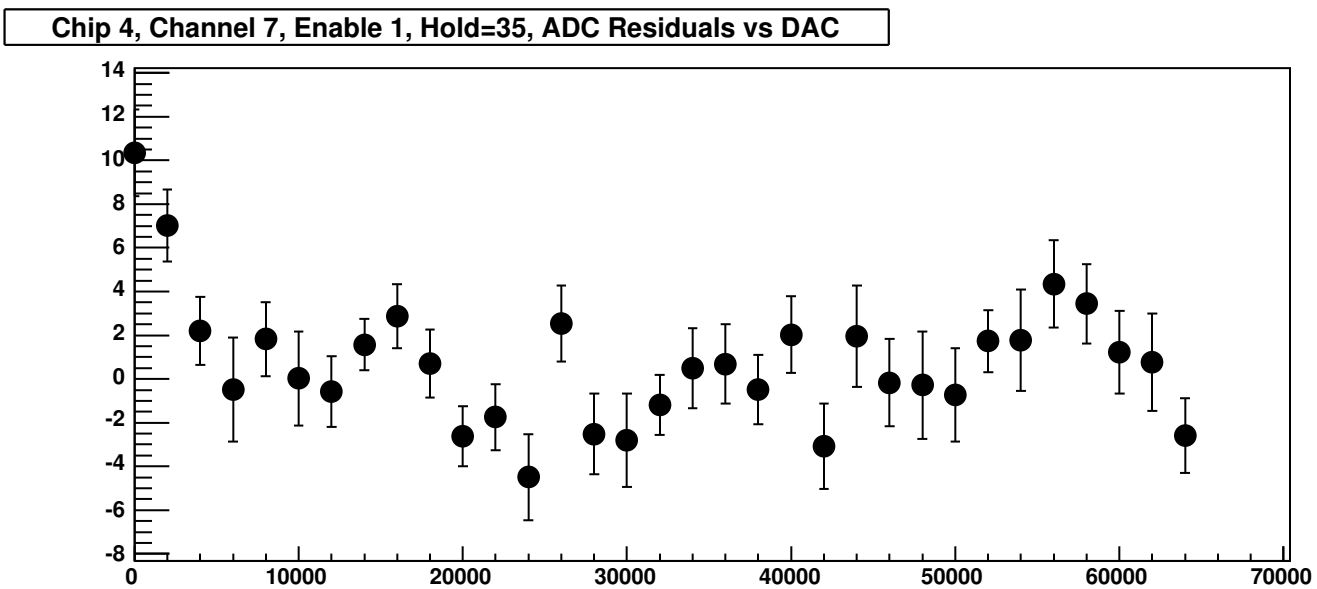


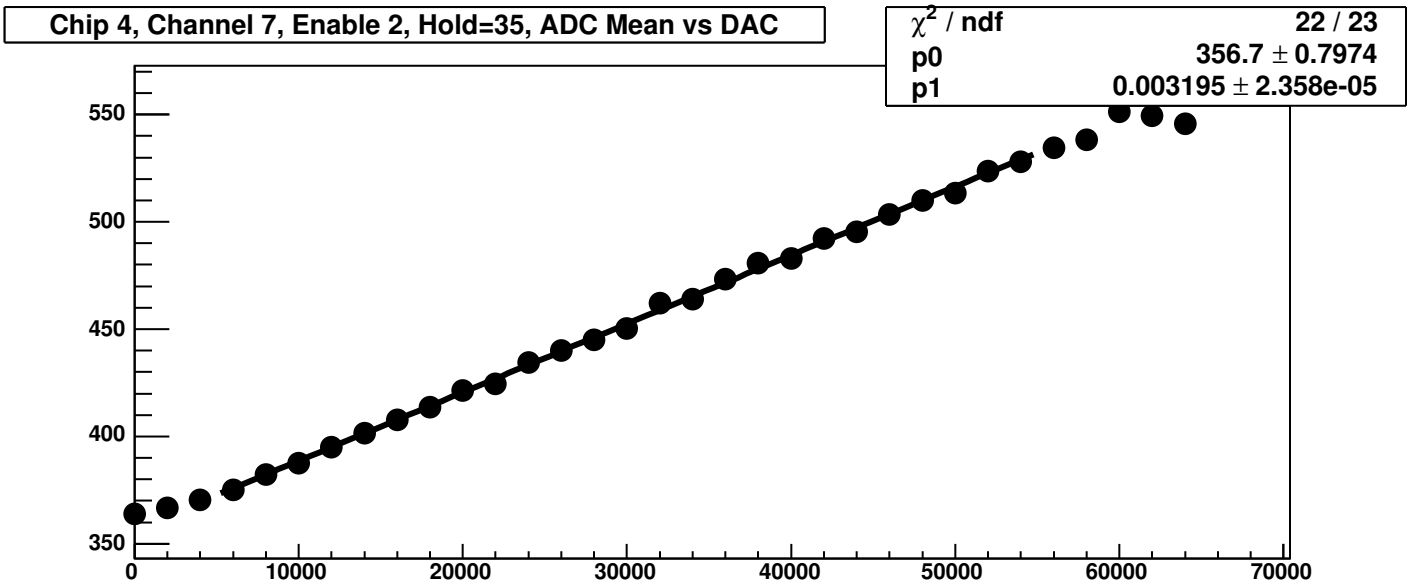
Chip 4, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC

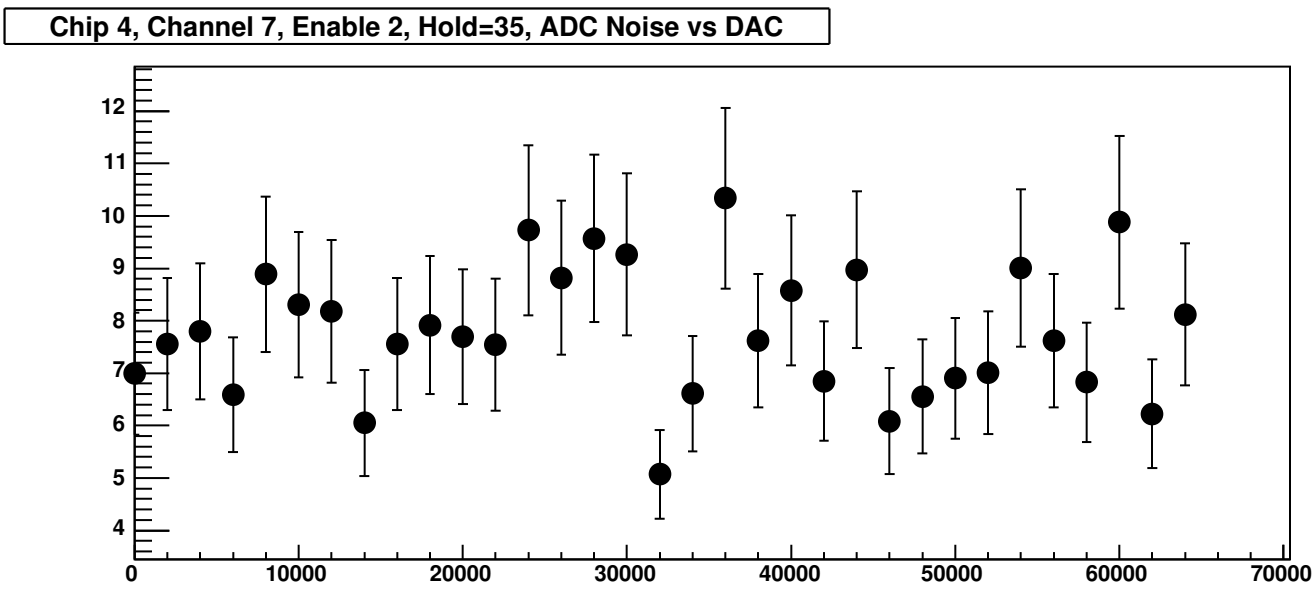


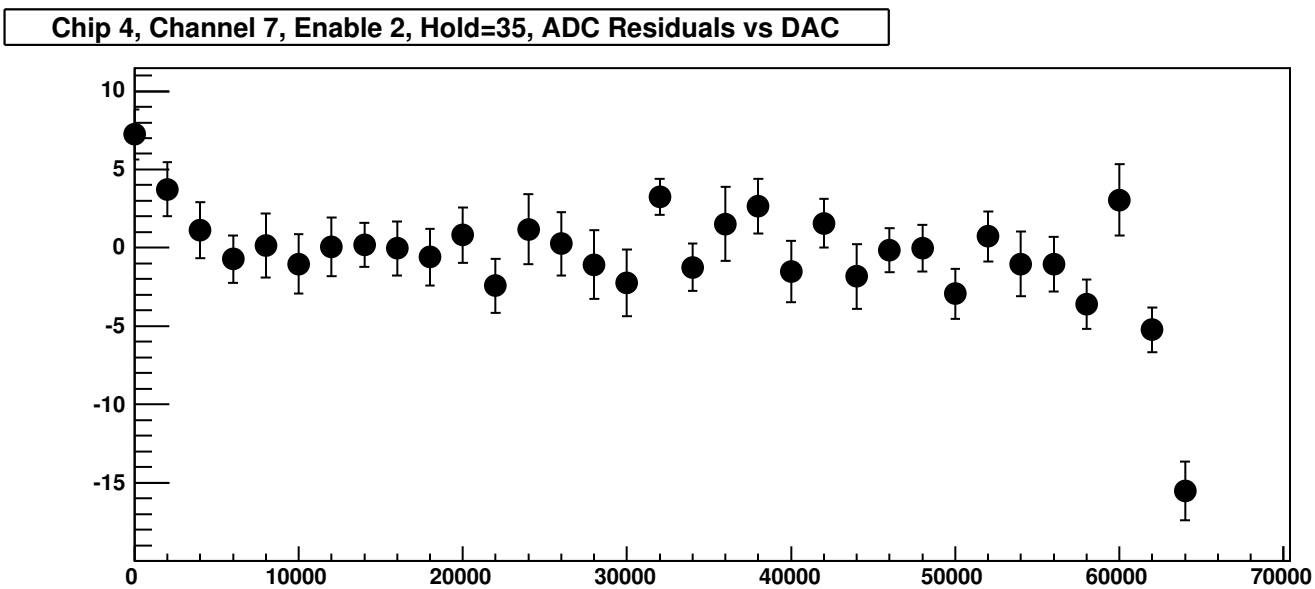

Chip 4, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.01 / 23
p0 354.5 ± 0.7857
p1 0.003375 ± 2.507e-05
550
500
450
400
350
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

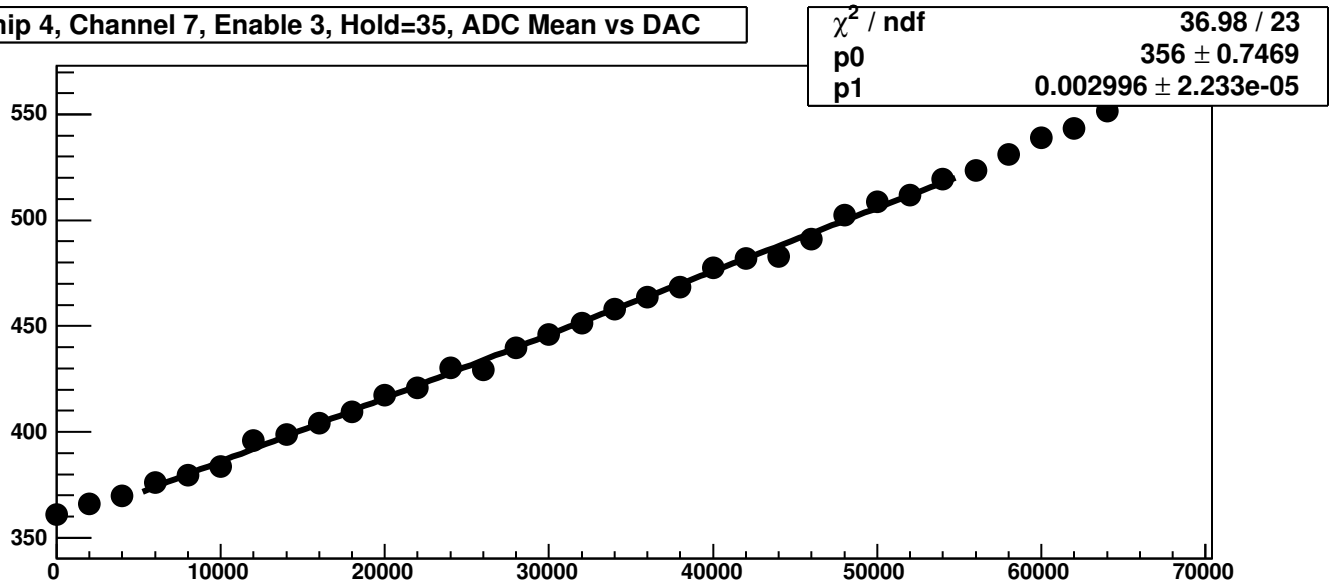

Chip 4, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 7, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 22 / 23
p0 356.7 ± 0.7974
p1 0.003195 ± 2.358e-05
550
500
450
400
350
0
10000
20000
30000
40000
50000
60000
70000

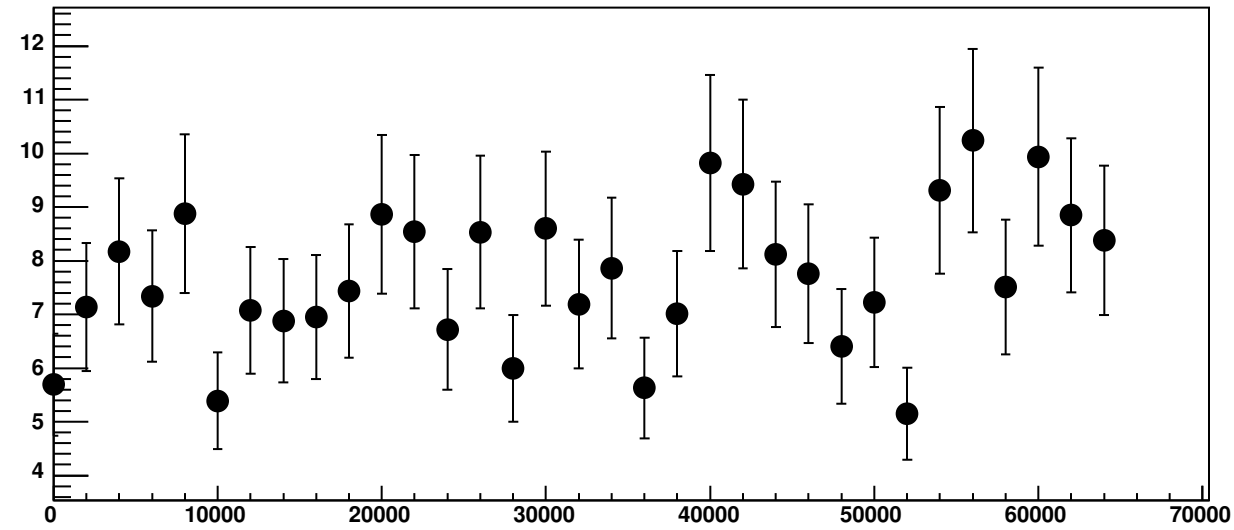

Chip 4, Channel 7, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 7, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

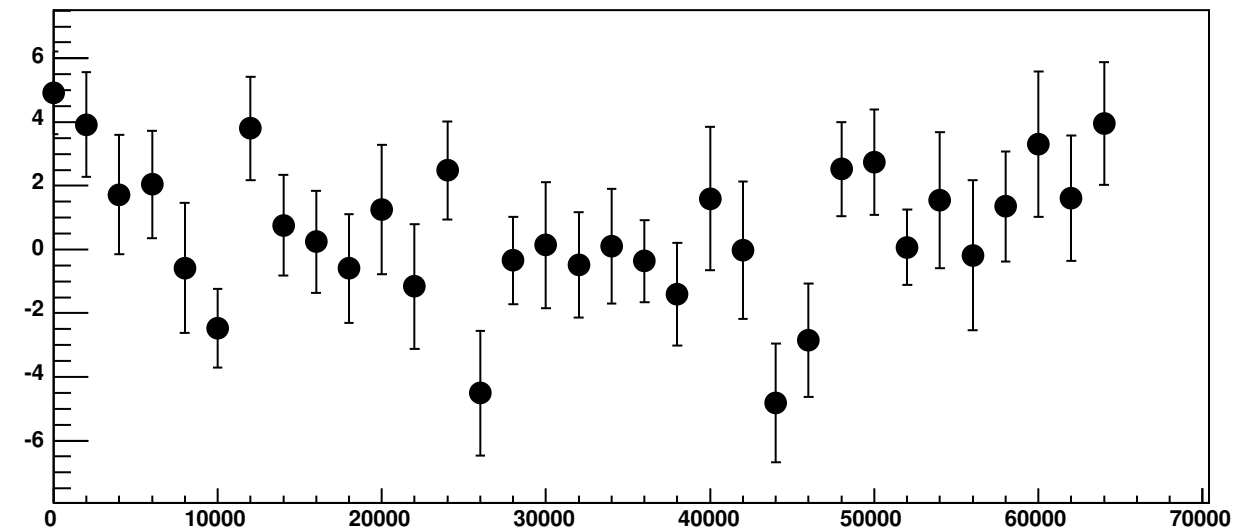
Chip 4, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



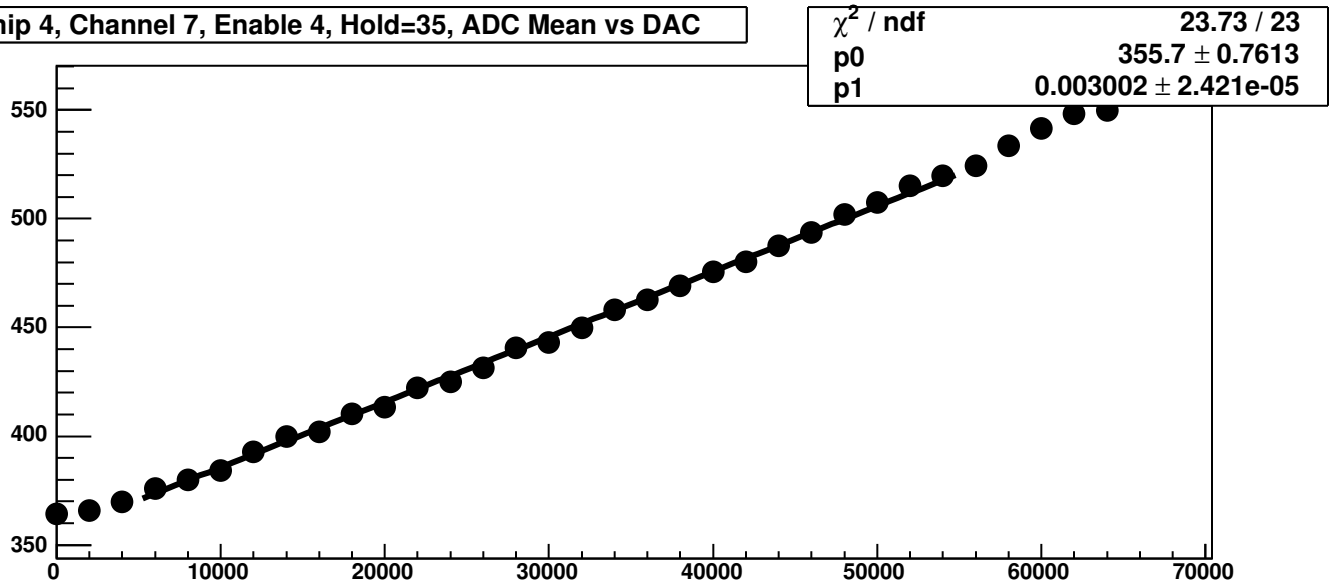
Chip 4, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



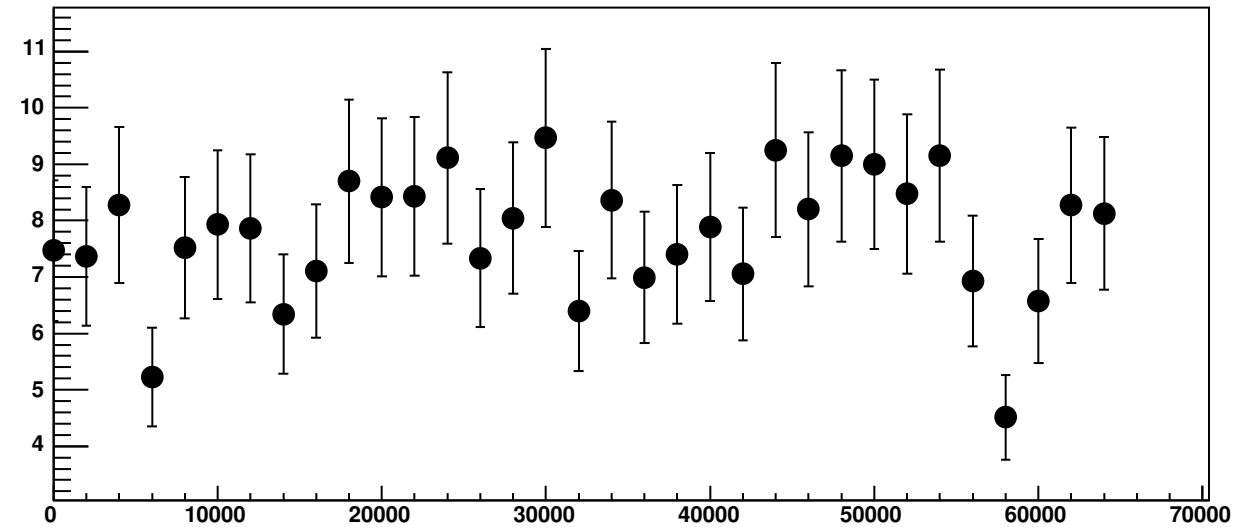
Chip 4, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



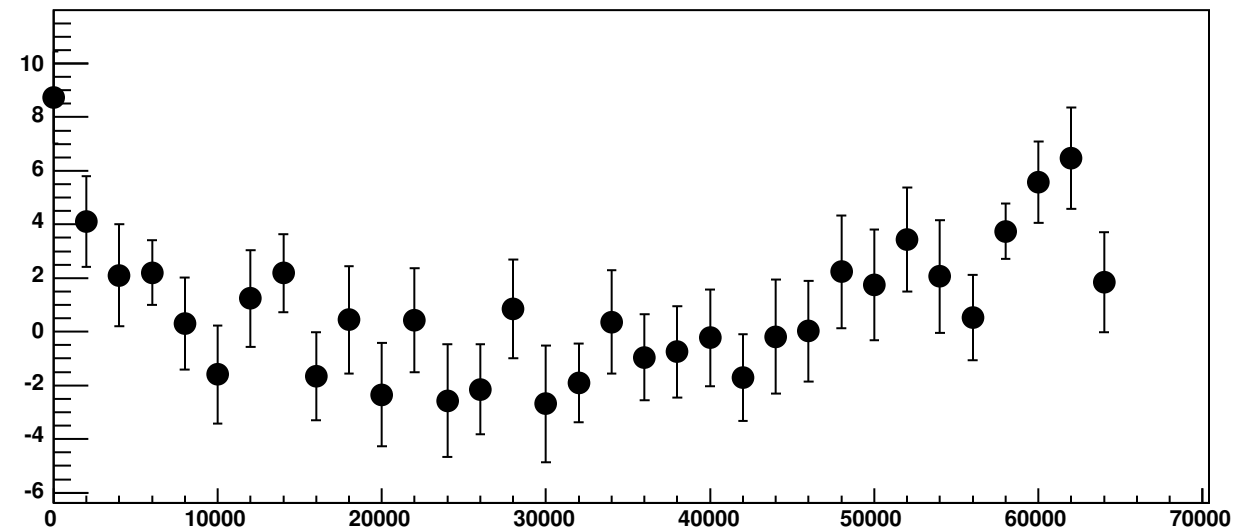
Chip 4, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

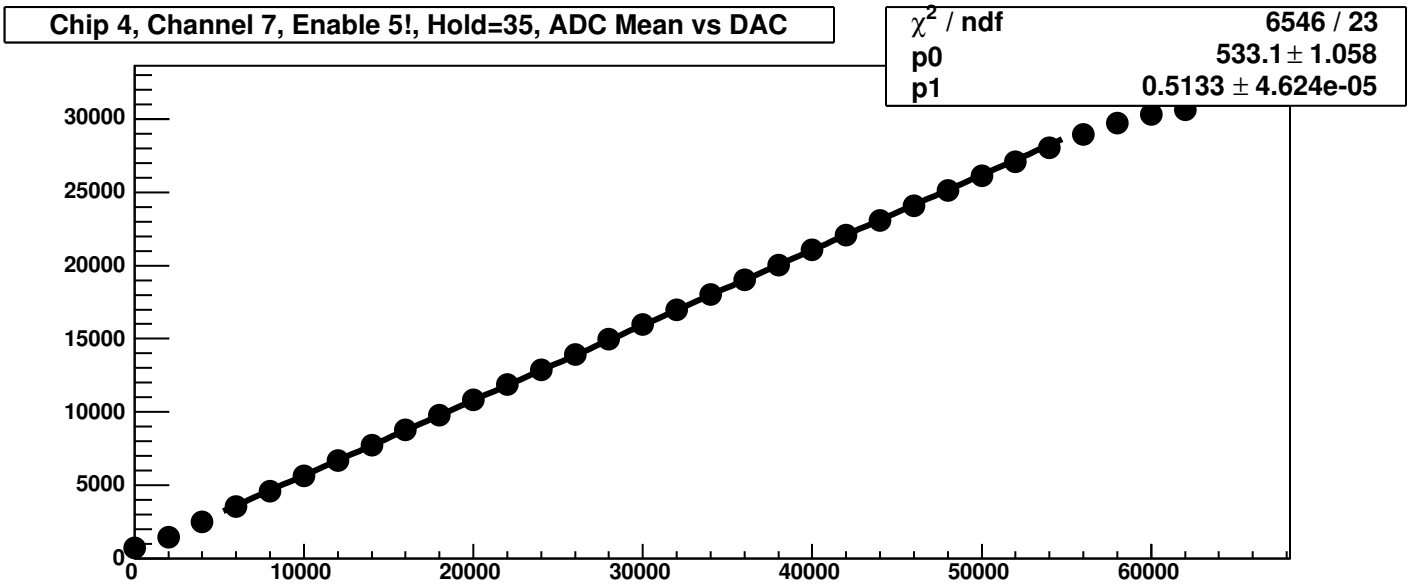


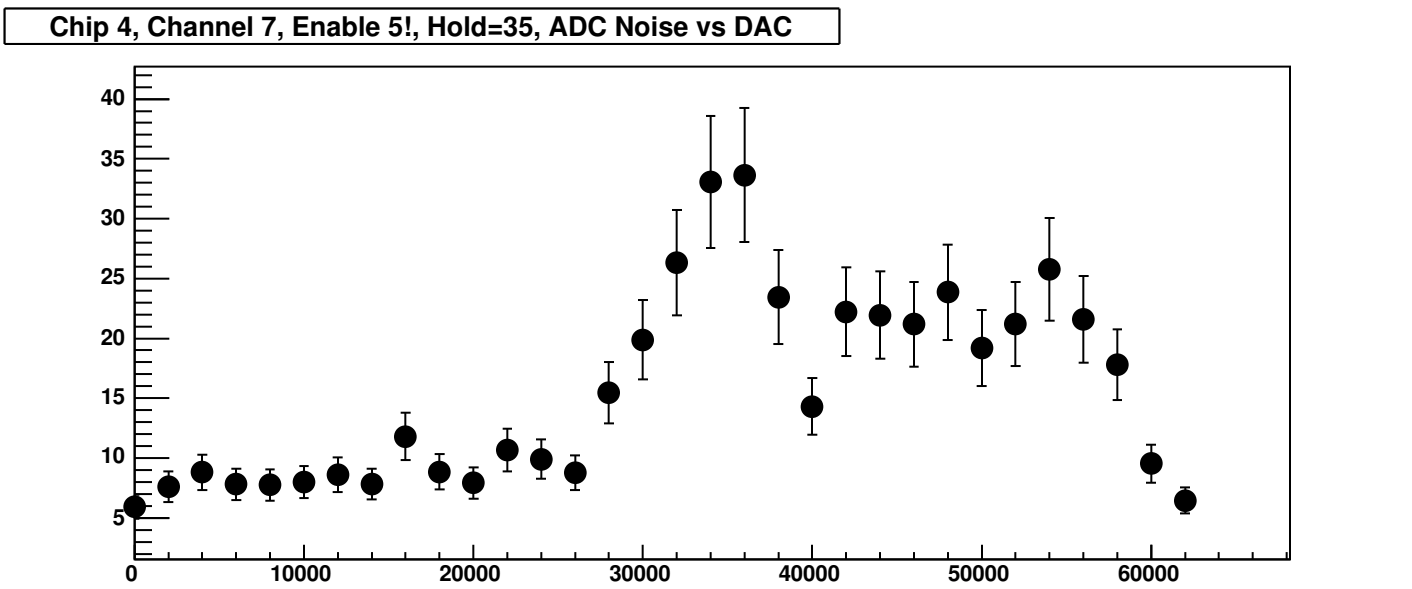
Chip 4, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

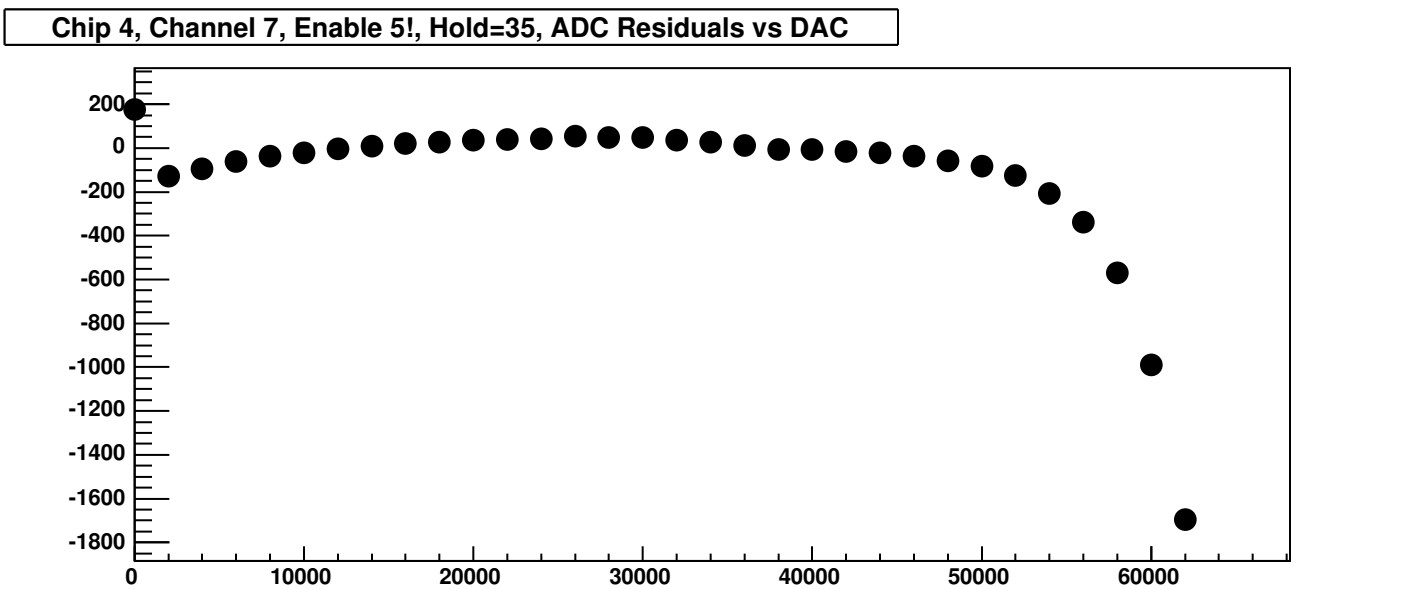


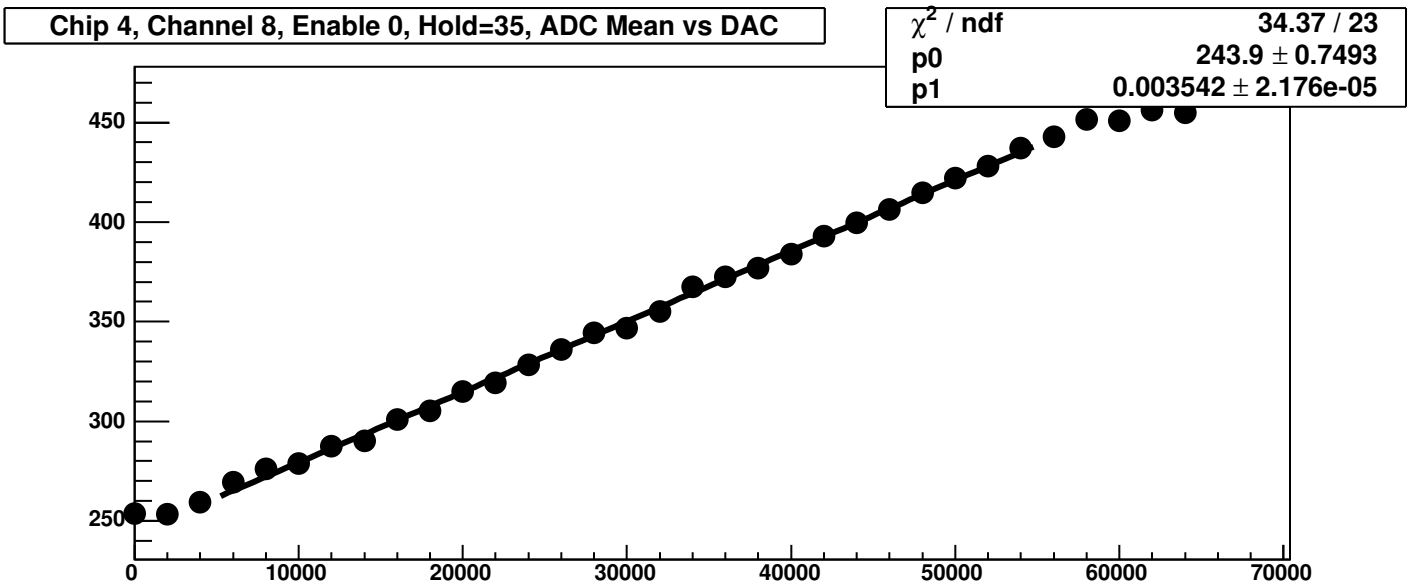
Chip 4, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

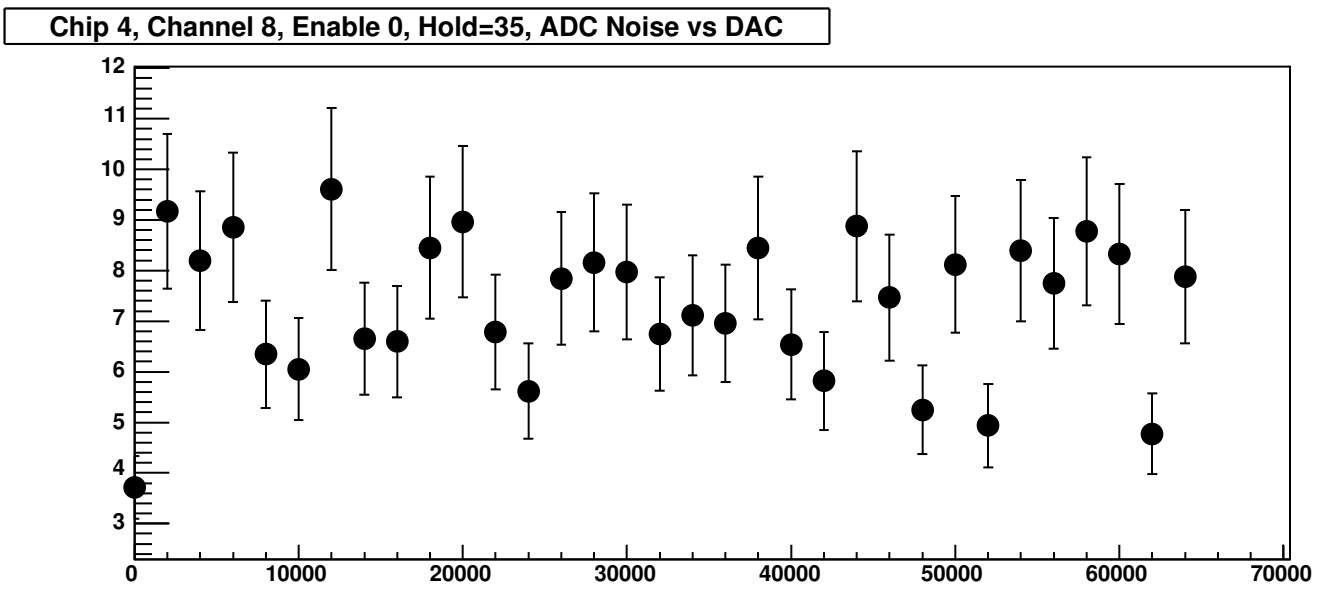


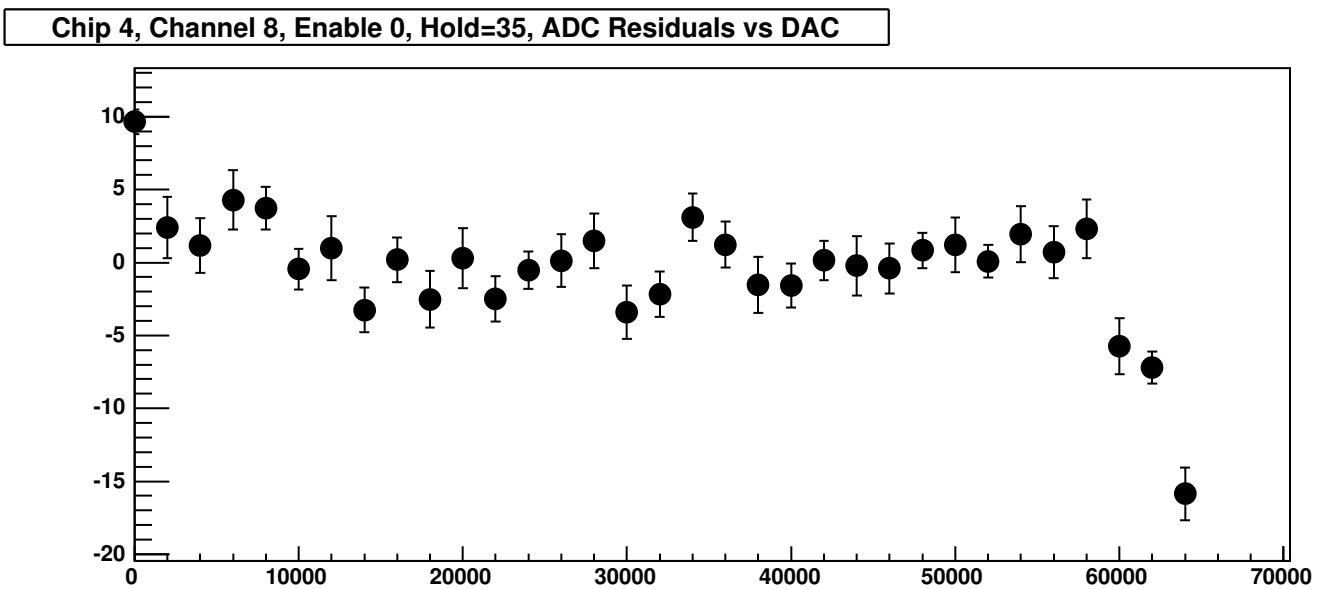

Chip 4, Channel 7, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 6546 / 23
p0 533.1 ± 1.058
p1 0.5133 ± 4.624e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

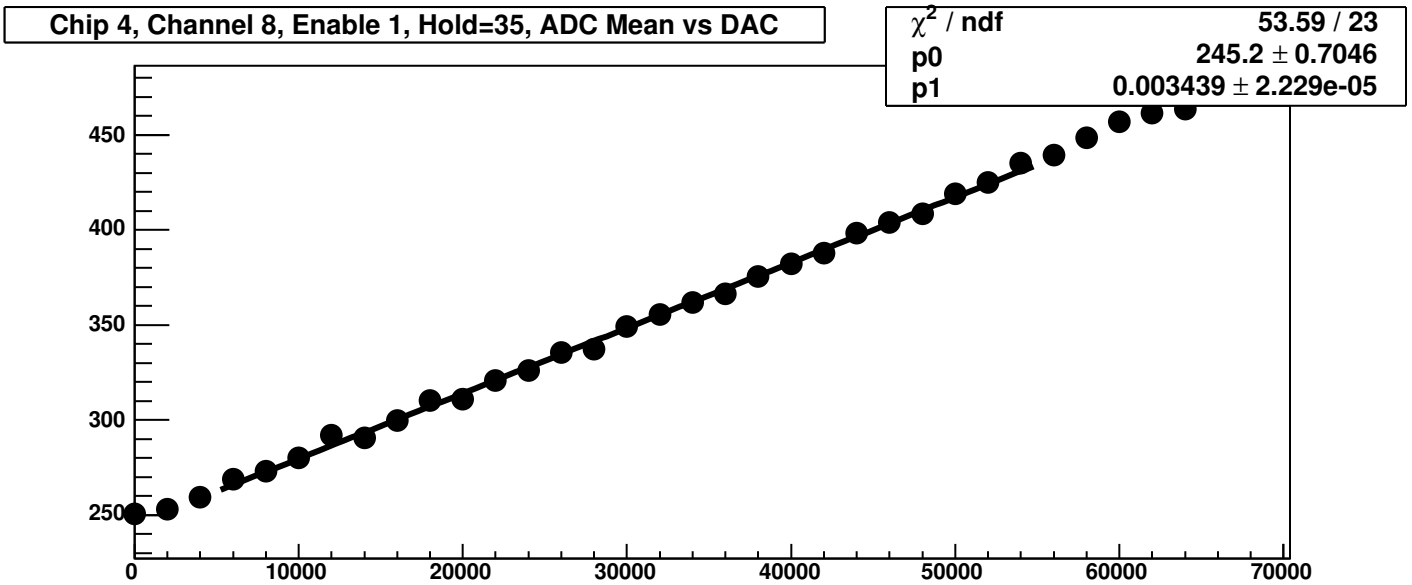

Chip 4, Channel 7, Enable 5!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000

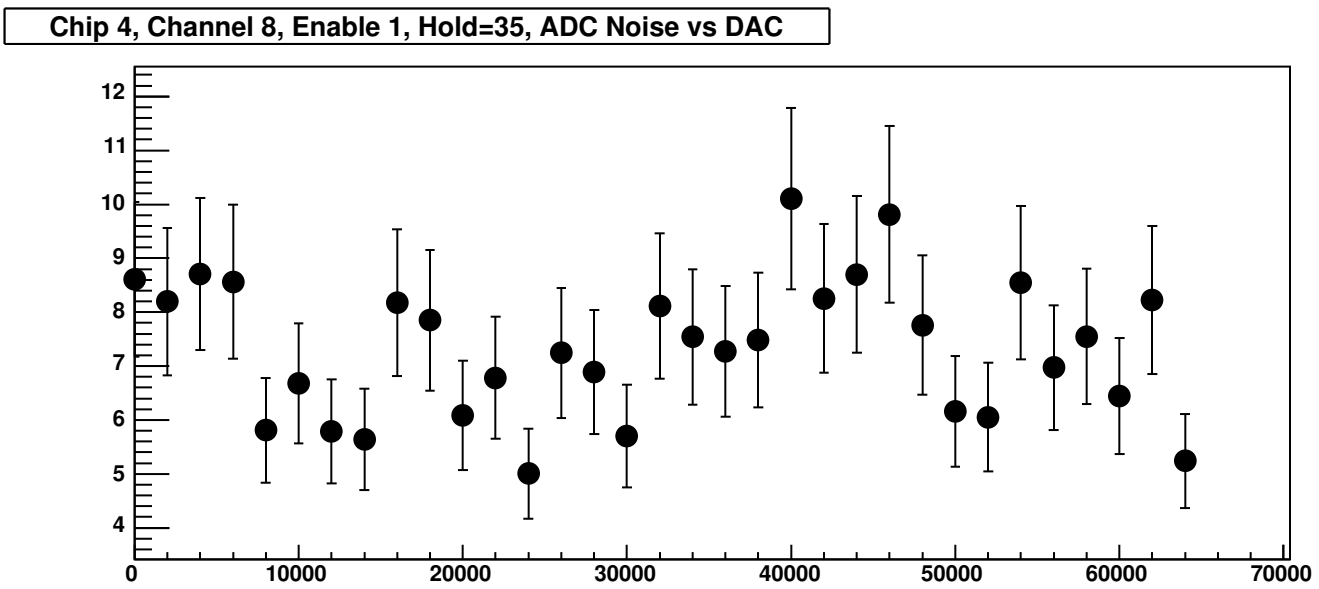

Chip 4, Channel 7, Enable 5!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
10000
20000
30000
40000
50000
60000

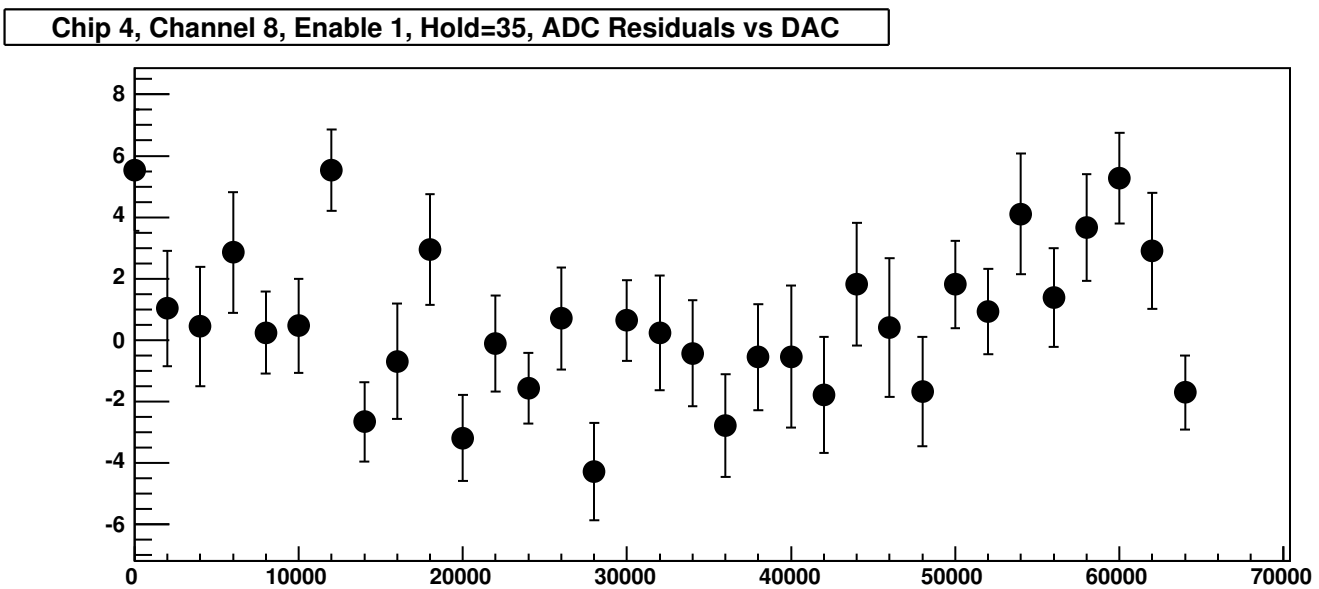

Chip 4, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 34.37 / 23
p0 243.9 ± 0.7493
p1 0.003542 ± 2.176e-05
450
400
350
300
250
0
10000
20000
30000
40000
50000
60000
70000

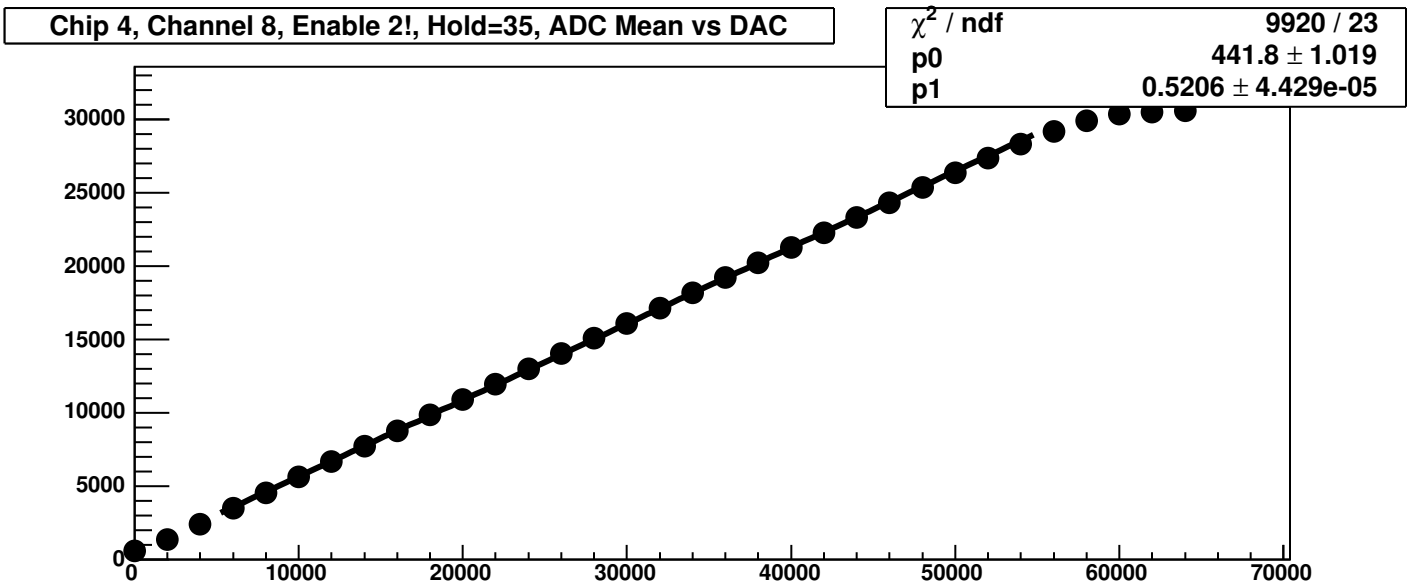

Chip 4, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

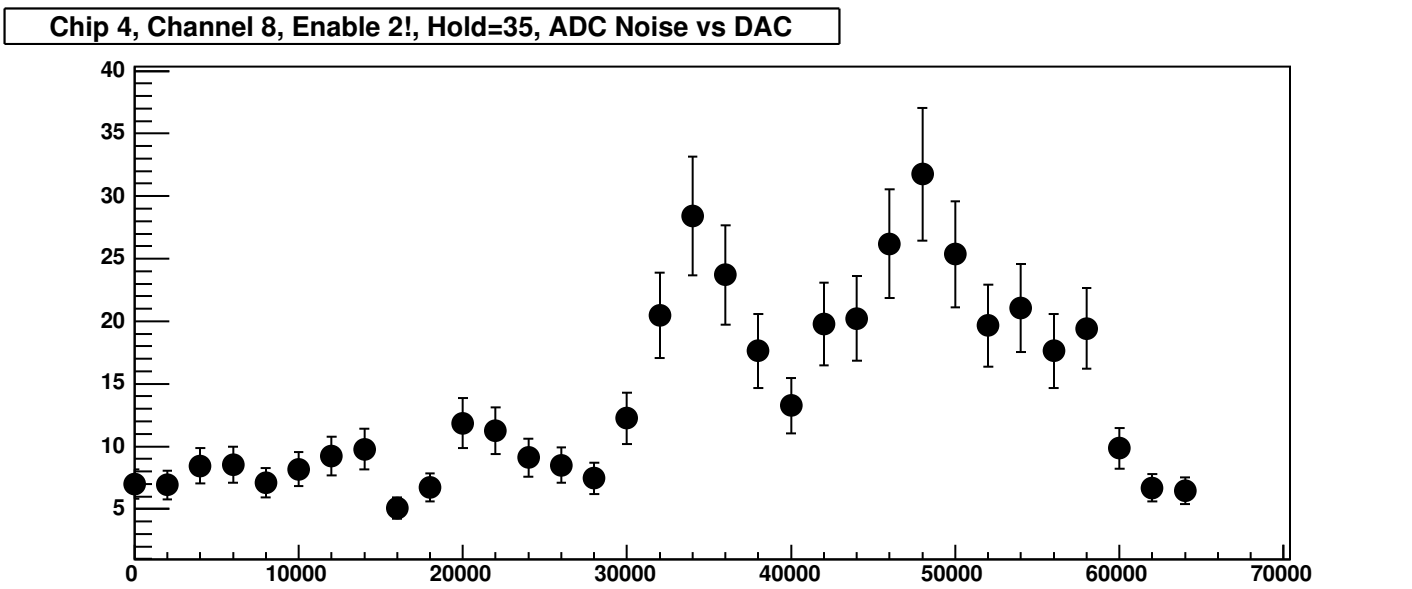

Chip 4, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
0
10000
20000
30000
40000
50000
60000
70000

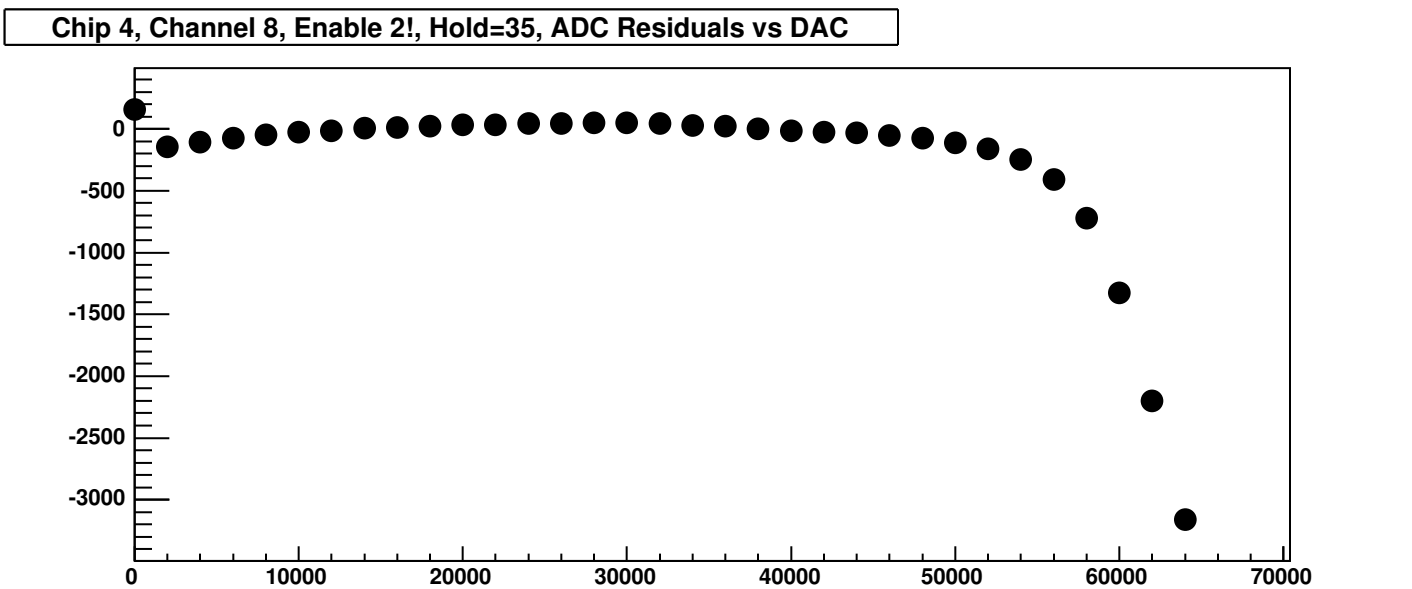

Chip 4, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 53.59 / 23
p0 245.2 ± 0.7046
p1 0.003439 ± 2.229e-05
450
400
350
300
250
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

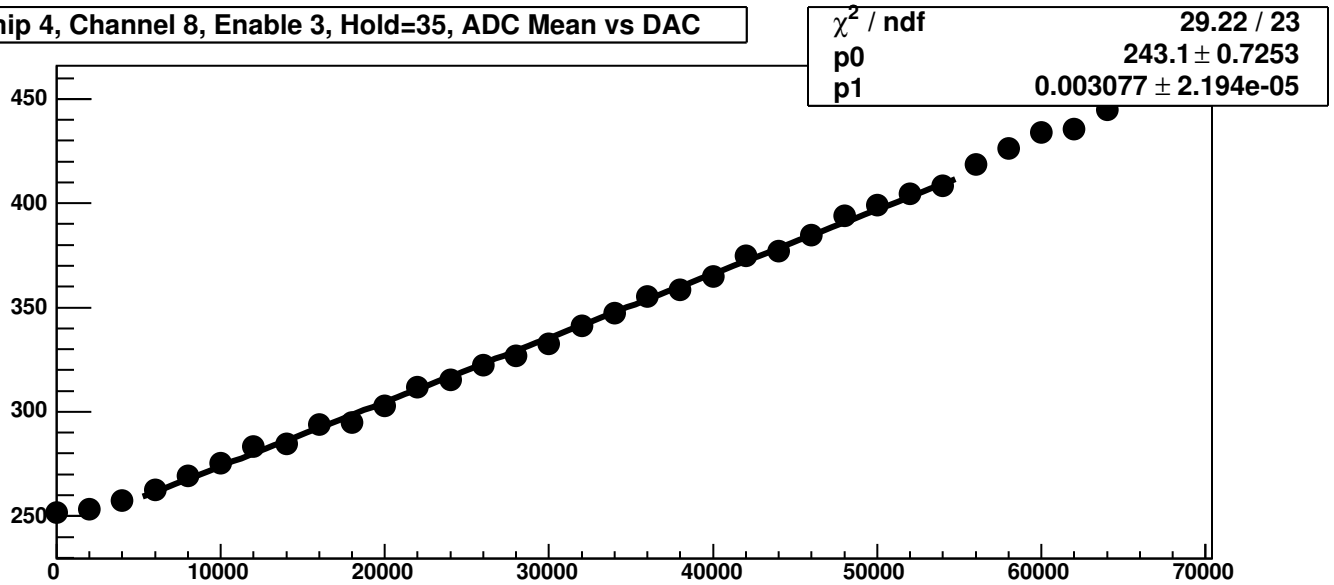

Chip 4, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 8, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 9920 / 23
p0 441.8 ± 1.019
p1 0.5206 ± 4.429e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

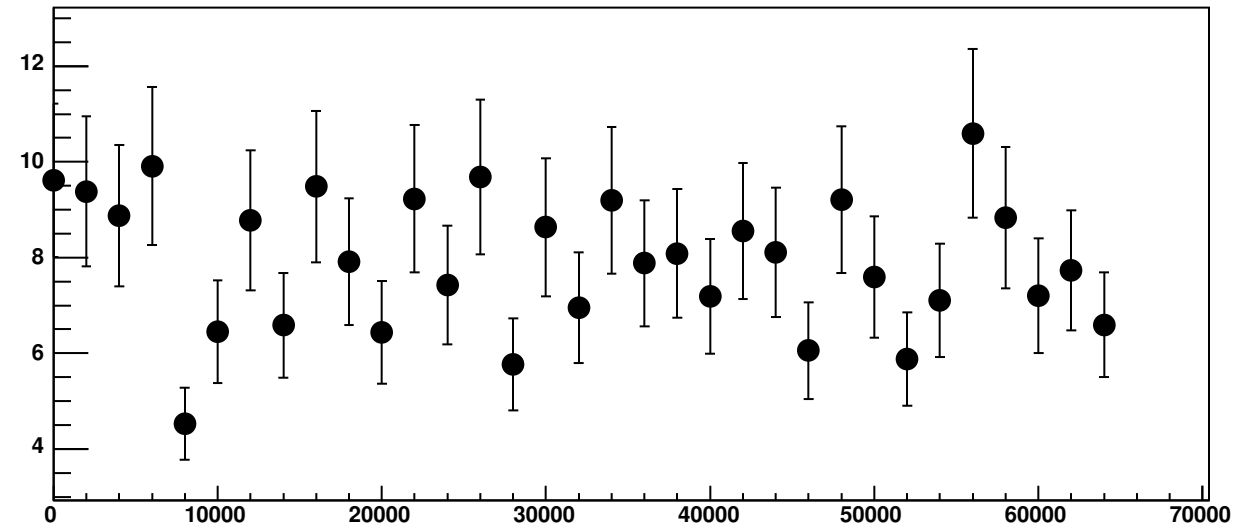

Chip 4, Channel 8, Enable 2!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 8, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
0
10000
20000
30000
40000
50000
60000
70000

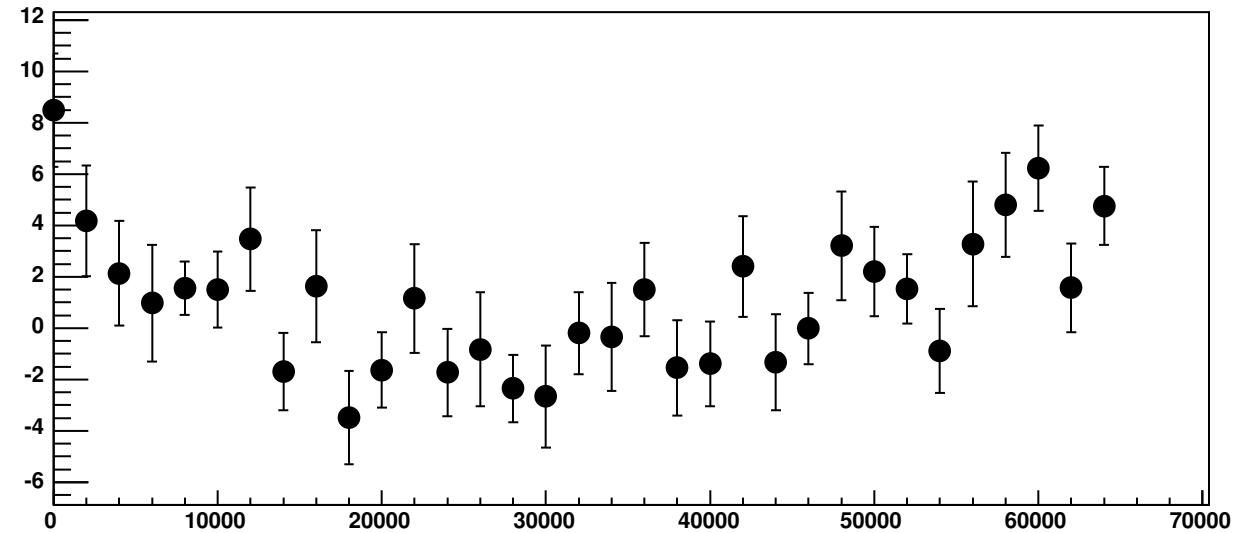
Chip 4, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC

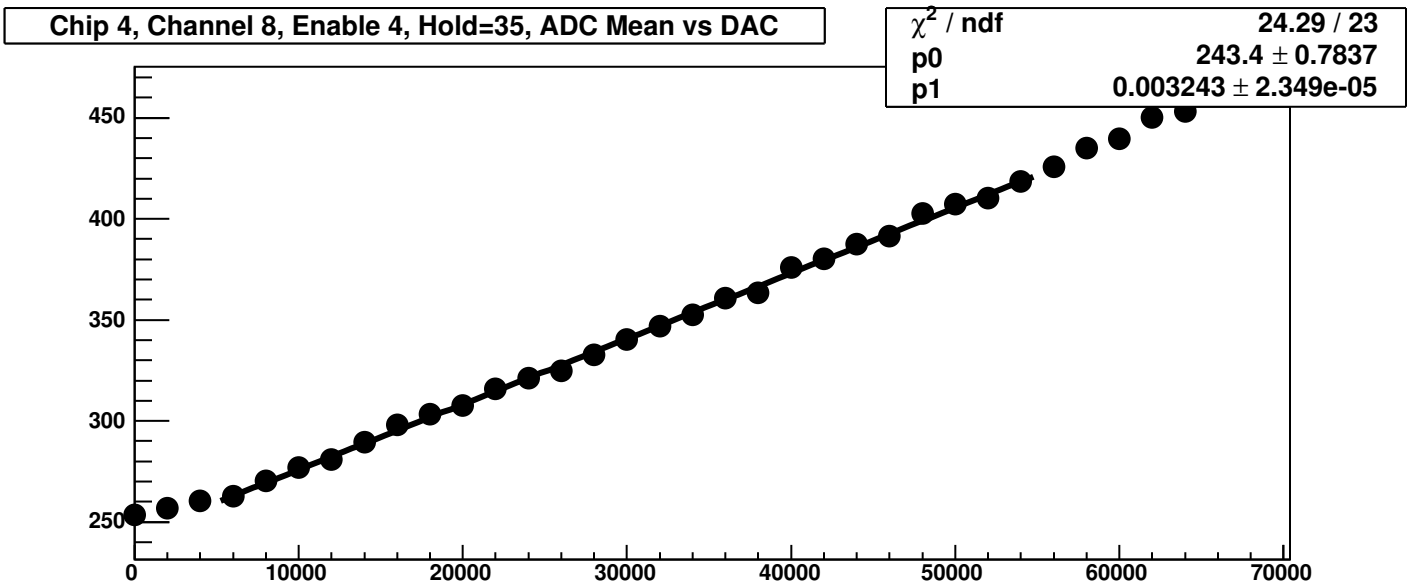


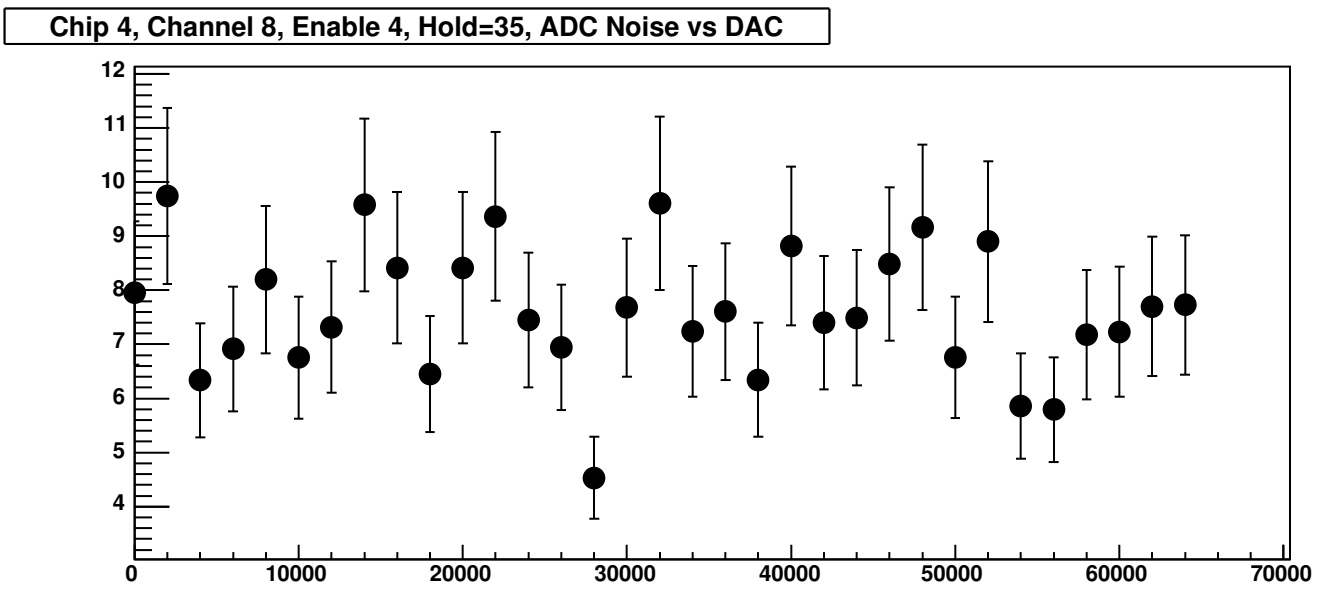
Chip 4, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC

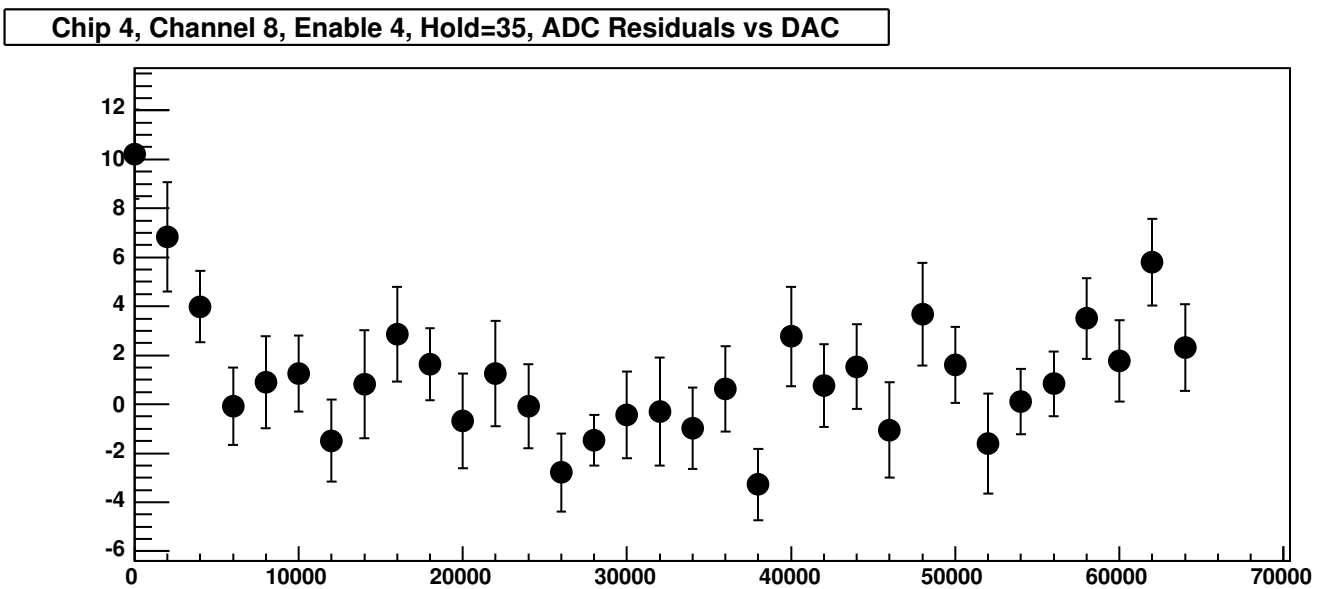


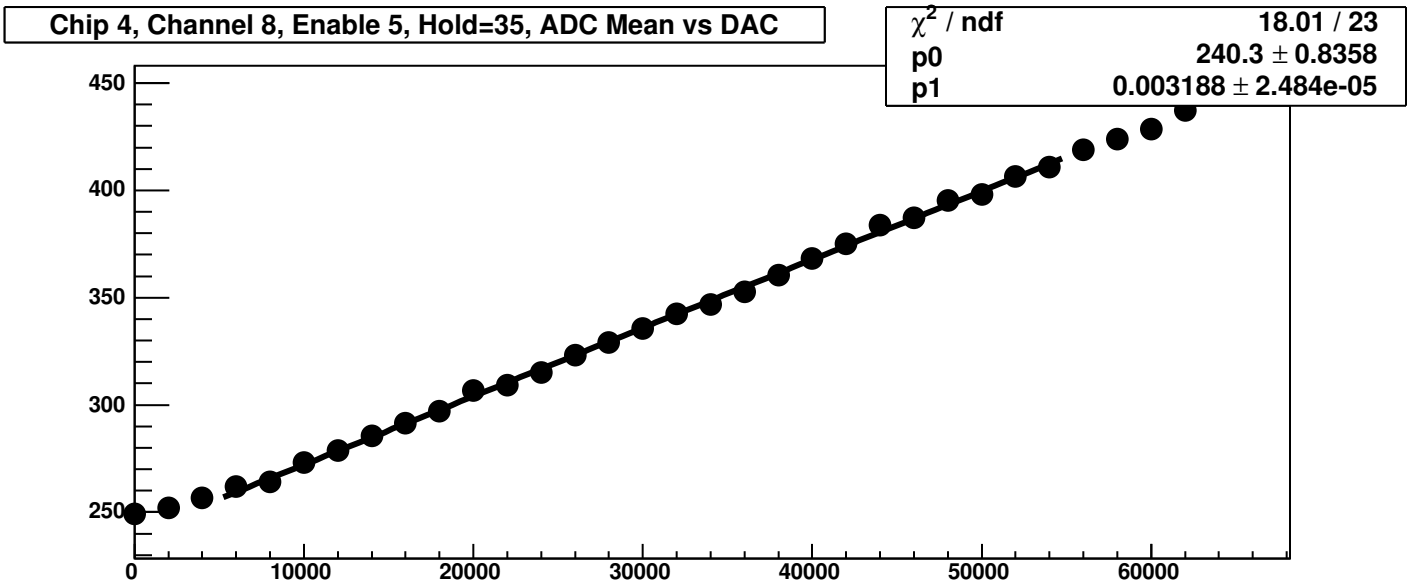
Chip 4, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC

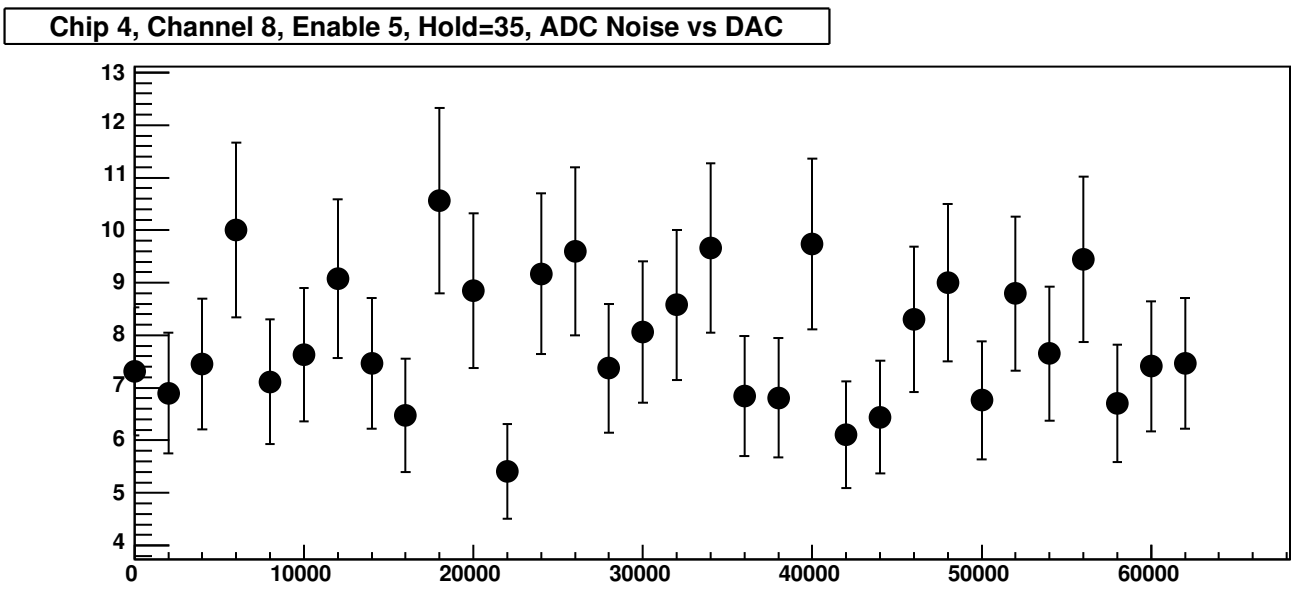


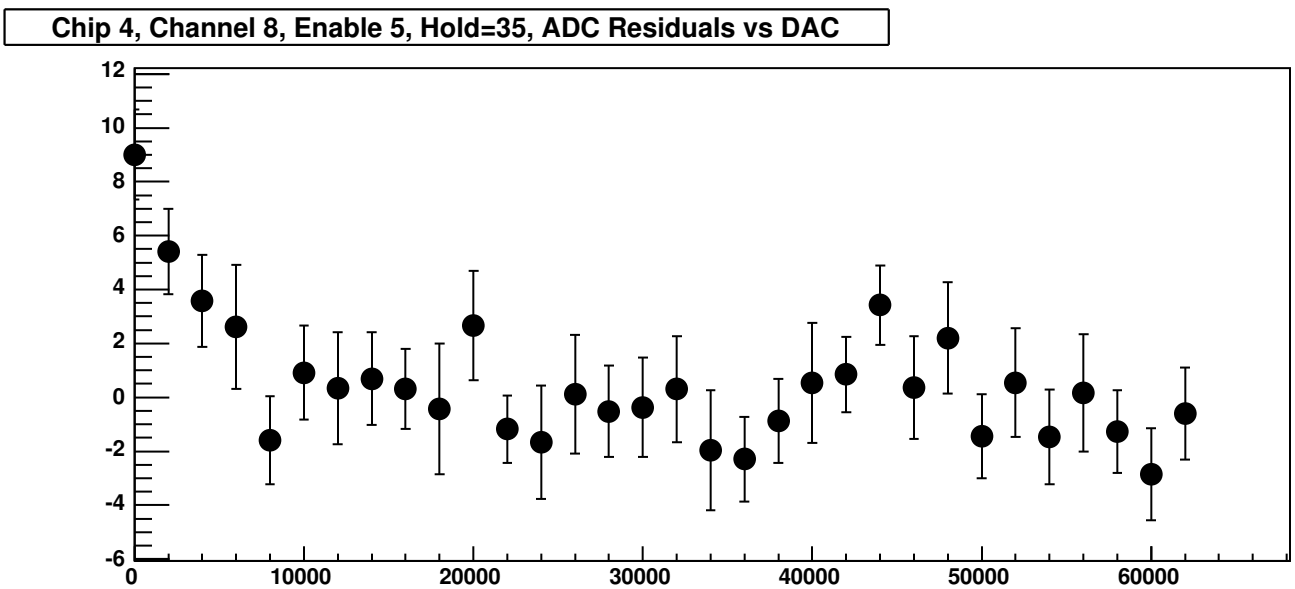

Chip 4, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.29 / 23
p0 243.4 ± 0.7837
p1 0.003243 ± 2.349e-05
450
400
350
300
250
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

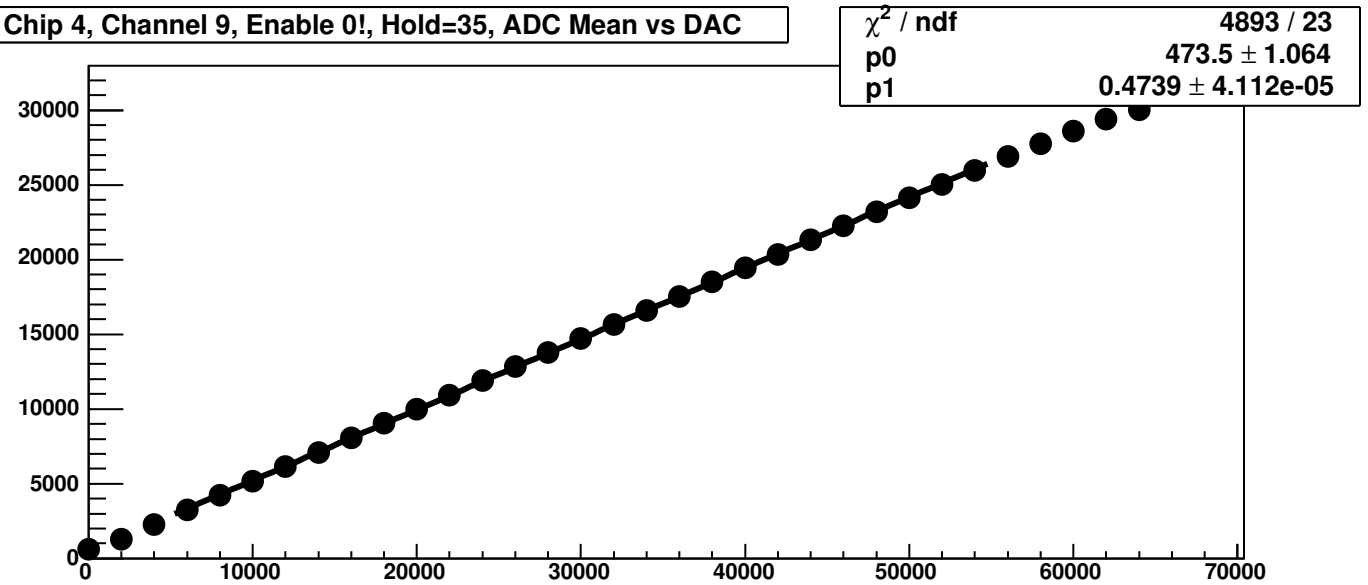

Chip 4, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 8, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 18.01 / 23
p0 240.3 ± 0.8358
p1 0.003188 ± 2.484e-05
450
400
350
300
250
0
10000
20000
30000
40000
50000
60000

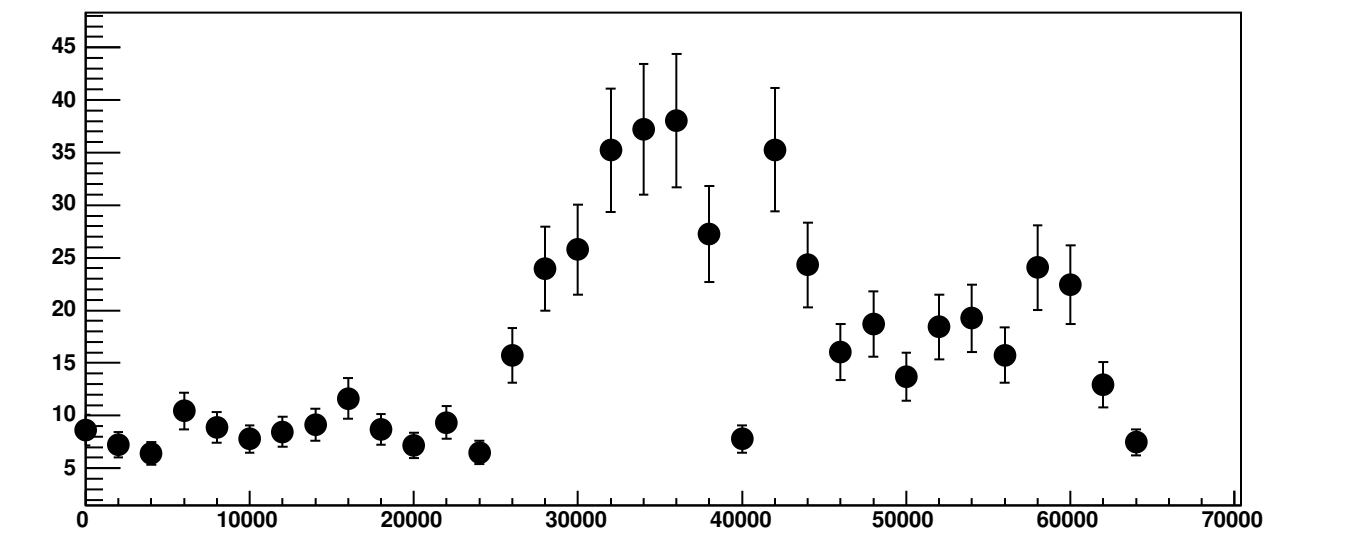

Chip 4, Channel 8, Enable 5, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 8, Enable 5, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000

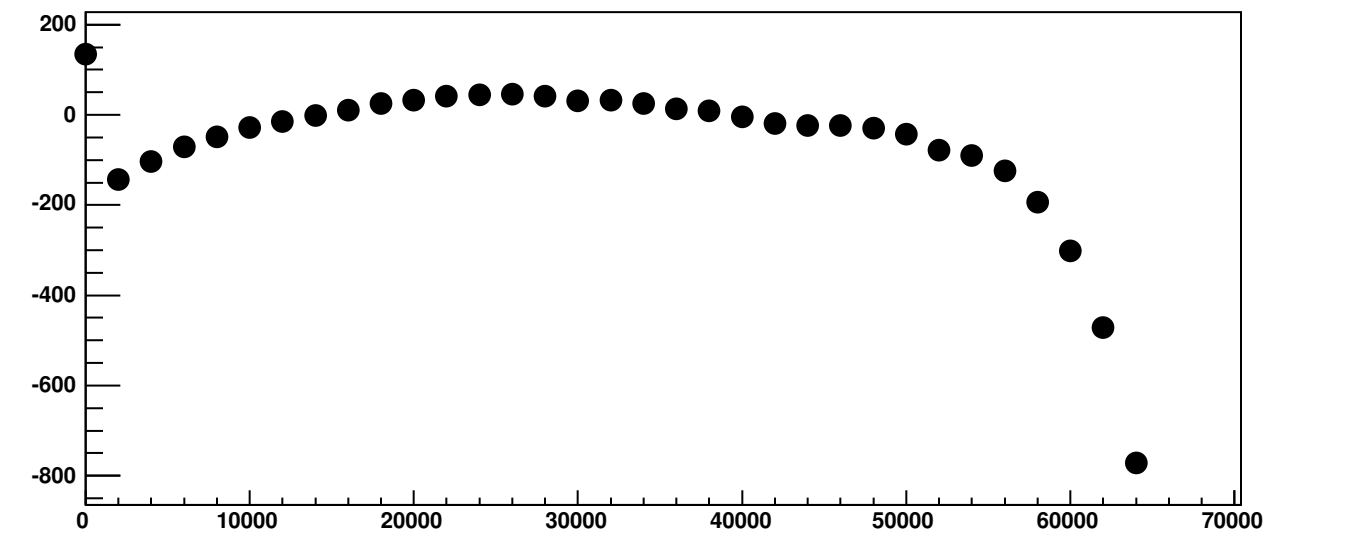
Chip 4, Channel 9, Enable 0!, Hold=35, ADC Mean vs DAC

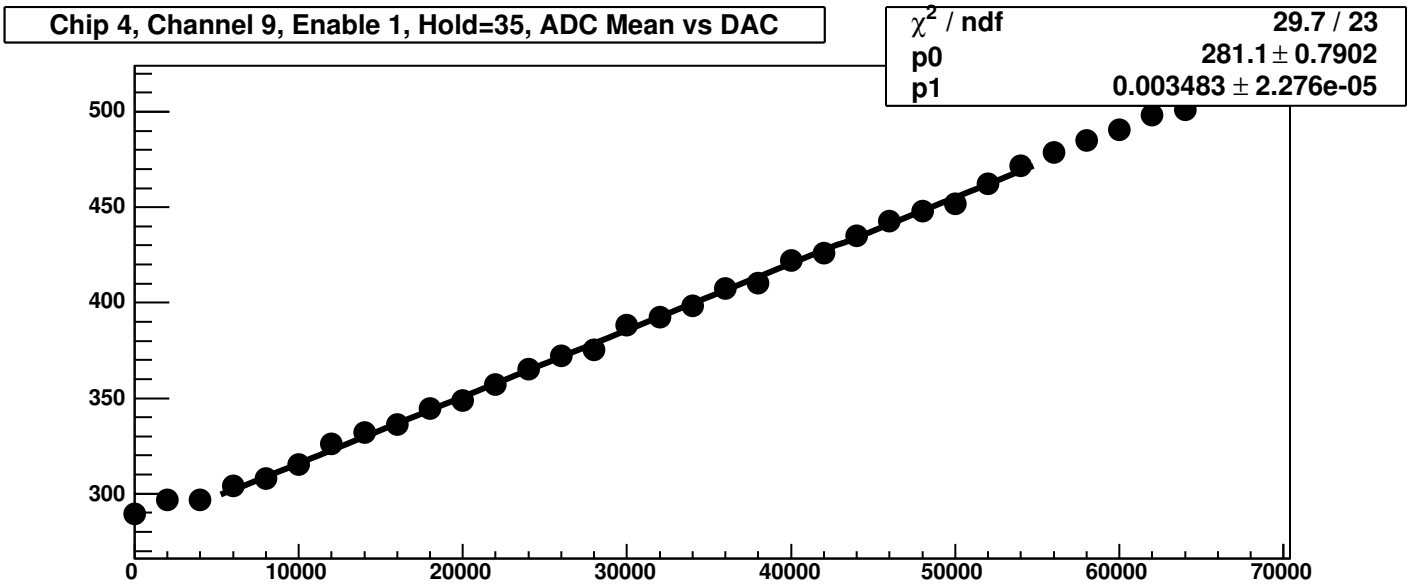


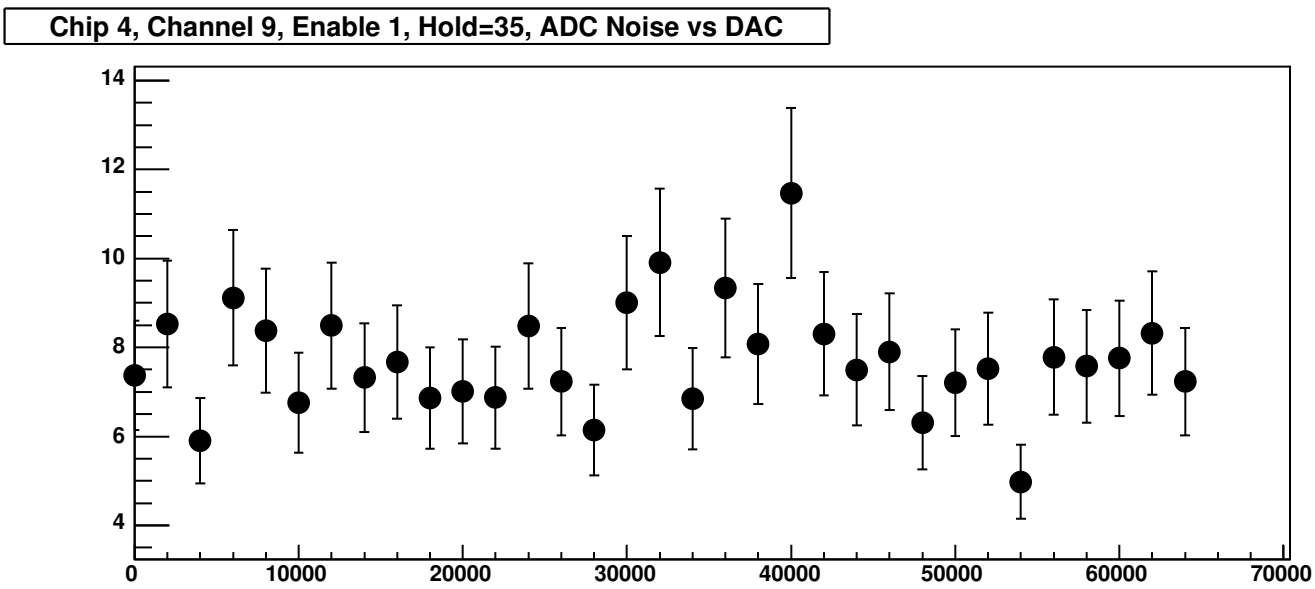
Chip 4, Channel 9, Enable 0!, Hold=35, ADC Noise vs DAC

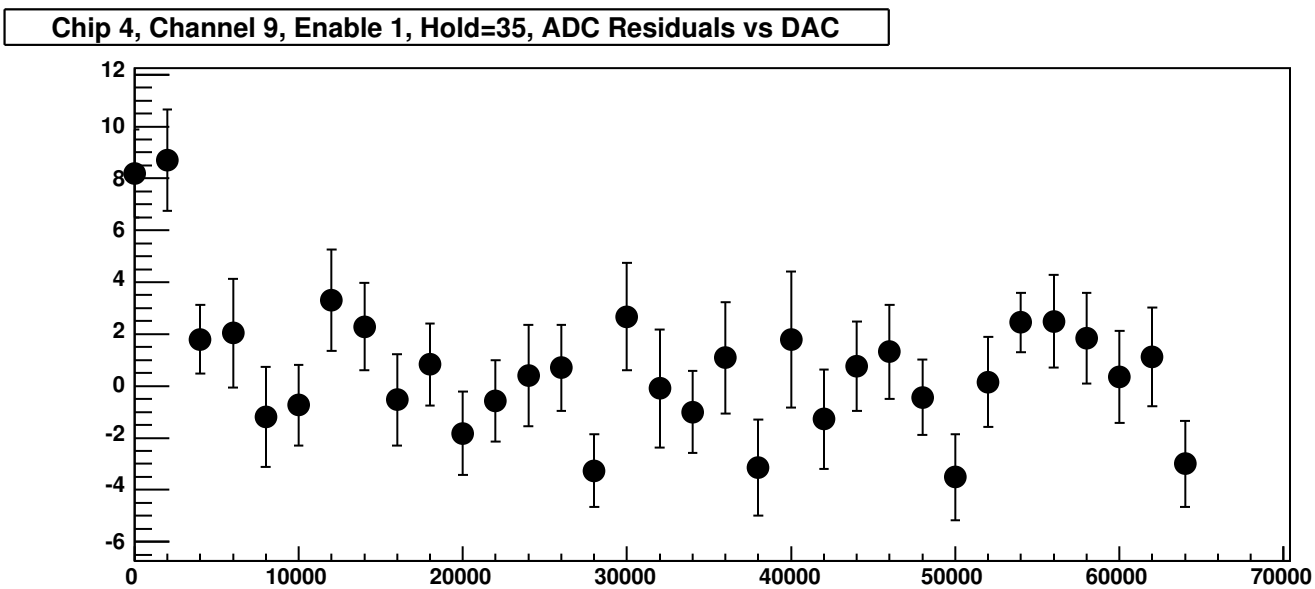


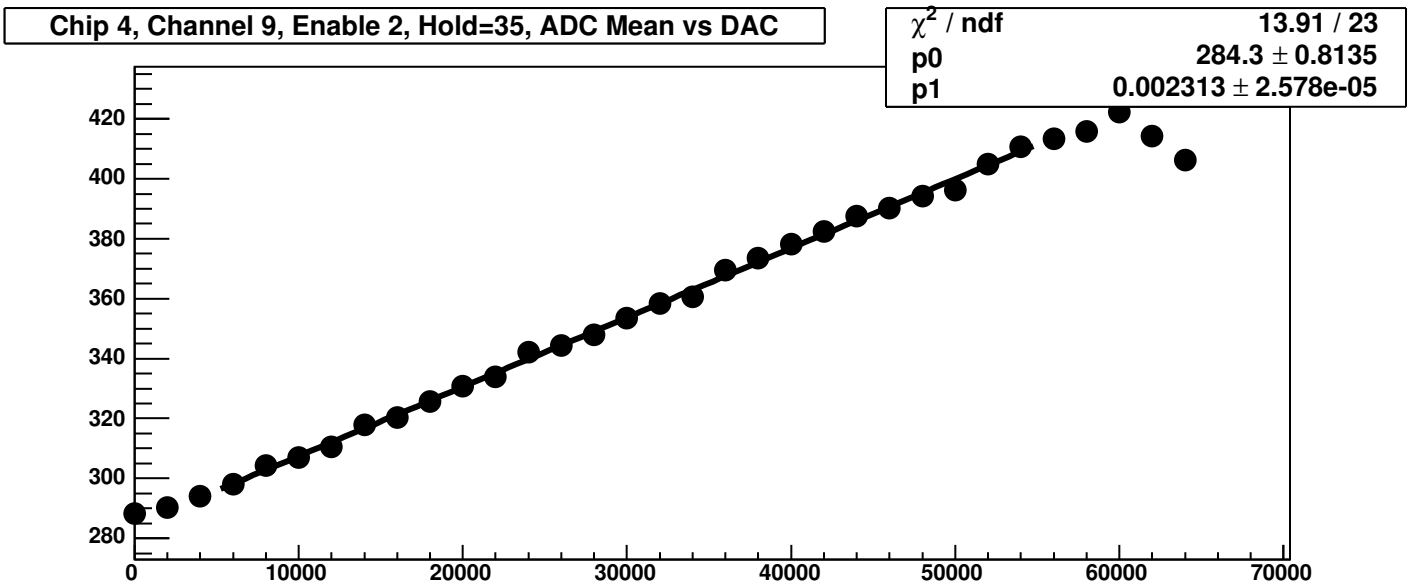
Chip 4, Channel 9, Enable 0!, Hold=35, ADC Residuals vs DAC

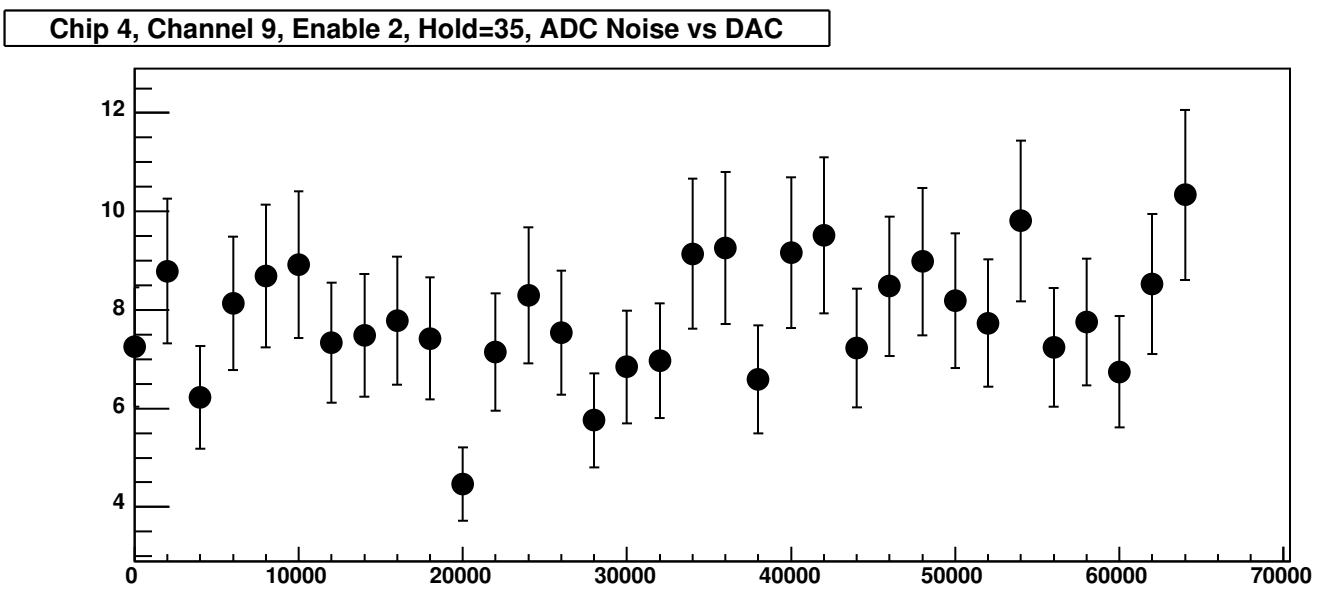


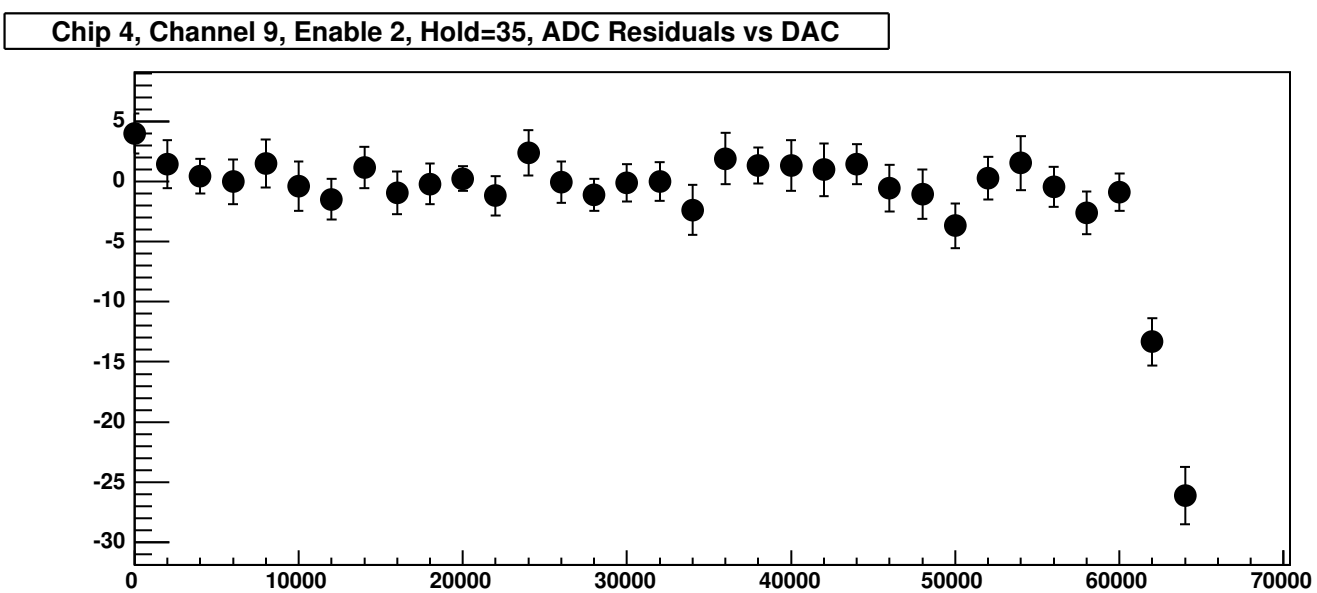

Chip 4, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 29.7 / 23
p0 281.1 ± 0.7902
p1 0.003483 ± 2.276e-05
500
450
400
350
300
0
10000
20000
30000
40000
50000
60000
70000

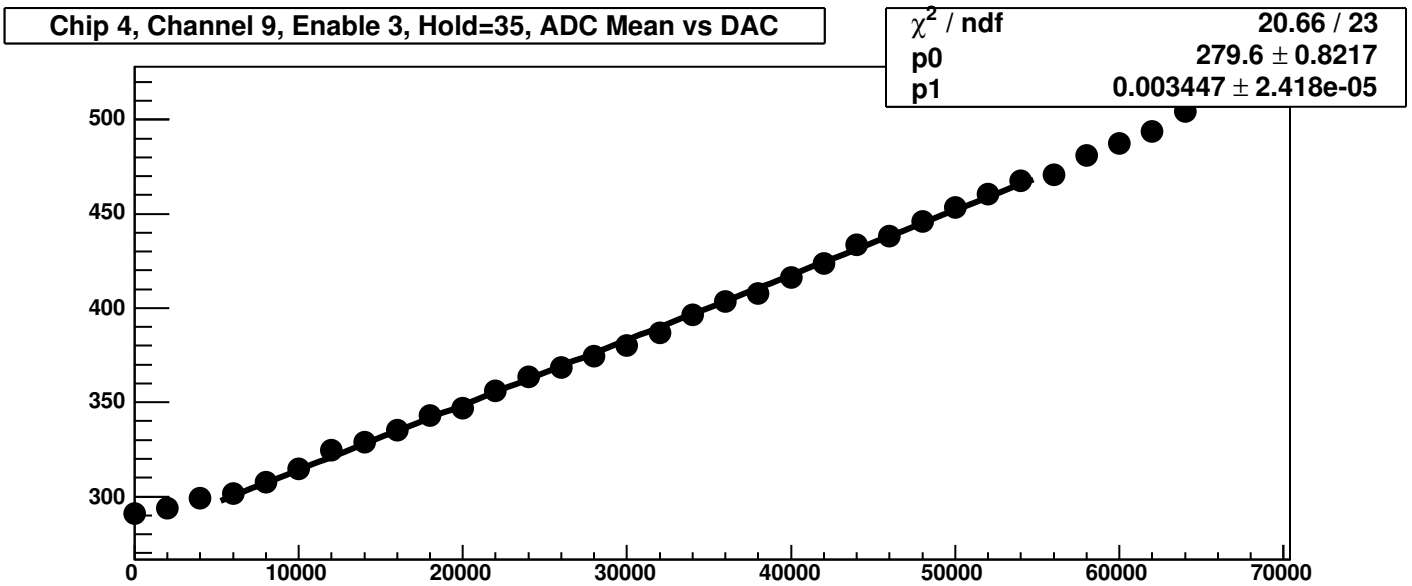

Chip 4, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

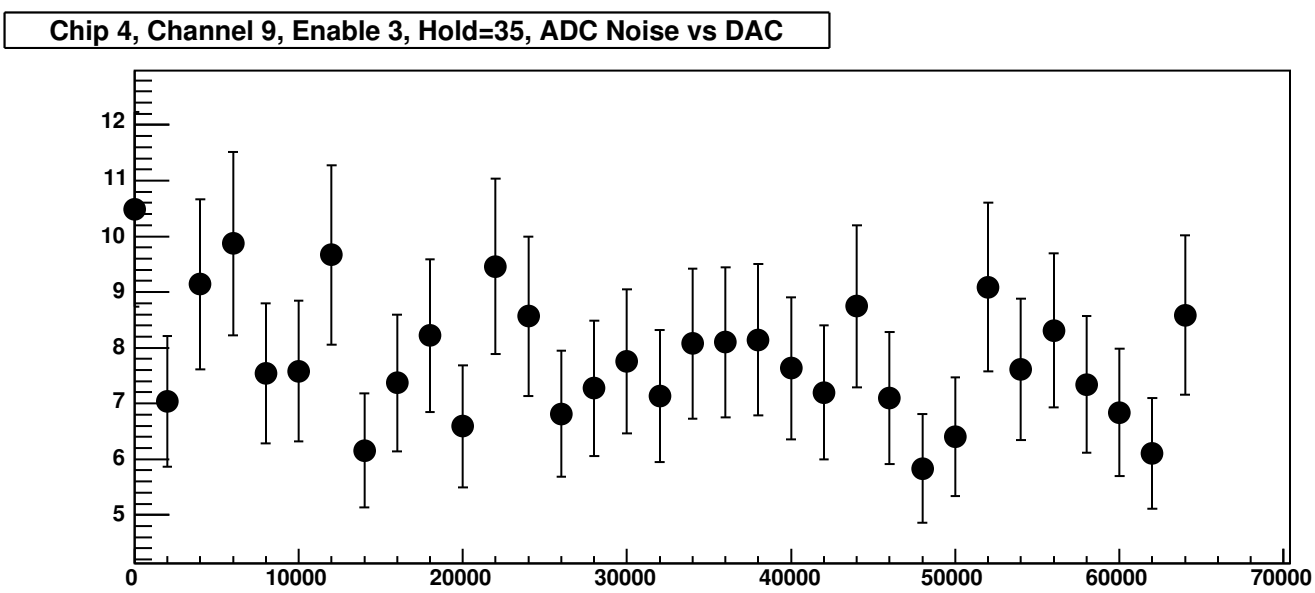

Chip 4, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

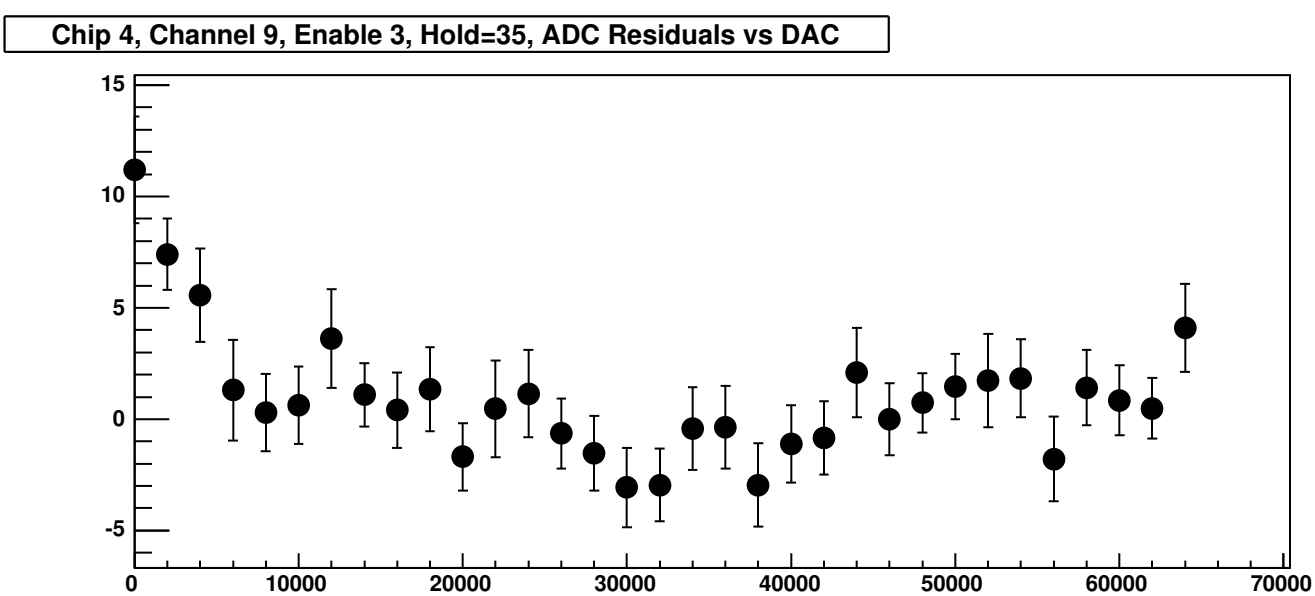

Chip 4, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 13.91 / 23
p0 284.3 ± 0.8135
p1 0.002313 ± 2.578e-05
420
400
380
360
340
320
300
280
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

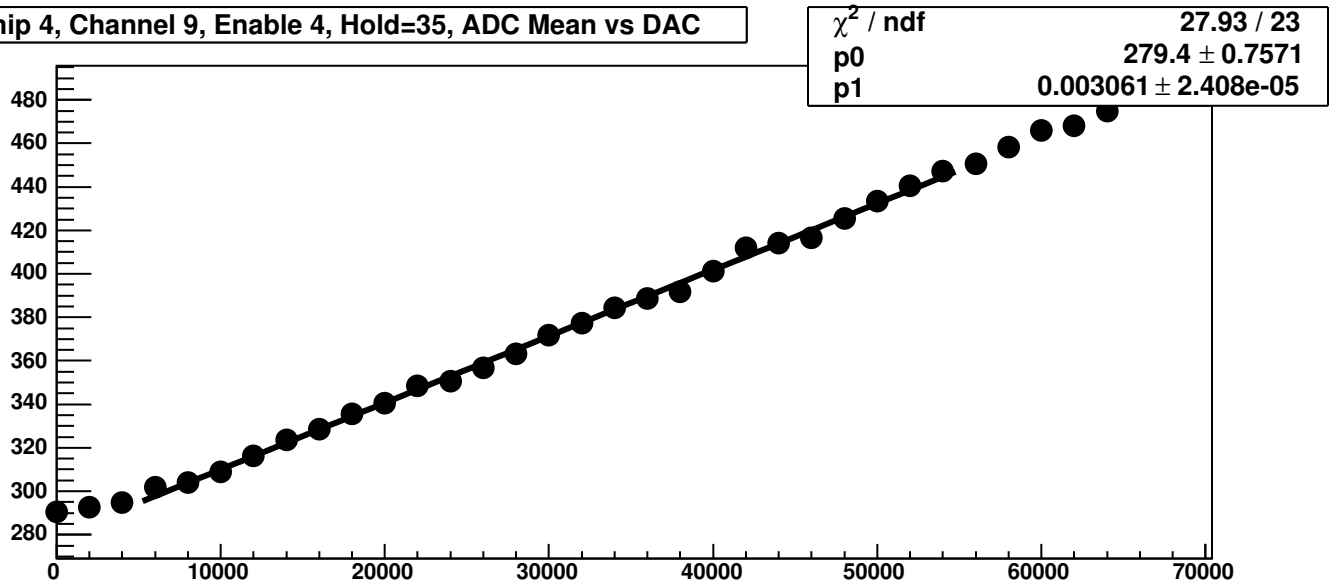

Chip 4, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 9, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 20.66 / 23
p0 279.6 ± 0.8217
p1 0.003447 ± 2.418e-05
500
450
400
350
300
0
10000
20000
30000
40000
50000
60000
70000

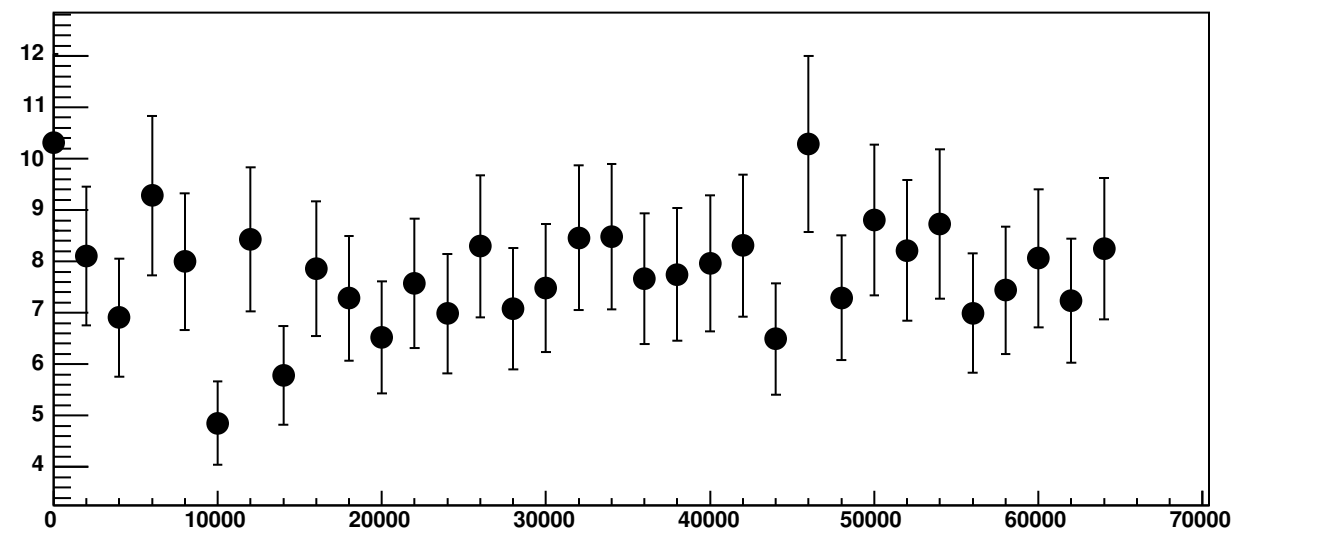

Chip 4, Channel 9, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 9, Enable 3, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

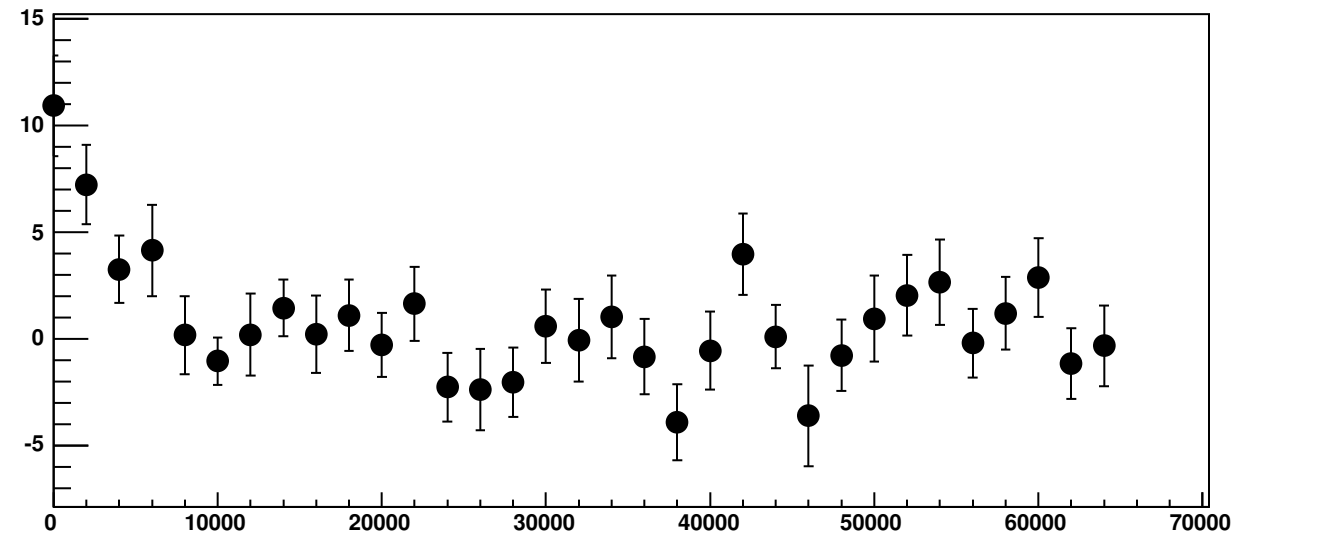
Chip 4, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC

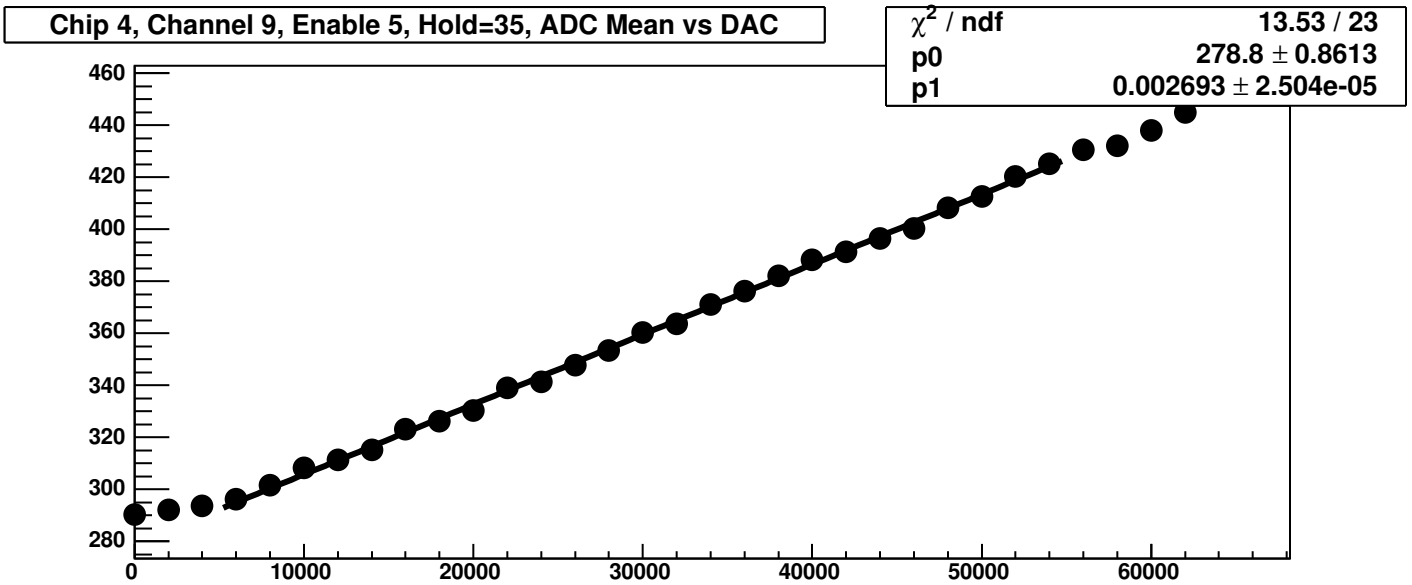


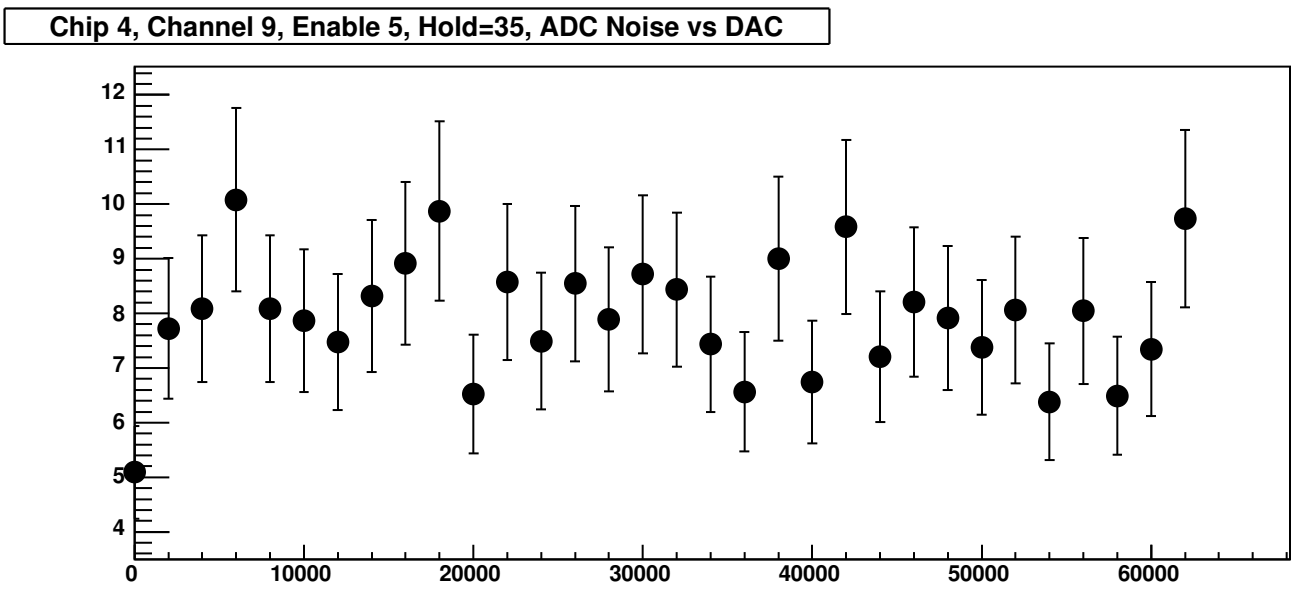
Chip 4, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC

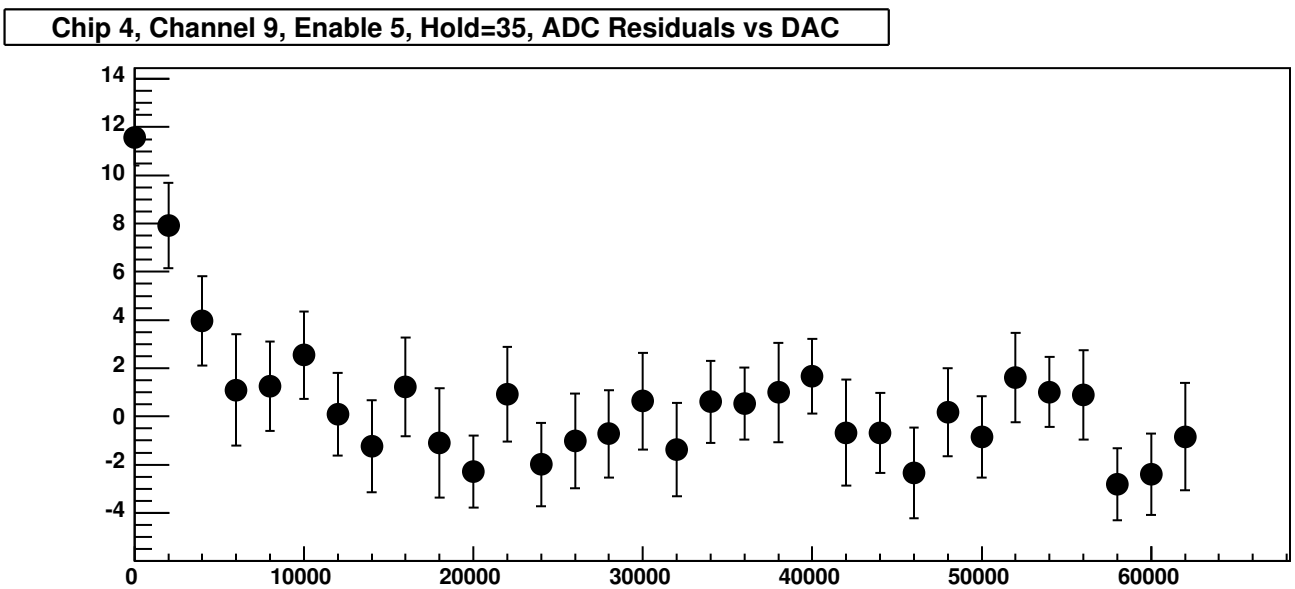


Chip 4, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC

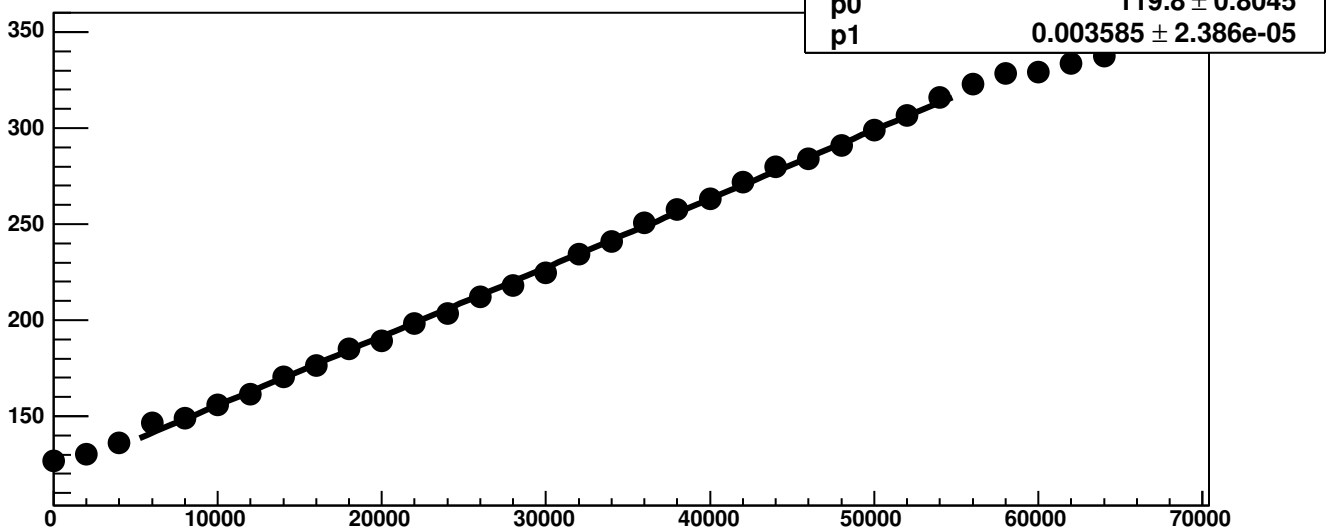



Chip 4, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 13.53 / 23
p0 278.8 ± 0.8613
p1 0.002693 ± 2.504e-05
460
440
420
400
380
360
340
320
300
280
0
10000
20000
30000
40000
50000
60000

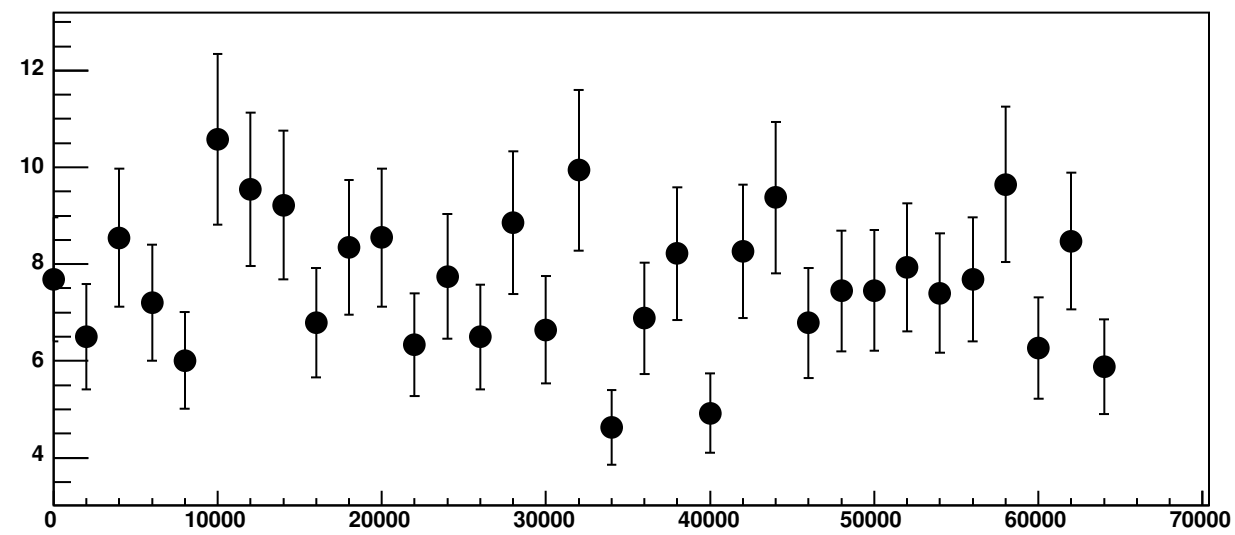

Chip 4, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
0
10000
20000
30000
40000
50000
60000

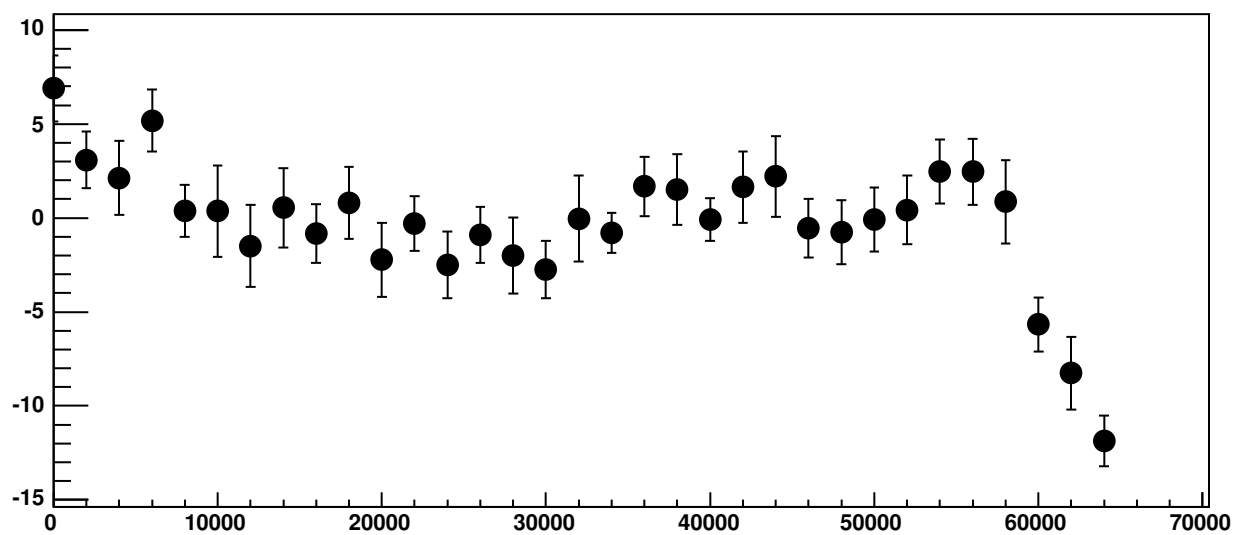
Chip 4, Channel 10, Enable 0, Hold=35, ADC Mean vs DAC



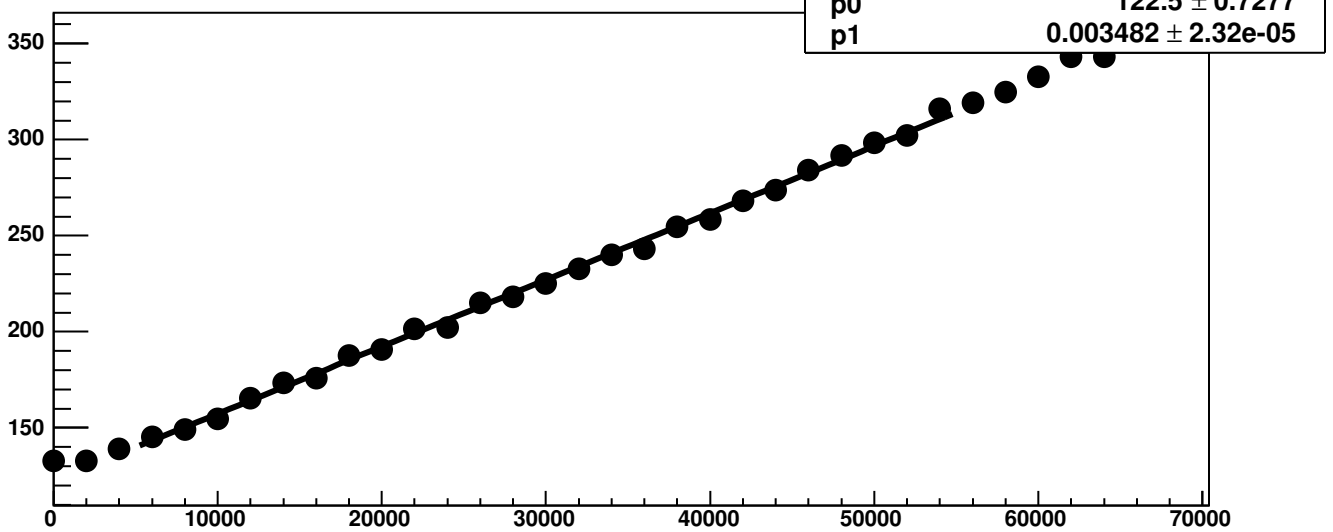
Chip 4, Channel 10, Enable 0, Hold=35, ADC Noise vs DAC



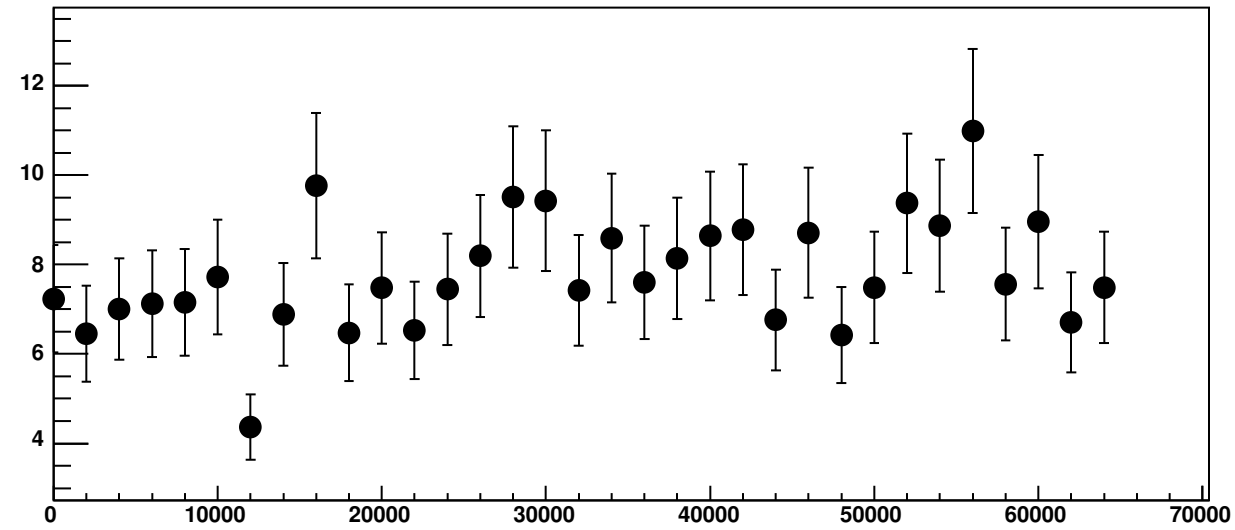
Chip 4, Channel 10, Enable 0, Hold=35, ADC Residuals vs DAC



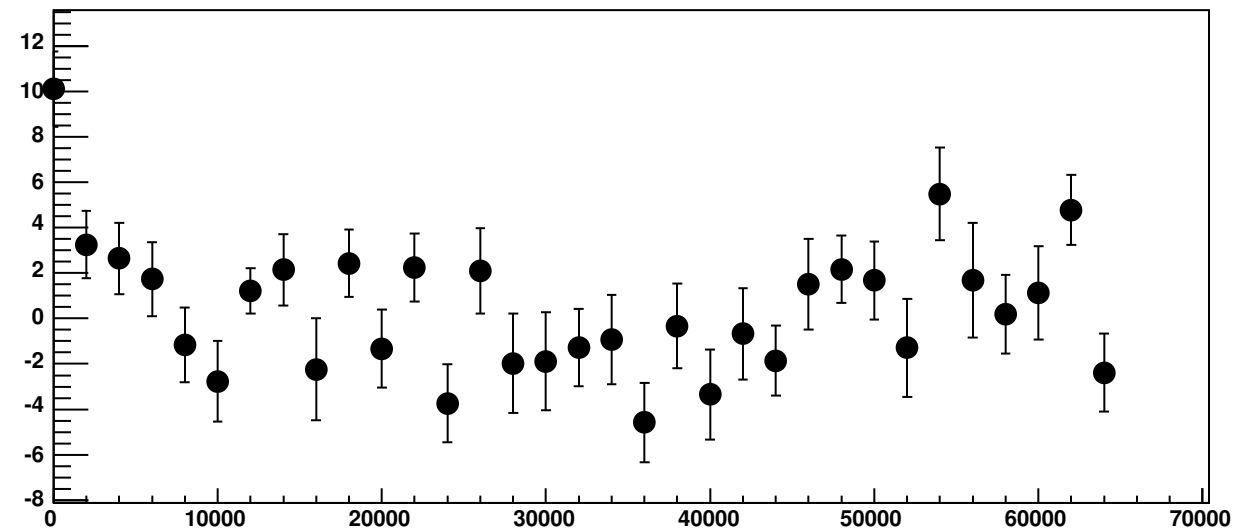
Chip 4, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

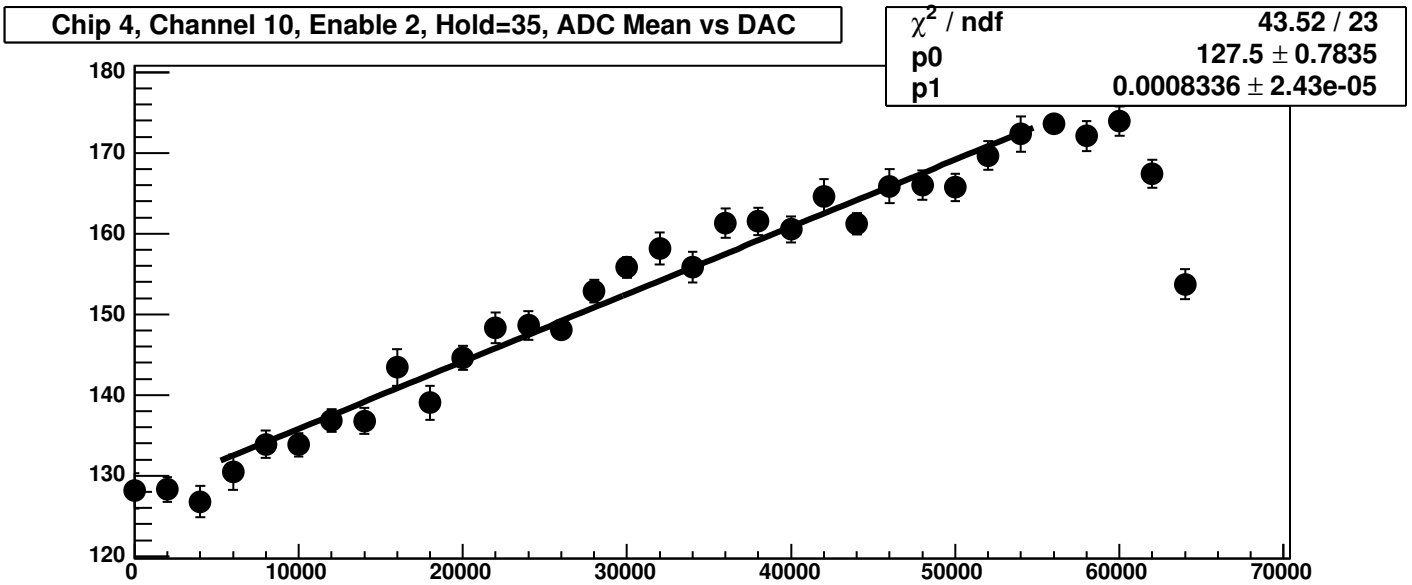


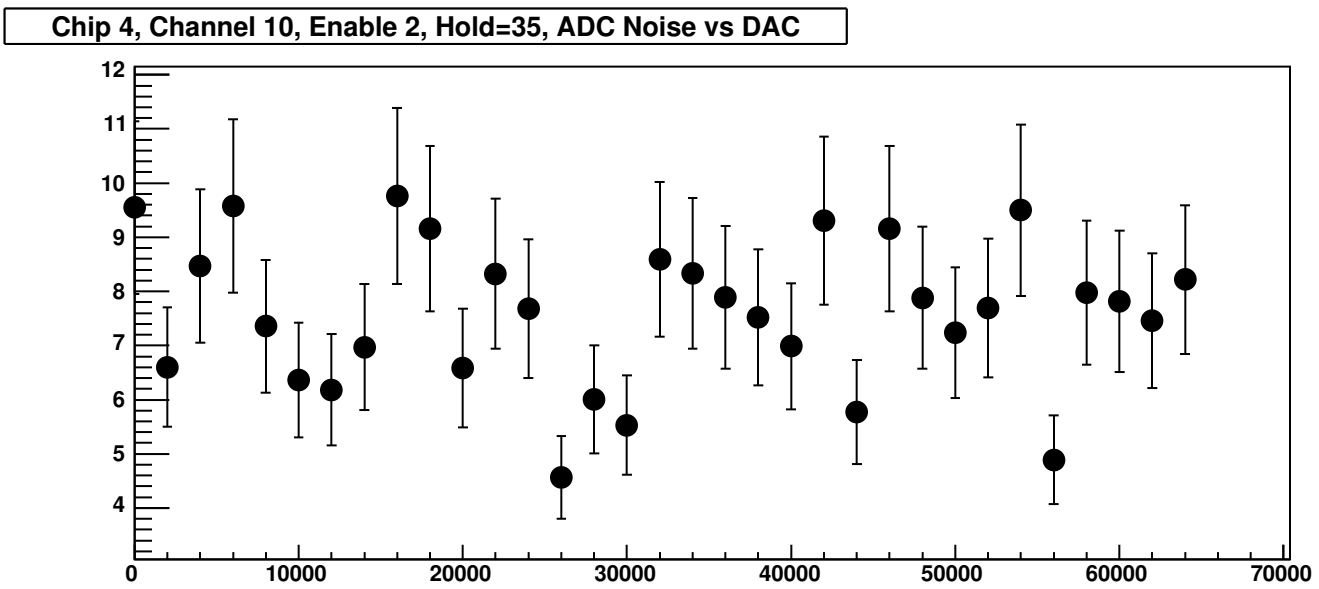
Chip 4, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC

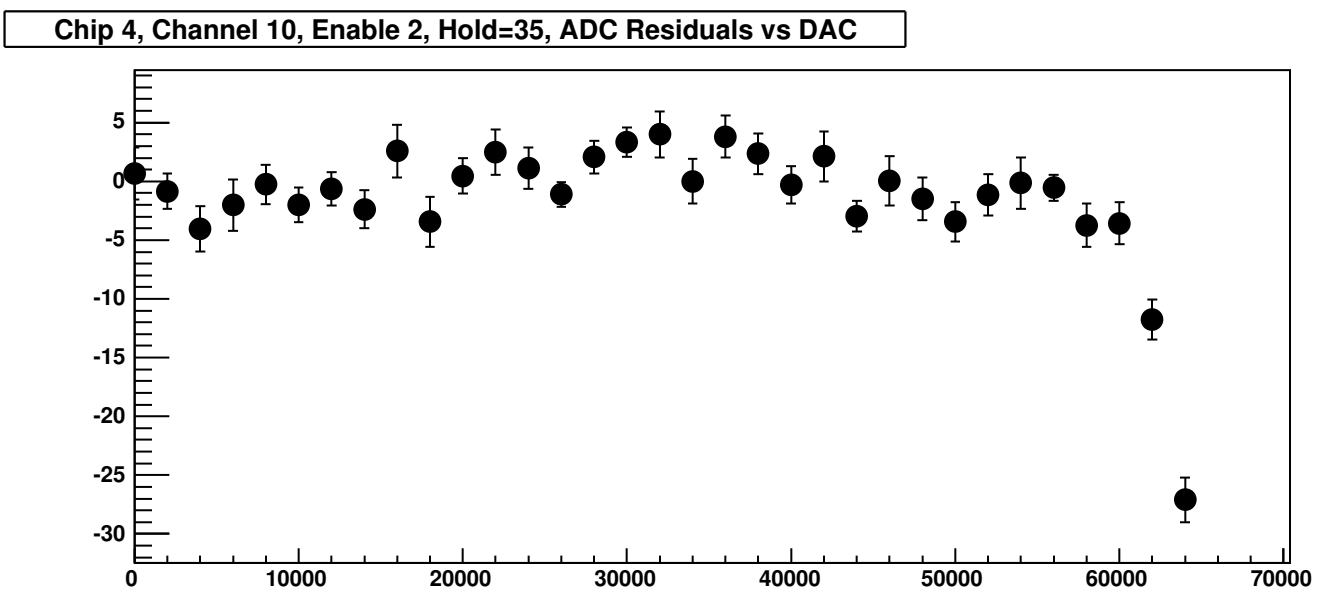


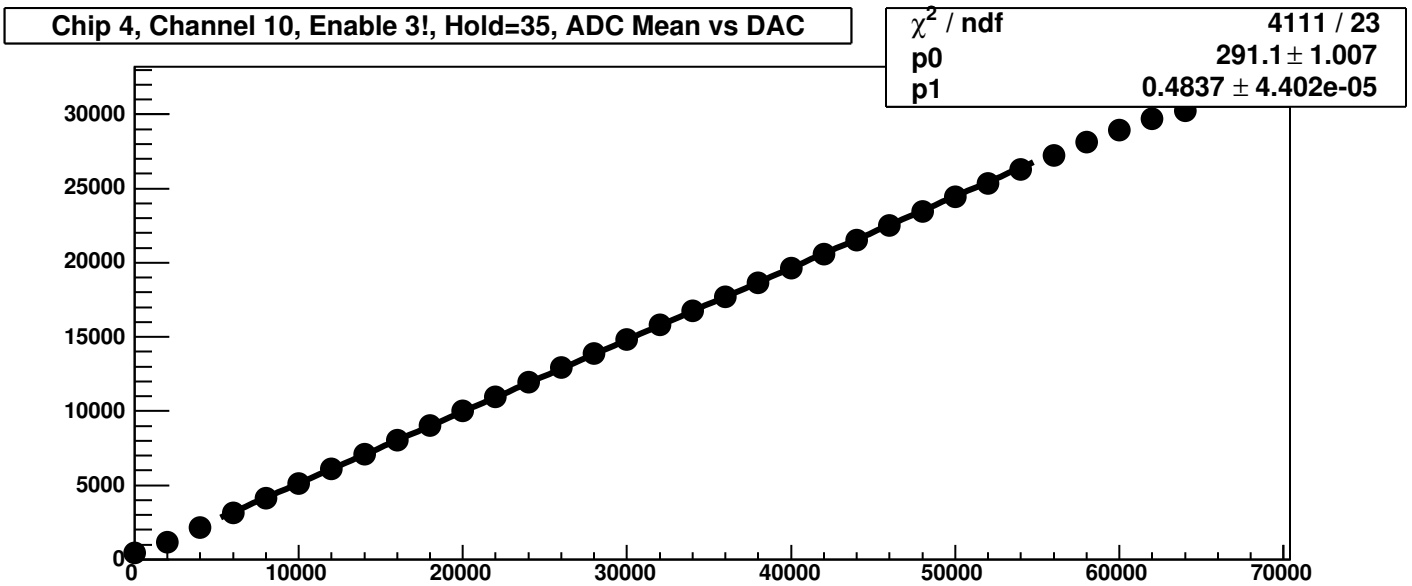
Chip 4, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

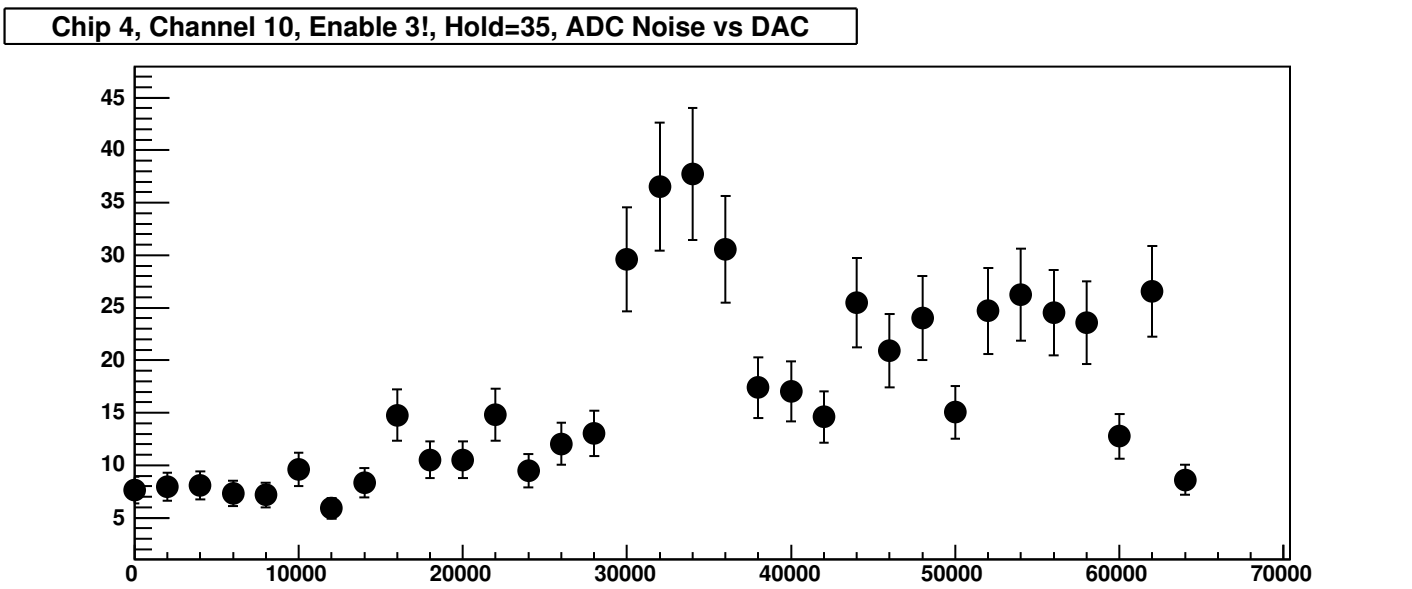


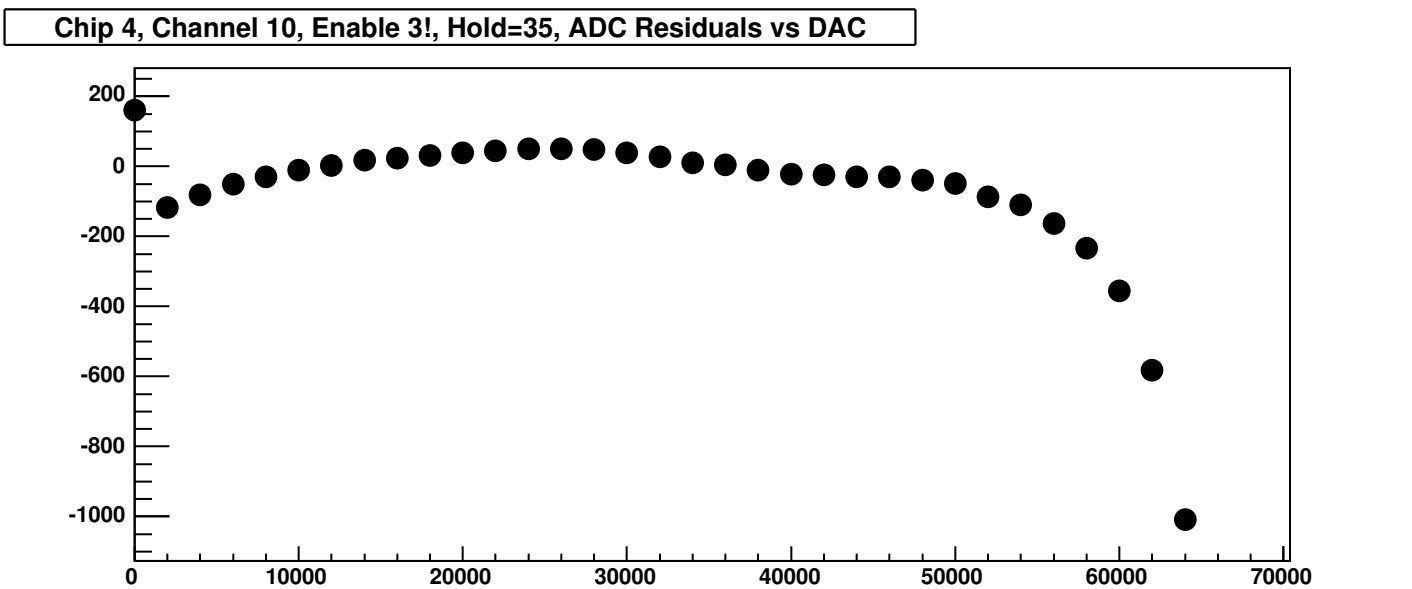

Chip 4, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 43.52 / 23
p0 127.5 ± 0.7835
p1 0.0008336 ± 2.43e-05
180
170
160
150
140
130
120
0
10000
20000
30000
40000
50000
60000
70000

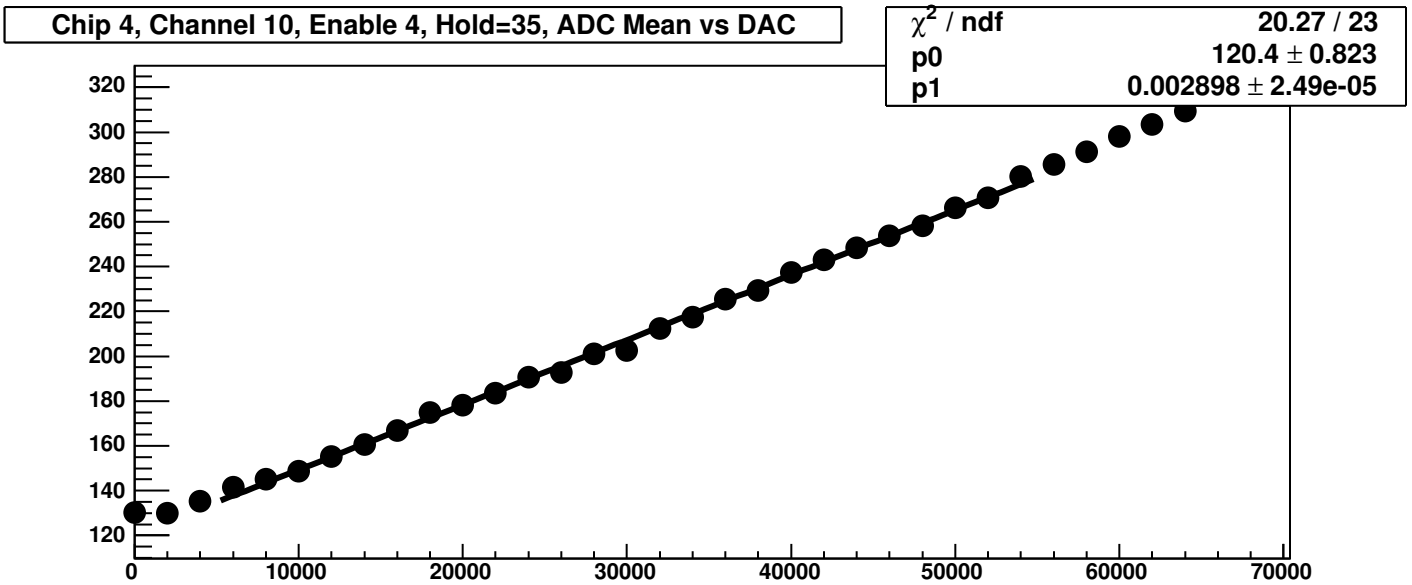

Chip 4, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

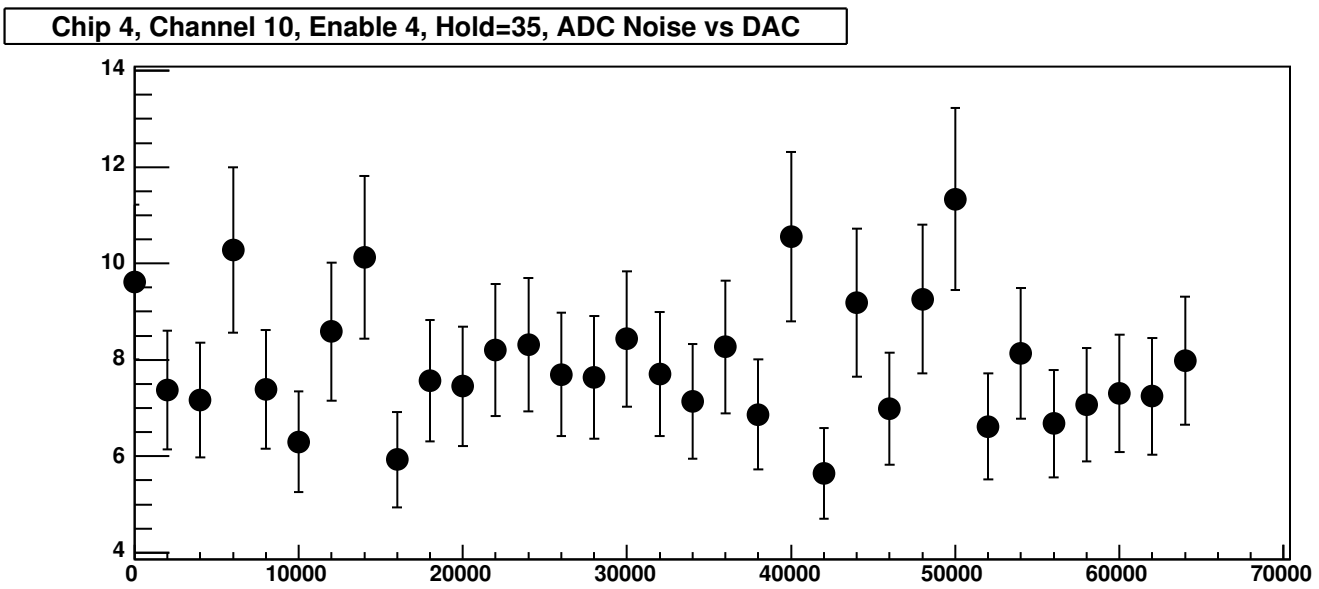

Chip 4, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
-20
-25
-30
0
10000
20000
30000
40000
50000
60000
70000

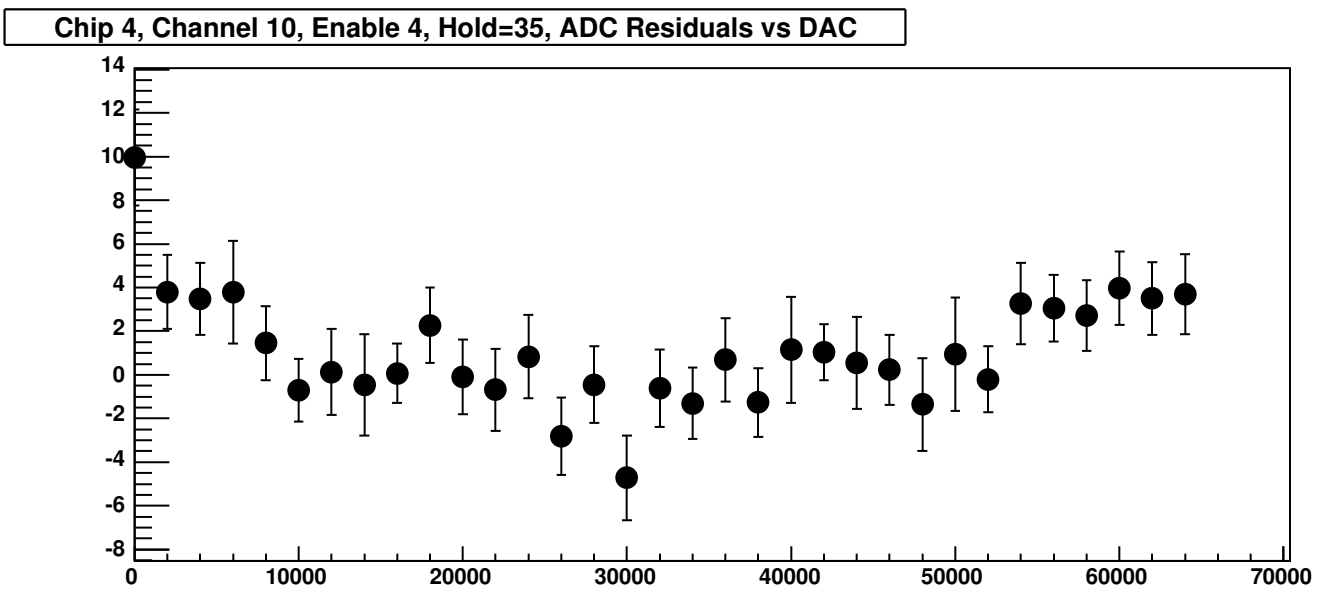

Chip 4, Channel 10, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 4111 / 23
p0 291.1 ± 1.007
p1 0.4837 ± 4.402e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 10, Enable 3!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000

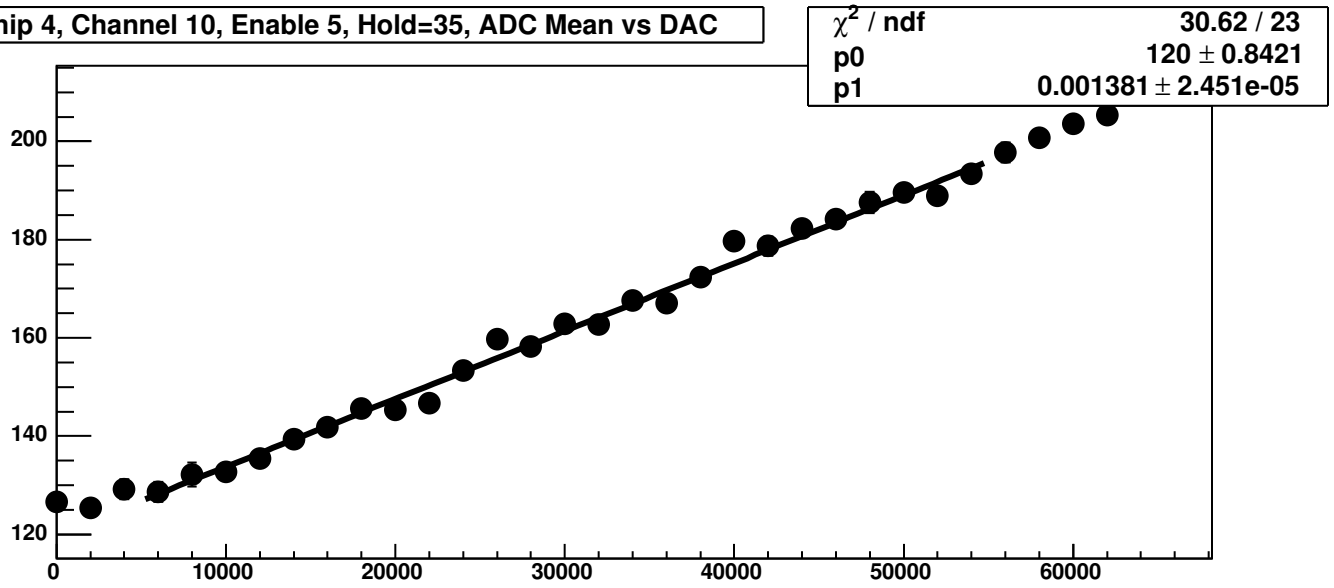

Chip 4, Channel 10, Enable 3!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 20.27 / 23
p0 120.4 ± 0.823
p1 0.002898 ± 2.49e-05
320
300
280
260
240
220
200
180
160
140
120
0
10000
20000
30000
40000
50000
60000
70000

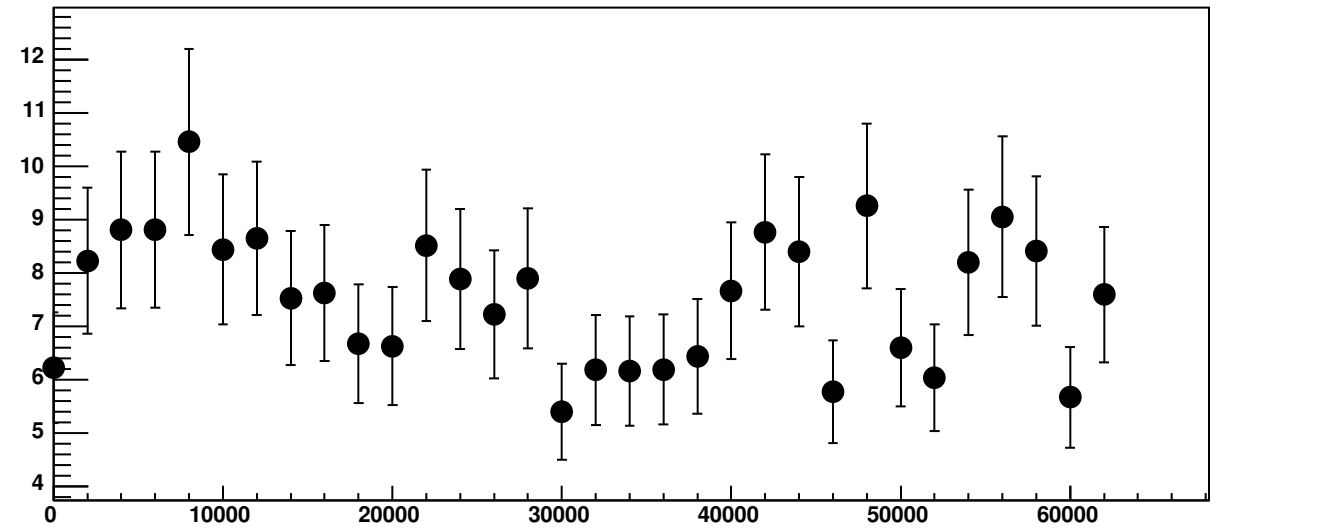

Chip 4, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

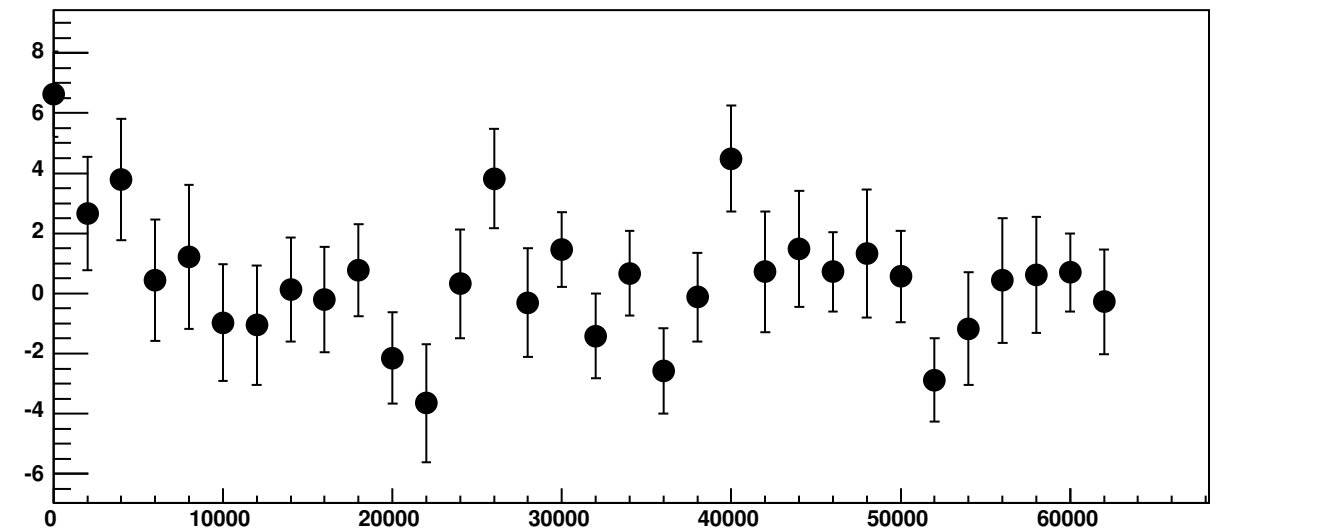
Chip 4, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC

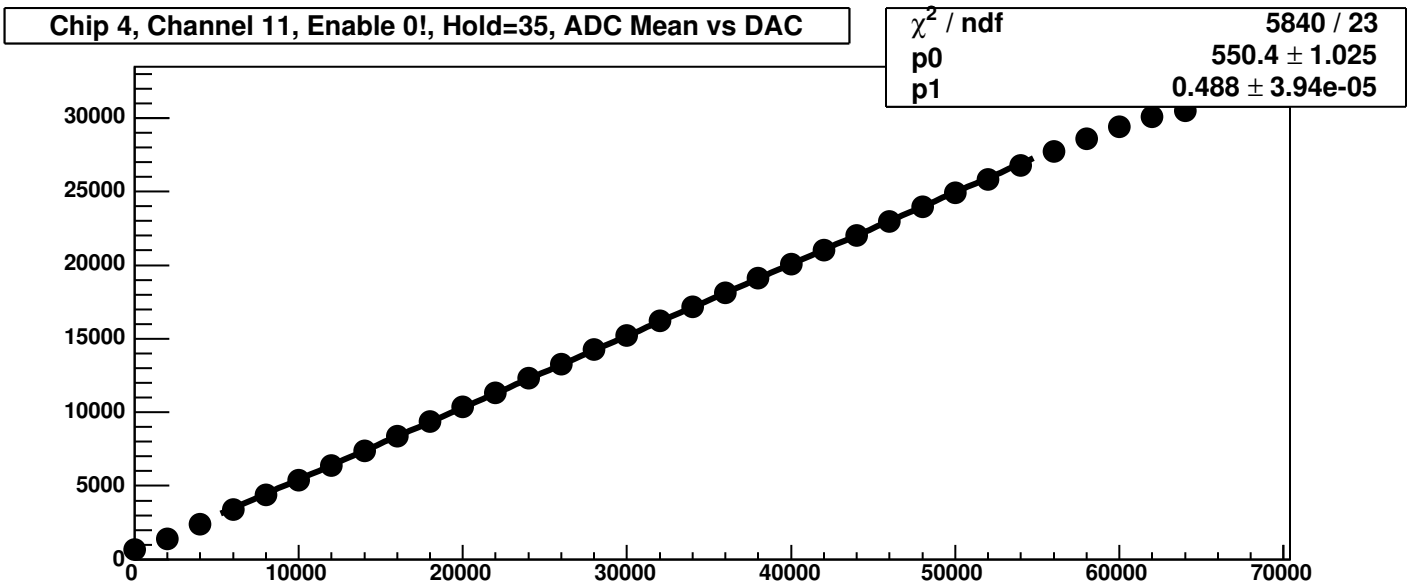


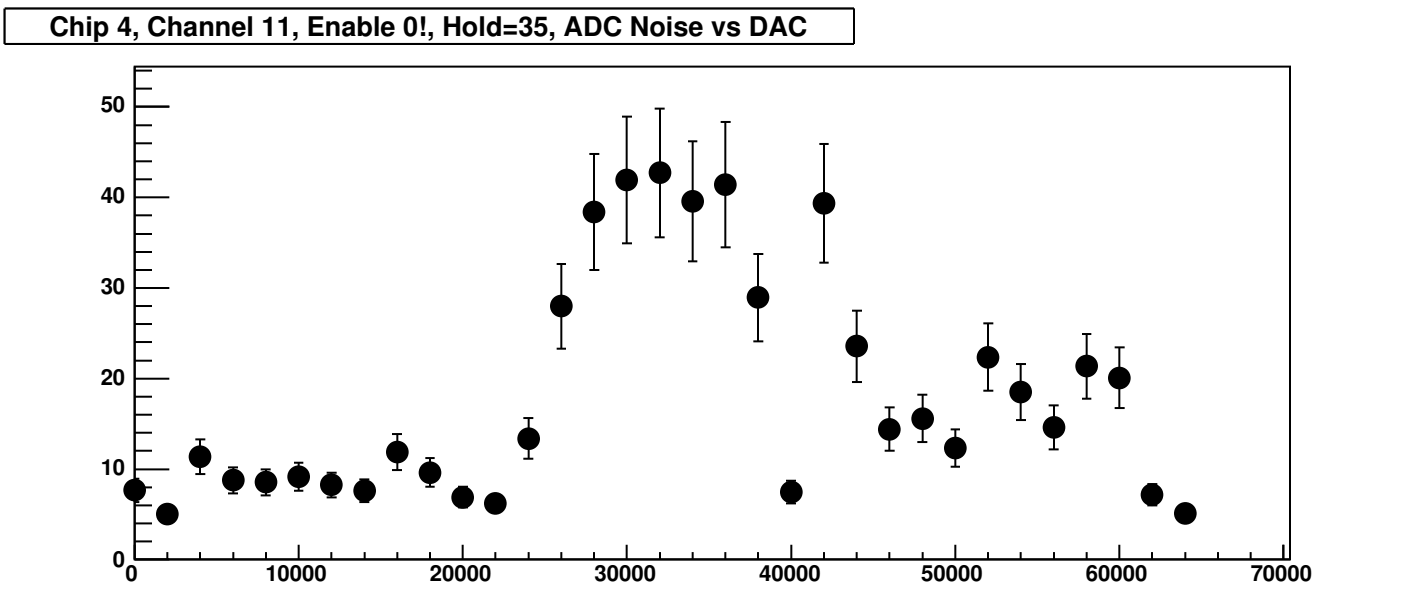
Chip 4, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC

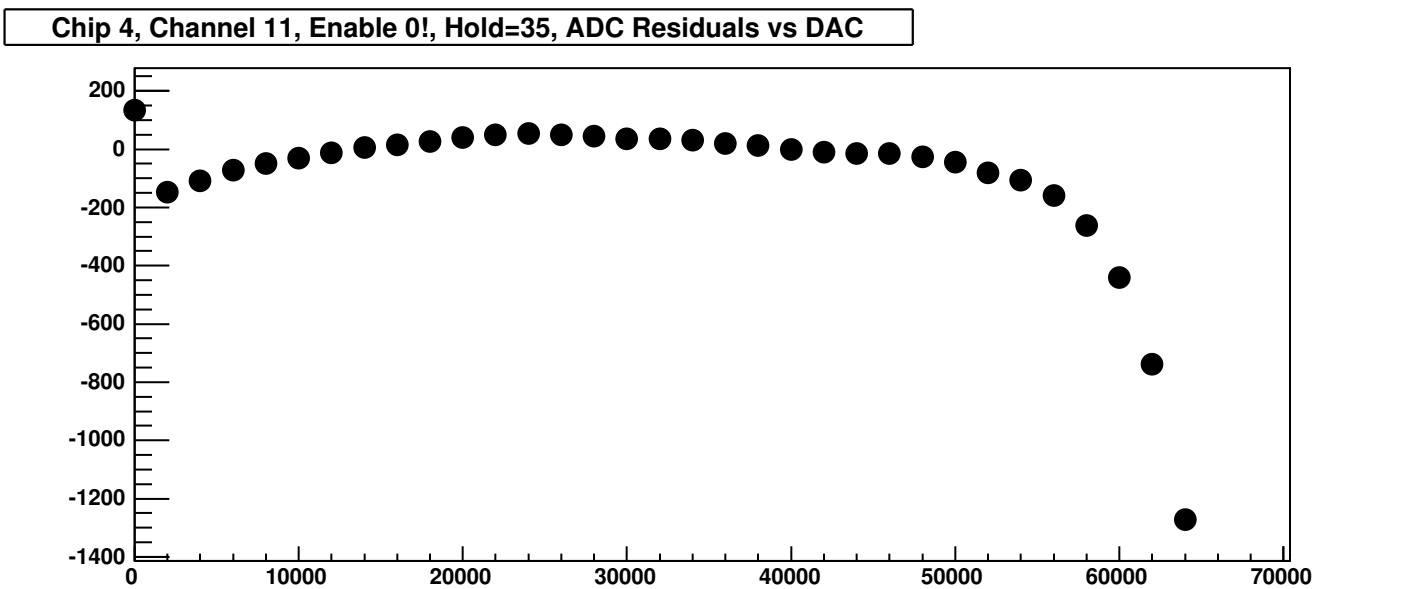


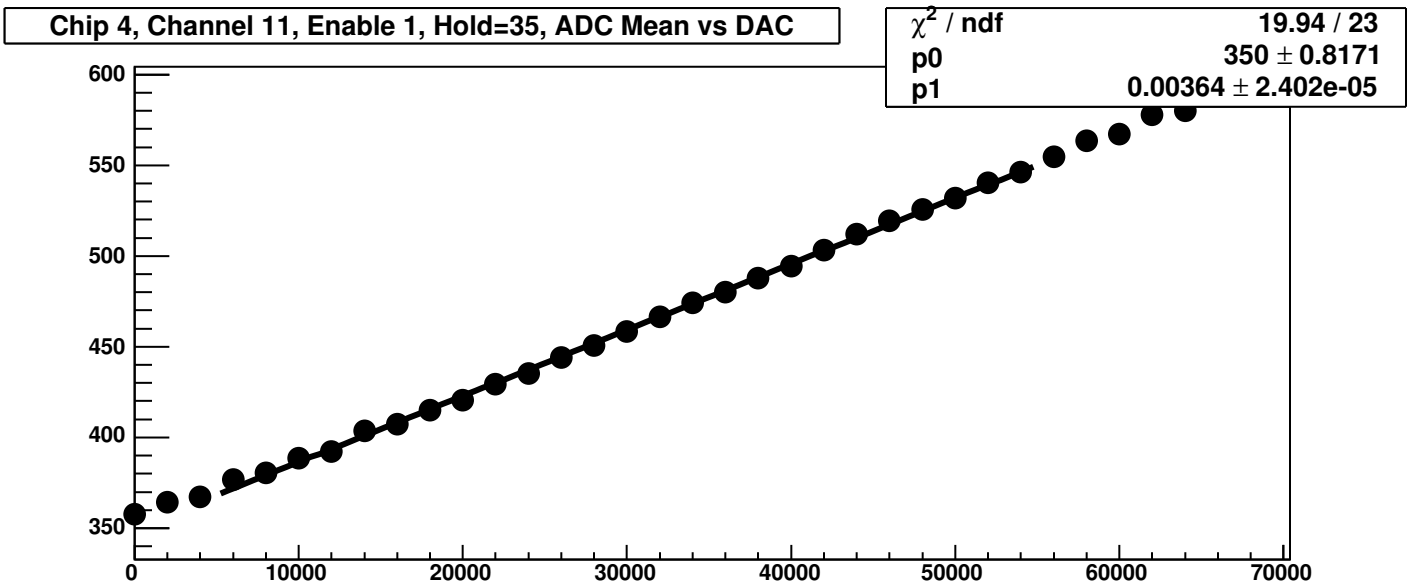
Chip 4, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC

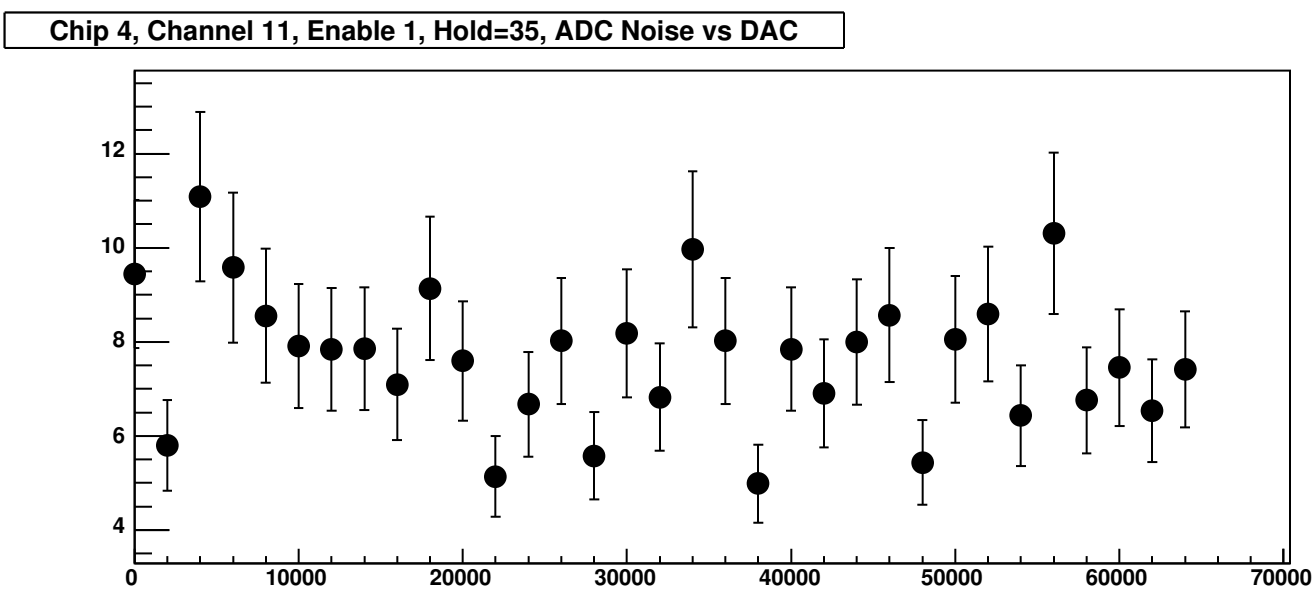


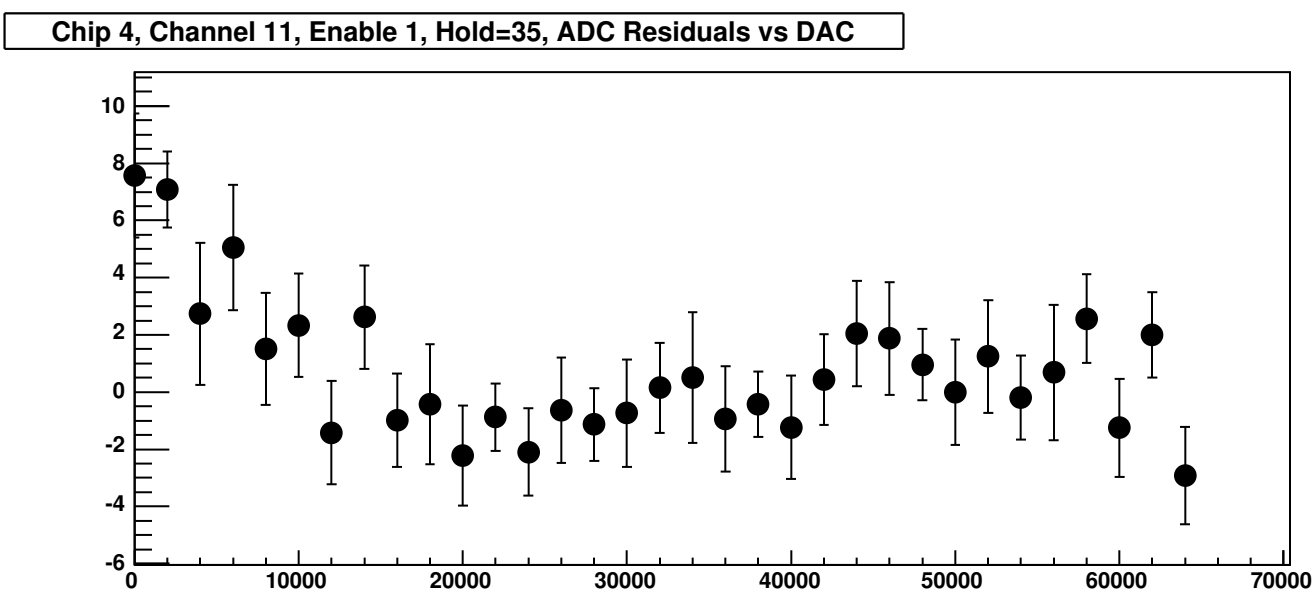

Chip 4, Channel 11, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 5840 / 23
p0 550.4 ± 1.025
p1 0.488 ± 3.94e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

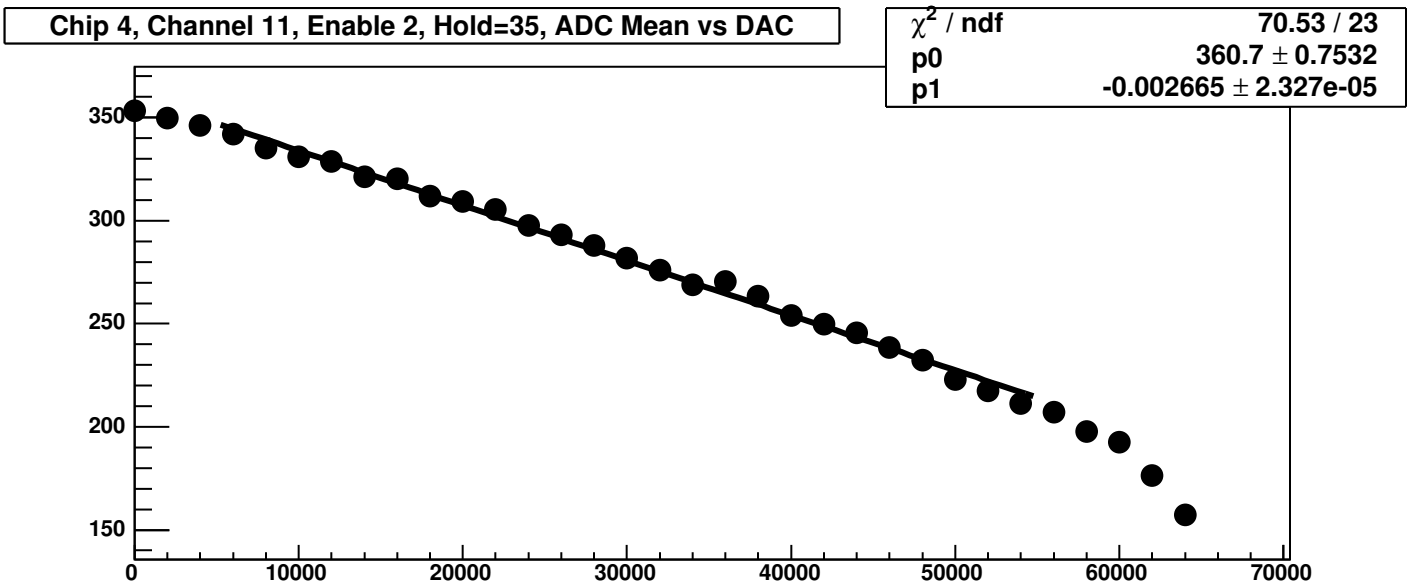

Chip 4, Channel 11, Enable 0!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

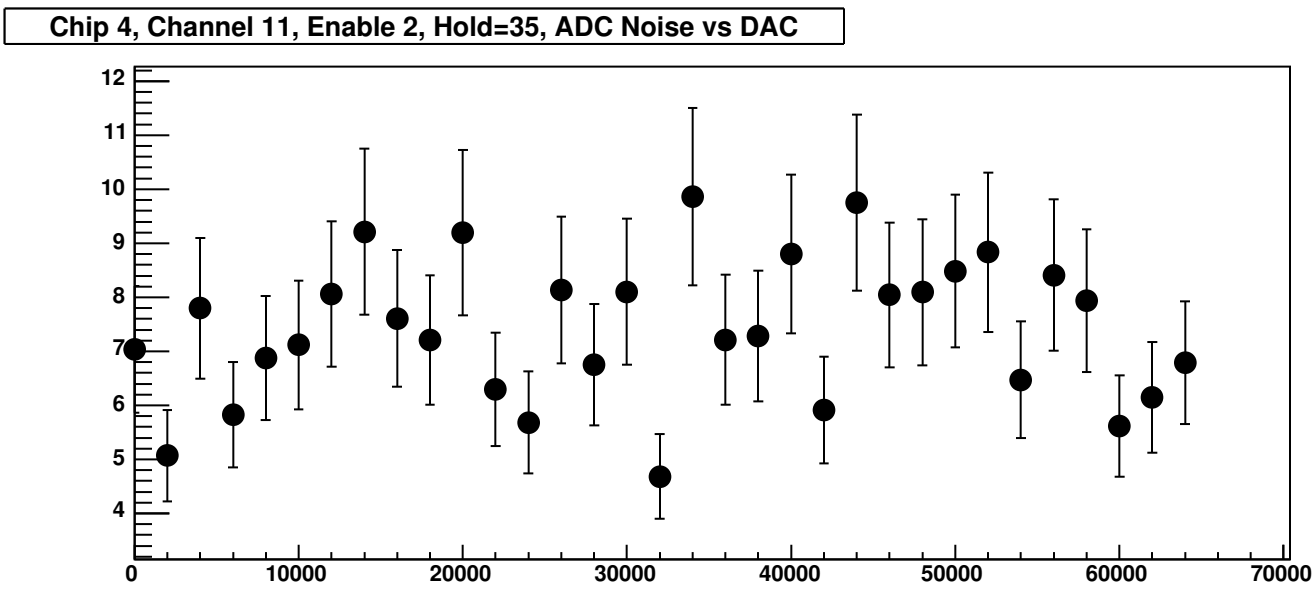

Chip 4, Channel 11, Enable 0!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
10000
20000
30000
40000
50000
60000
70000

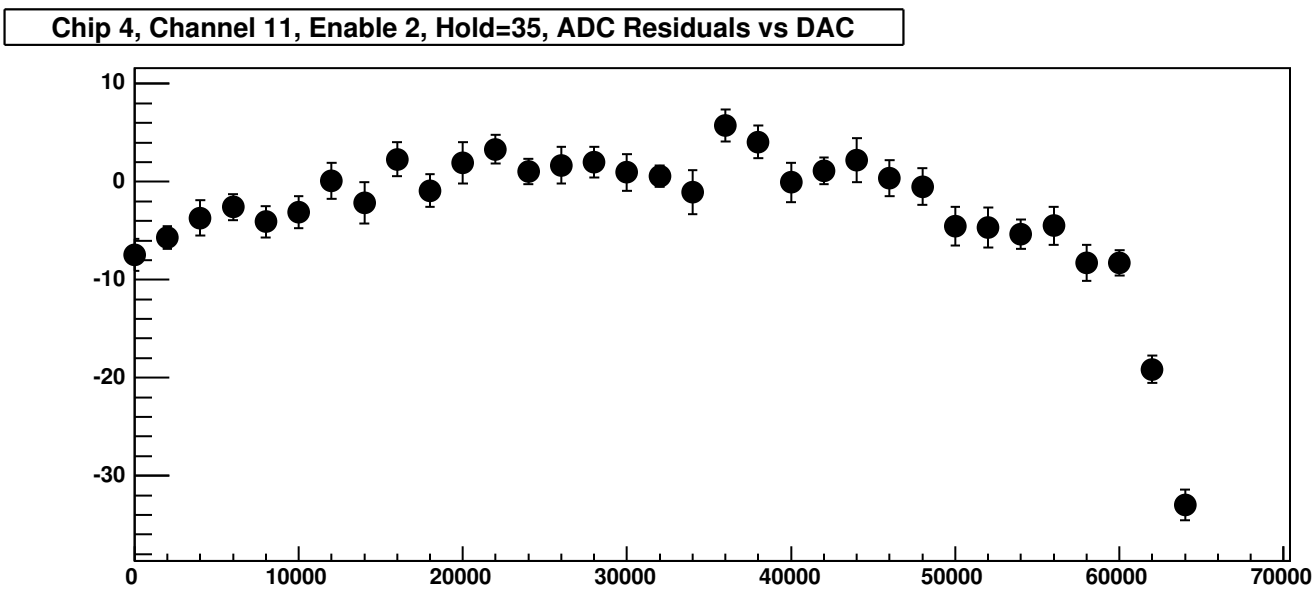

Chip 4, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 19.94 / 23
p0 350 ± 0.8171
p1 0.00364 ± 2.402e-05
600
550
500
450
400
350
0
10000
20000
30000
40000
50000
60000
70000

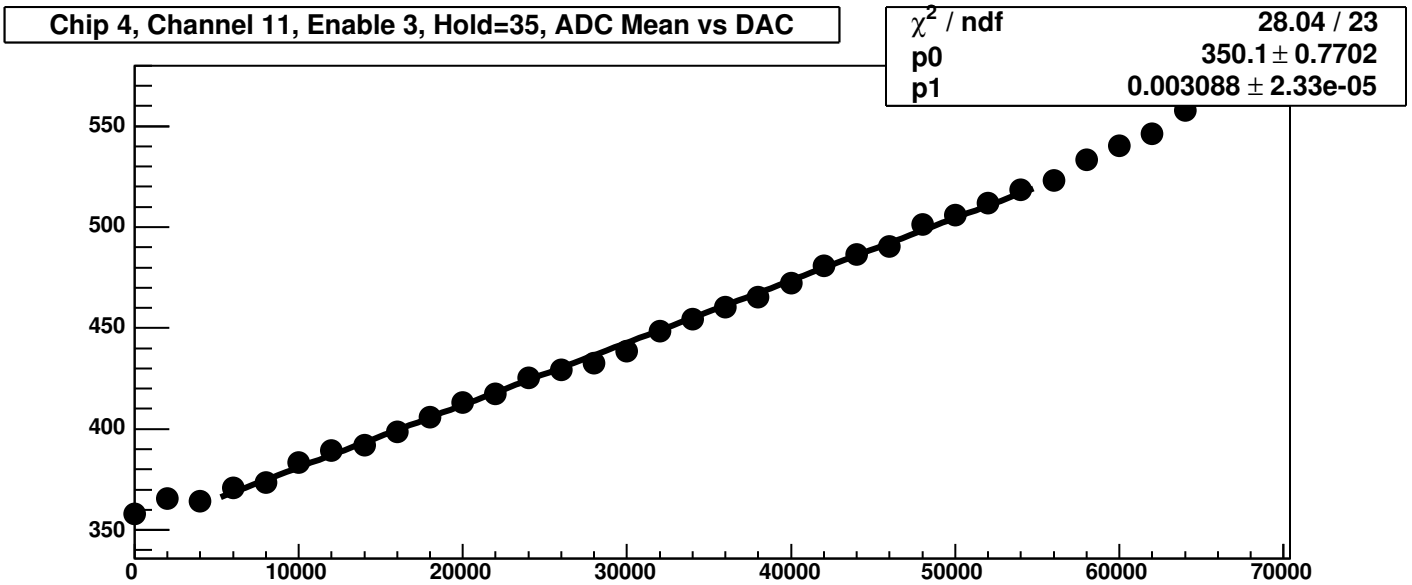

Chip 4, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

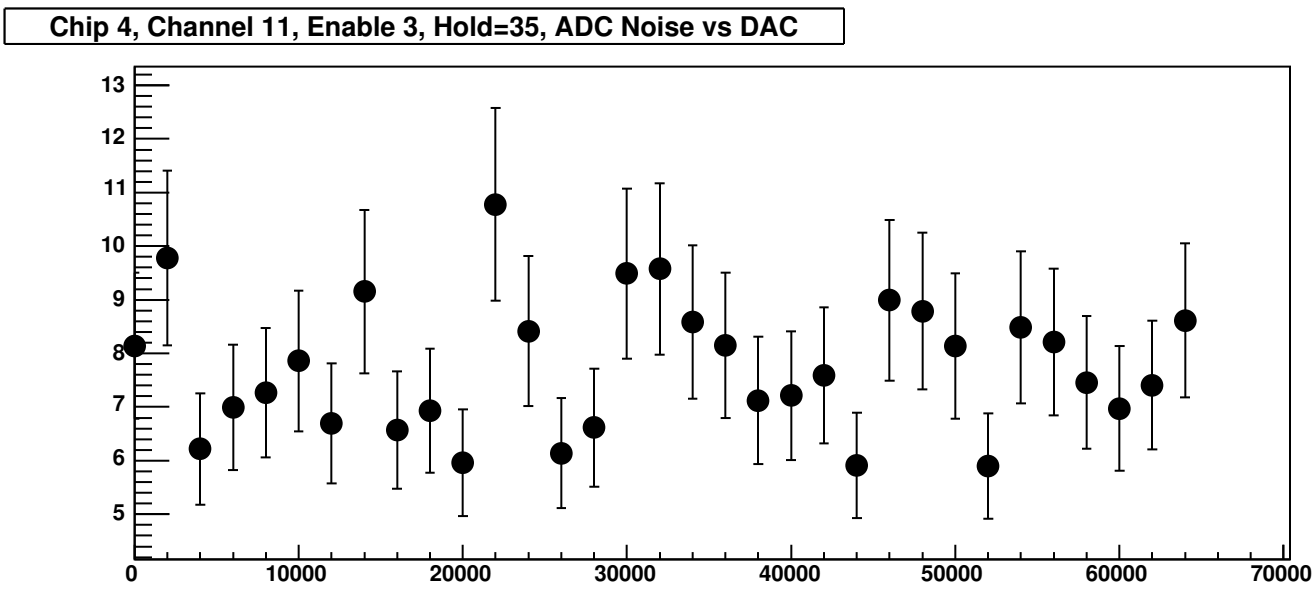

Chip 4, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

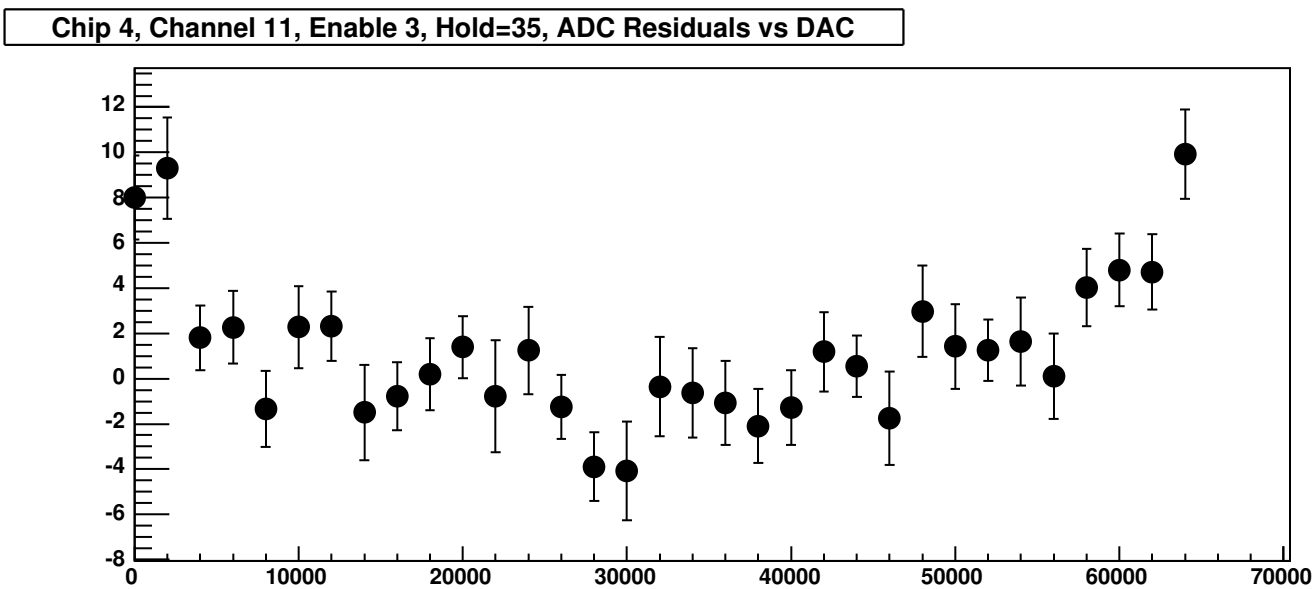

Chip 4, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 70.53 / 23
p0 360.7 ± 0.7532
p1 -0.002665 ± 2.327e-05
350
300
250
200
150
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

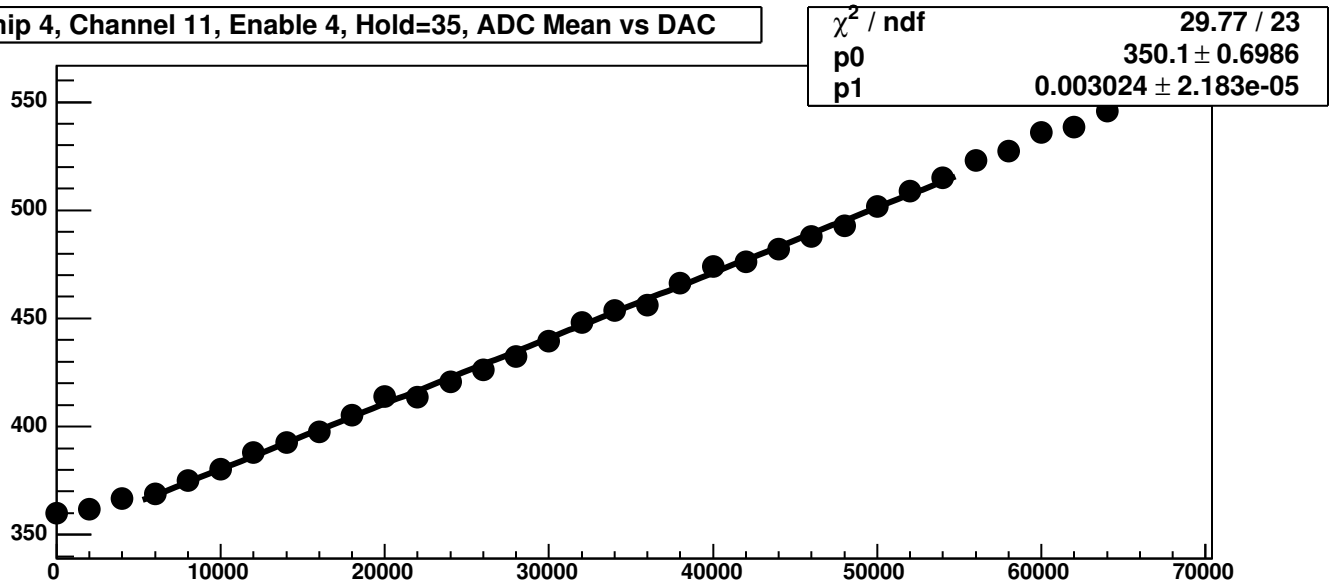

Chip 4, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 11, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 28.04 / 23
p0 350.1 ± 0.7702
p1 0.003088 ± 2.33e-05
550
500
450
400
350
0
10000
20000
30000
40000
50000
60000
70000

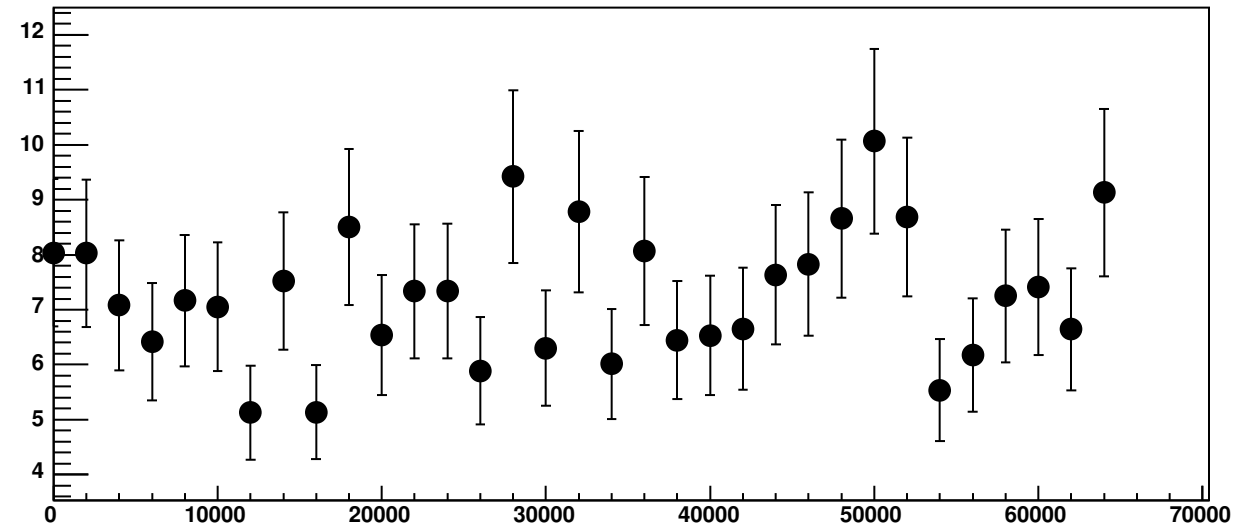

Chip 4, Channel 11, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 11, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

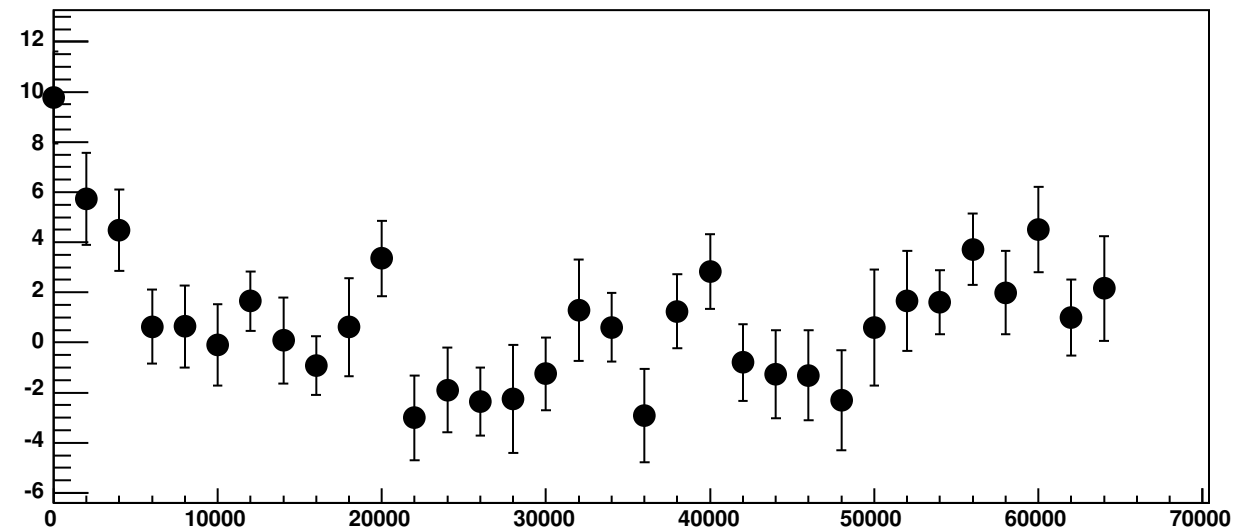
Chip 4, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



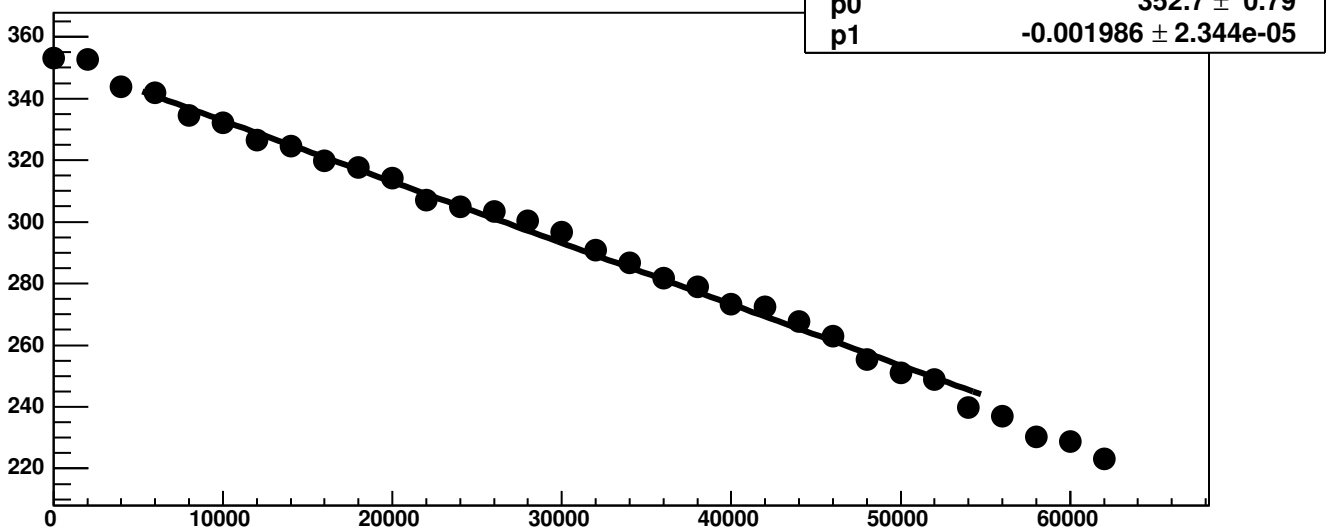
Chip 4, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



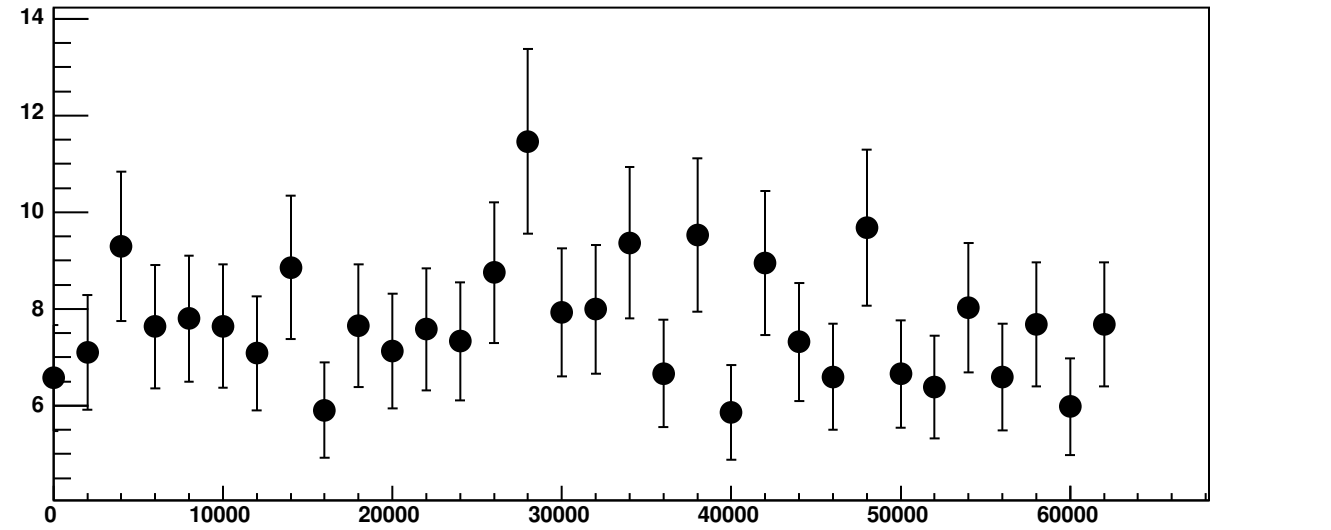
Chip 4, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



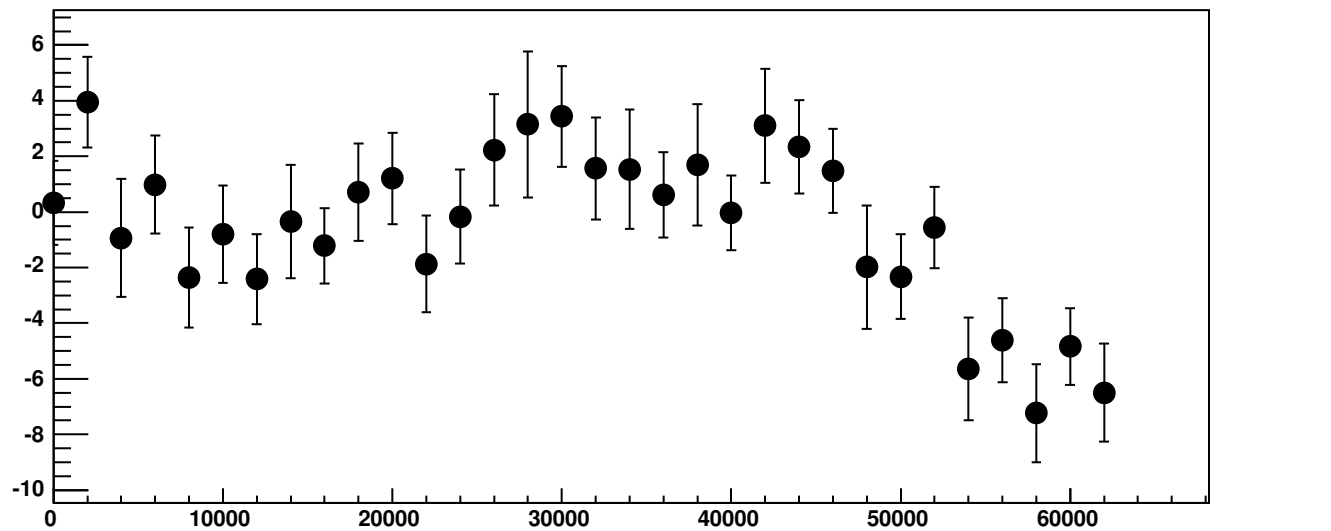
Chip 4, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

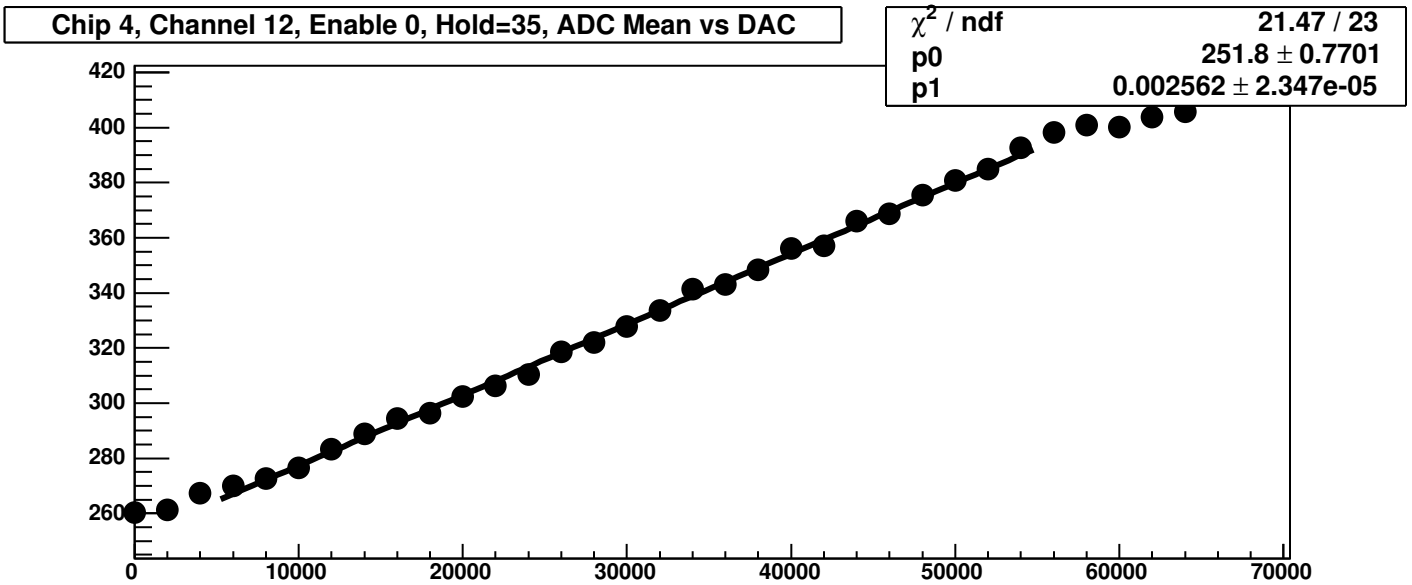


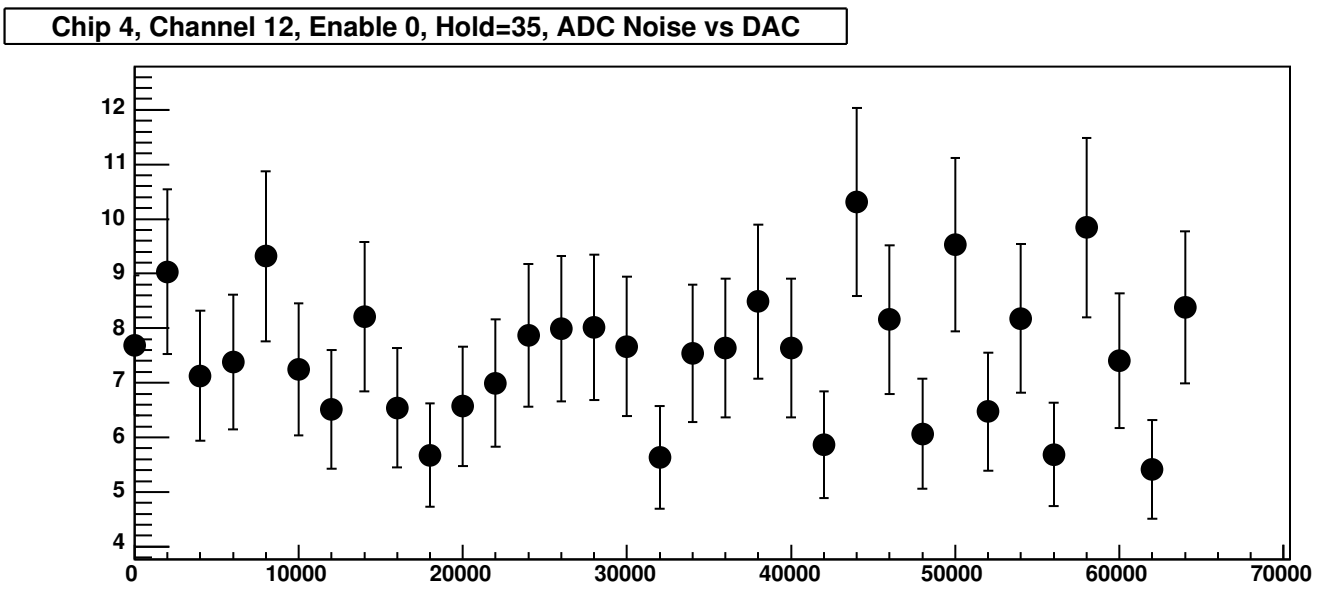
Chip 4, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

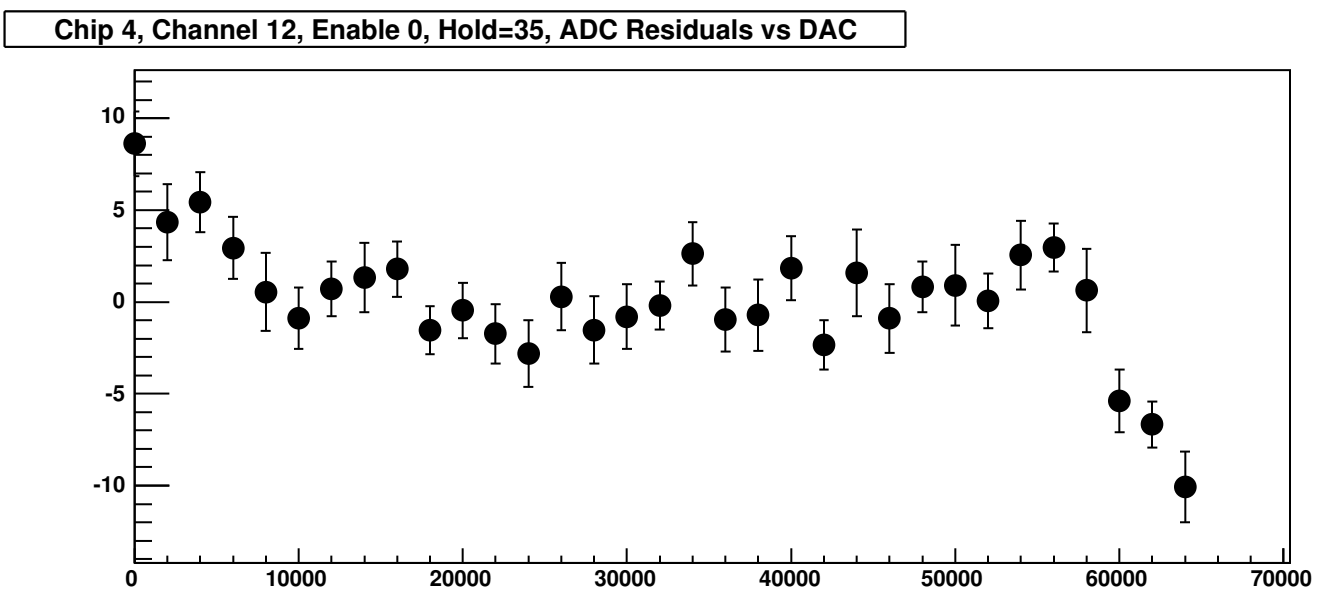


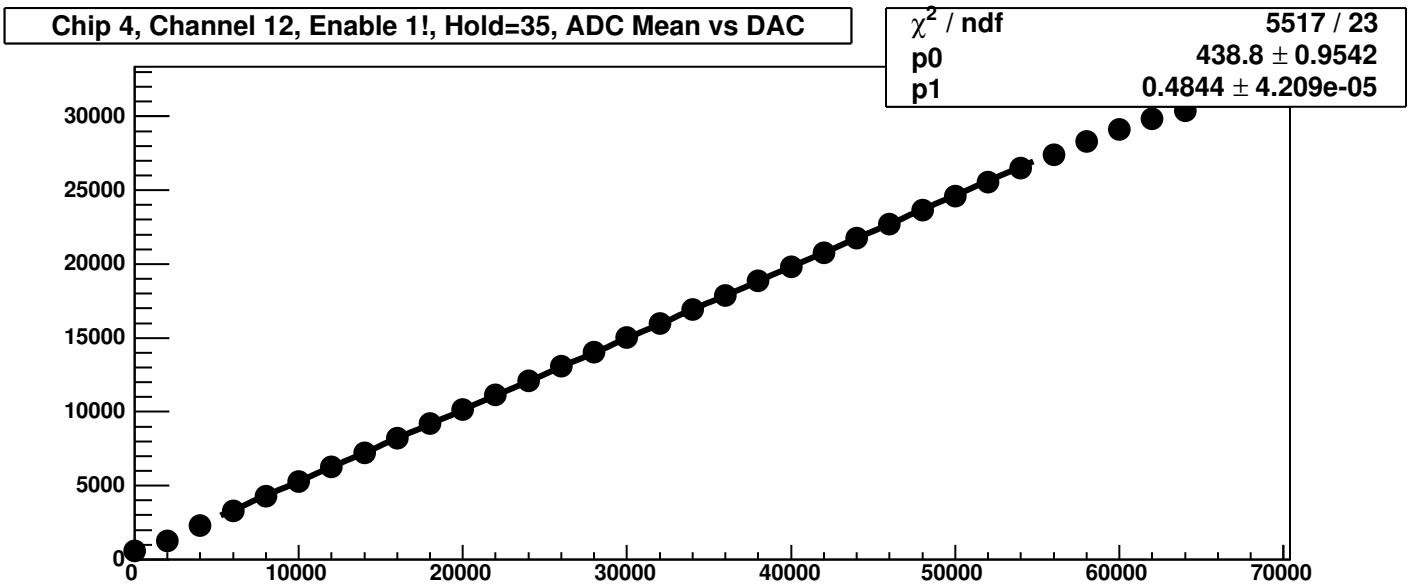
Chip 4, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

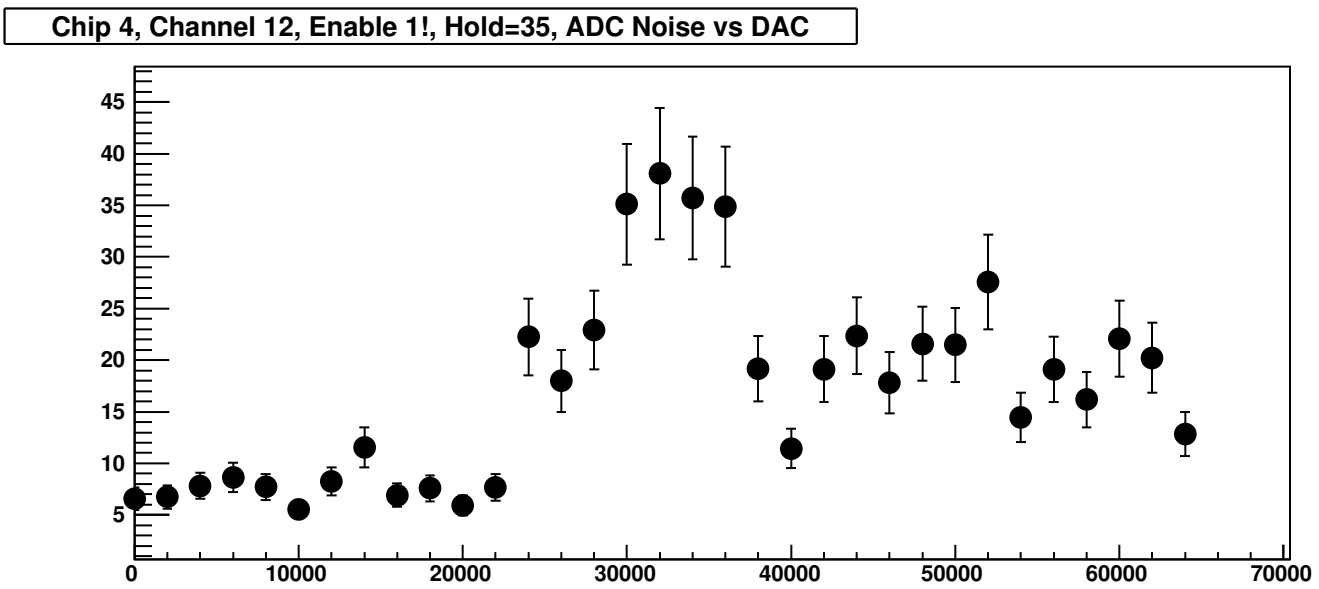


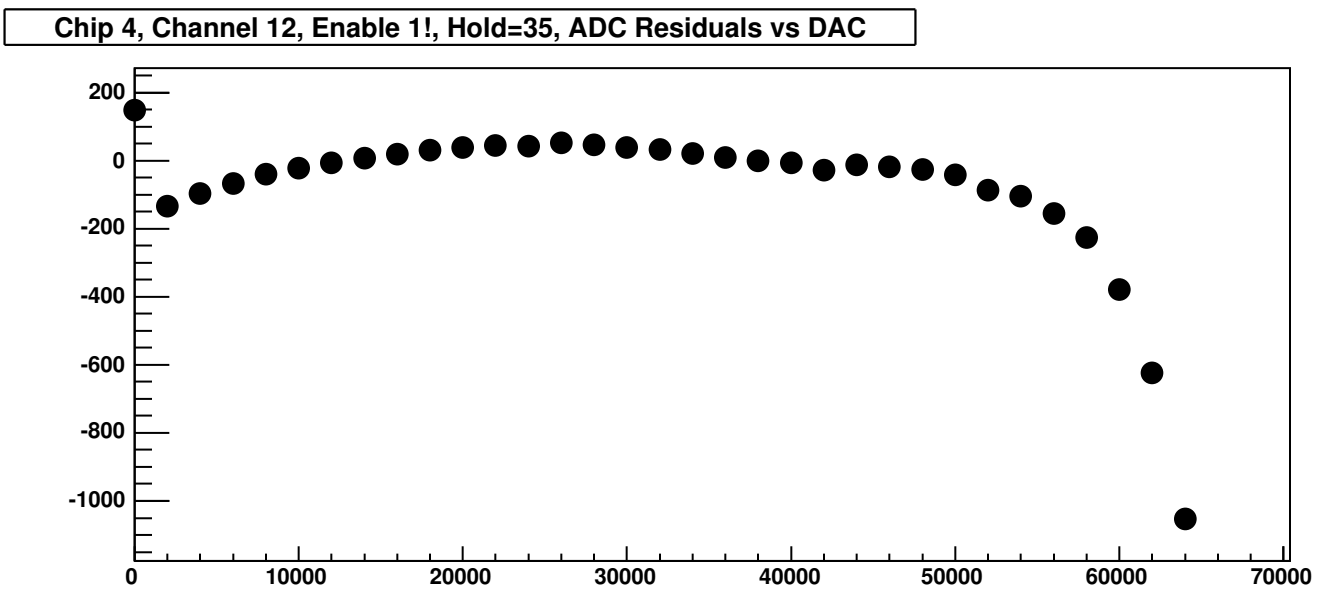

Chip 4, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 21.47 / 23
p0 251.8 ± 0.7701
p1 0.002562 ± 2.347e-05
420
400
380
360
340
320
300
280
260
0
10000
20000
30000
40000
50000
60000
70000

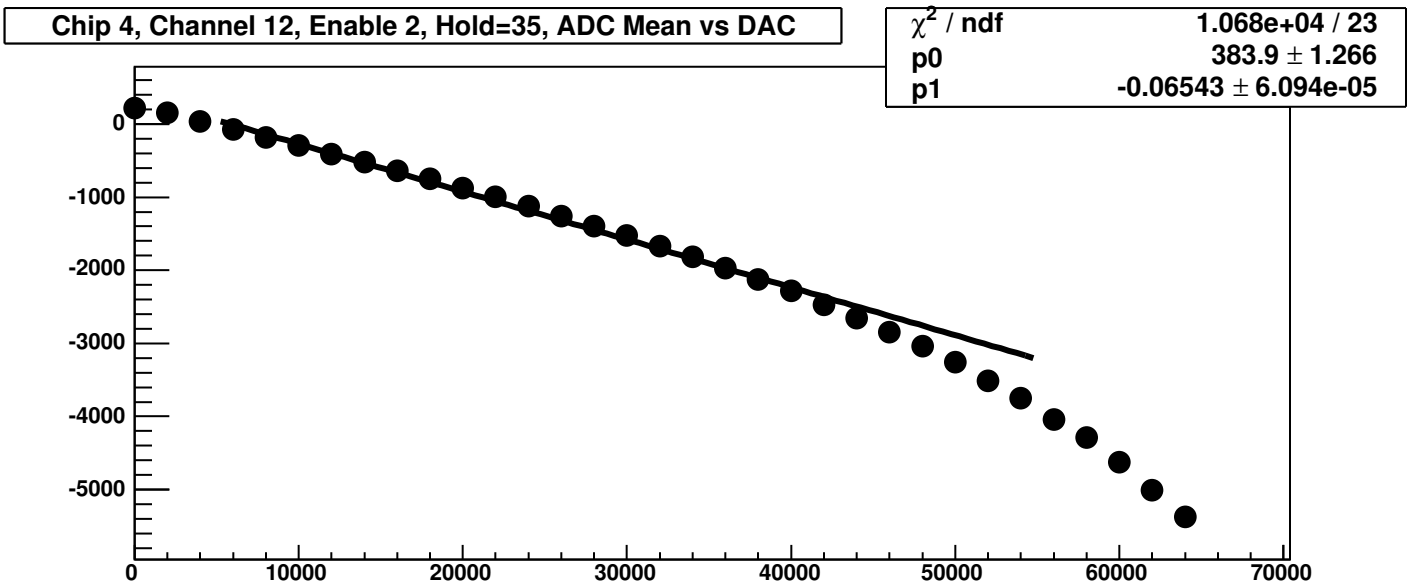

Chip 4, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

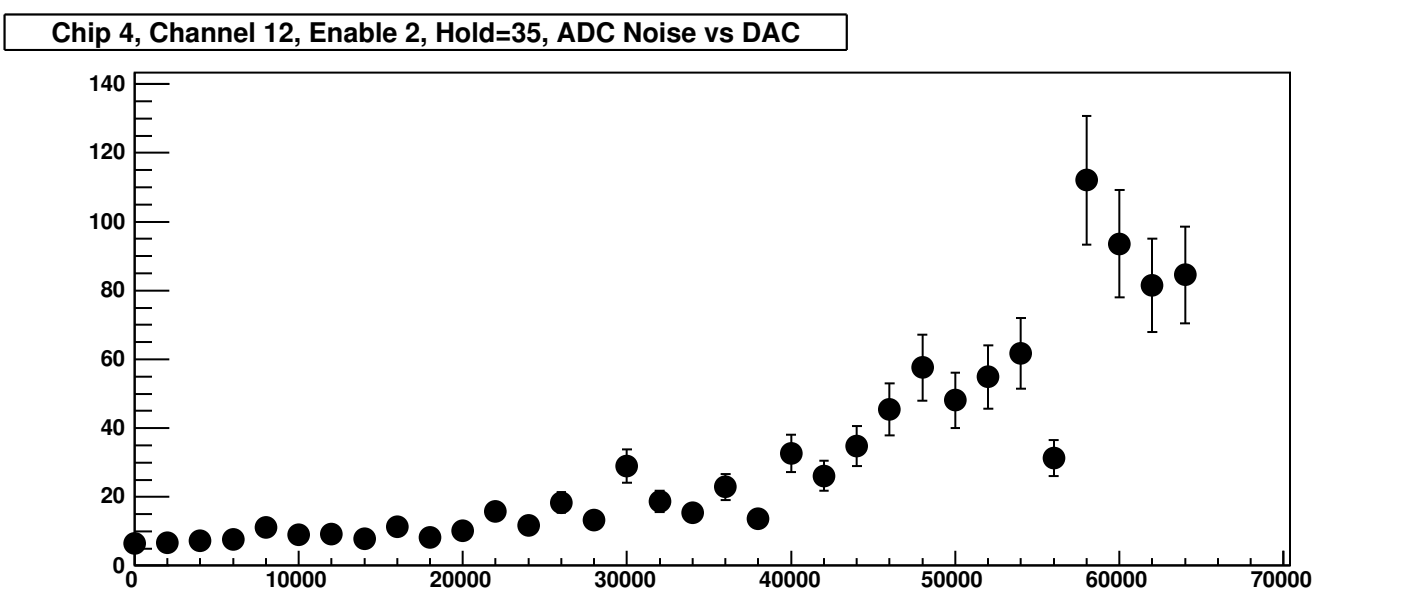

Chip 4, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

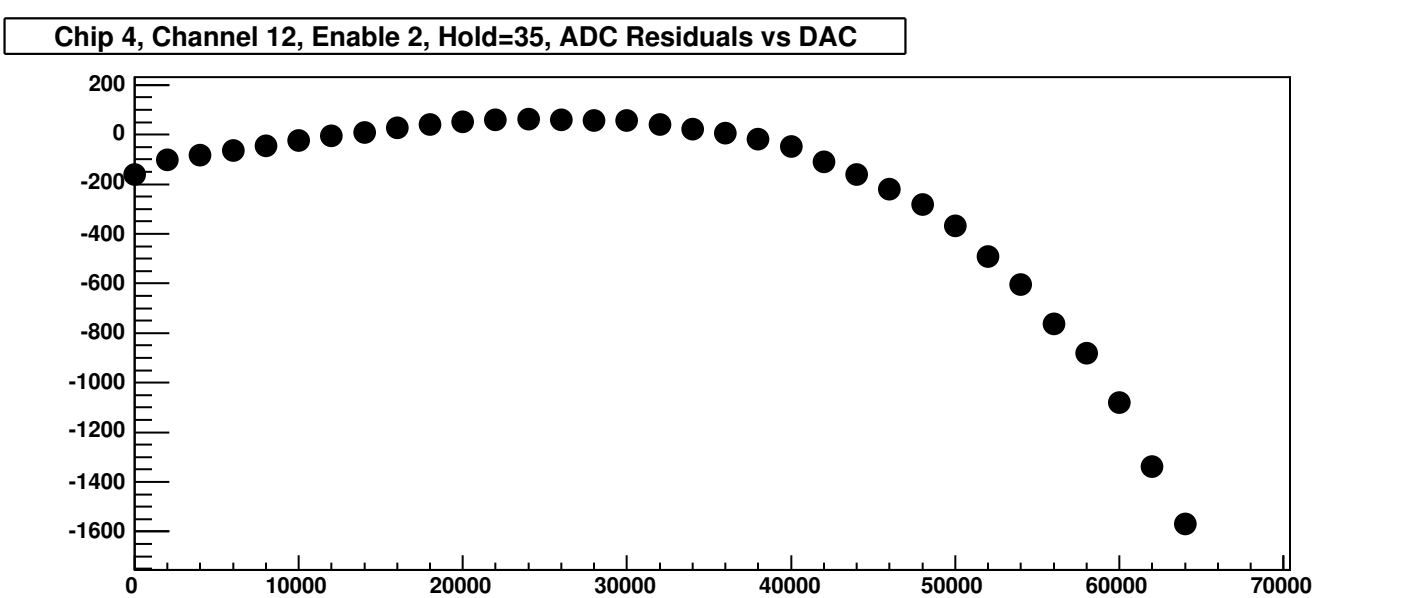

Chip 4, Channel 12, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 5517 / 23
p0 438.8 ± 0.9542
p1 0.4844 ± 4.209e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

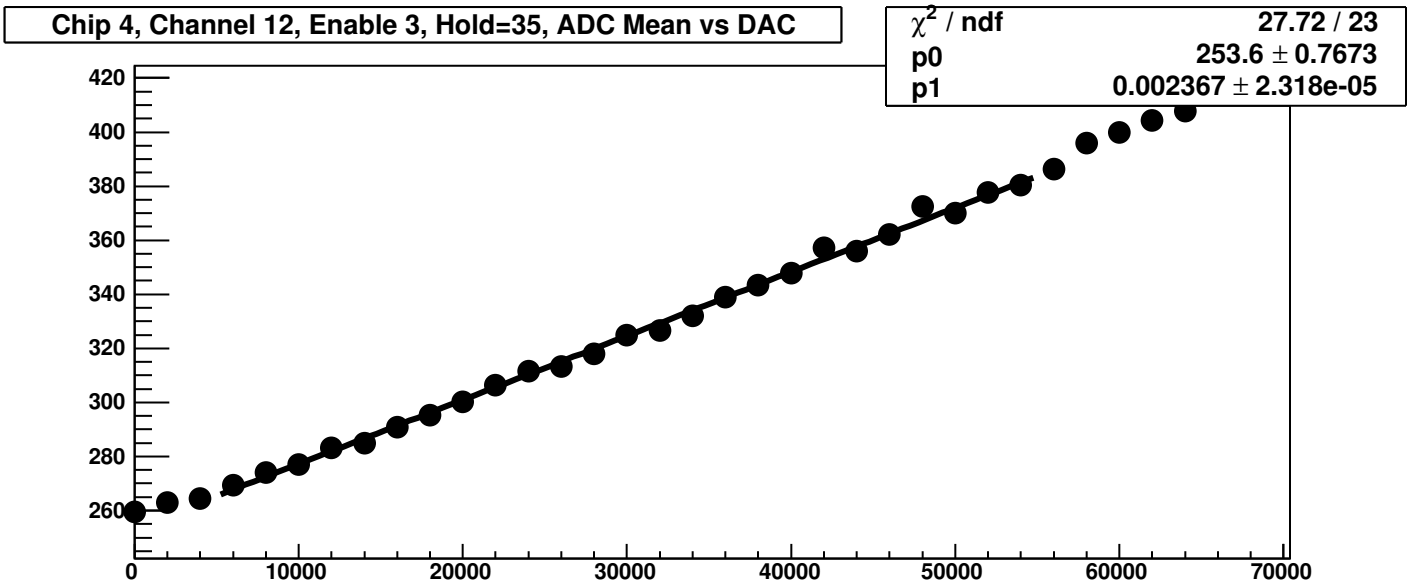

Chip 4, Channel 12, Enable 1!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000

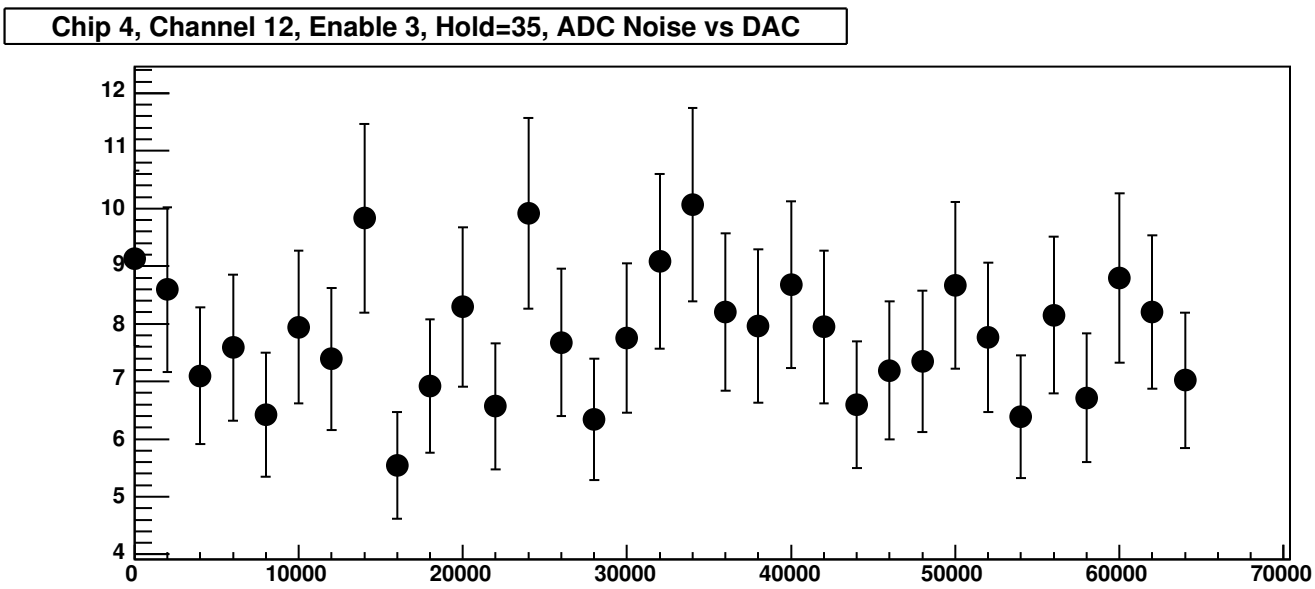

Chip 4, Channel 12, Enable 1!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
0
10000
20000
30000
40000
50000
60000
70000

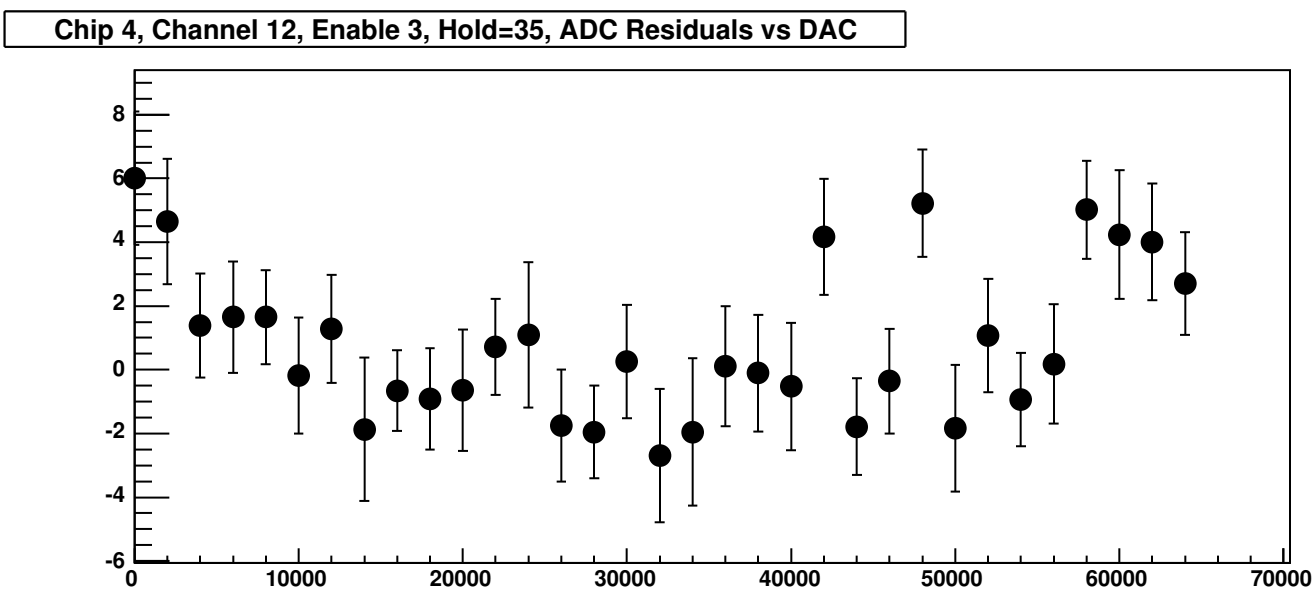

Chip 4, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 1.068e+04 / 23
p0 383.9 ± 1.266
p1 -0.06543 ± 6.094e-05
0
-1000
-2000
-3000
-4000
-5000
0
10000
20000
30000
40000
50000
60000
70000

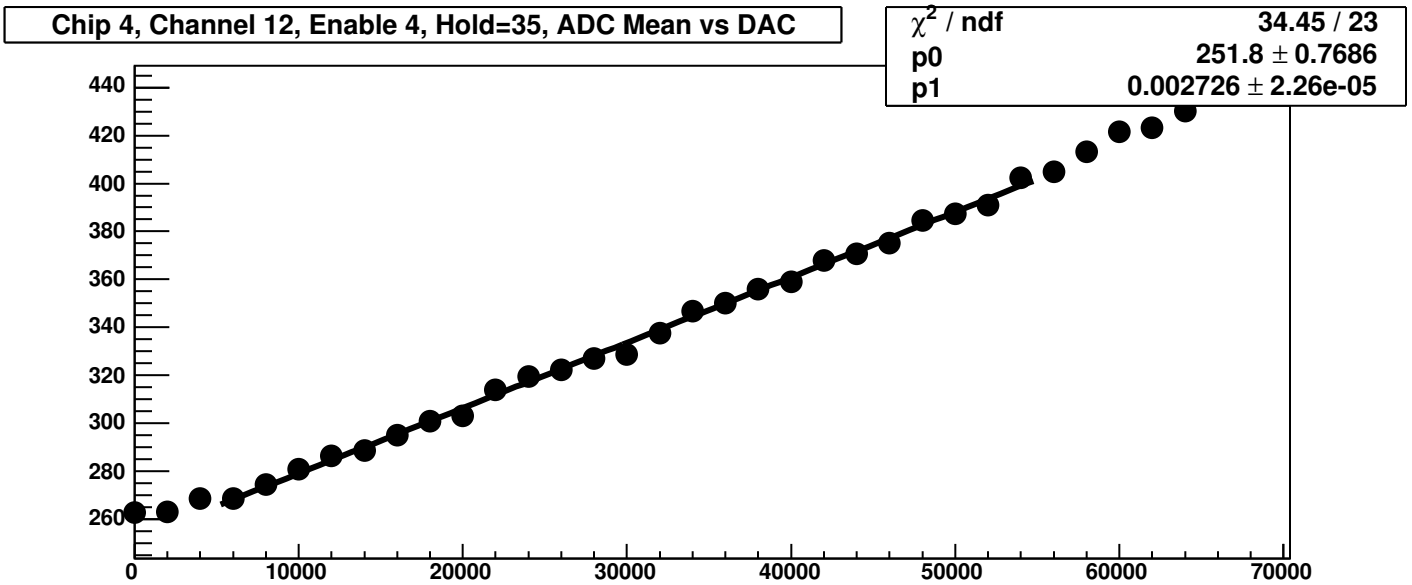

Chip 4, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000
70000

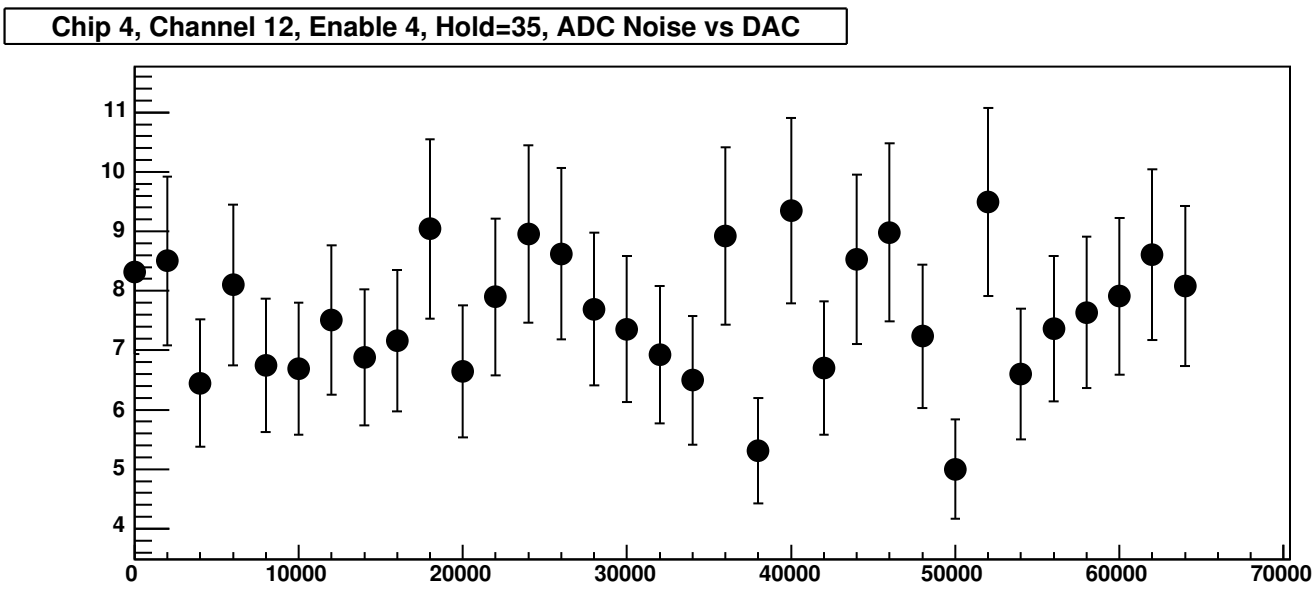

Chip 4, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
0
10000
20000
30000
40000
50000
60000
70000

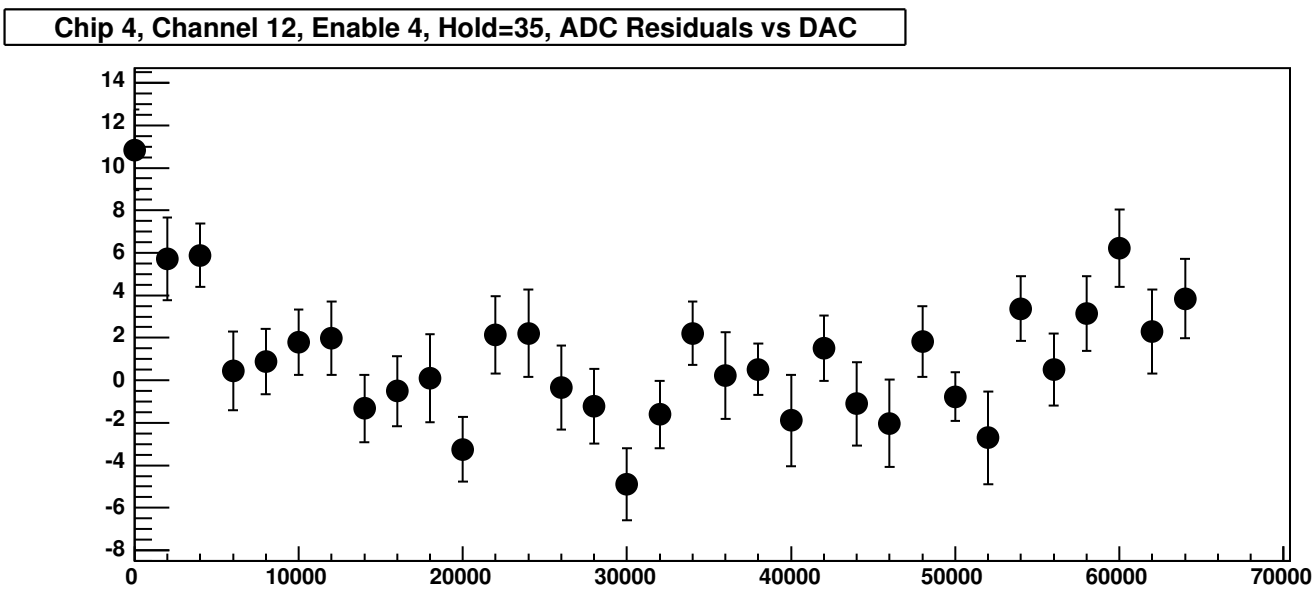

Chip 4, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 27.72 / 23
p0 253.6 ± 0.7673
p1 0.002367 ± 2.318e-05
420
400
380
360
340
320
300
280
260
0
10000
20000
30000
40000
50000
60000
70000

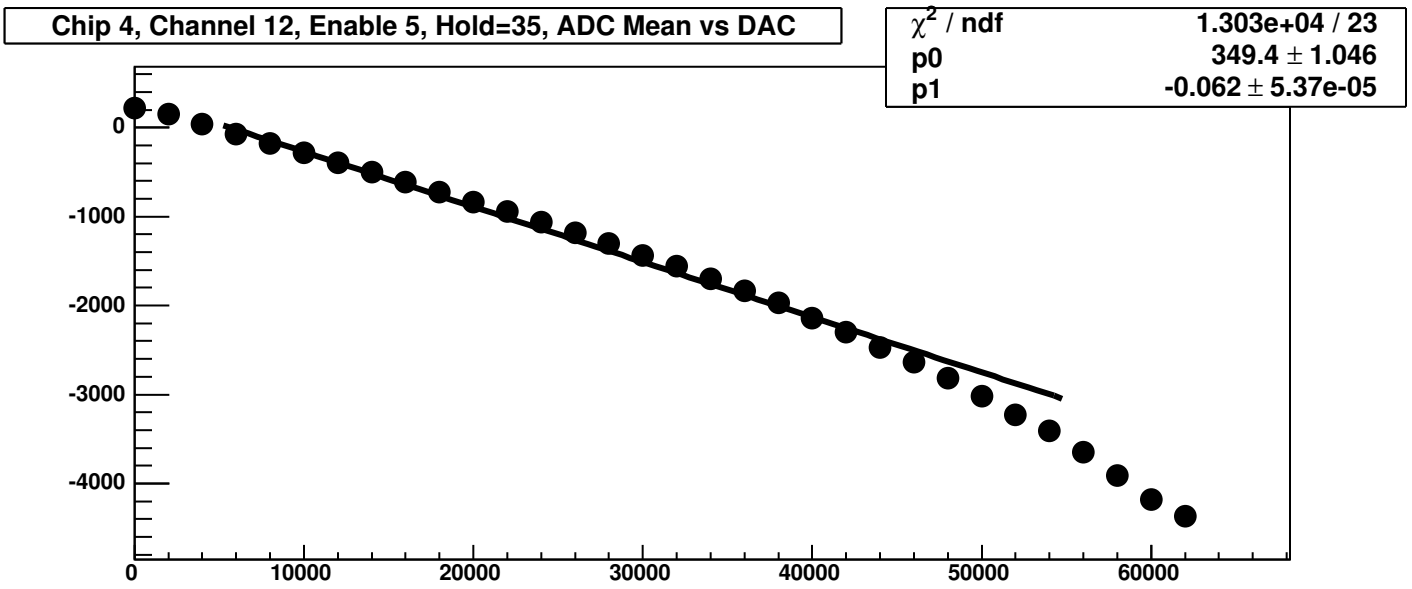

Chip 4, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

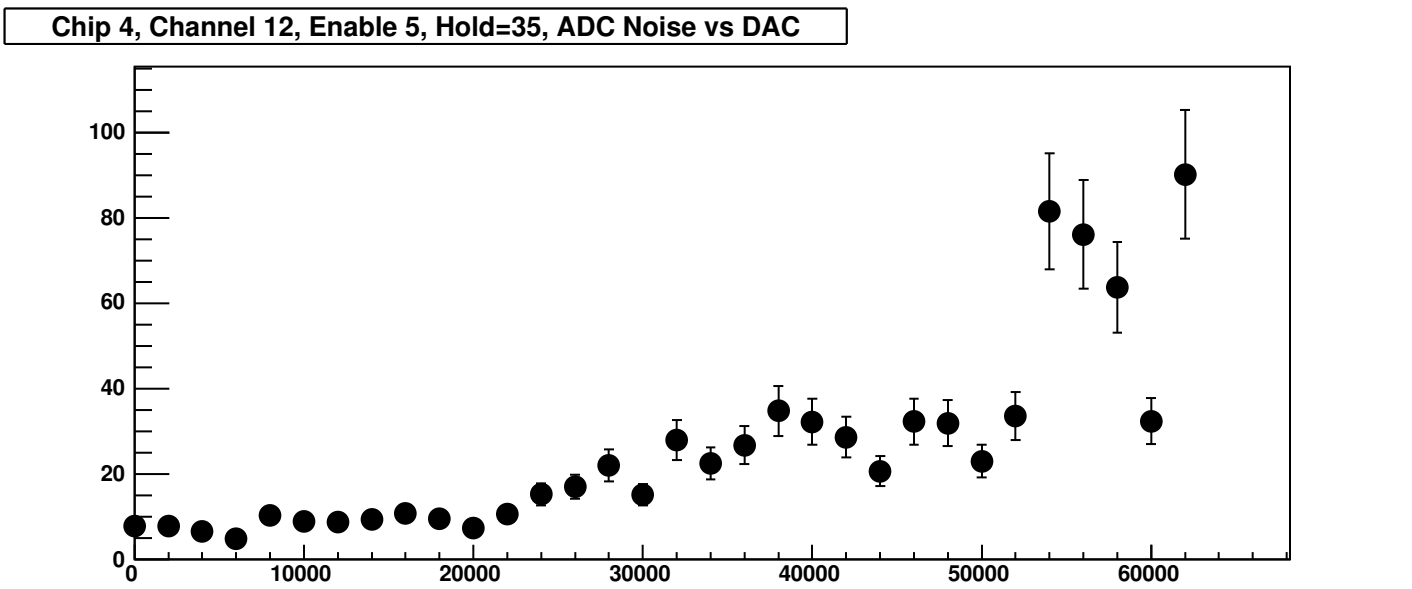

Chip 4, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

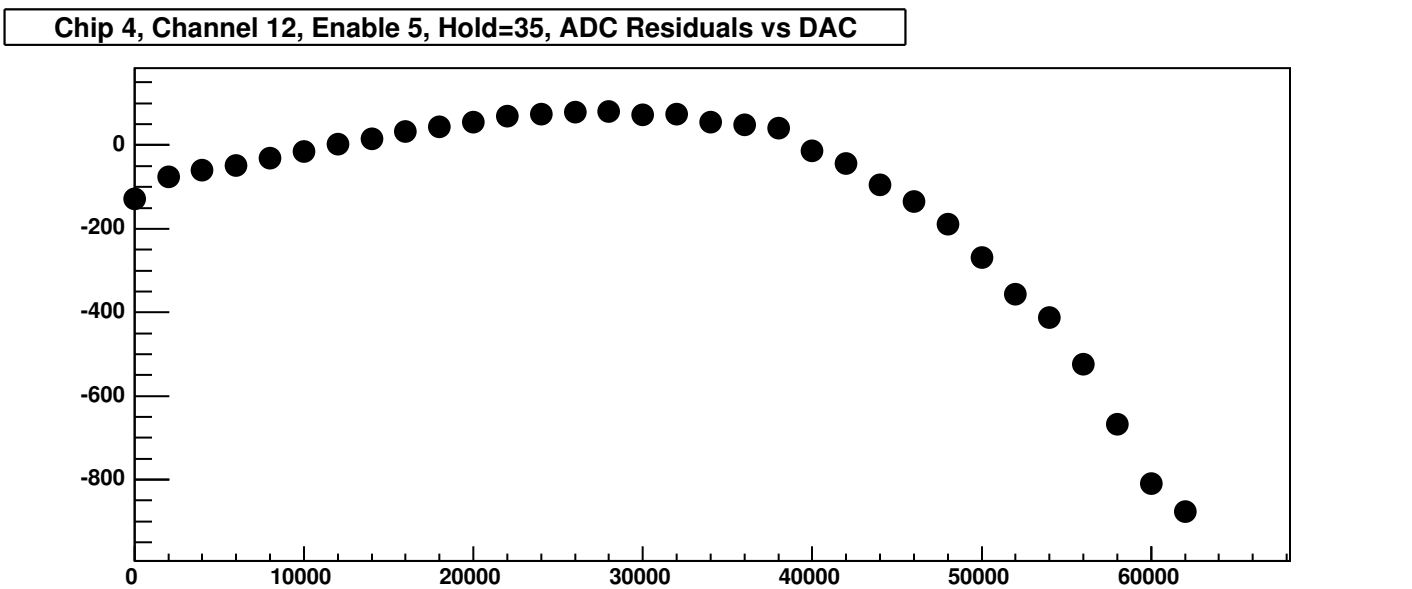

Chip 4, Channel 12, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 34.45 / 23
p0 251.8 ± 0.7686
p1 0.002726 ± 2.26e-05
440
420
400
380
360
340
320
300
280
260
0
10000
20000
30000
40000
50000
60000
70000

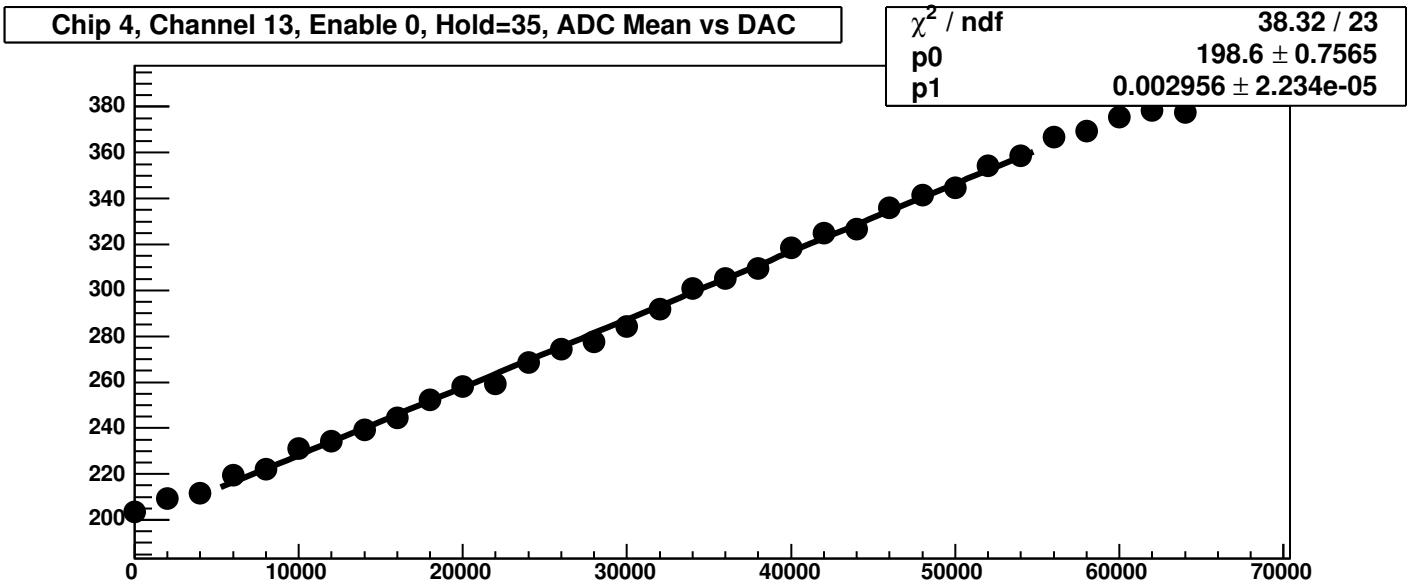

Chip 4, Channel 12, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

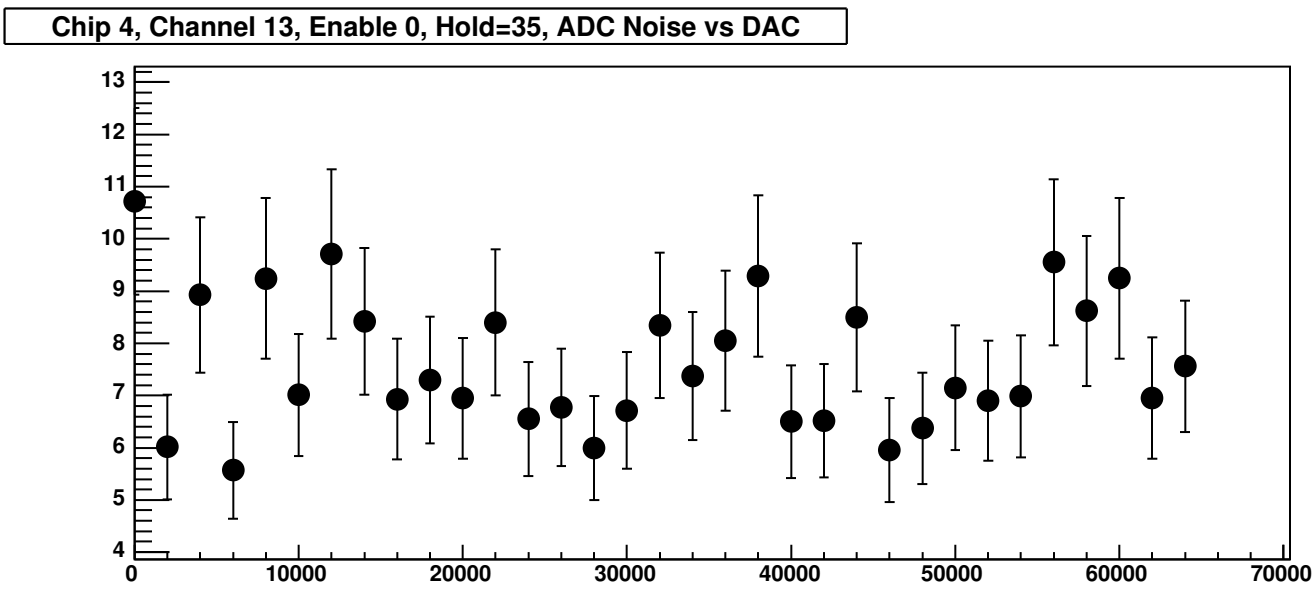

Chip 4, Channel 12, Enable 4, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

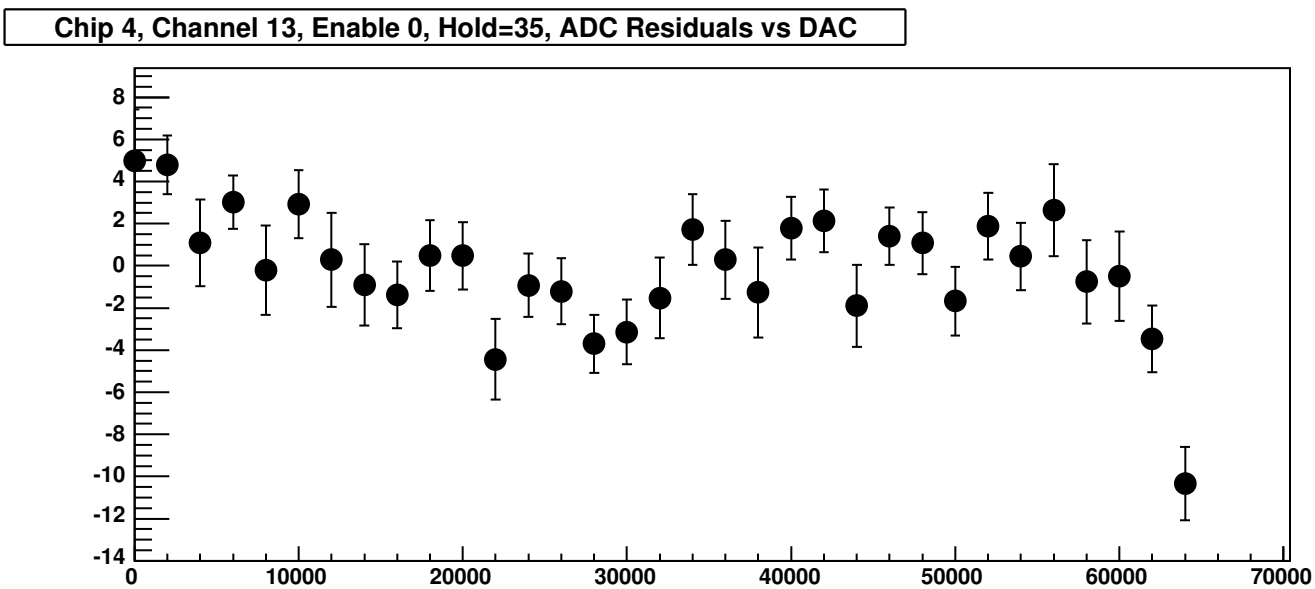

Chip 4, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 1.303e+04 / 23
p0 349.4 ± 1.046
p1 -0.062 ± 5.37e-05
0
-1000
-2000
-3000
-4000
0
10000
20000
30000
40000
50000
60000

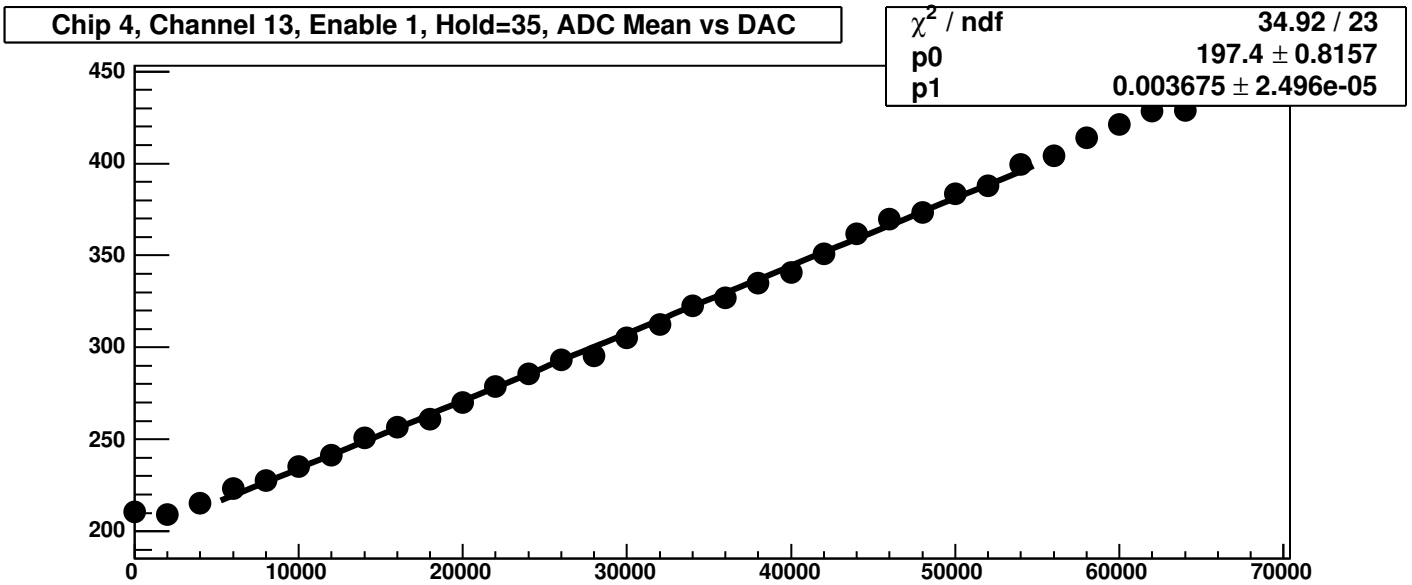

Chip 4, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000

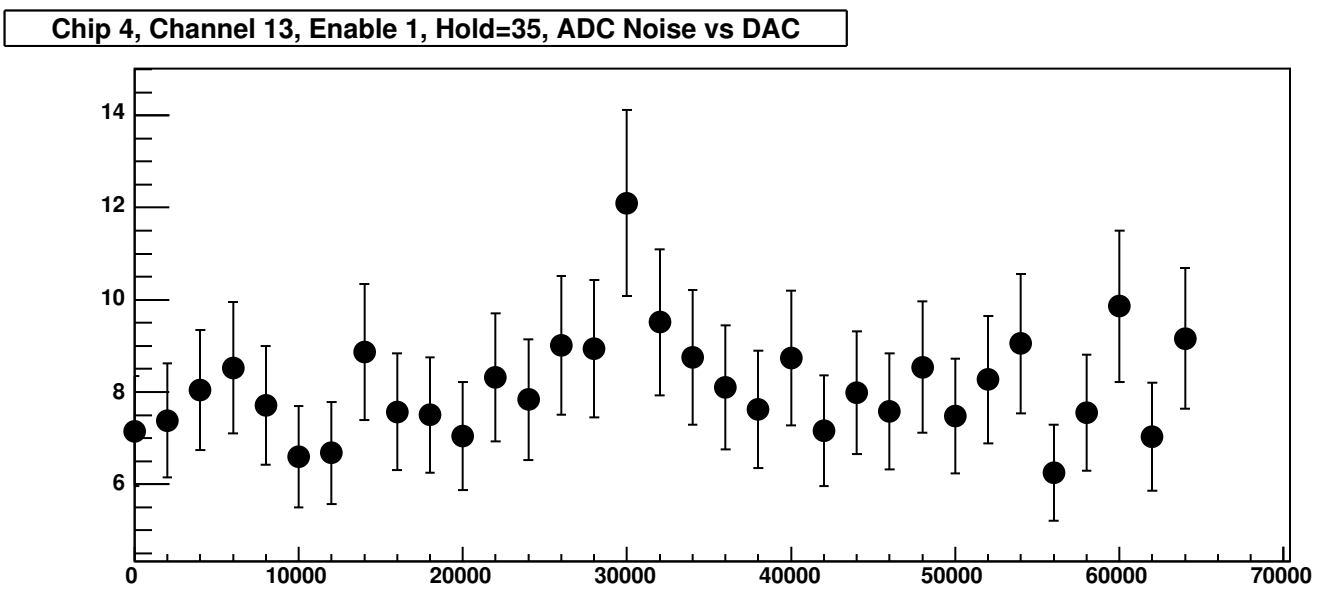

Chip 4, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
0
-200
-400
-600
-800
0
10000
20000
30000
40000
50000
60000

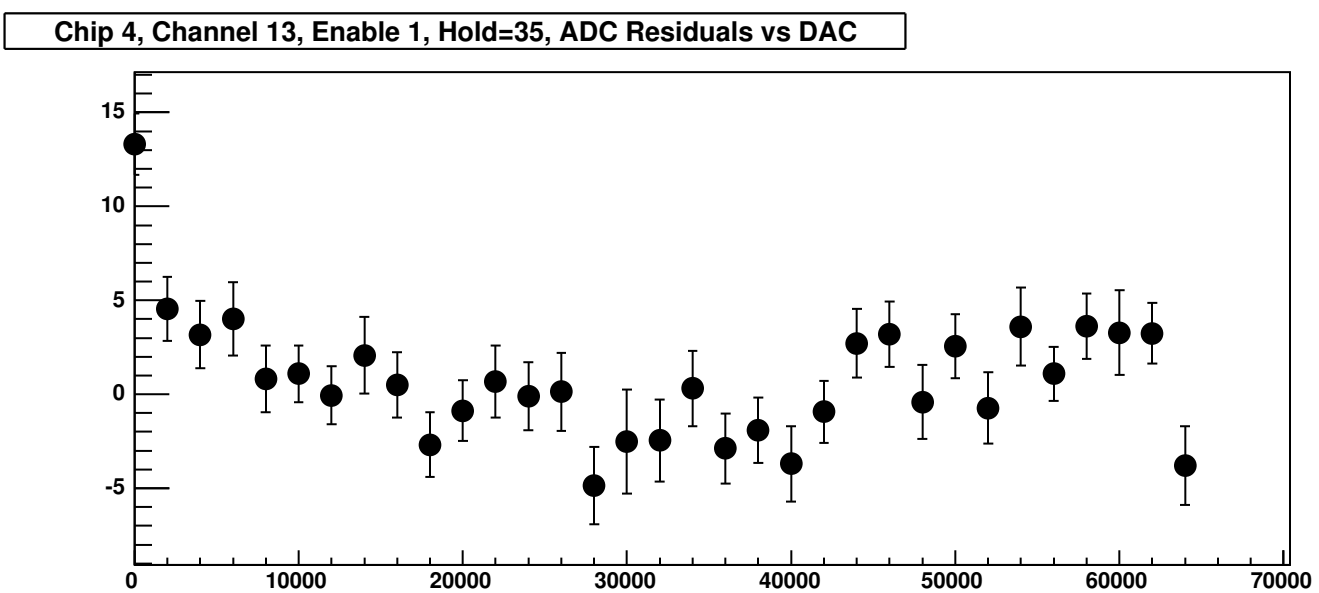

Chip 4, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 38.32 / 23
p0 198.6 ± 0.7565
p1 0.002956 ± 2.234e-05
380
360
340
320
300
280
260
240
220
200
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

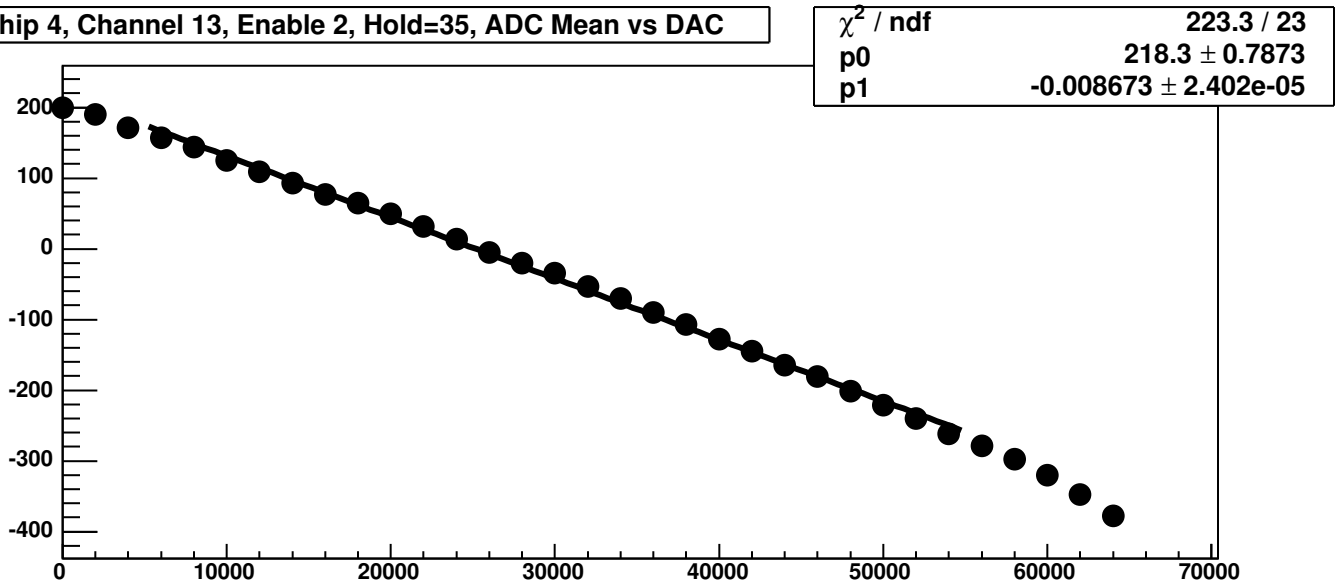

Chip 4, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
-14
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 13, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 34.92 / 23
p0 197.4 ± 0.8157
p1 0.003675 ± 2.496e-05
450
400
350
300
250
200
0
10000
20000
30000
40000
50000
60000
70000

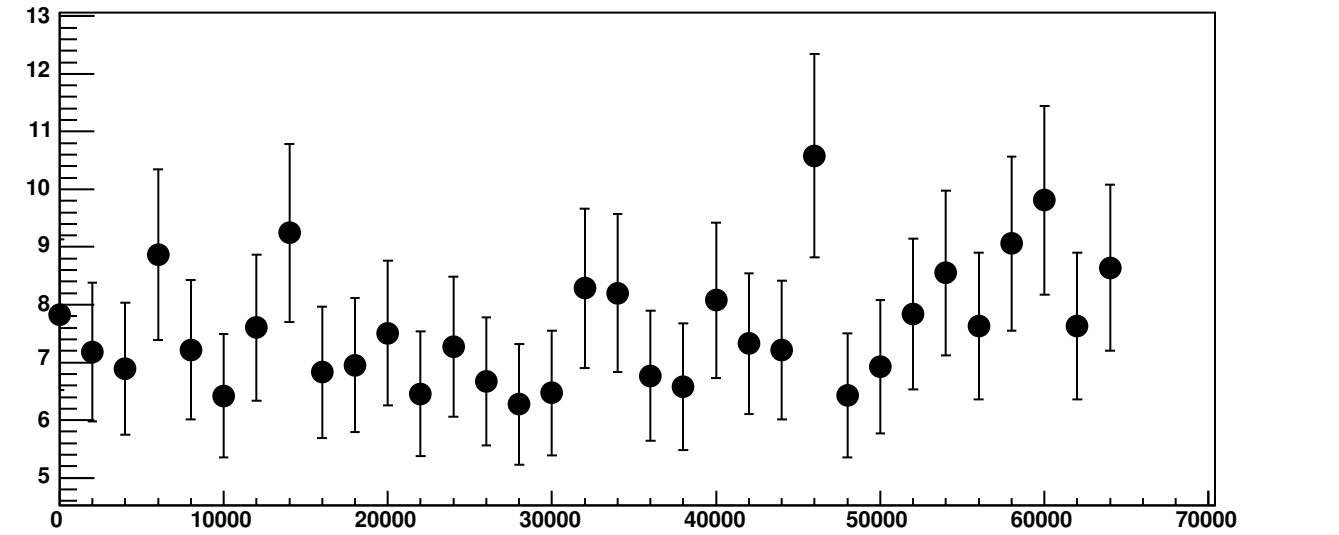

Chip 4, Channel 13, Enable 1, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 13, Enable 1, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
0
10000
20000
30000
40000
50000
60000
70000

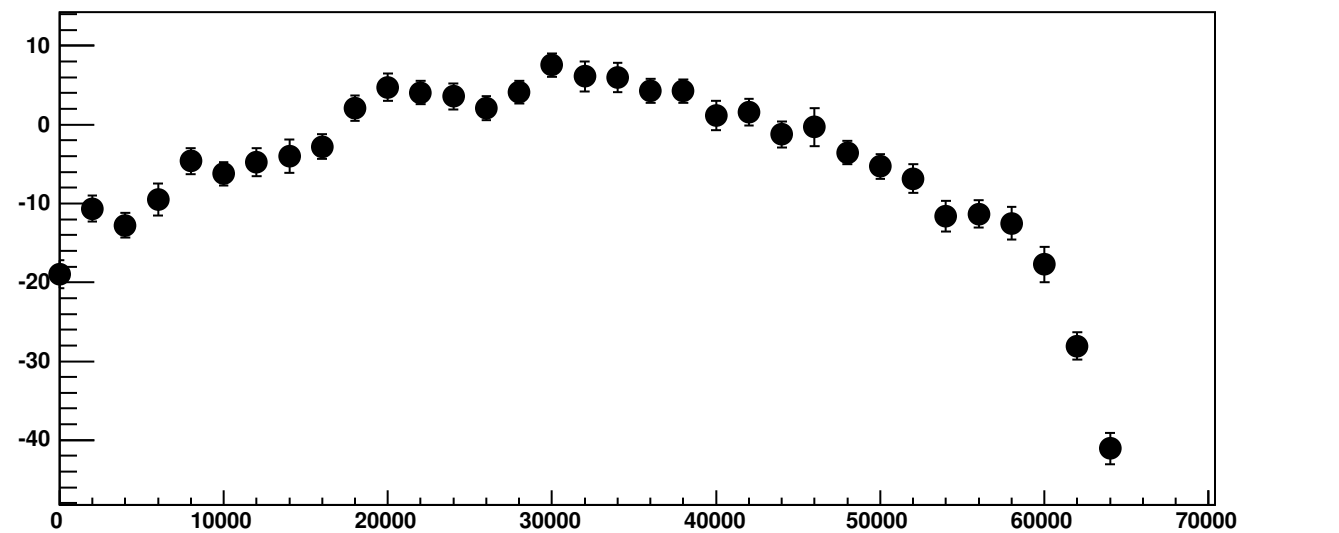
Chip 4, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

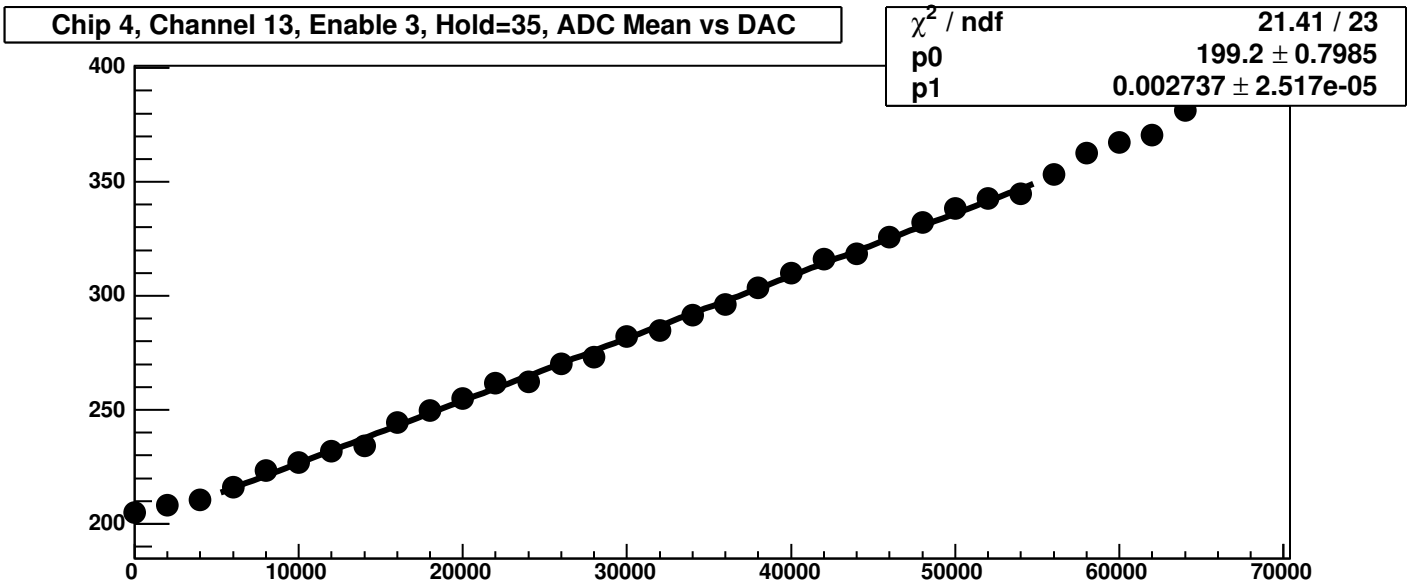


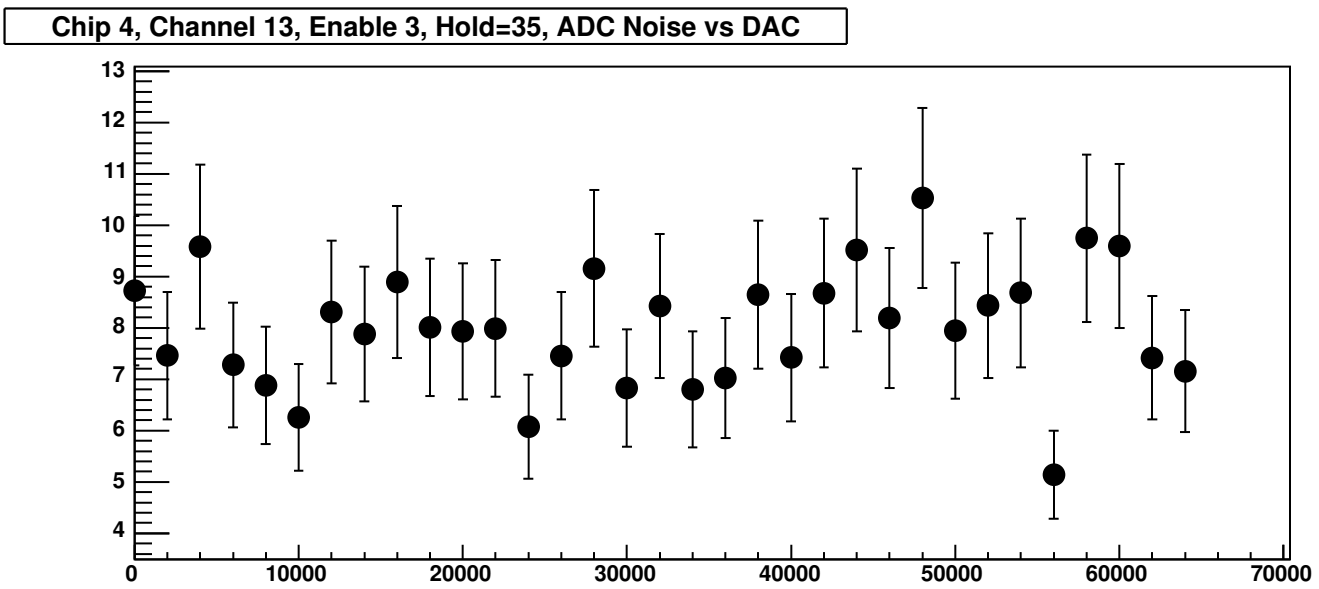
Chip 4, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC

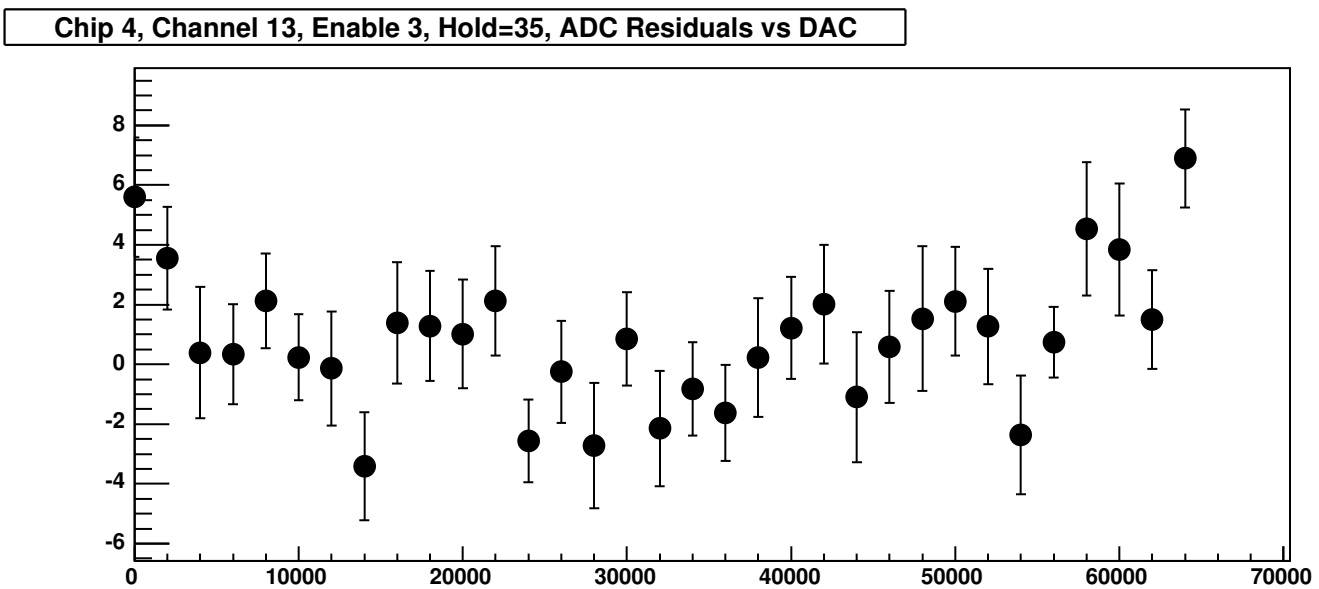


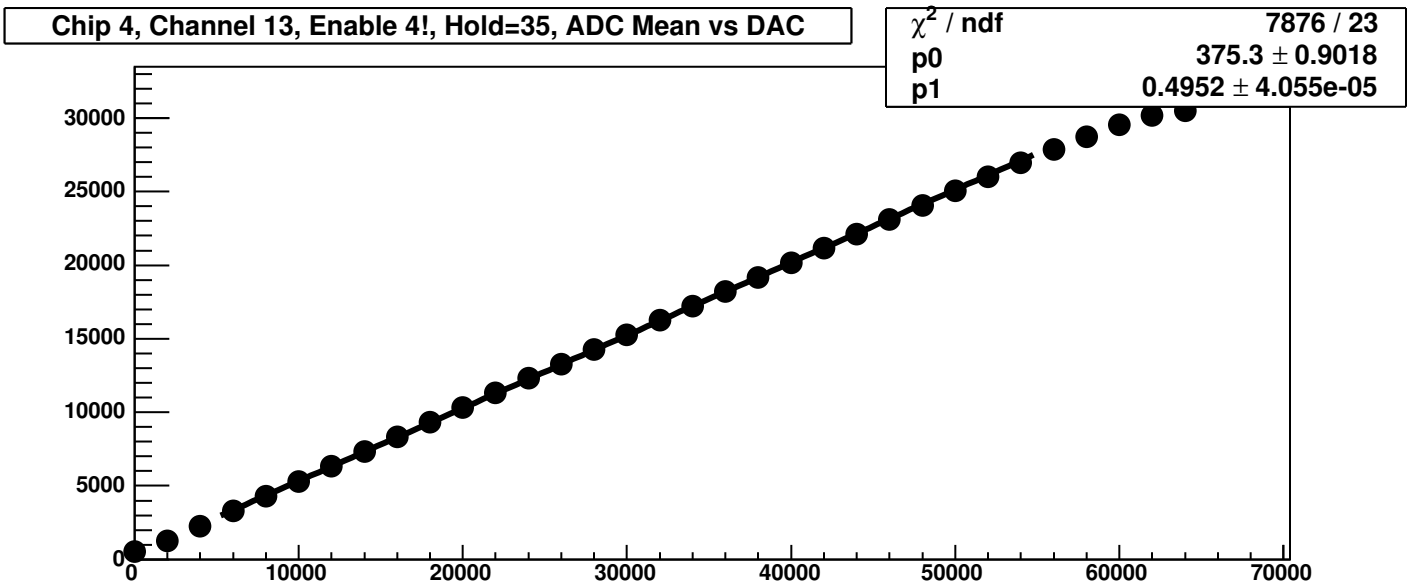
Chip 4, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

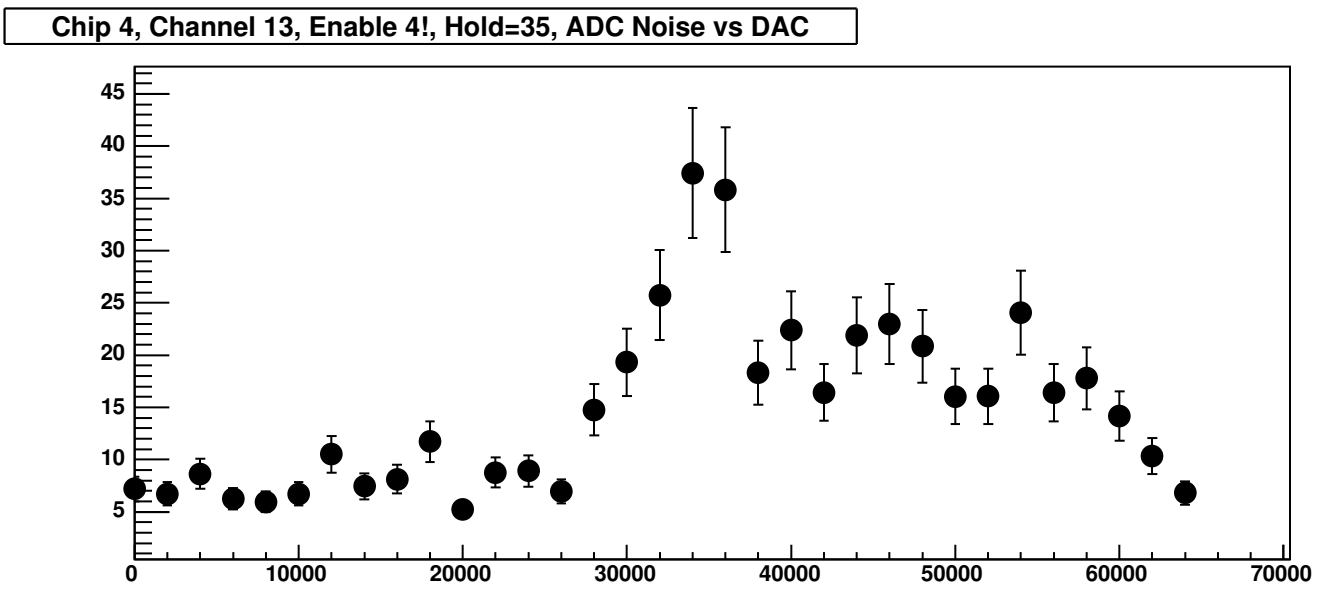


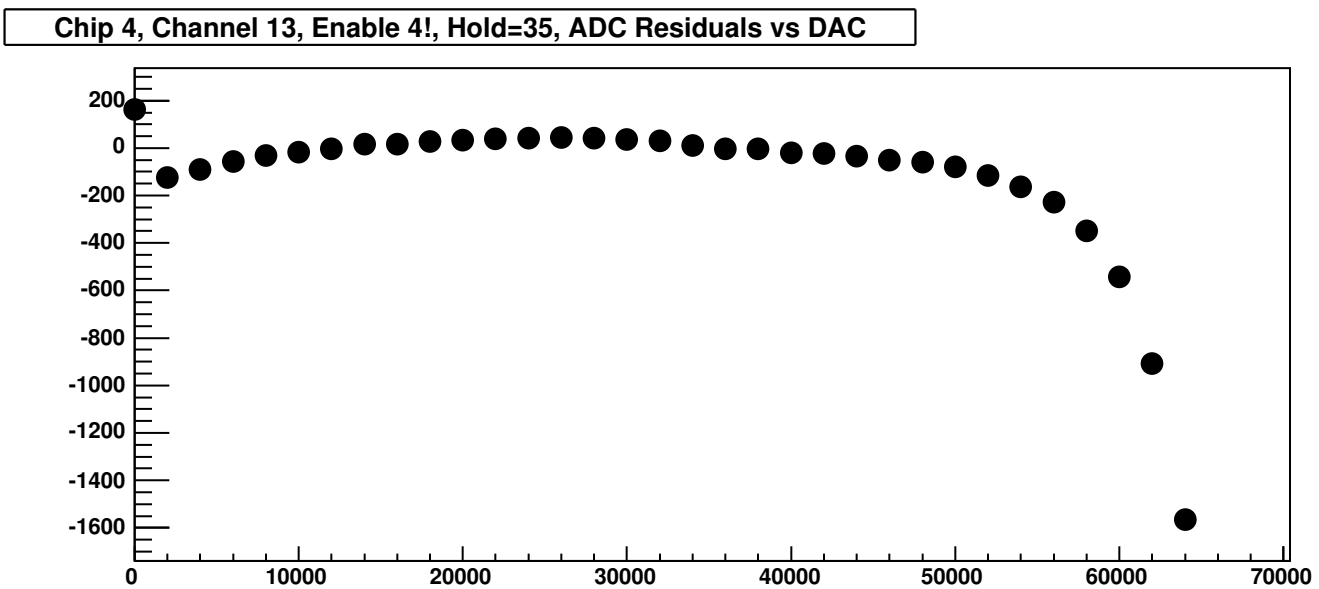

Chip 4, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 21.41 / 23
p0 199.2 ± 0.7985
p1 0.002737 ± 2.517e-05
400
350
300
250
200
0
10000
20000
30000
40000
50000
60000
70000

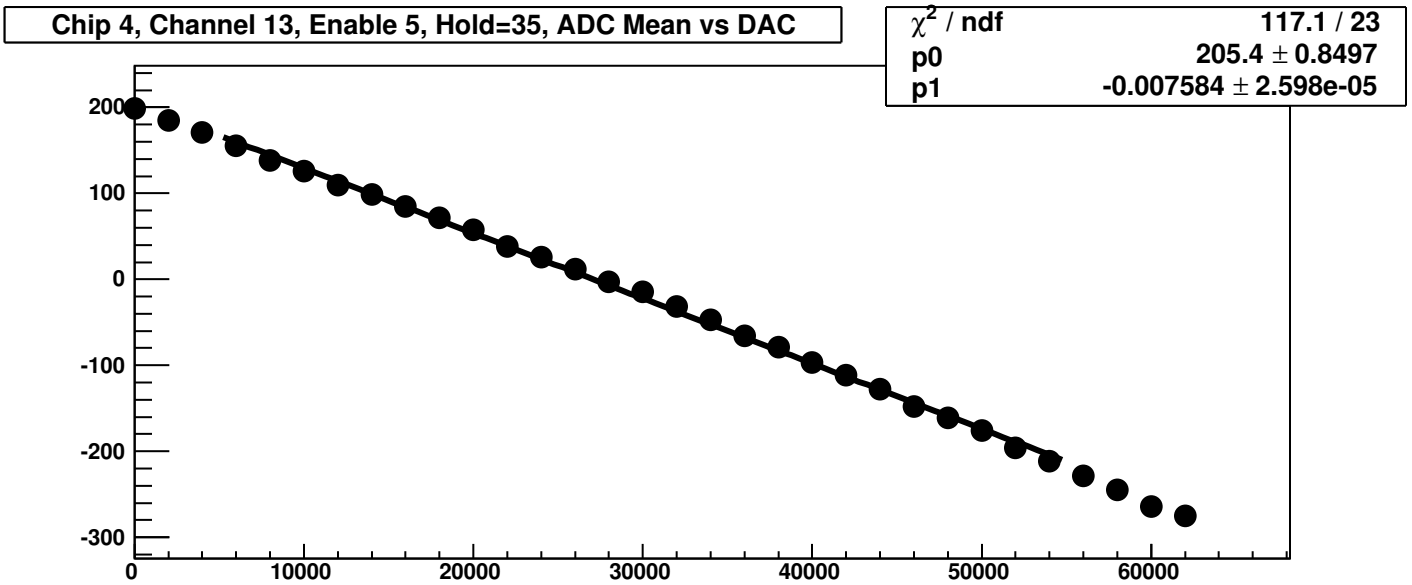

Chip 4, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

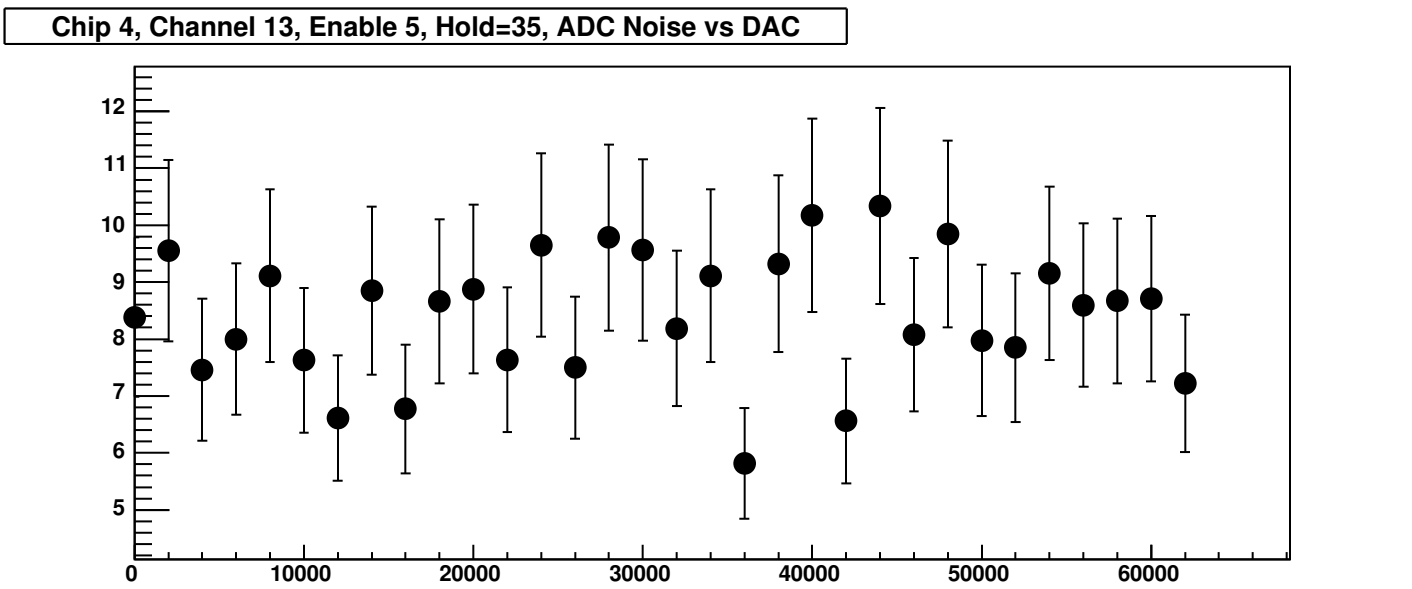

Chip 4, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

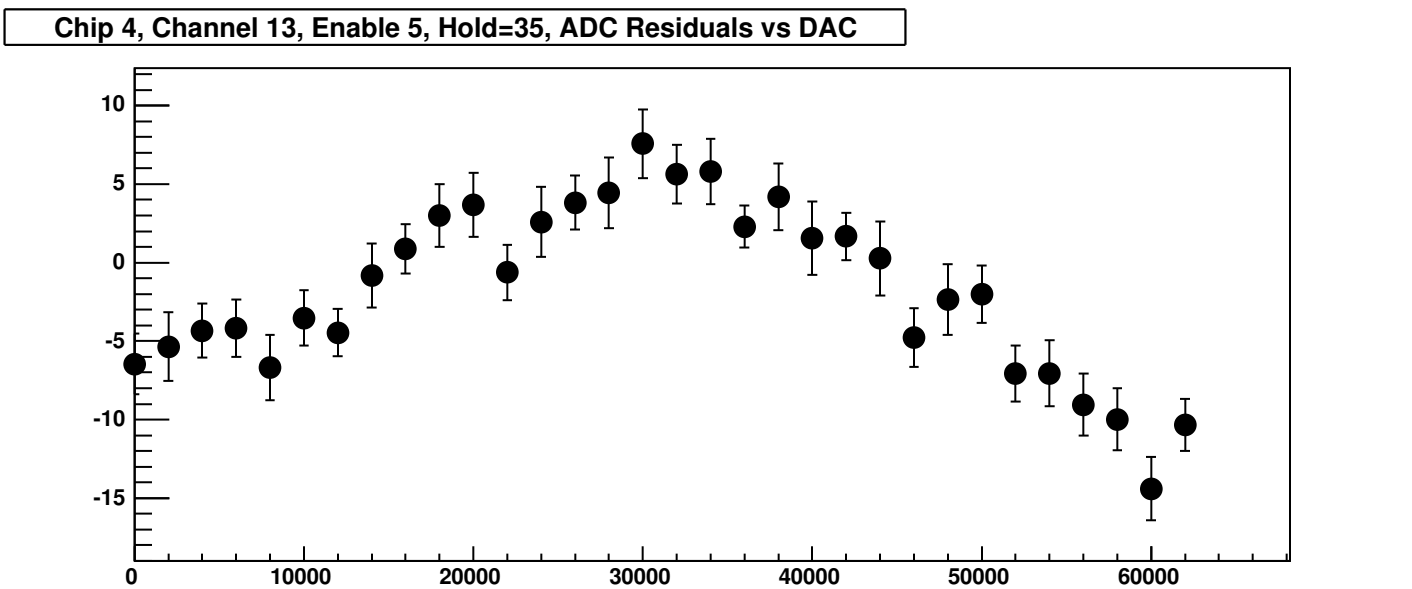

Chip 4, Channel 13, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 7876 / 23
p0 375.3 ± 0.9018
p1 0.4952 ± 4.055e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

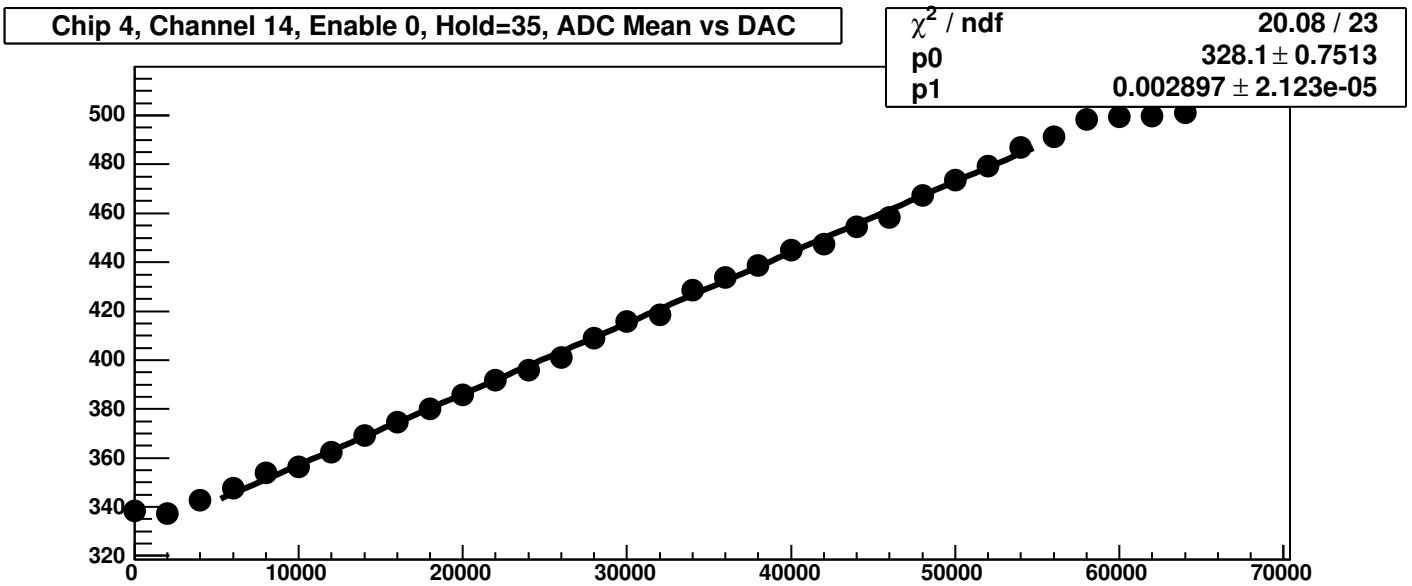

Chip 4, Channel 13, Enable 4!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000

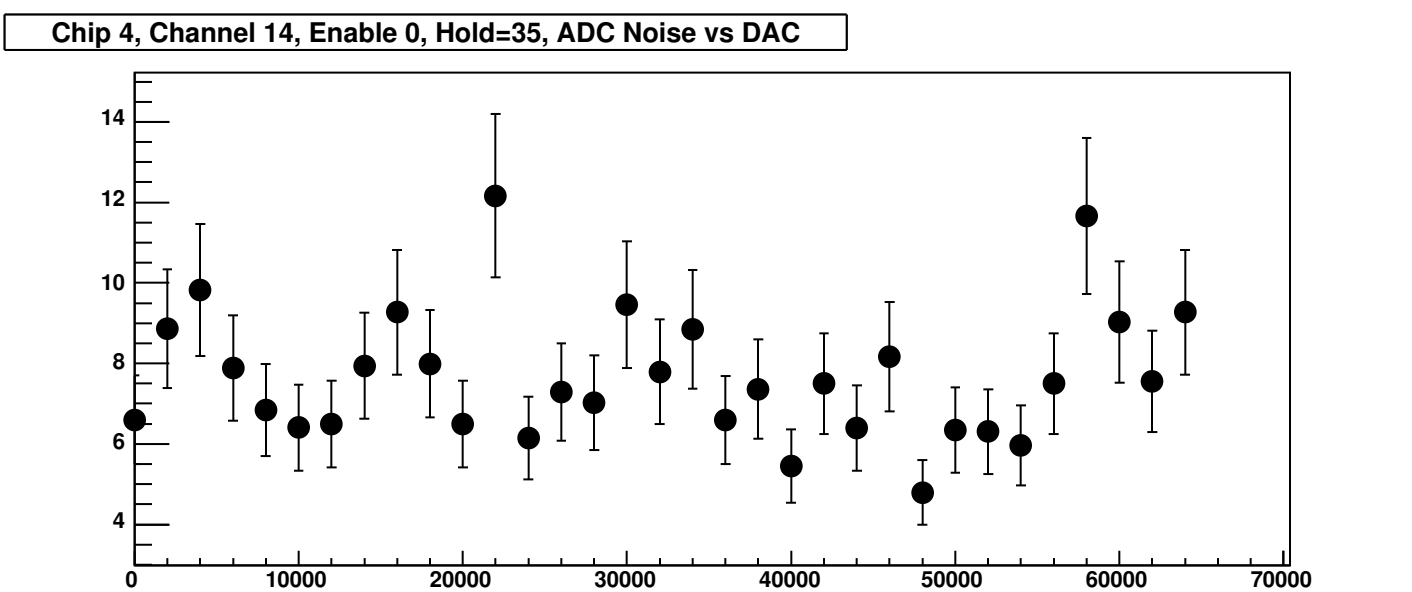

Chip 4, Channel 13, Enable 4!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
0
10000
20000
30000
40000
50000
60000
70000

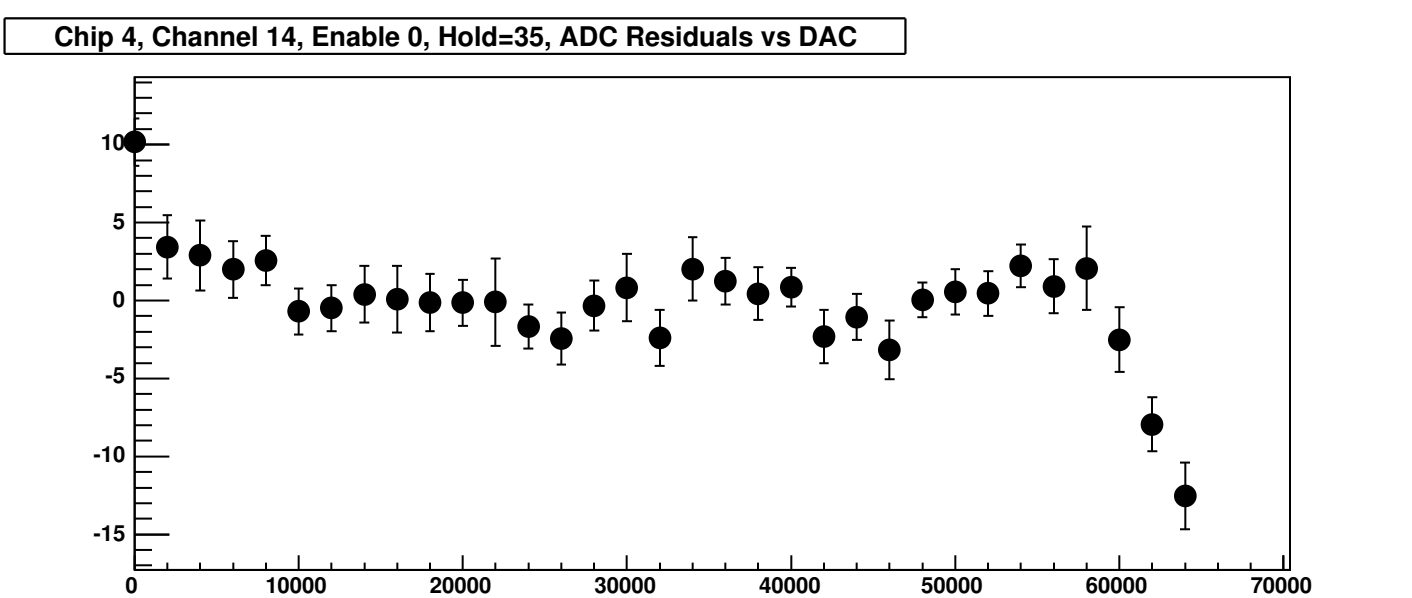

Chip 4, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 117.1 / 23
p0 205.4 ± 0.8497
p1 -0.007584 ± 2.598e-05
200
100
0
-100
-200
-300
0
10000
20000
30000
40000
50000
60000

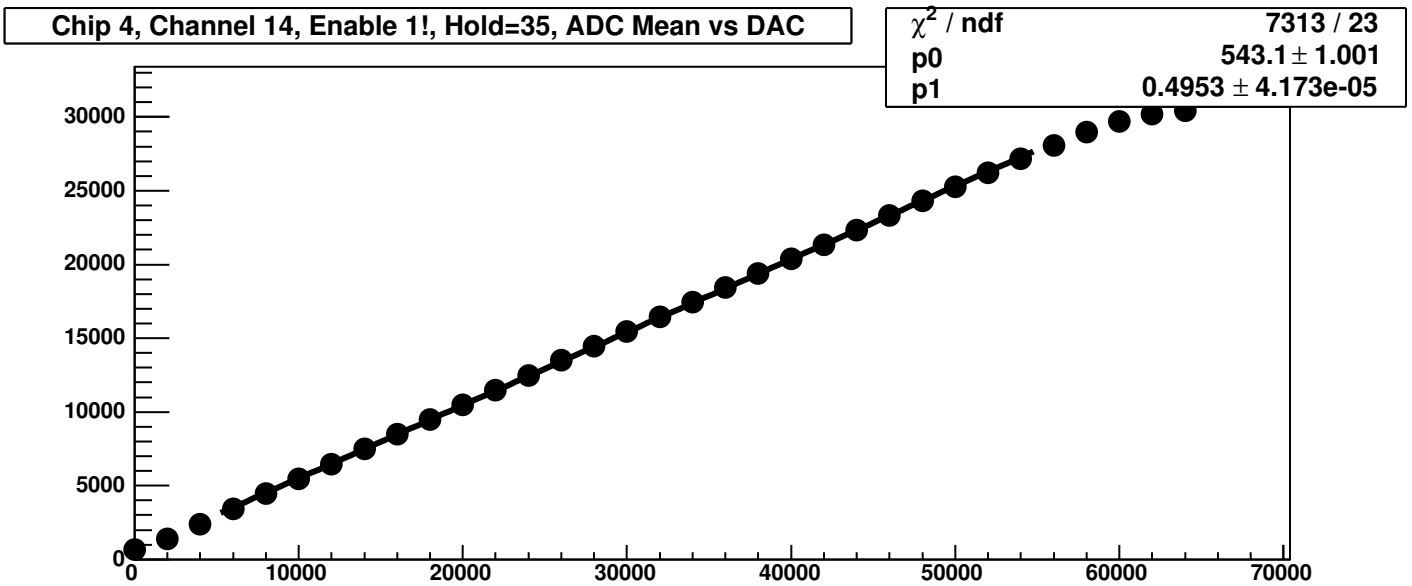

Chip 4, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000

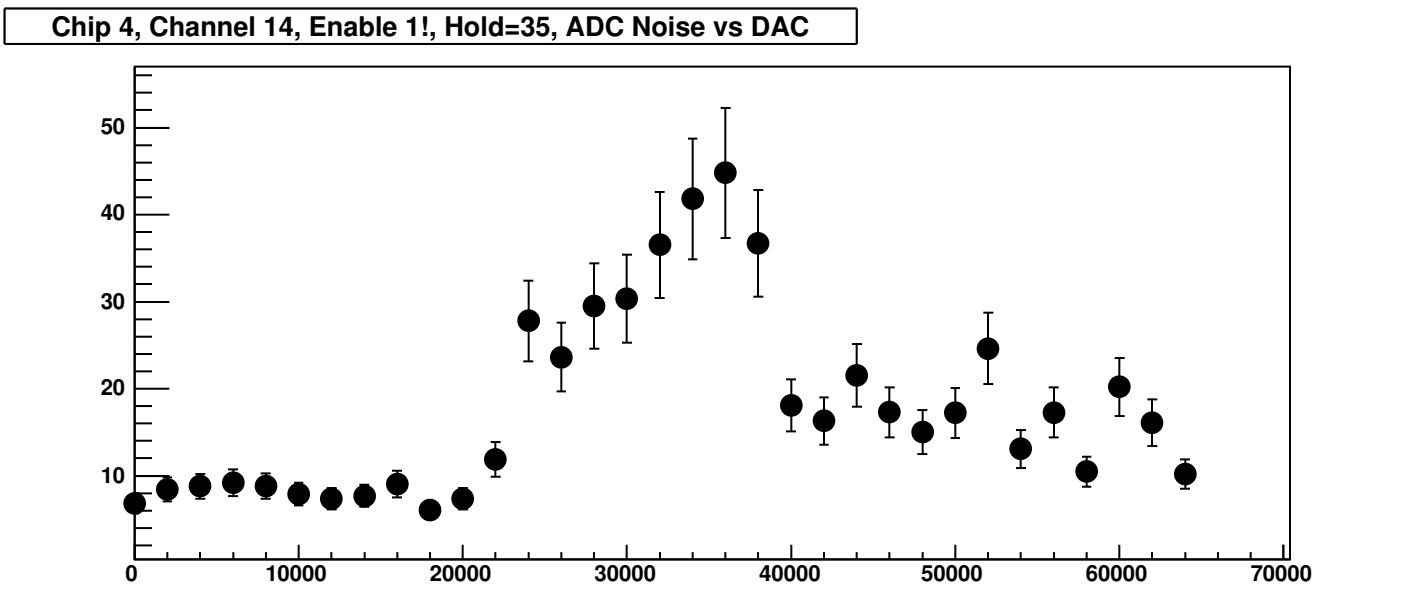

Chip 4, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000

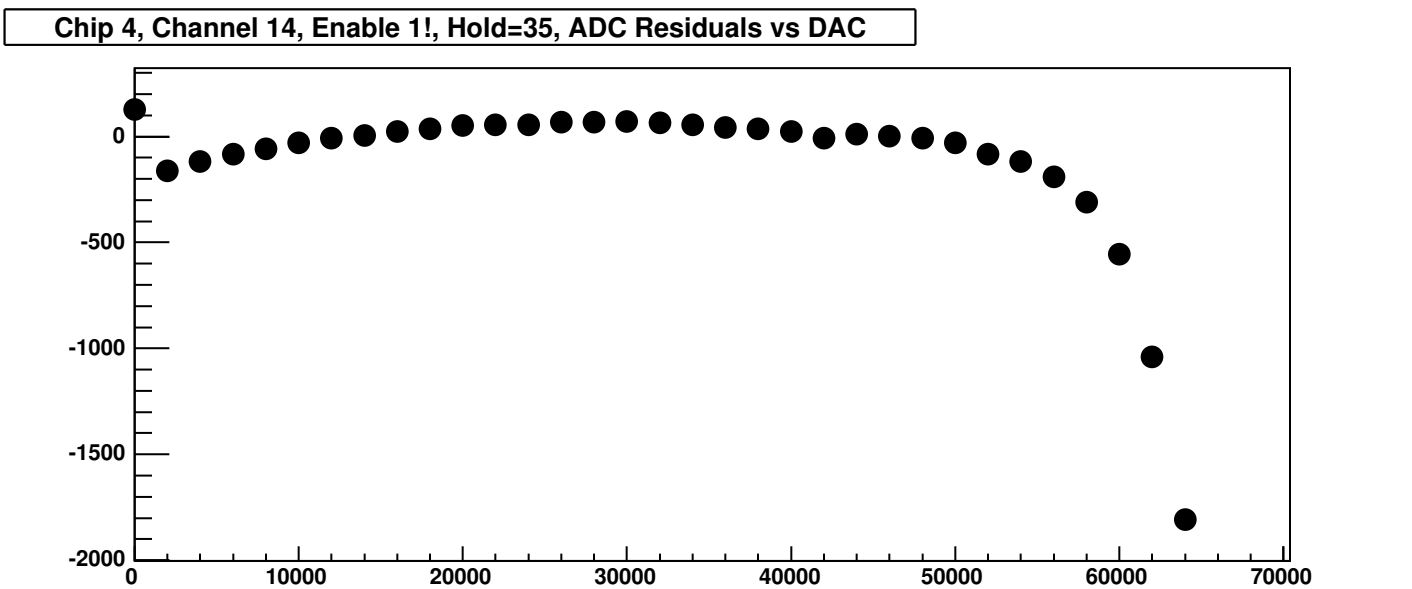

Chip 4, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 20.08 / 23
p0 328.1 ± 0.7513
p1 0.002897 ± 2.123e-05
500
480
460
440
420
400
380
360
340
320
0
10000
20000
30000
40000
50000
60000
70000

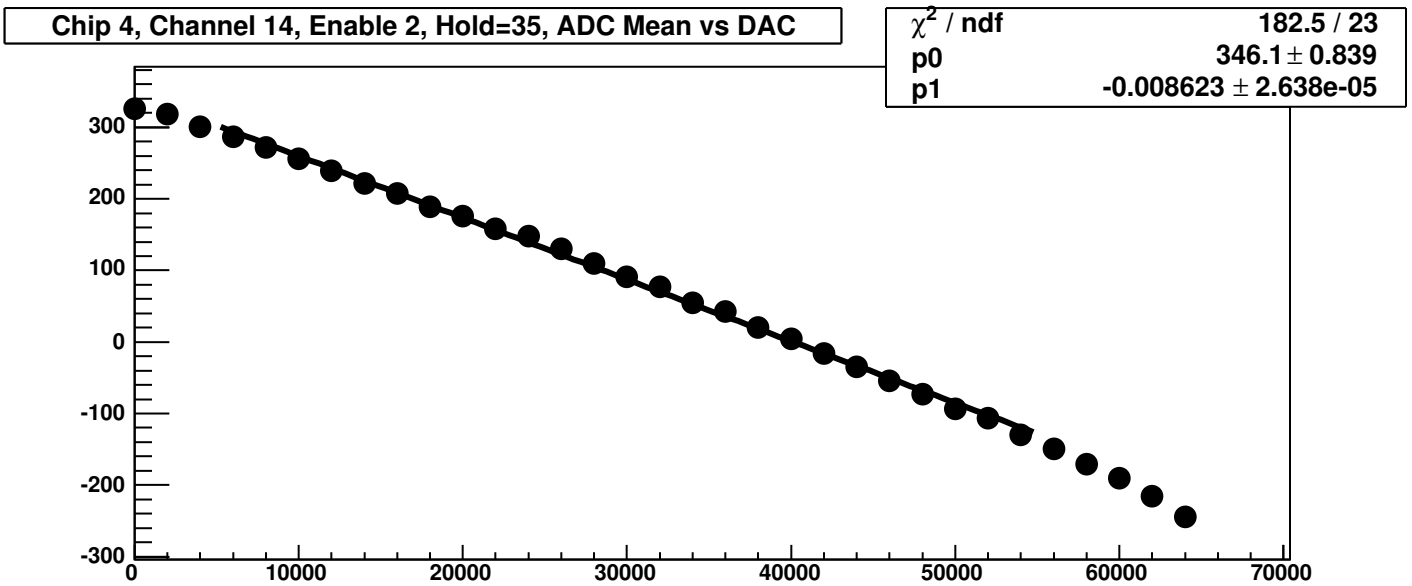

Chip 4, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

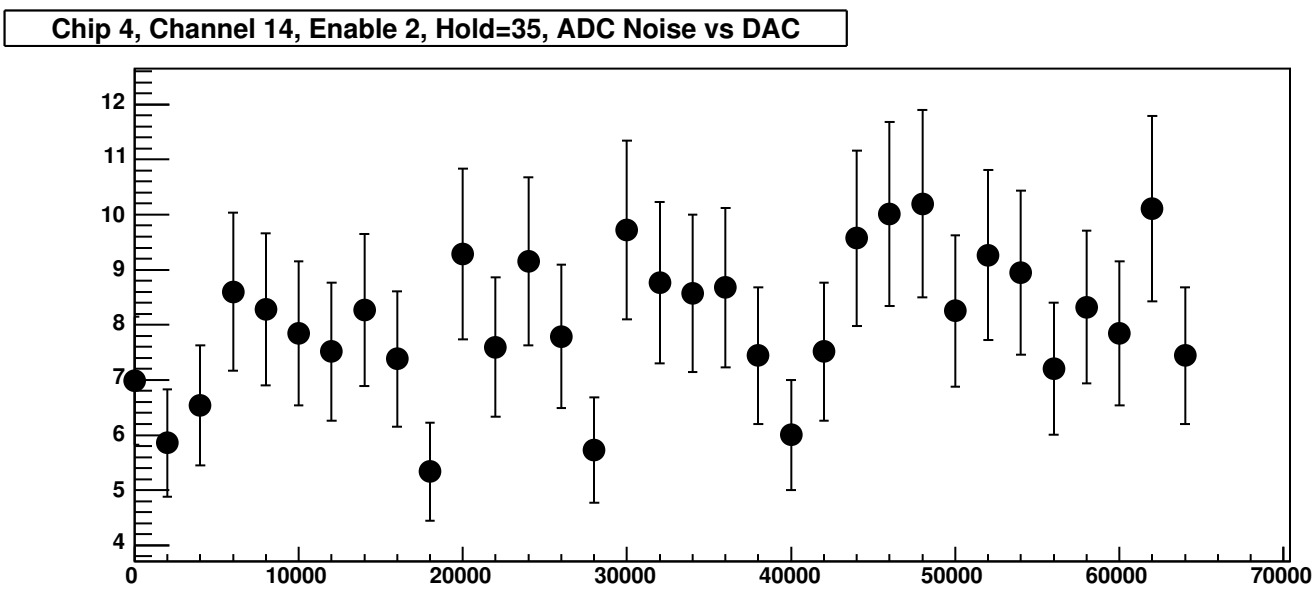

Chip 4, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

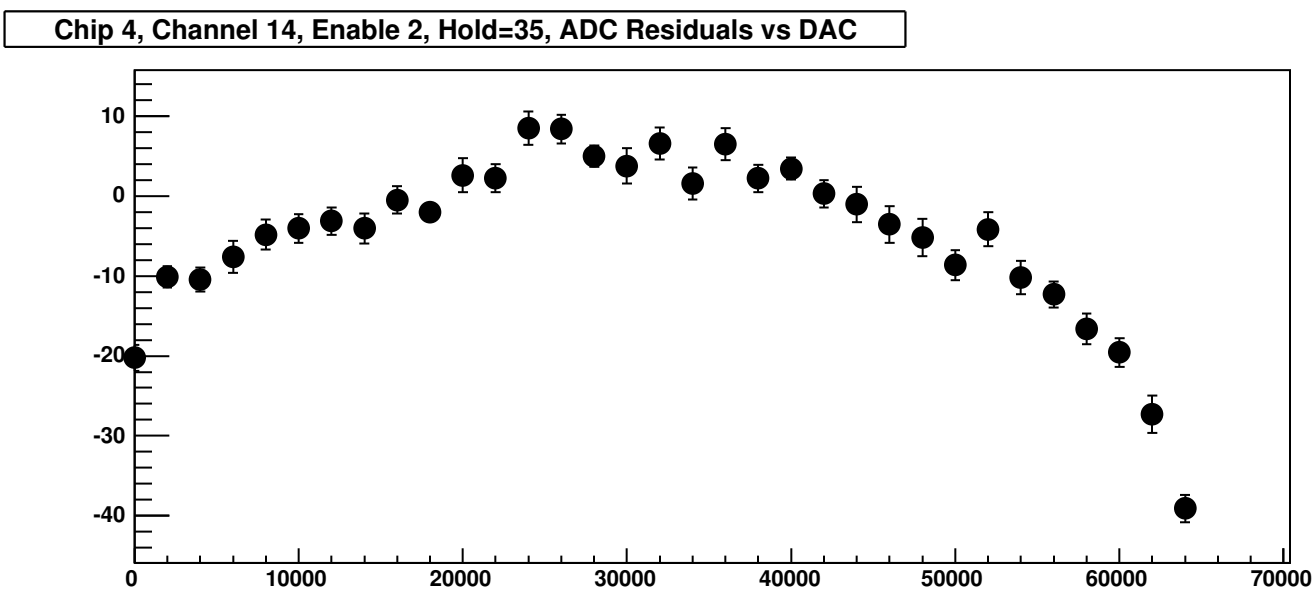

Chip 4, Channel 14, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 7313 / 23
p0 543.1 ± 1.001
p1 0.4953 ± 4.173e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

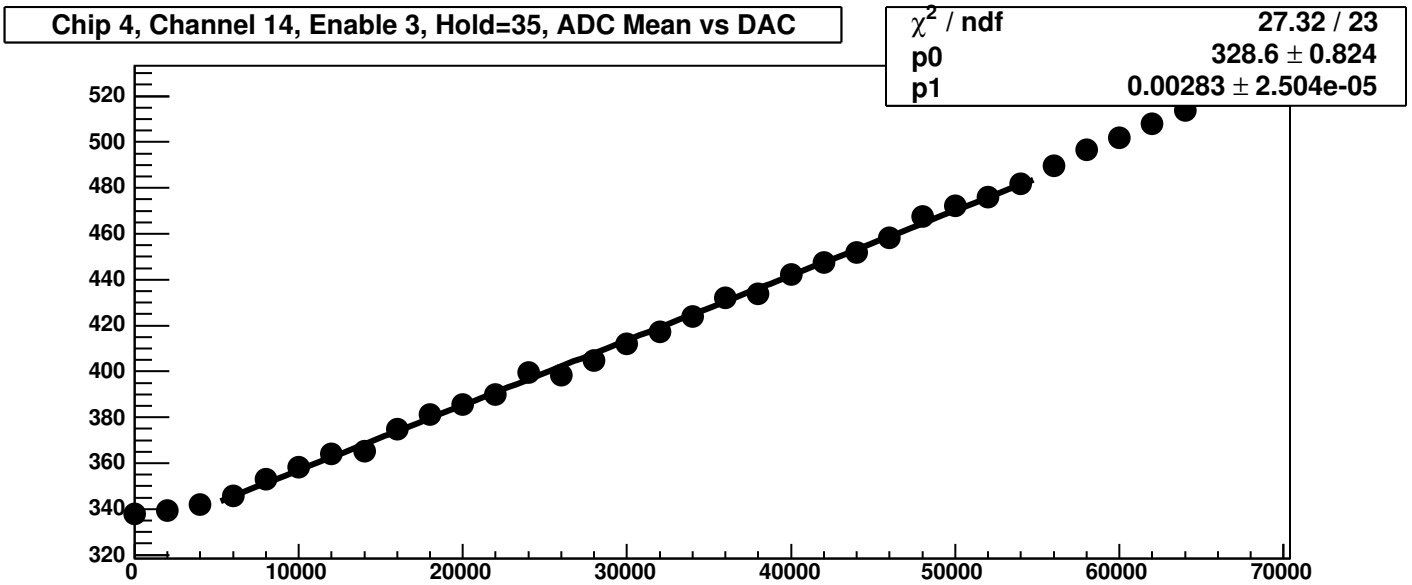

Chip 4, Channel 14, Enable 1!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000

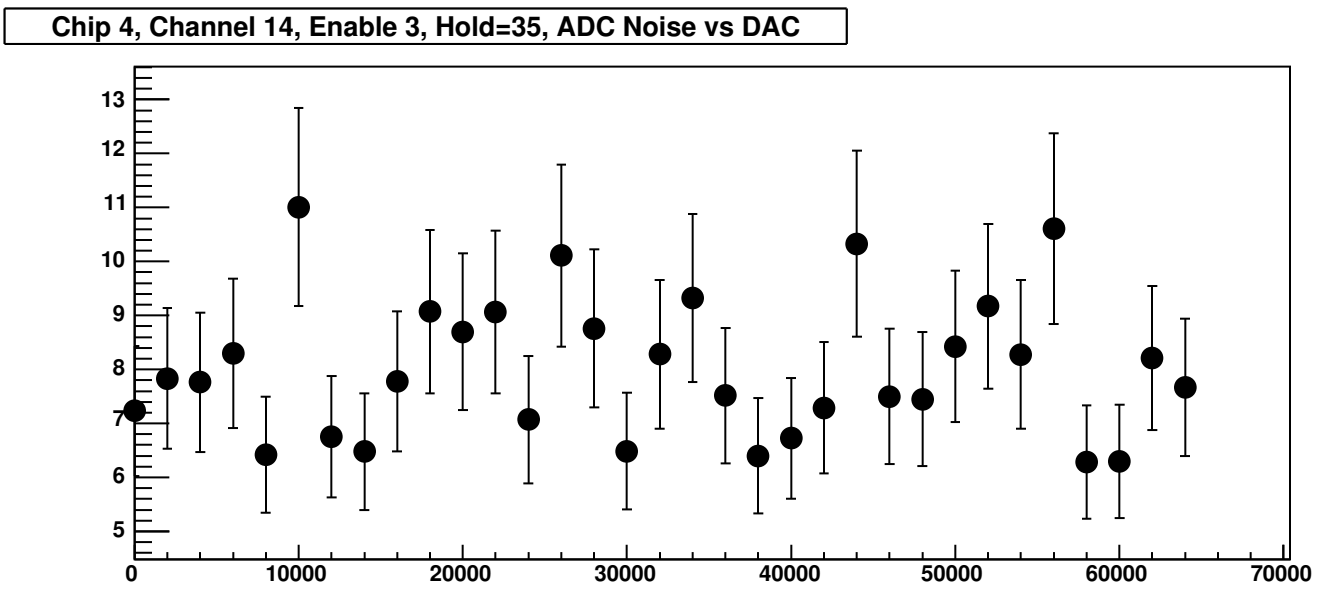

Chip 4, Channel 14, Enable 1!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000

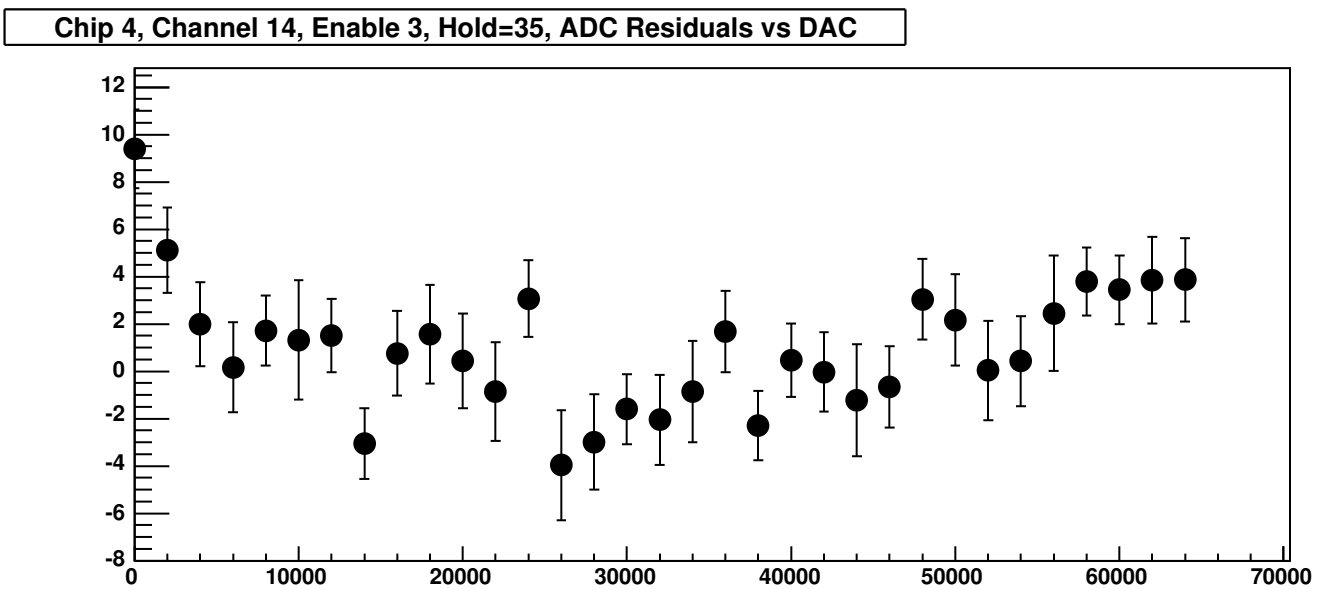

Chip 4, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 182.5 / 23
p0 346.1 ± 0.839
p1 -0.008623 ± 2.638e-05
300
200
100
0
-100
-200
-300
0
10000
20000
30000
40000
50000
60000
70000

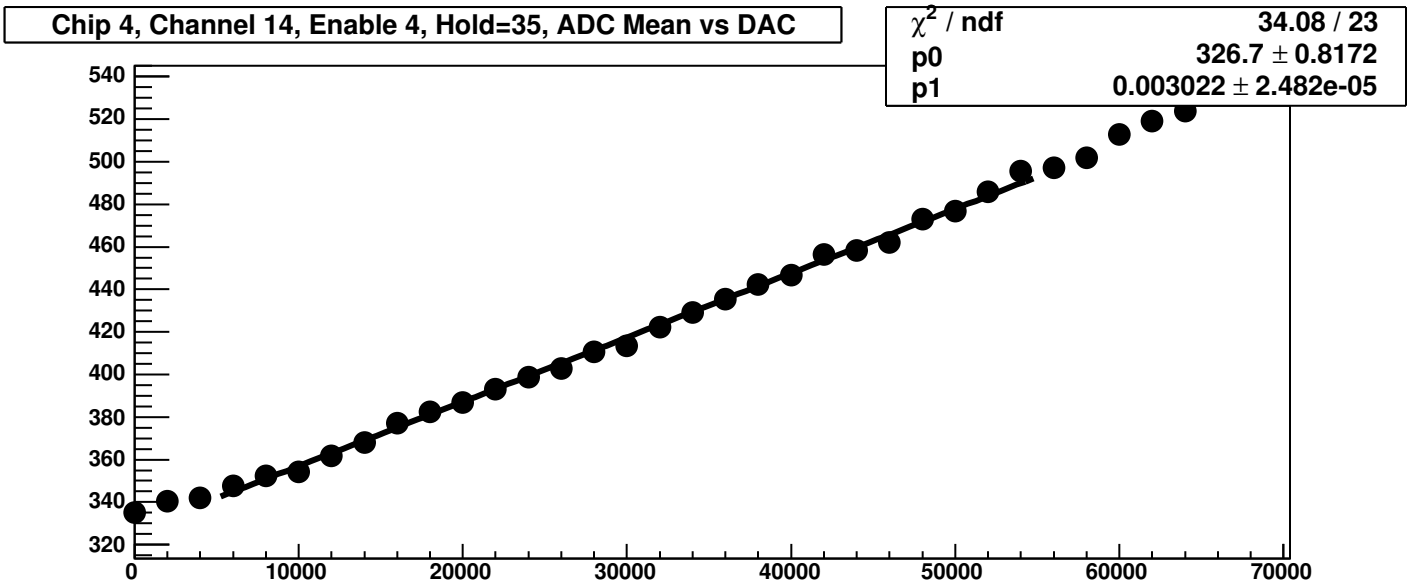

Chip 4, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

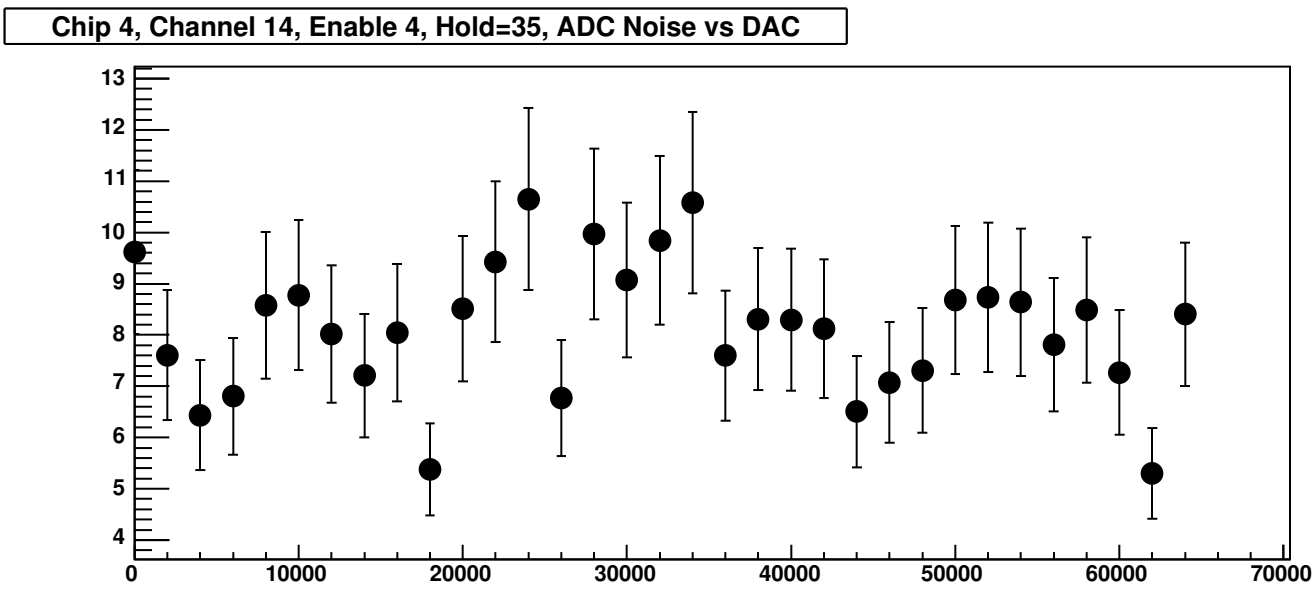

Chip 4, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
10000
20000
30000
40000
50000
60000
70000

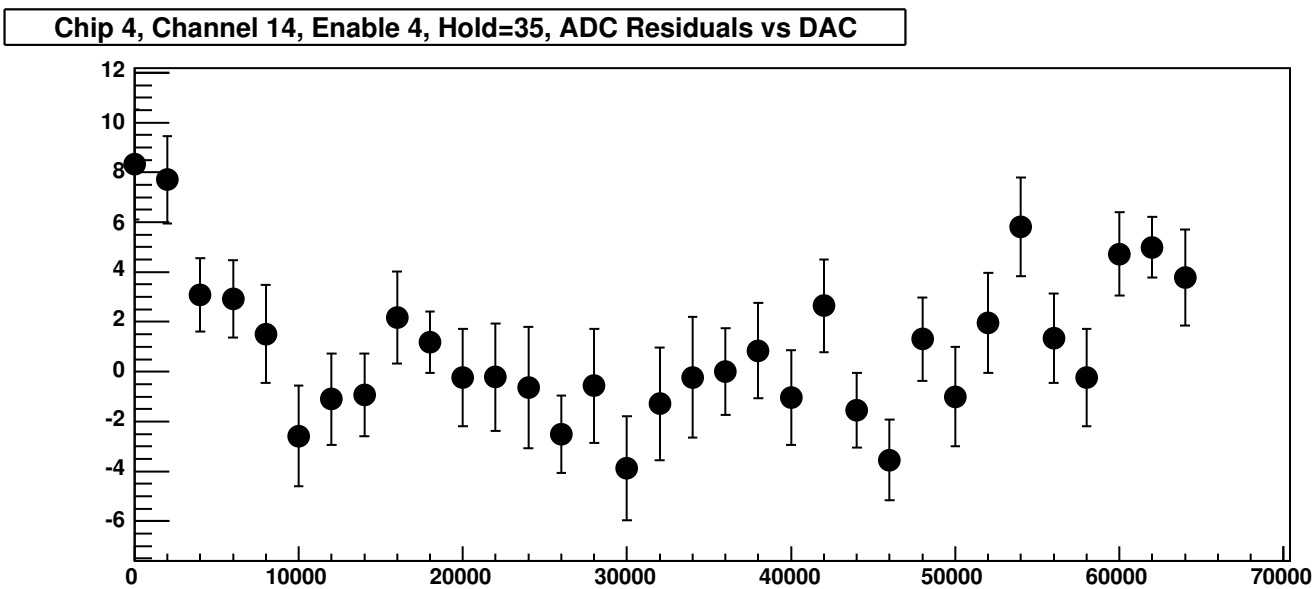

Chip 4, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 27.32 / 23
p0 328.6 ± 0.824
p1 0.00283 ± 2.504e-05
520
500
480
460
440
420
400
380
360
340
320
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000

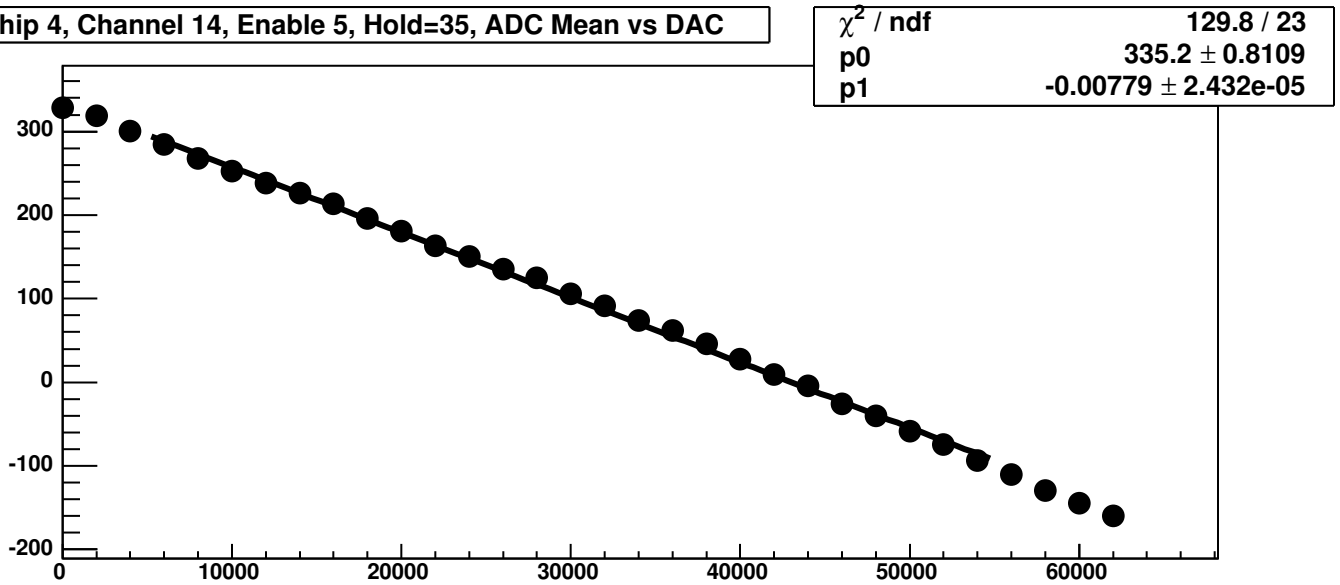

Chip 4, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 14, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 34.08 / 23
p0 326.7 ± 0.8172
p1 0.003022 ± 2.482e-05
540
520
500
480
460
440
420
400
380
360
340
320
0
10000
20000
30000
40000
50000
60000
70000

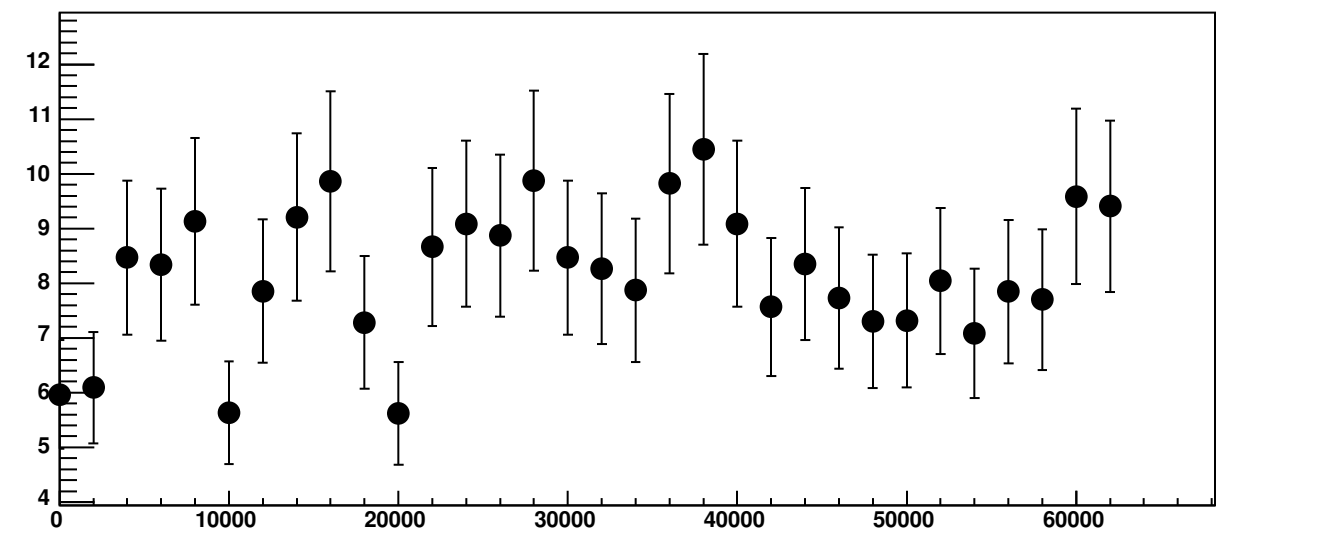

Chip 4, Channel 14, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 14, Enable 4, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

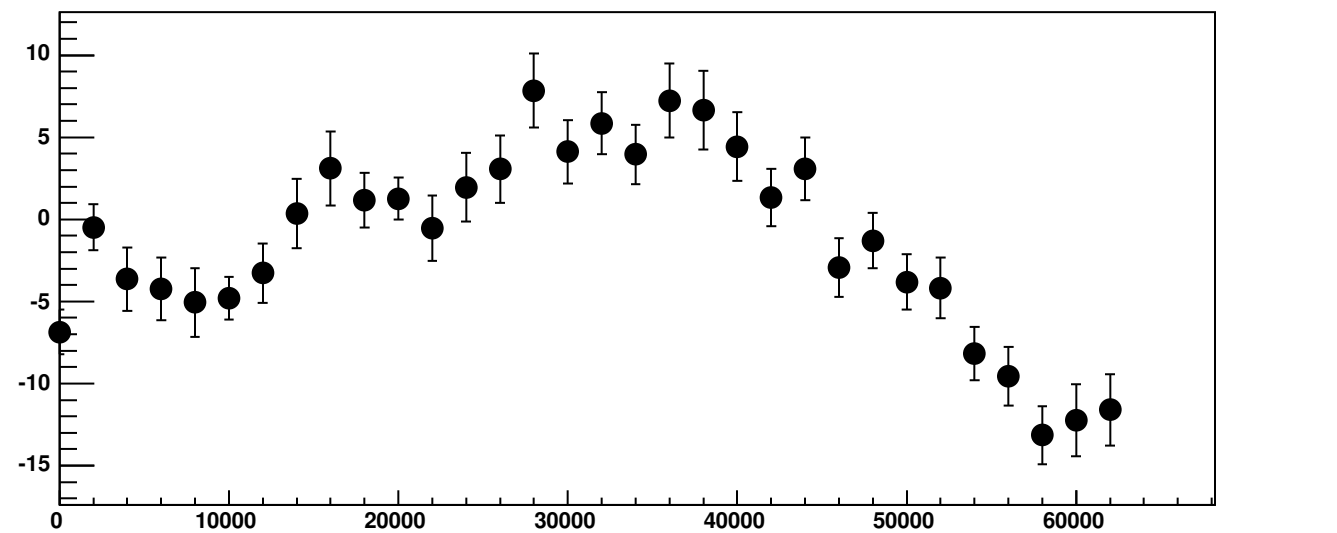
Chip 4, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC



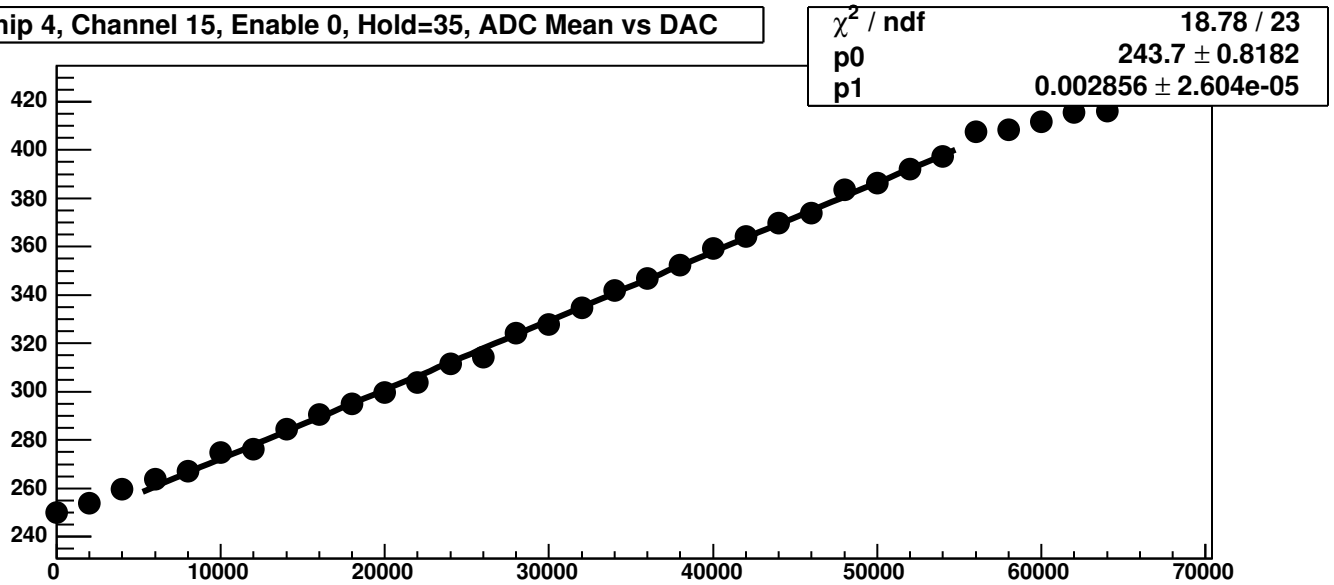
Chip 4, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC



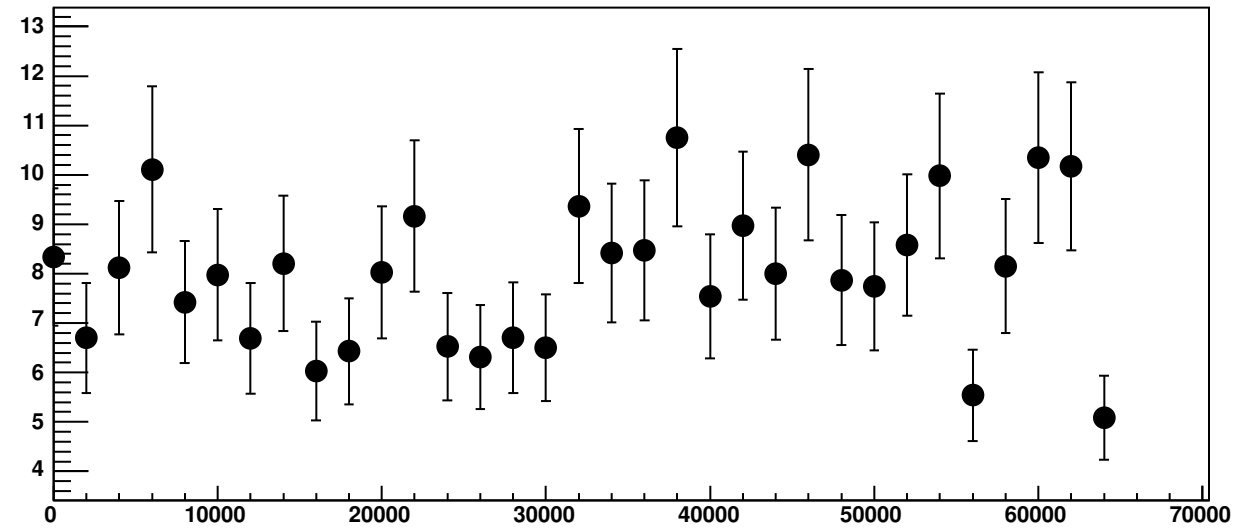
Chip 4, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC



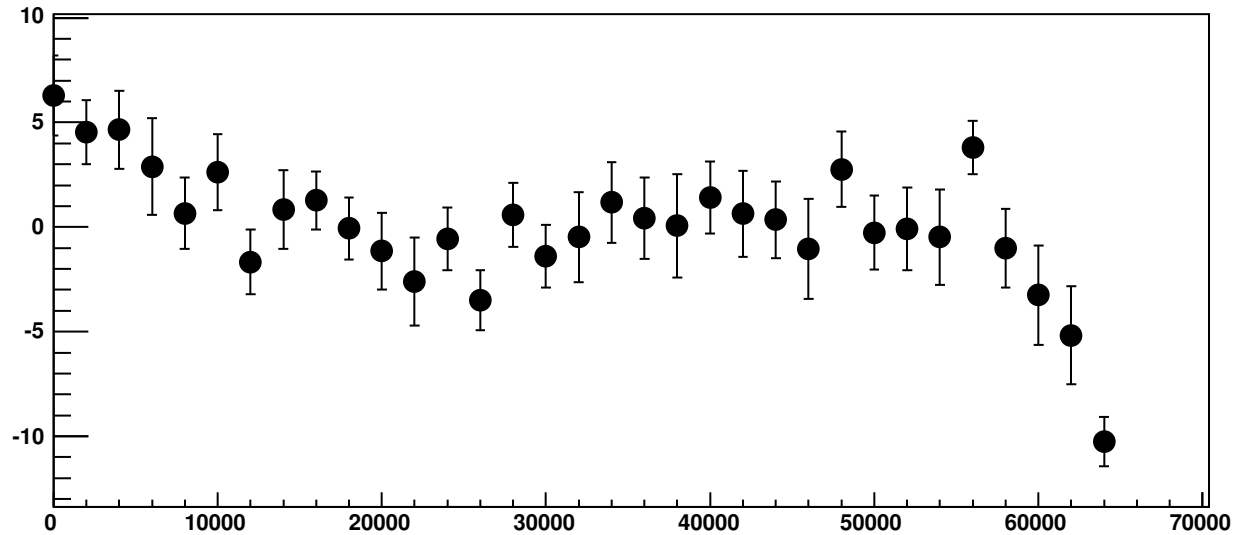
Chip 4, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

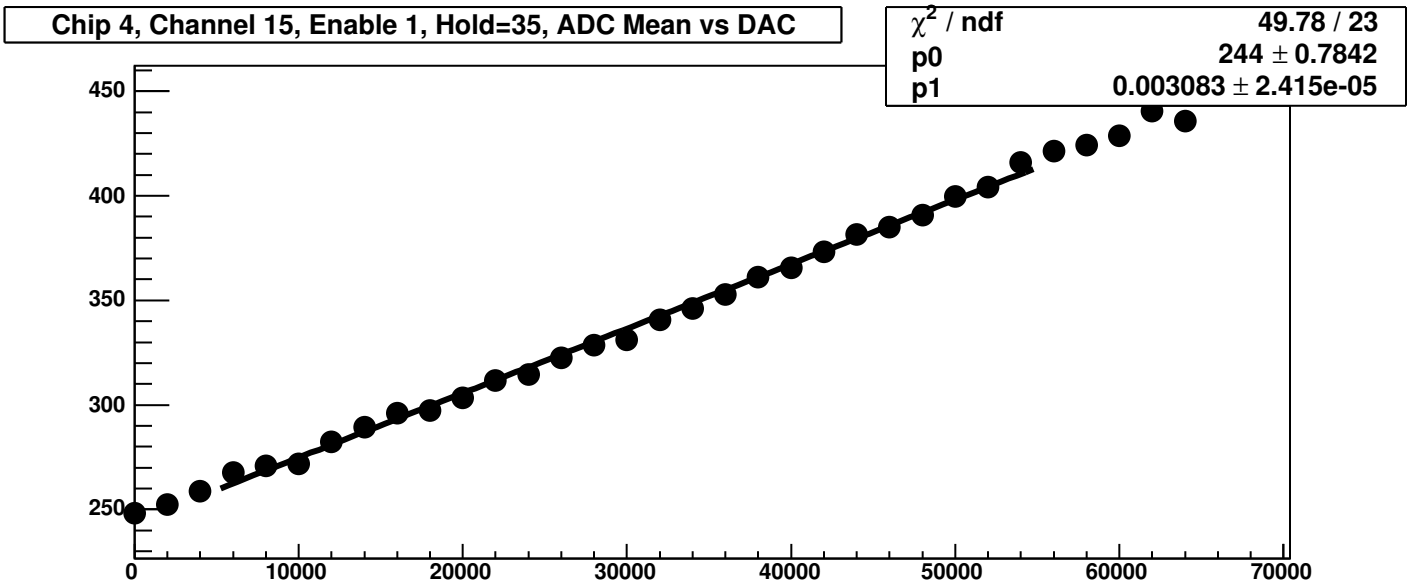


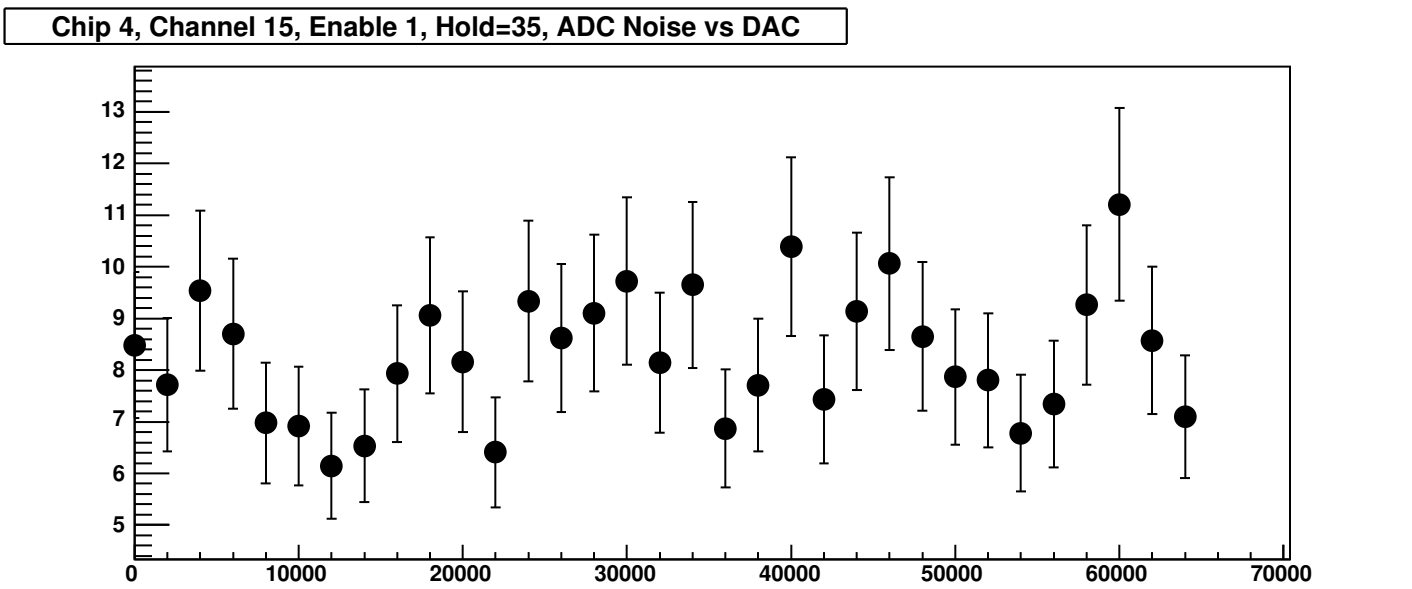
Chip 4, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC

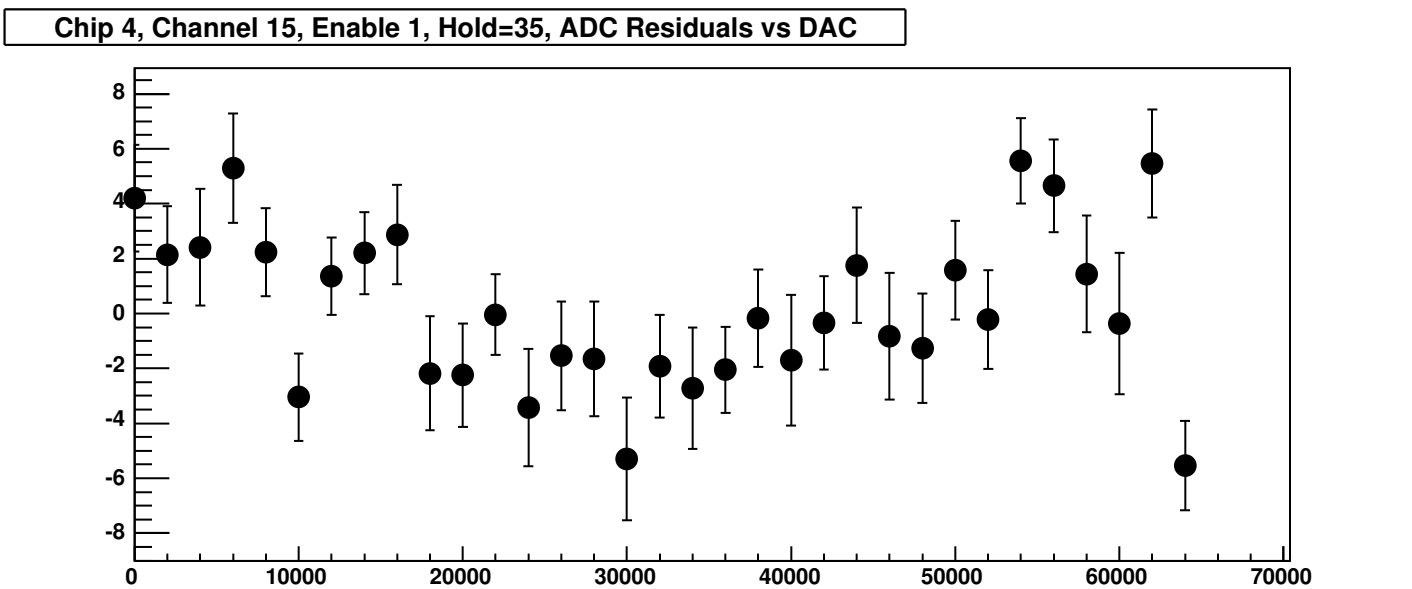


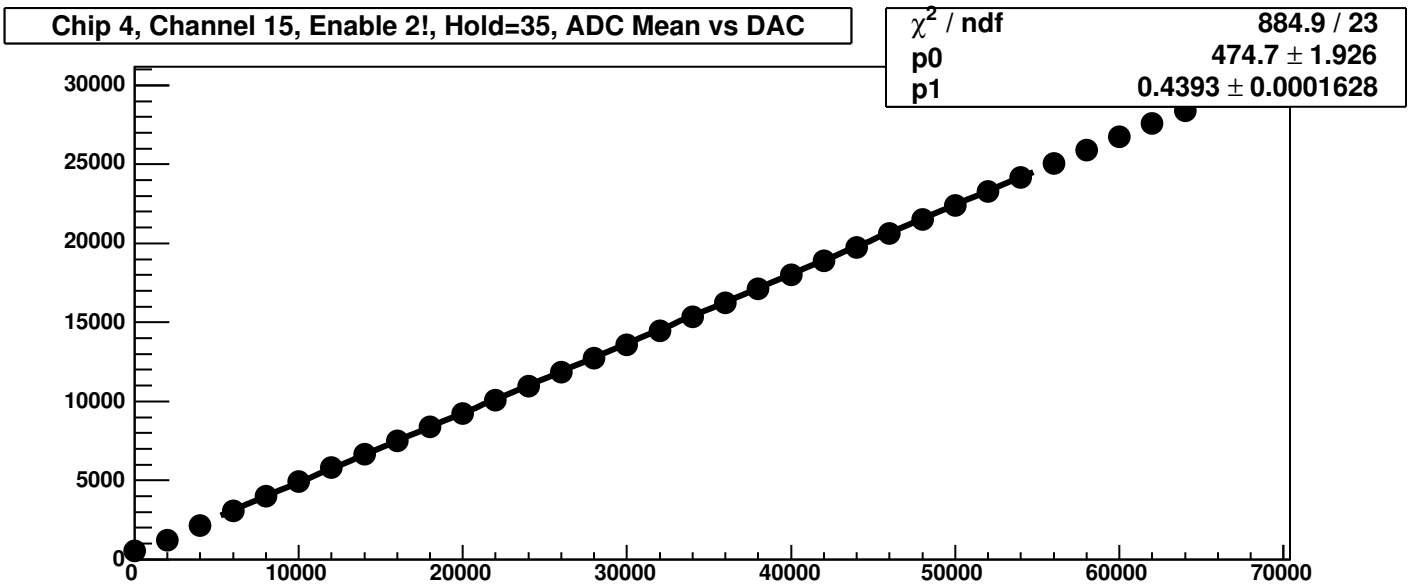
Chip 4, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

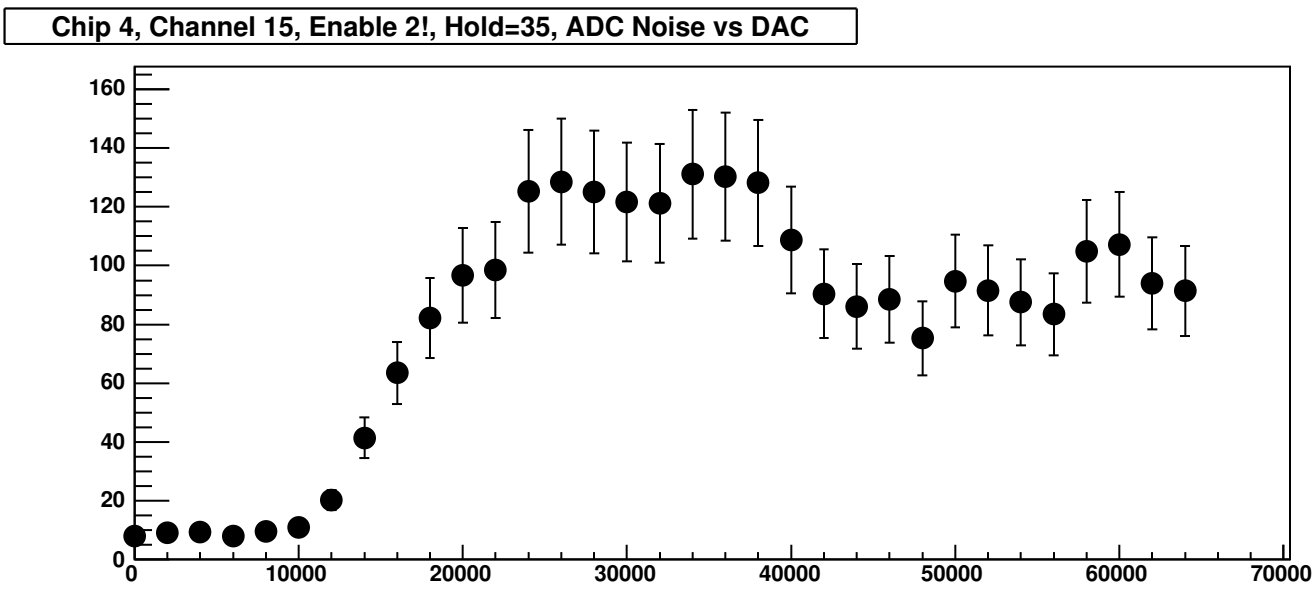


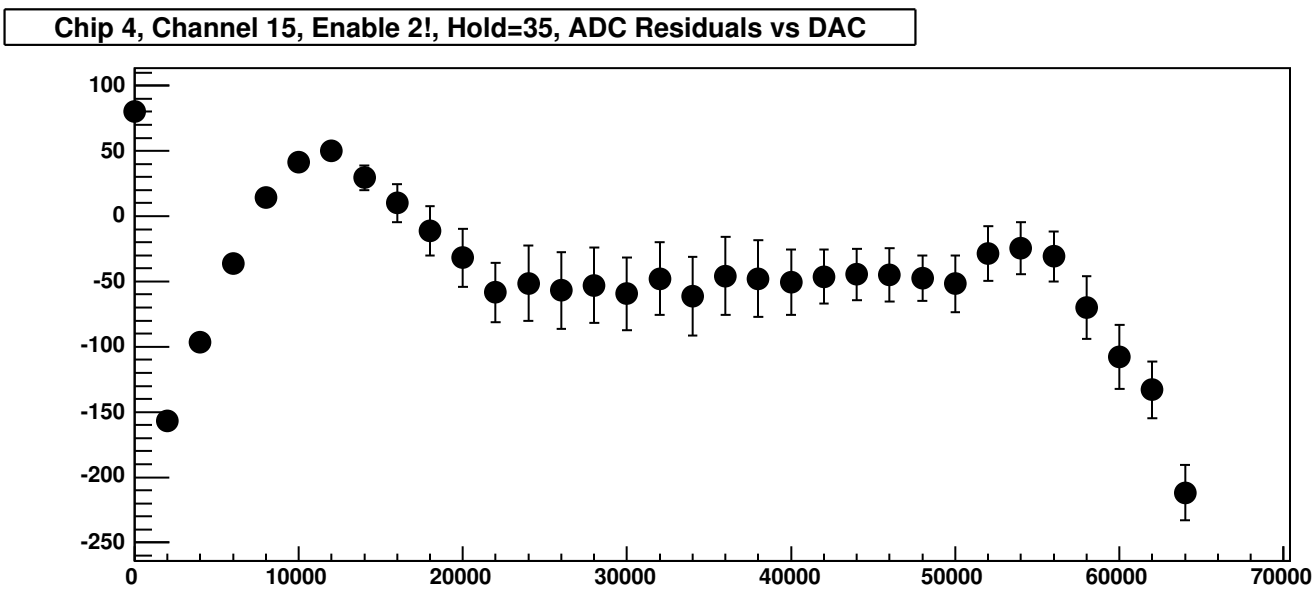

Chip 4, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 49.78 / 23
p0 244 ± 0.7842
p1 0.003083 ± 2.415e-05
450
400
350
300
250
0
10000
20000
30000
40000
50000
60000
70000

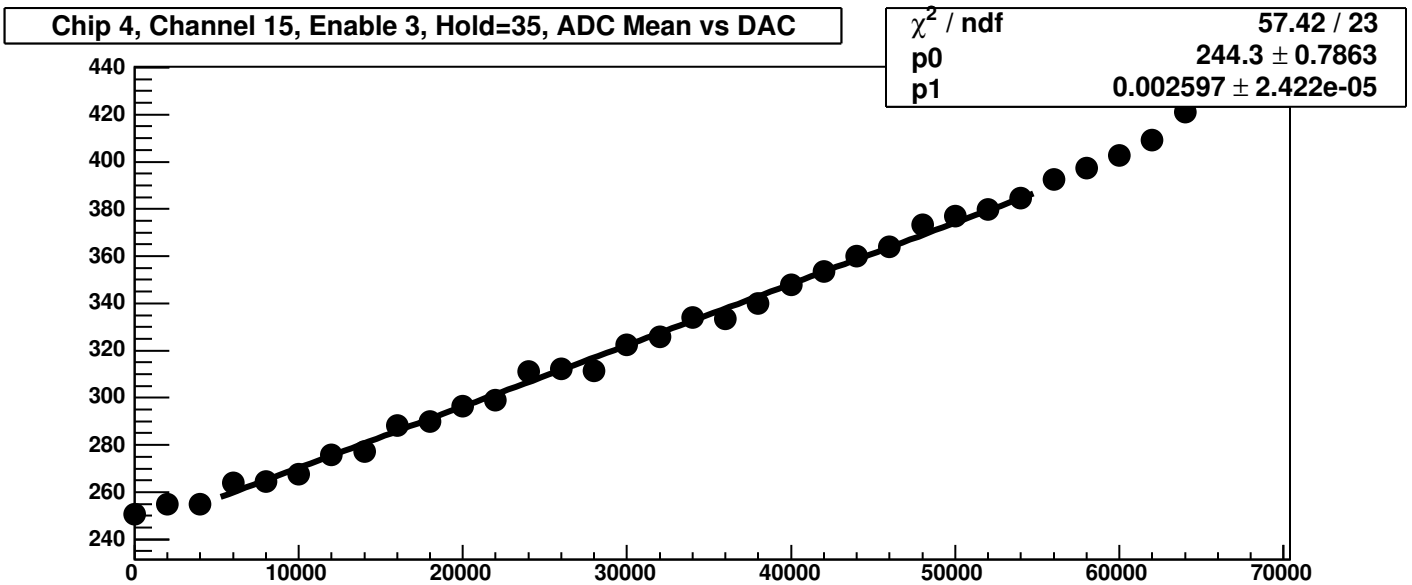

Chip 4, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000

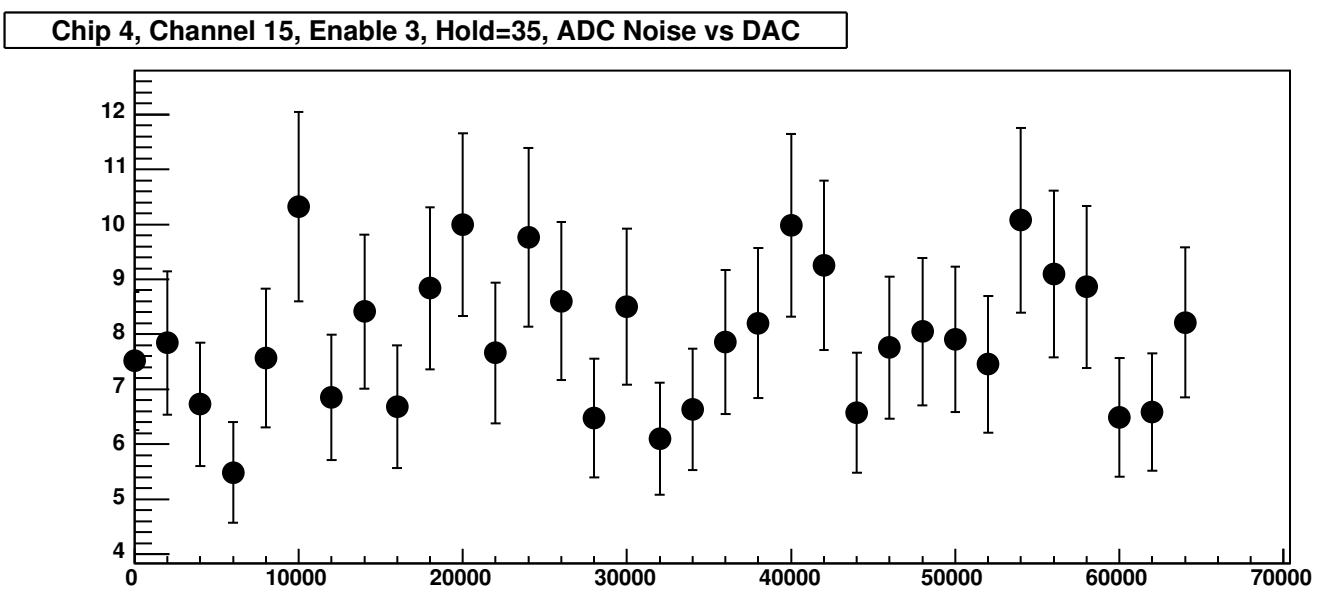

Chip 4, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

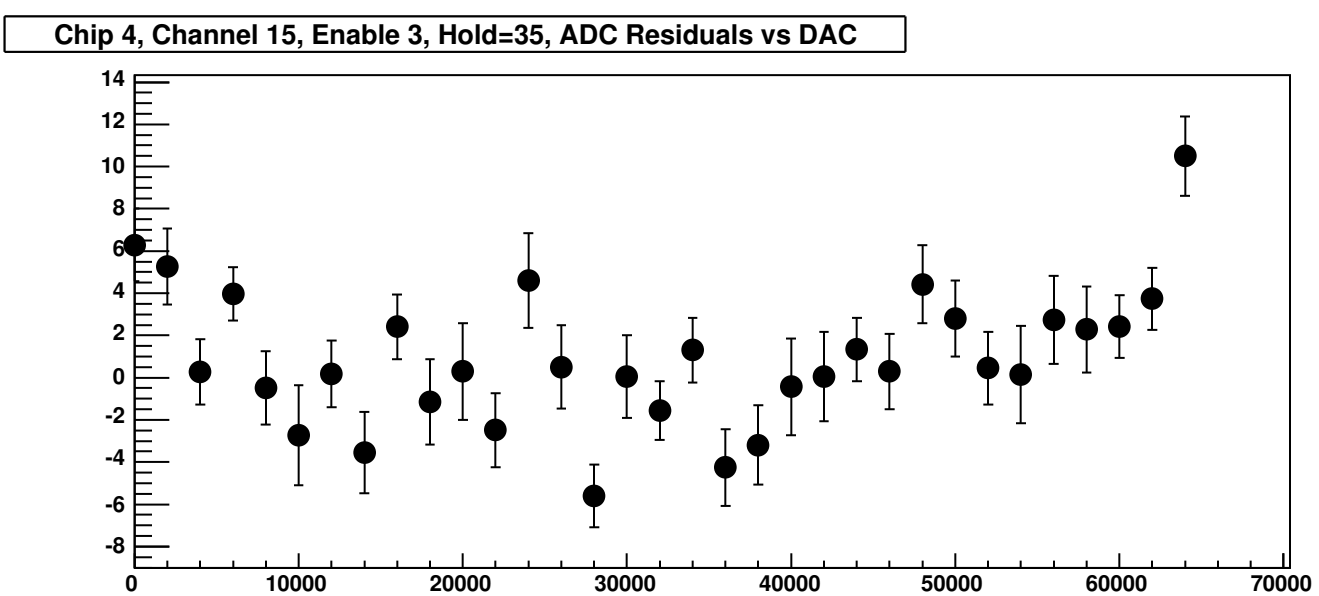

Chip 4, Channel 15, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 884.9 / 23
p0 474.7 ± 1.926
p1 0.4393 ± 0.0001628
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

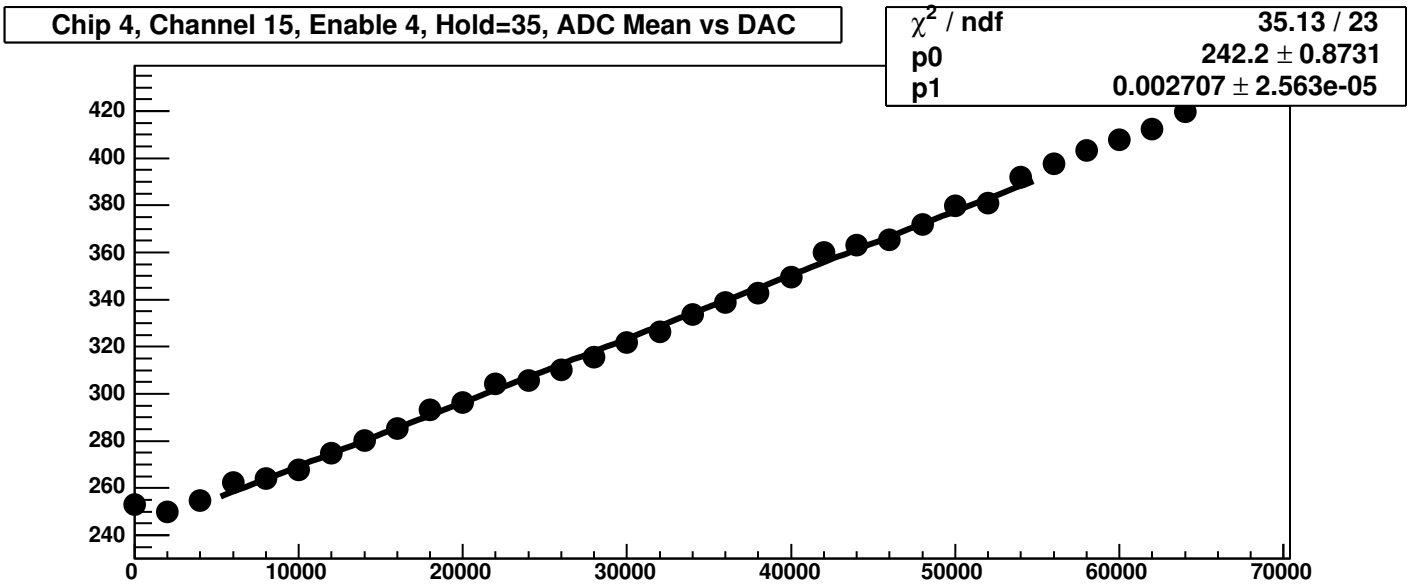

Chip 4, Channel 15, Enable 2!, Hold=35, ADC Noise vs DAC
160
140
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000
70000

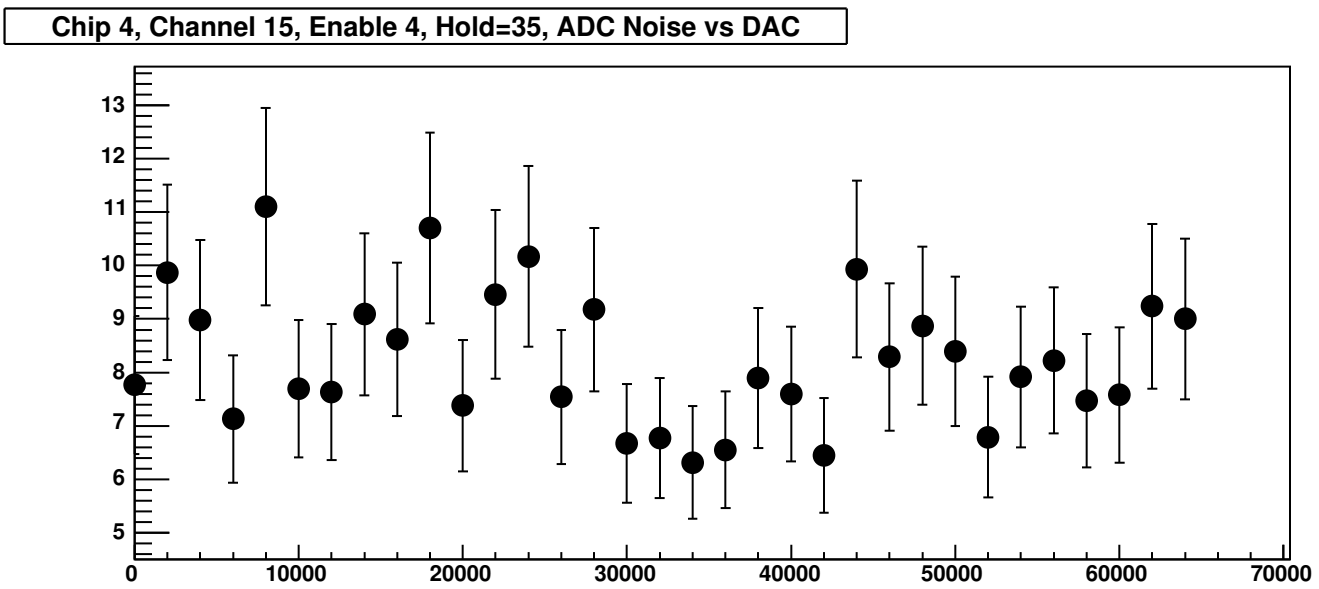

Chip 4, Channel 15, Enable 2!, Hold=35, ADC Residuals vs DAC
100
50
0
-50
-100
-150
-200
-250
0
10000
20000
30000
40000
50000
60000
70000

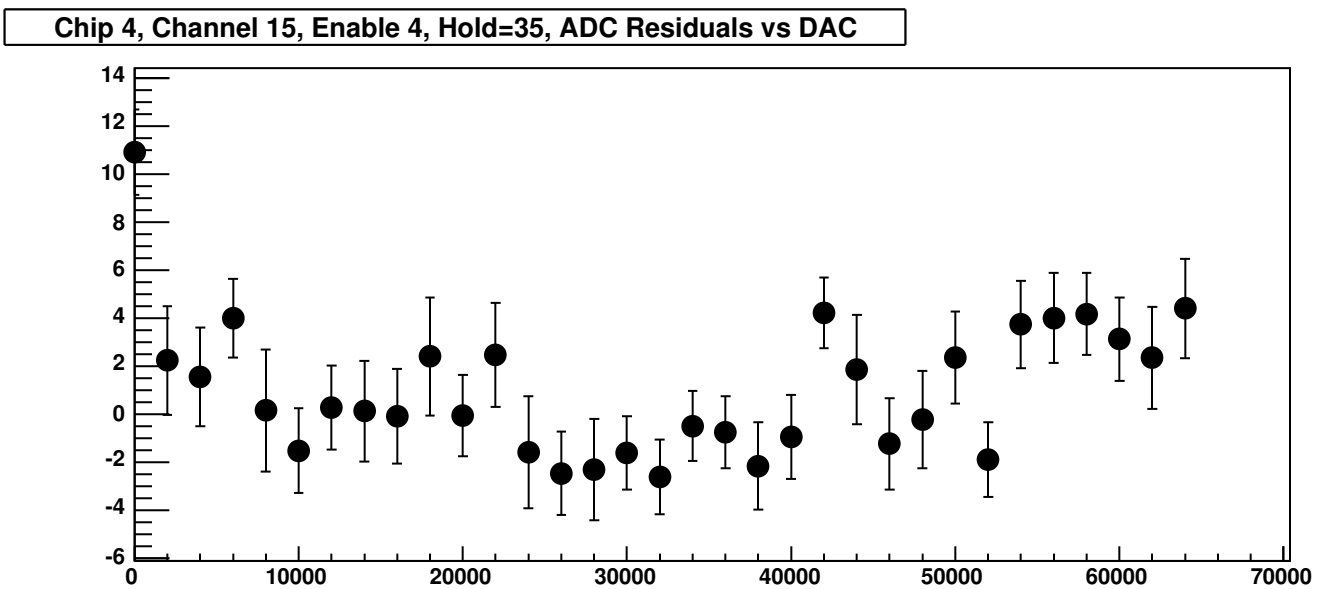

Chip 4, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 57.42 / 23
p0 244.3 ± 0.7863
p1 0.002597 ± 2.422e-05
440
420
400
380
360
340
320
300
280
260
240
0
10000
20000
30000
40000
50000
60000
70000

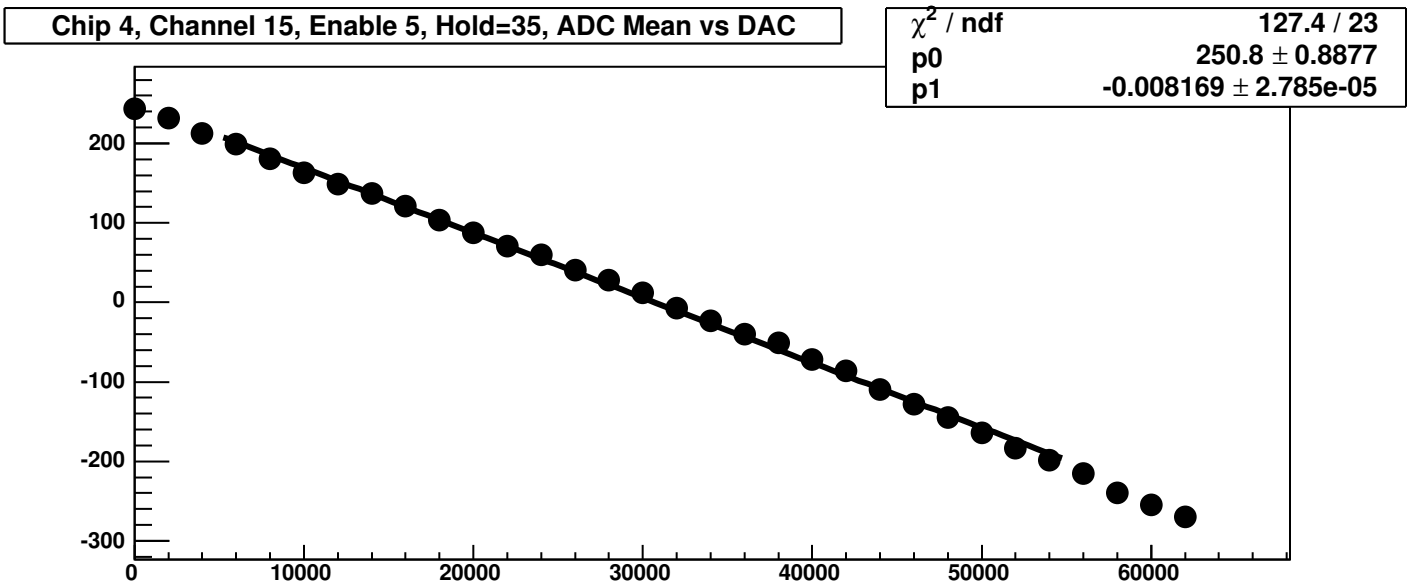

Chip 4, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

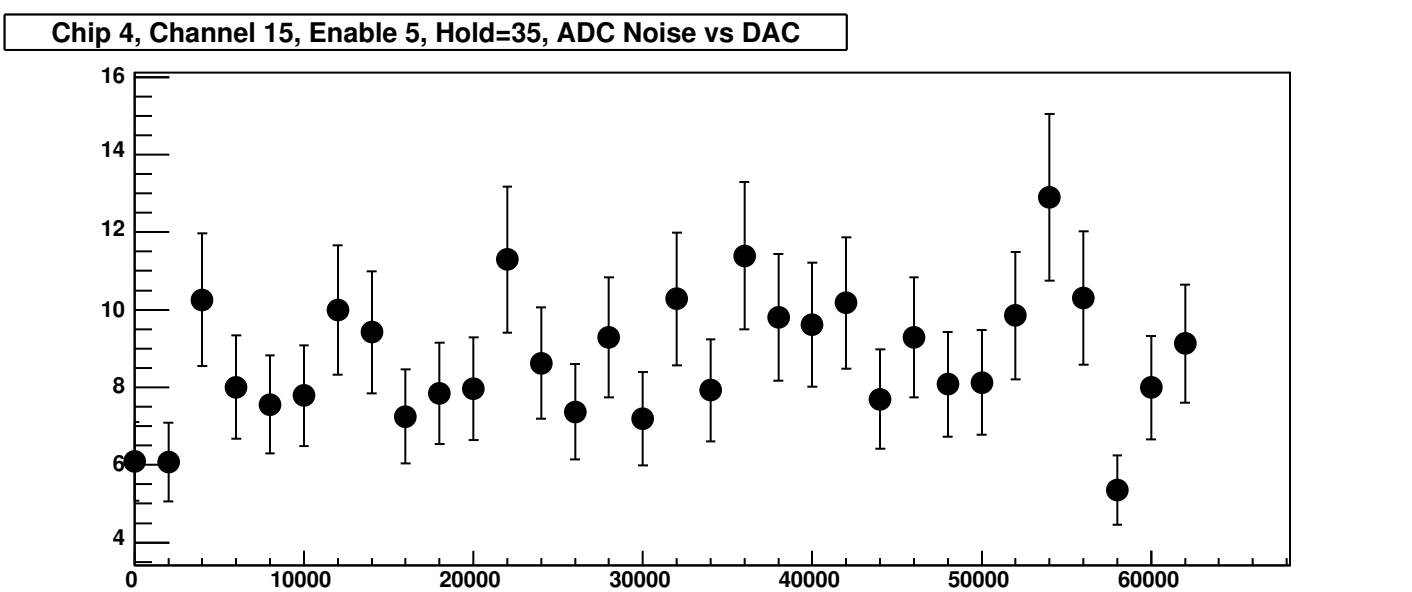

Chip 4, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

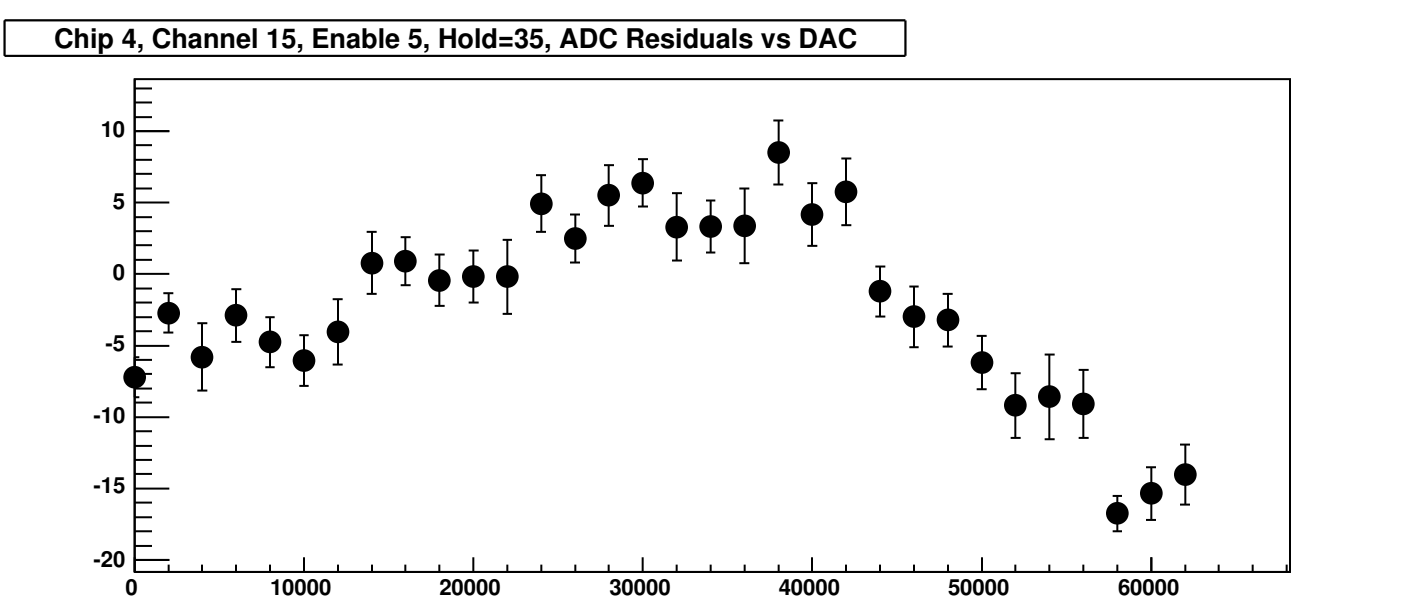

Chip 4, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 35.13 / 23
p0 242.2 ± 0.8731
p1 0.002707 ± 2.563e-05
420
400
380
360
340
320
300
280
260
240
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000

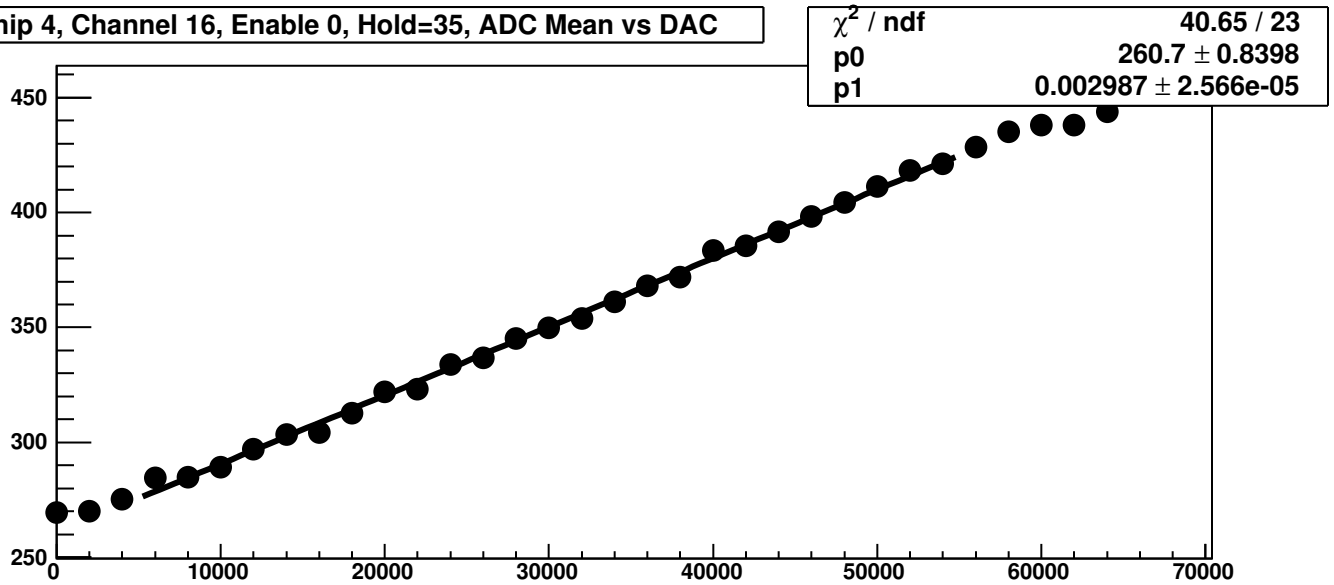

Chip 4, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
14
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 15, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 127.4 / 23
p0 250.8 ± 0.8877
p1 -0.008169 ± 2.785e-05
200
100
0
-100
-200
-300
0
10000
20000
30000
40000
50000
60000

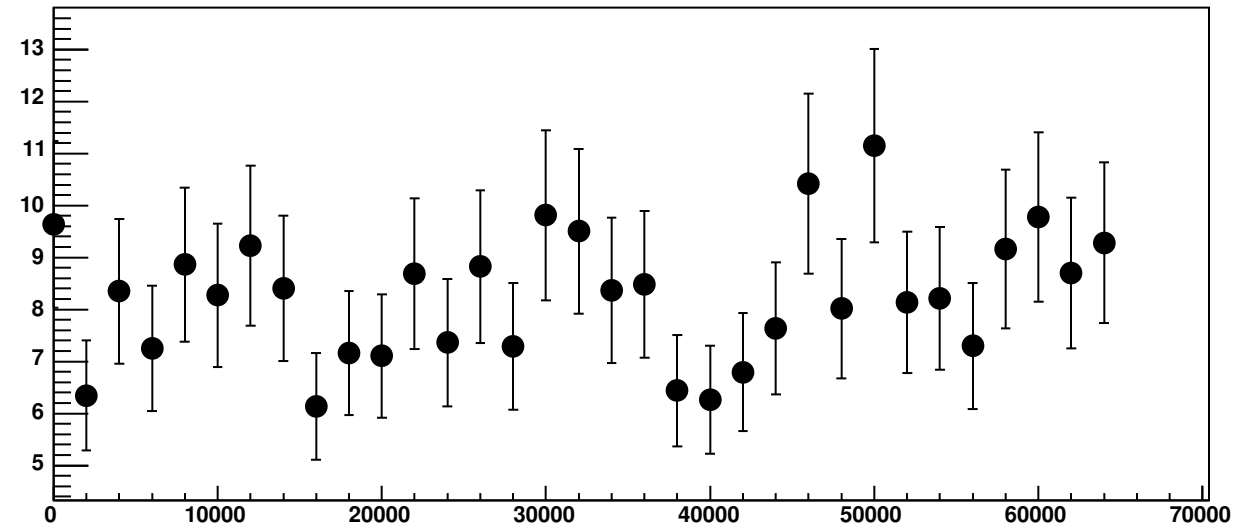

Chip 4, Channel 15, Enable 5, Hold=35, ADC Noise vs DAC
16
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 15, Enable 5, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
-20
0
10000
20000
30000
40000
50000
60000

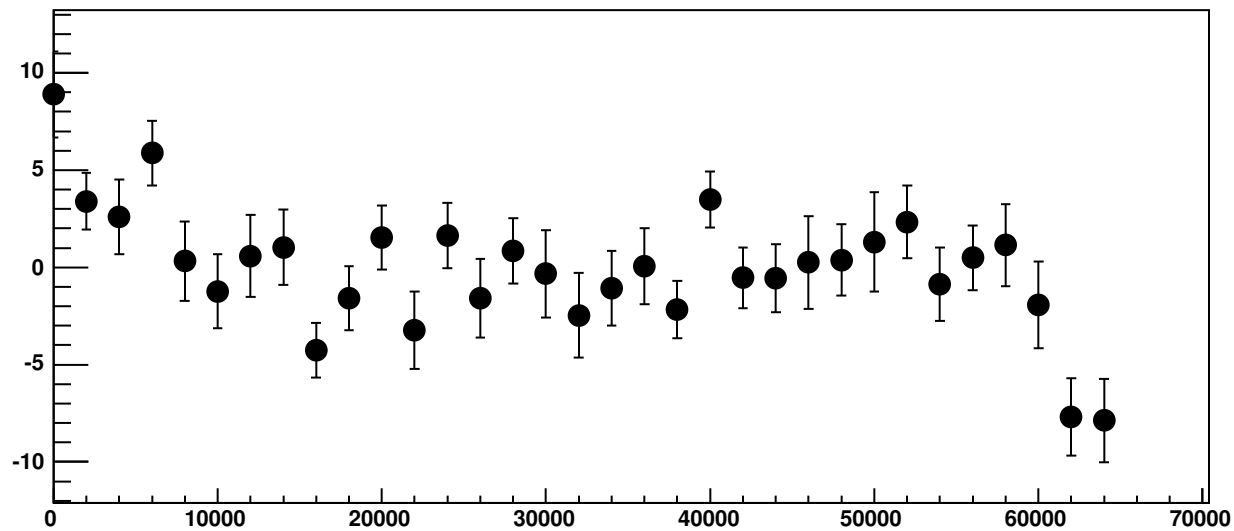
Chip 4, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC

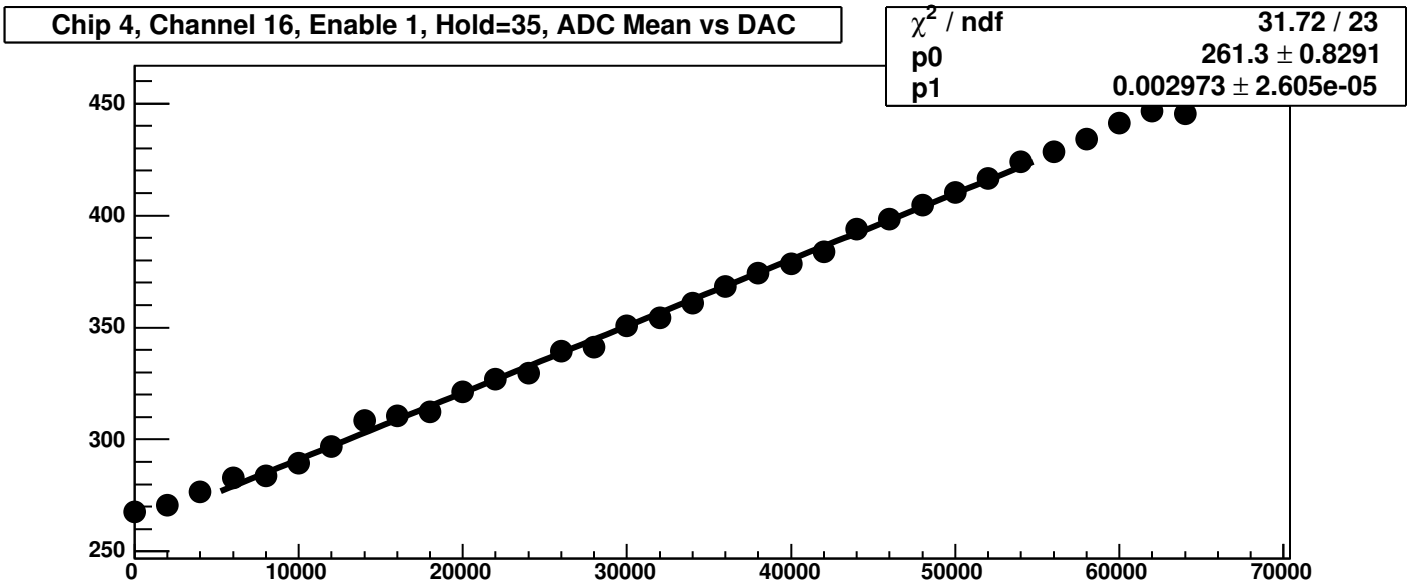


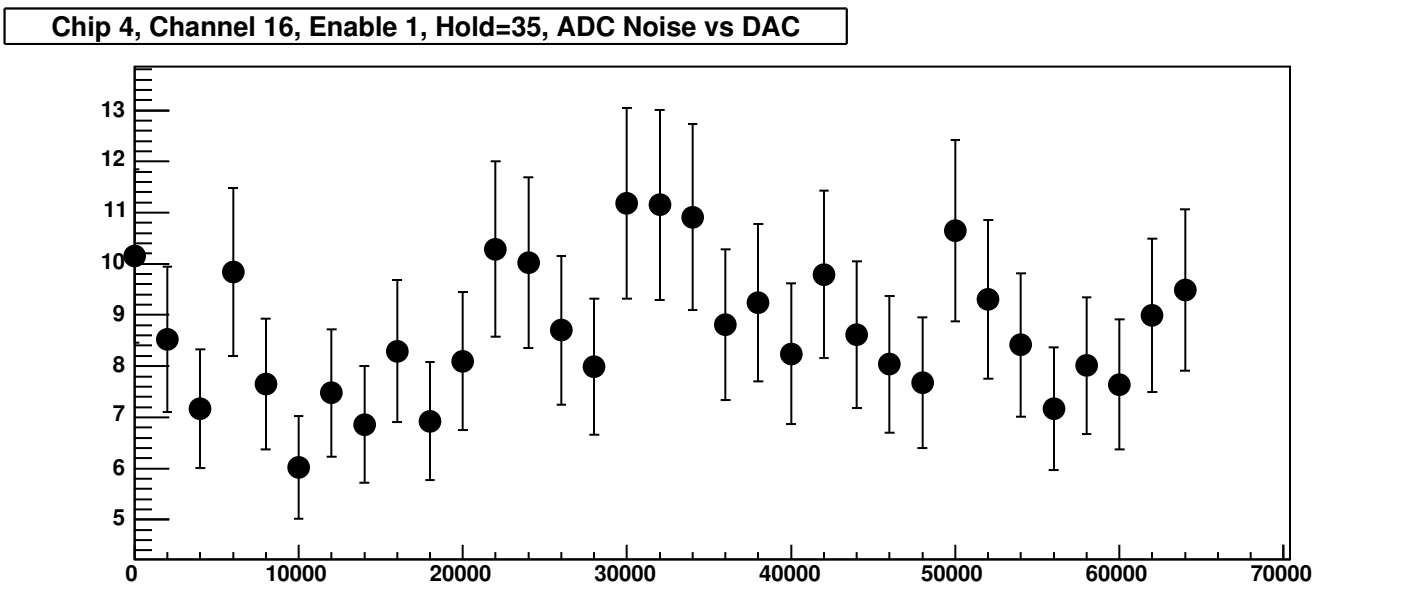
Chip 4, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC

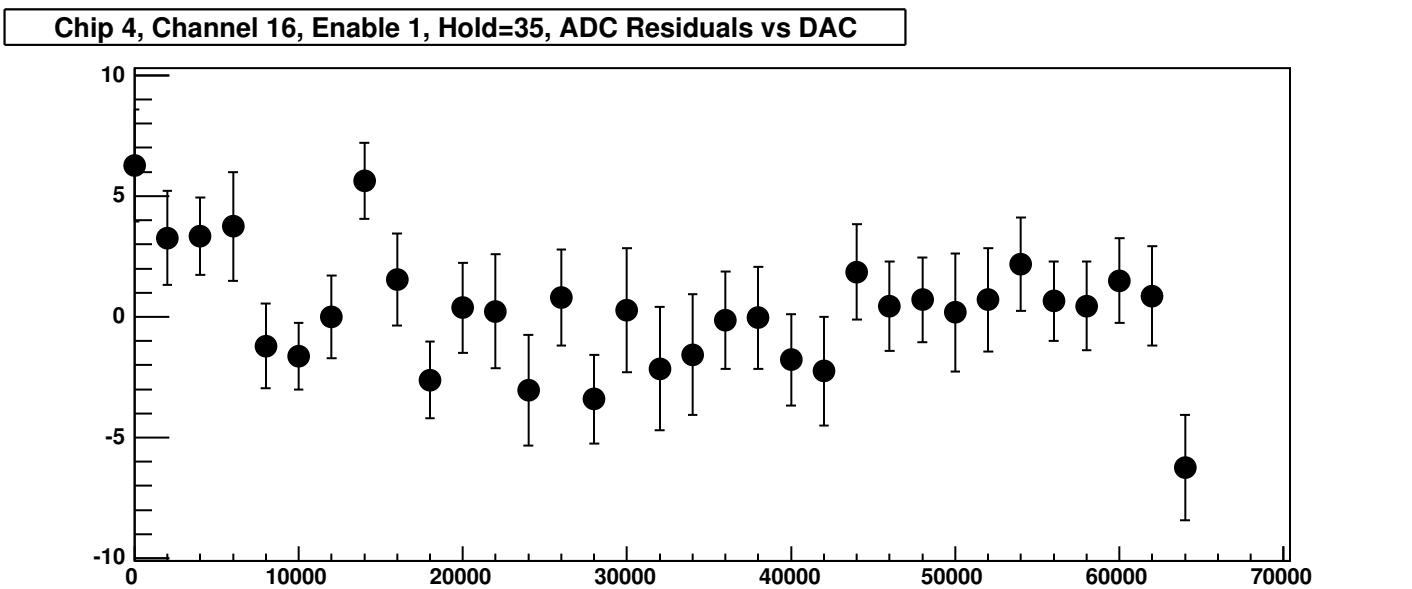


Chip 4, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC

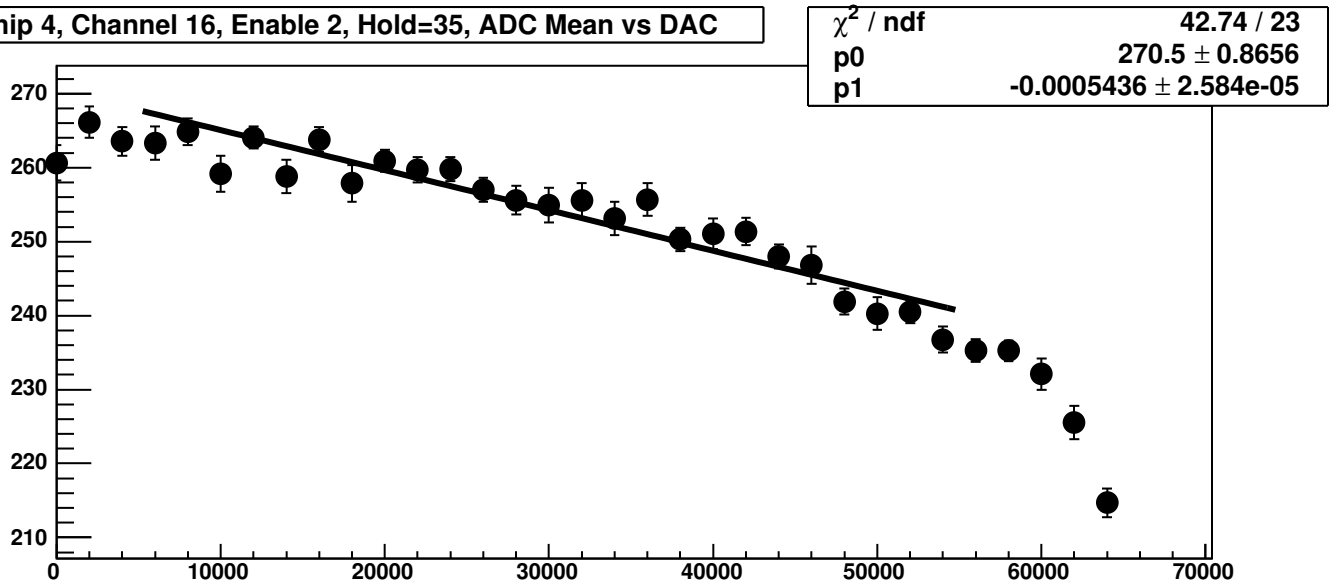



Chip 4, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 31.72 / 23
p0 261.3 ± 0.8291
p1 0.002973 ± 2.605e-05
450
400
350
300
250
0
10000
20000
30000
40000
50000
60000
70000

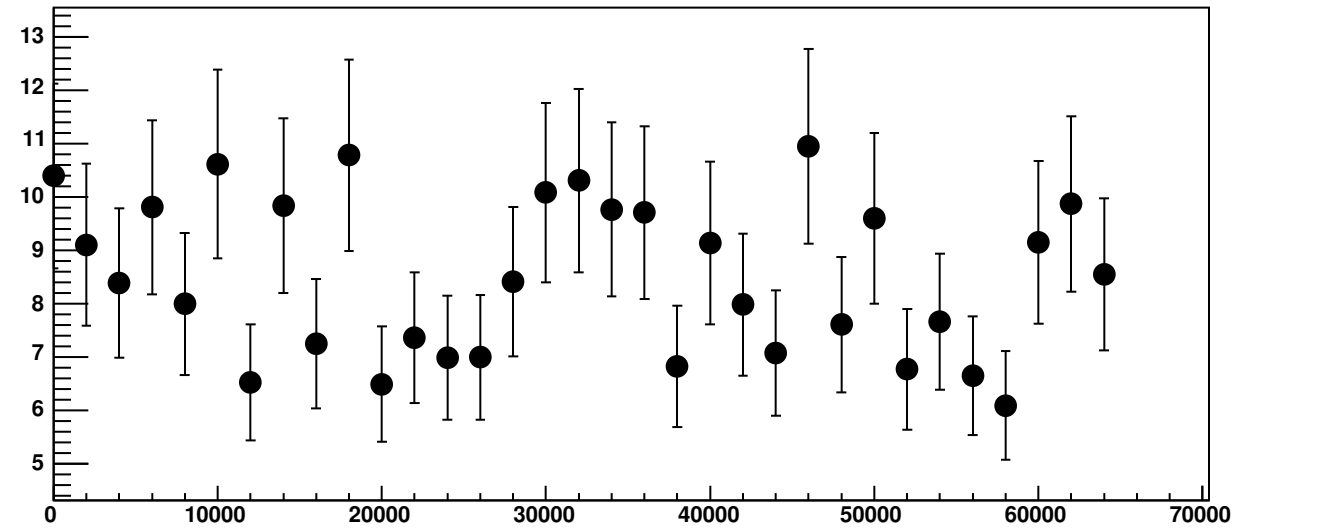

Chip 4, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

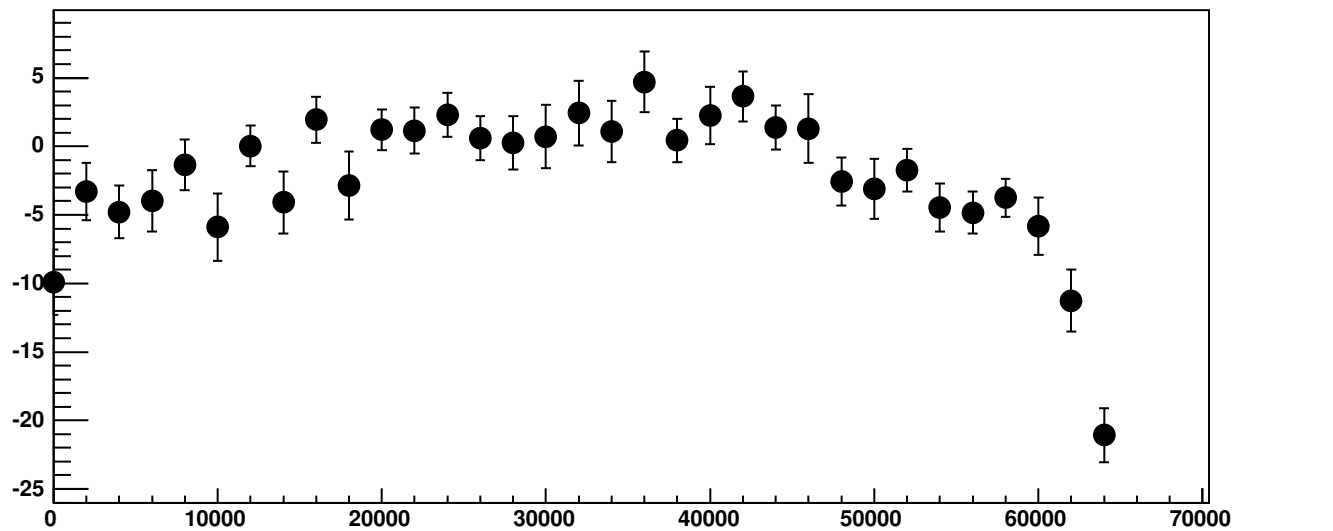
Chip 4, Channel 16, Enable 2, Hold=35, ADC Mean vs DAC

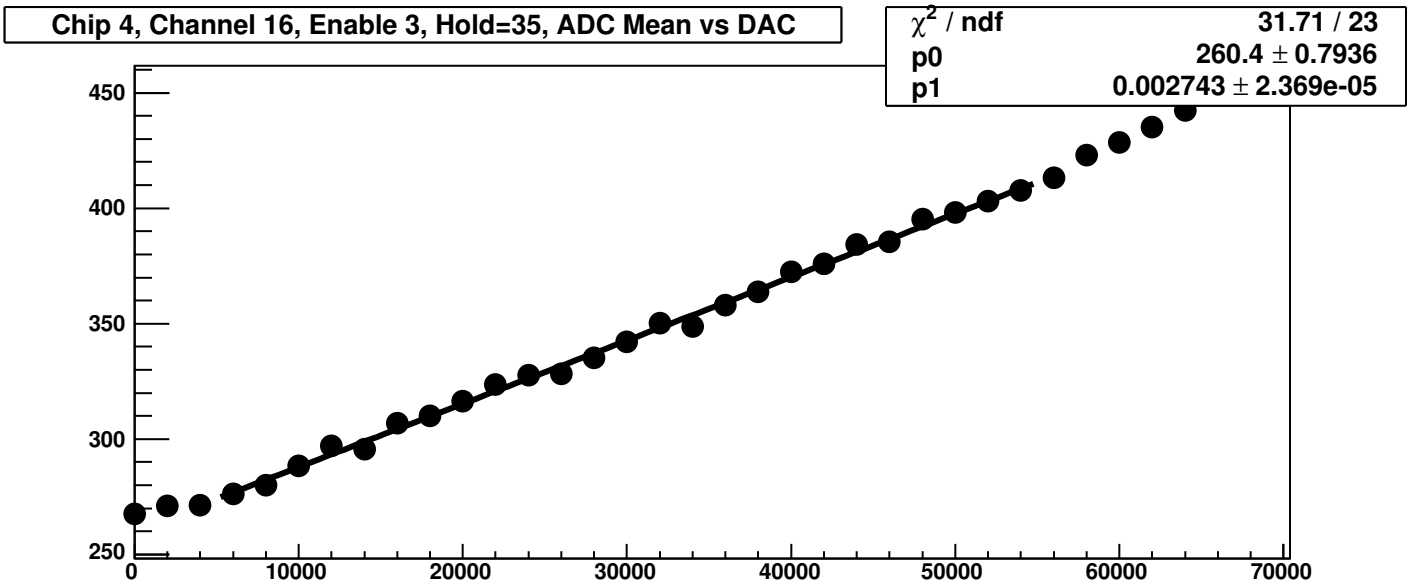


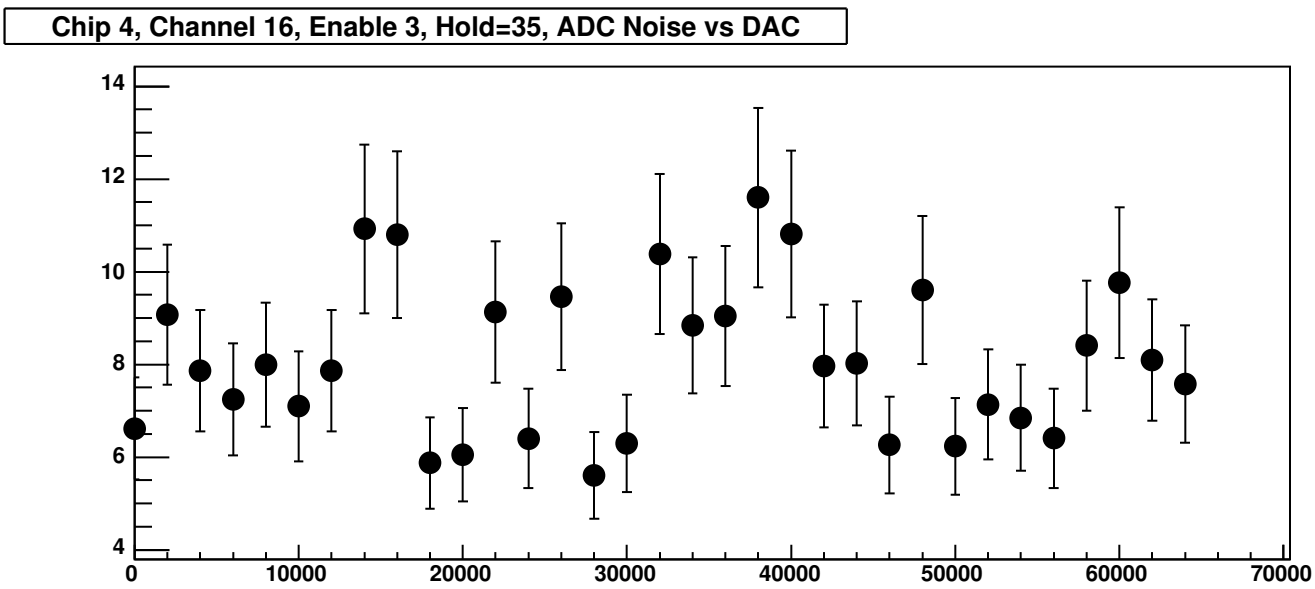
Chip 4, Channel 16, Enable 2, Hold=35, ADC Noise vs DAC

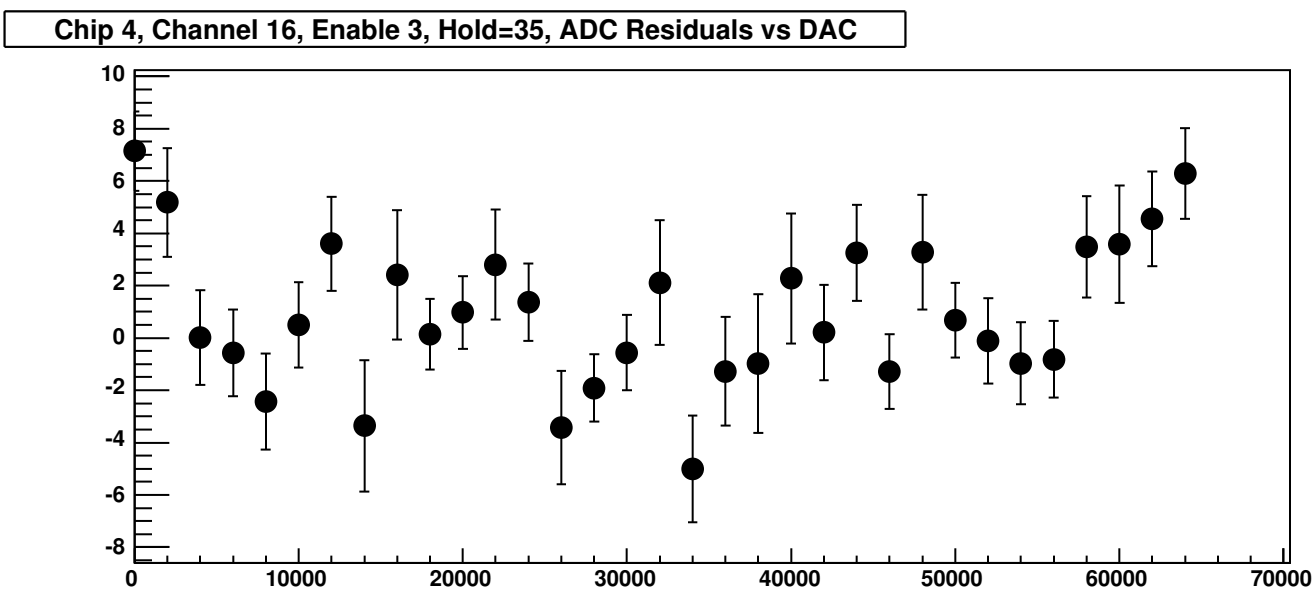


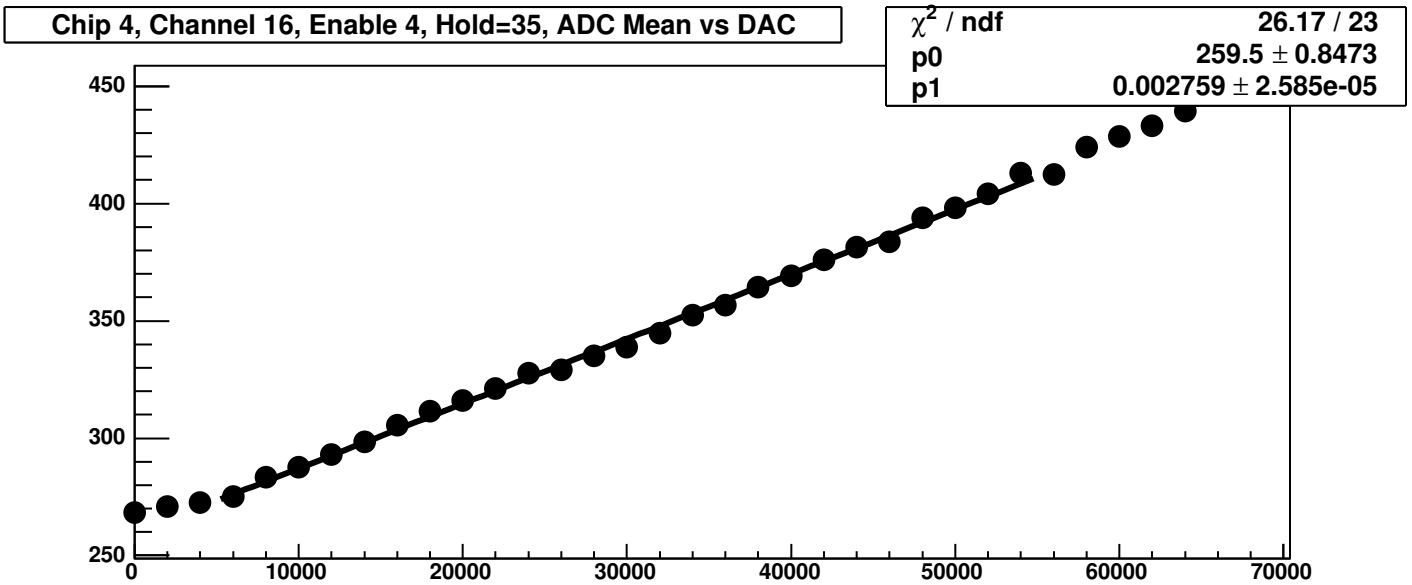
Chip 4, Channel 16, Enable 2, Hold=35, ADC Residuals vs DAC

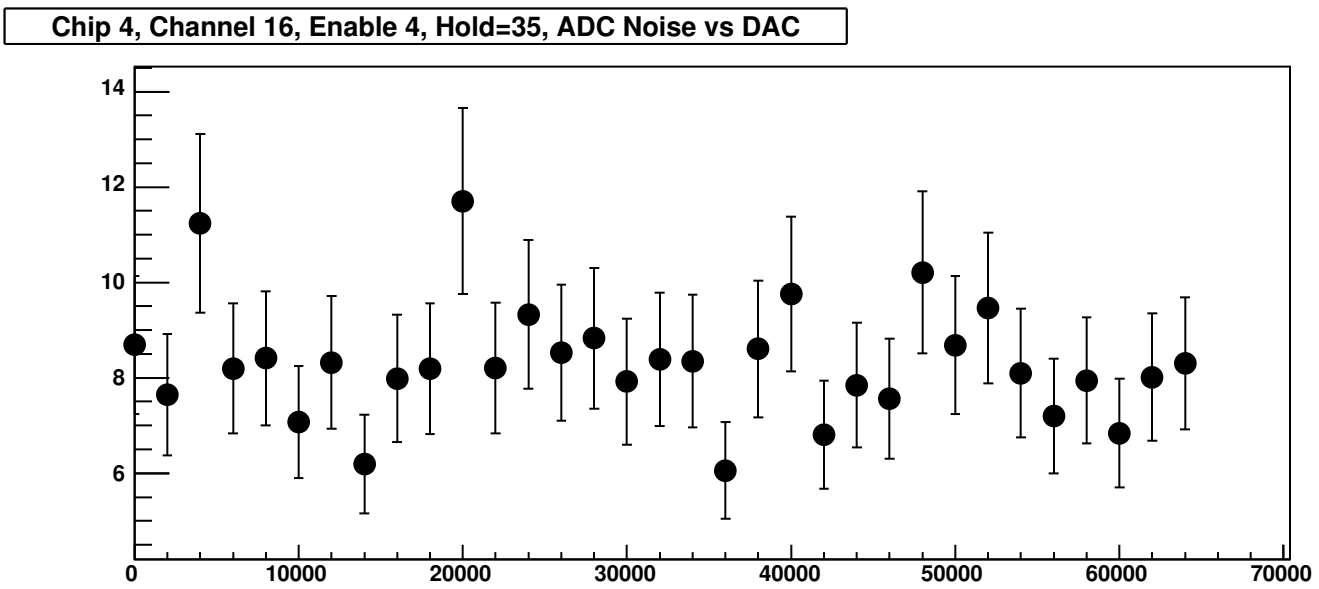


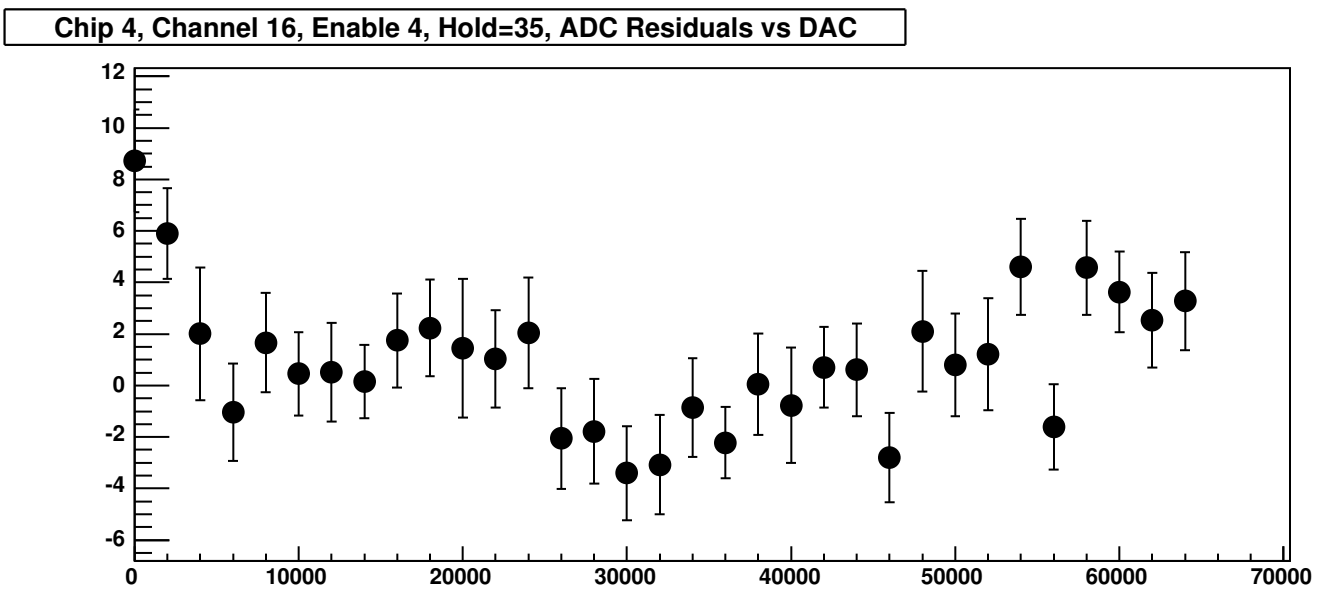

Chip 4, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 31.71 / 23
p0 260.4 ± 0.7936
p1 0.002743 ± 2.369e-05
450
400
350
300
250
0
10000
20000
30000
40000
50000
60000
70000

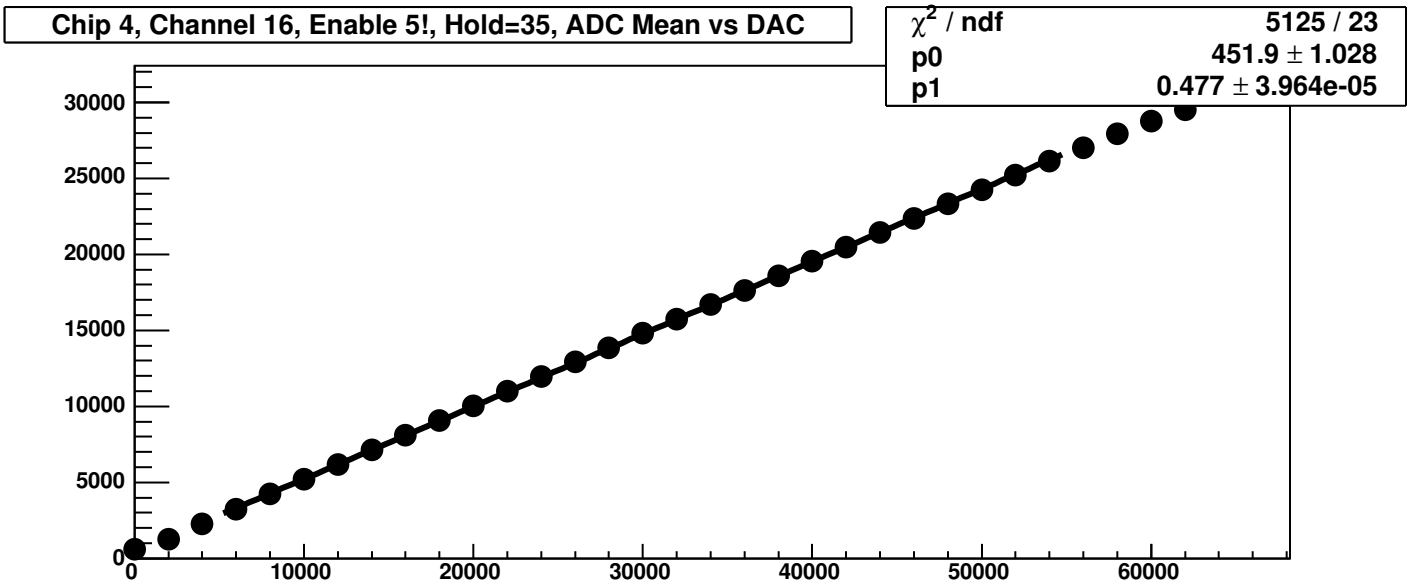

Chip 4, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

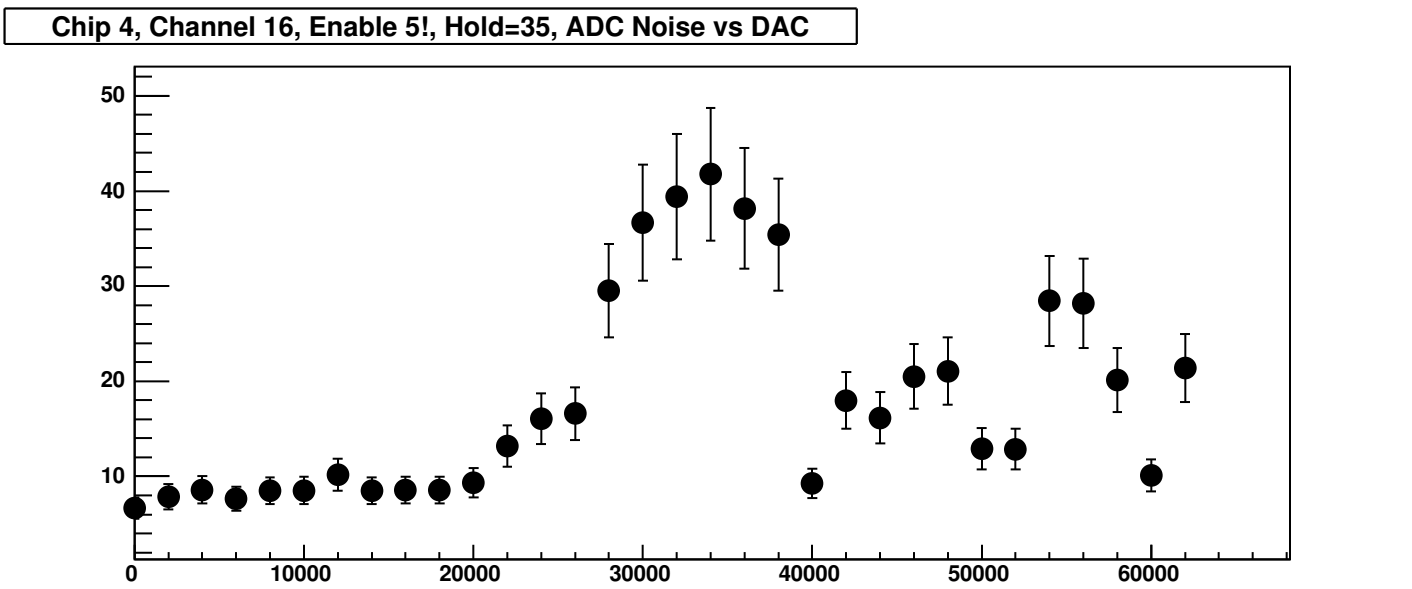

Chip 4, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
10
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

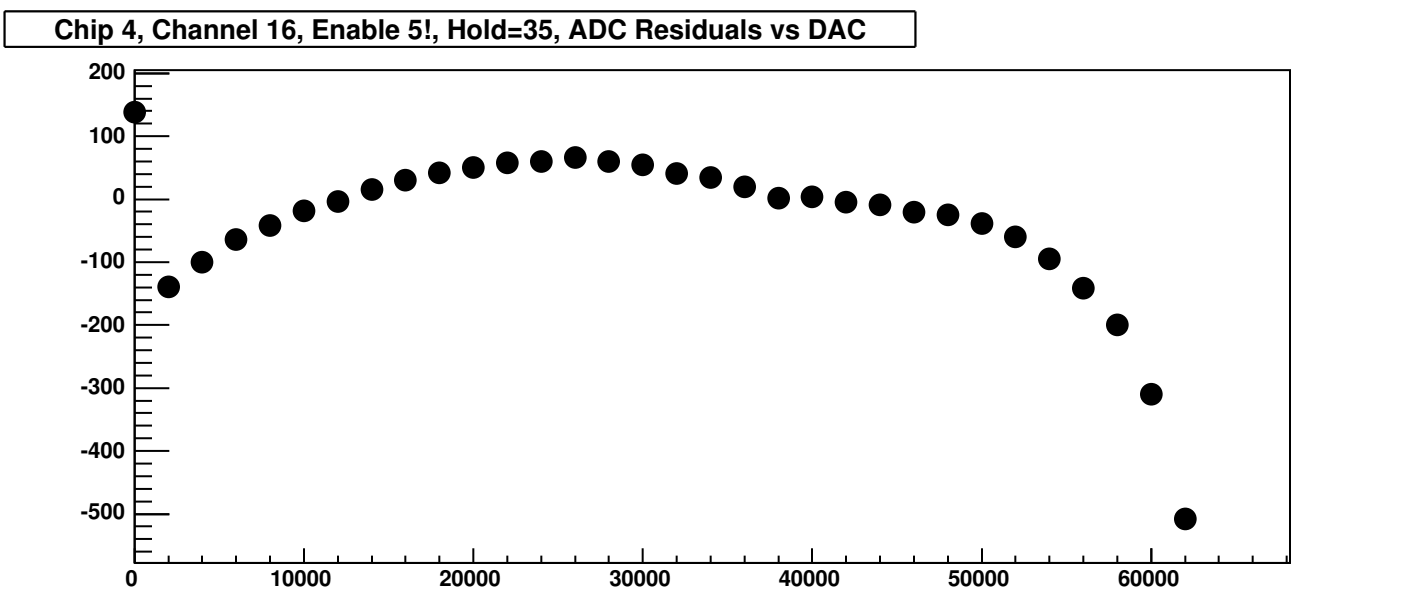

Chip 4, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 26.17 / 23
p0 259.5 ± 0.8473
p1 0.002759 ± 2.585e-05
450
400
350
300
250
0
10000
20000
30000
40000
50000
60000
70000

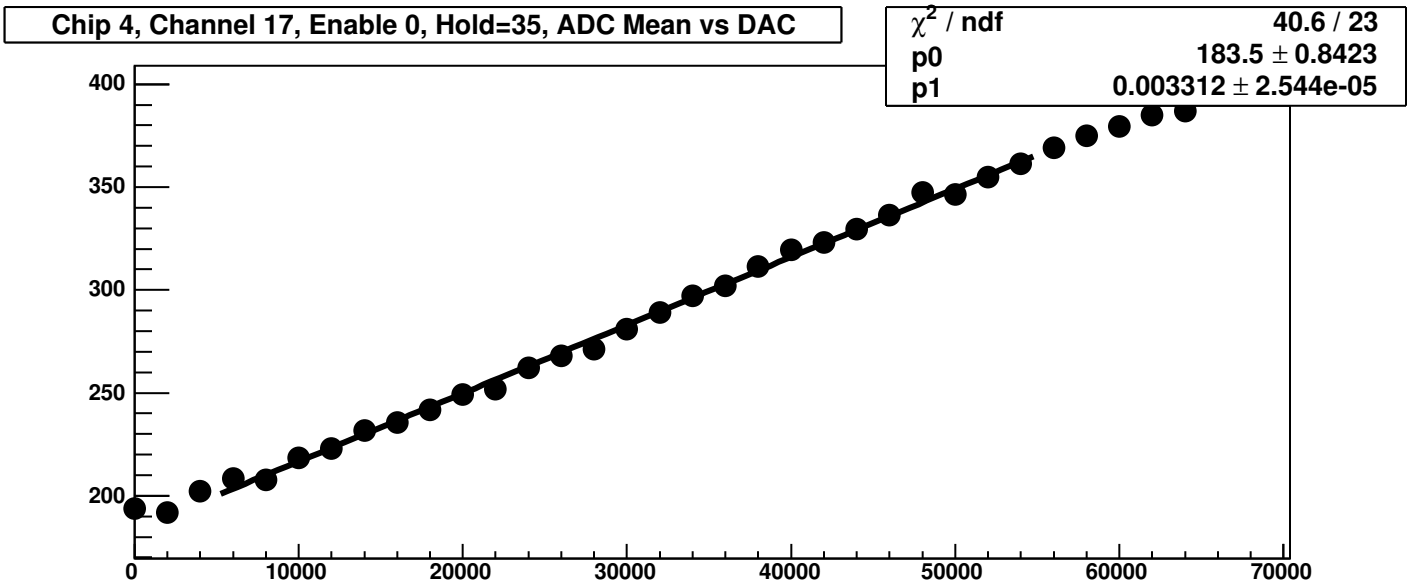

Chip 4, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
10000
20000
30000
40000
50000
60000
70000

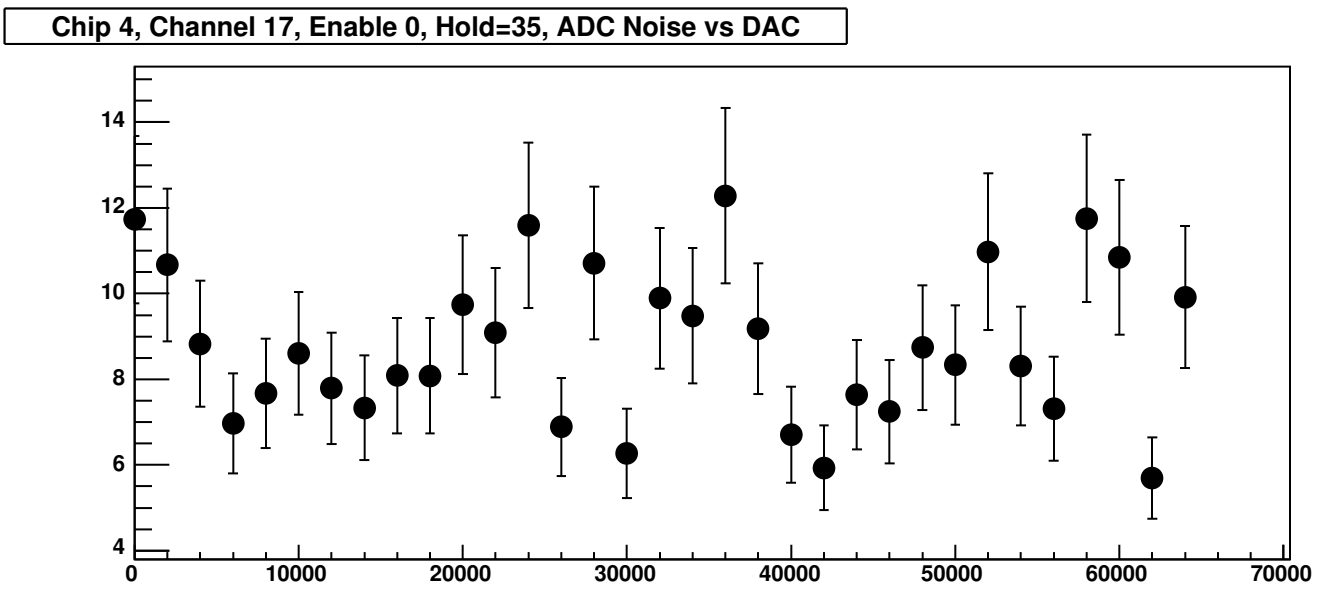

Chip 4, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

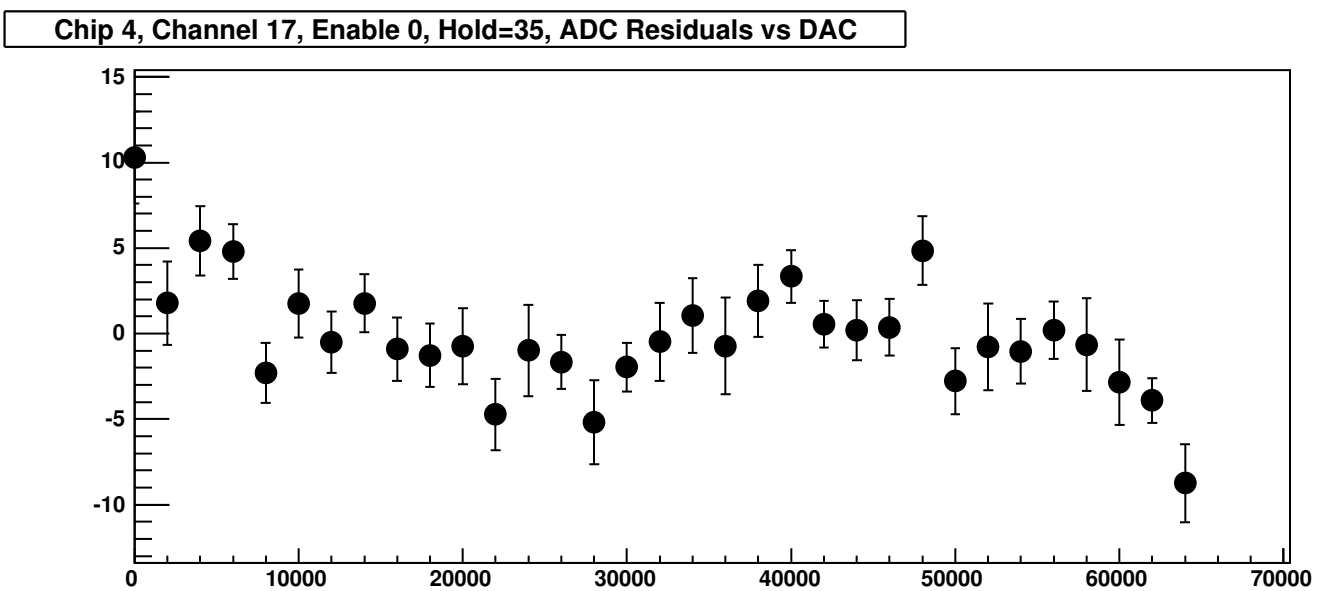

Chip 4, Channel 16, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 5125 / 23
p0 451.9 ± 1.028
p1 0.477 ± 3.964e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 16, Enable 5!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000

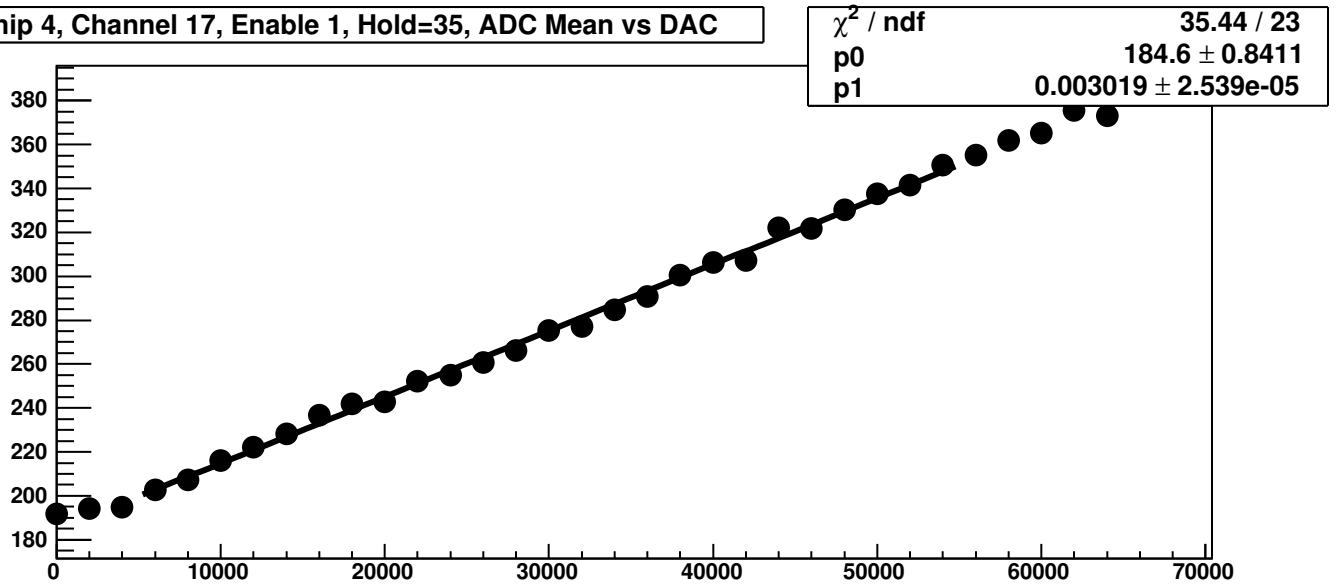

Chip 4, Channel 16, Enable 5!, Hold=35, ADC Residuals vs DAC
200
100
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 40.6 / 23
p0 183.5 ± 0.8423
p1 0.003312 ± 2.544e-05
400
350
300
250
200
0
10000
20000
30000
40000
50000
60000
70000

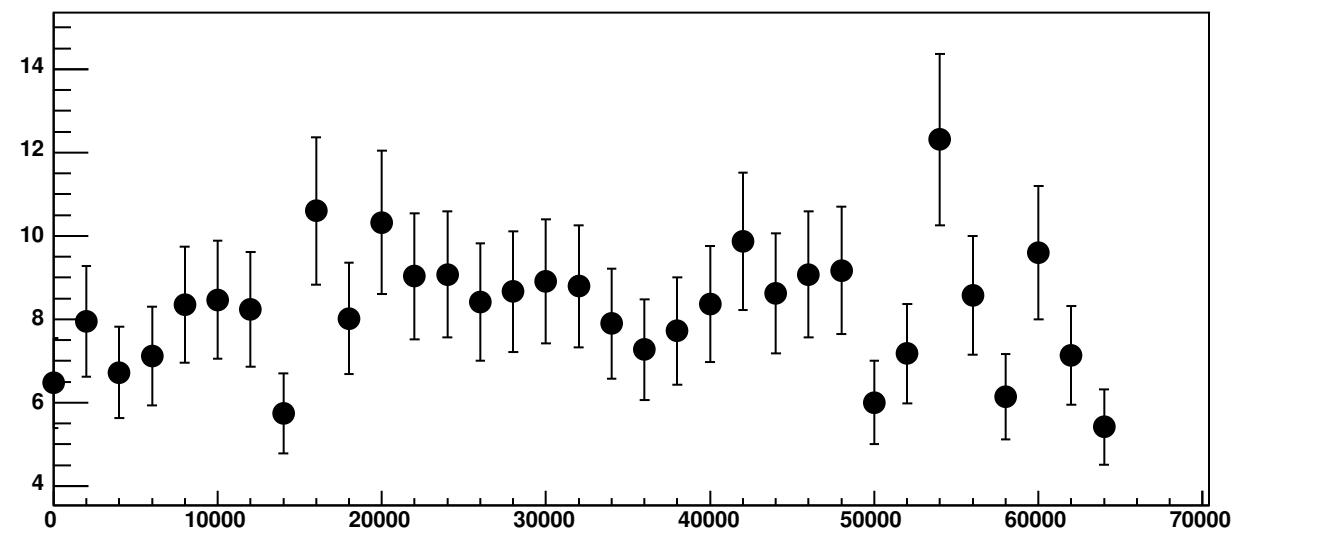

Chip 4, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
15
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

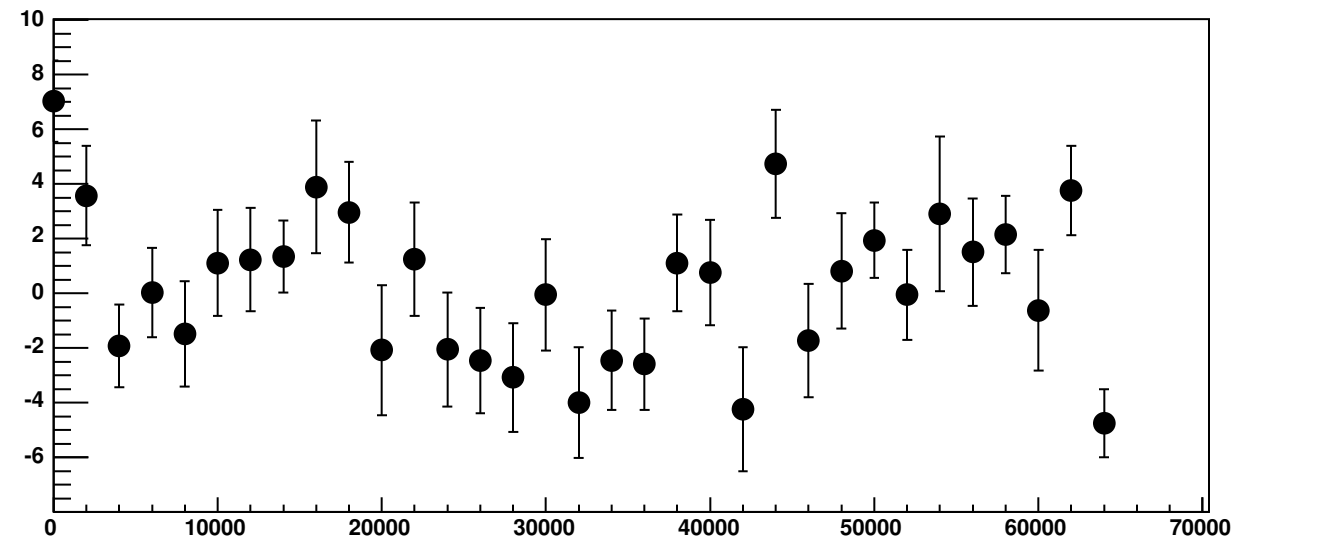
Chip 4, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

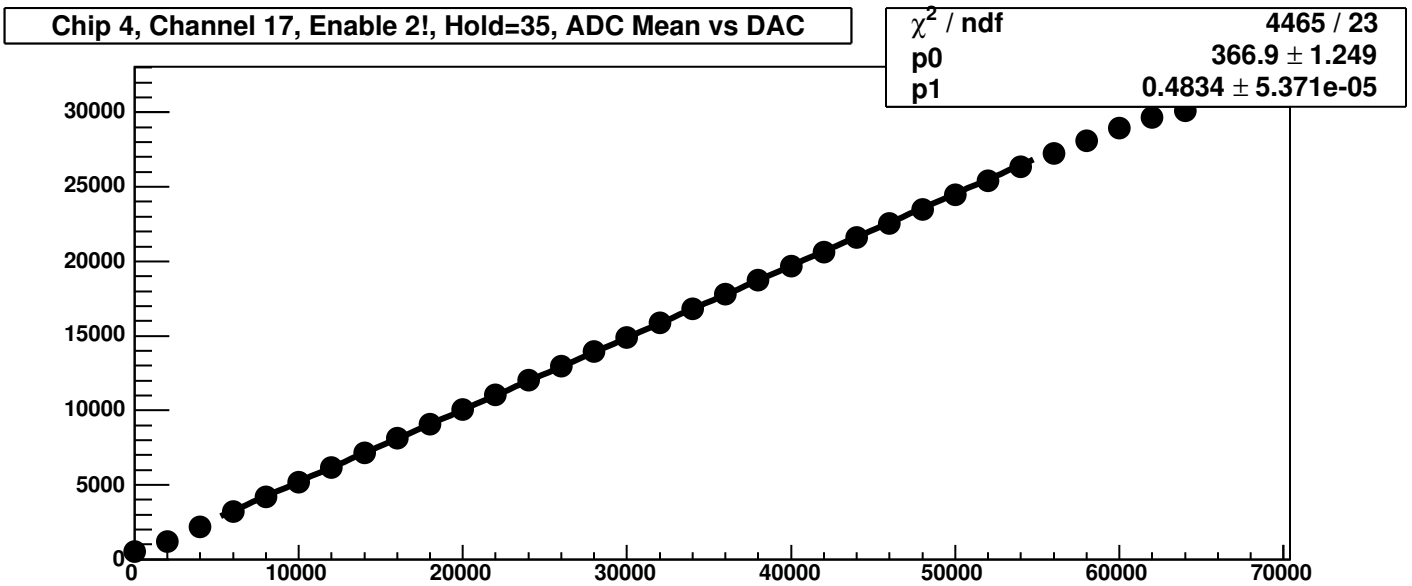


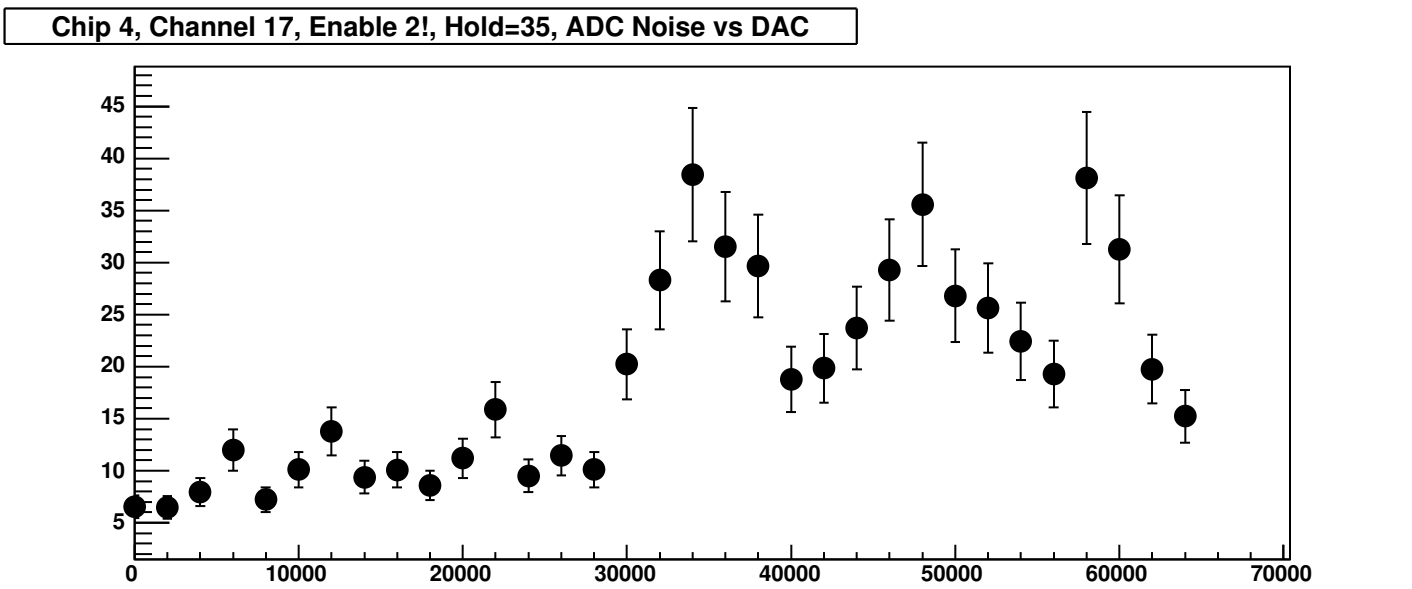
Chip 4, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

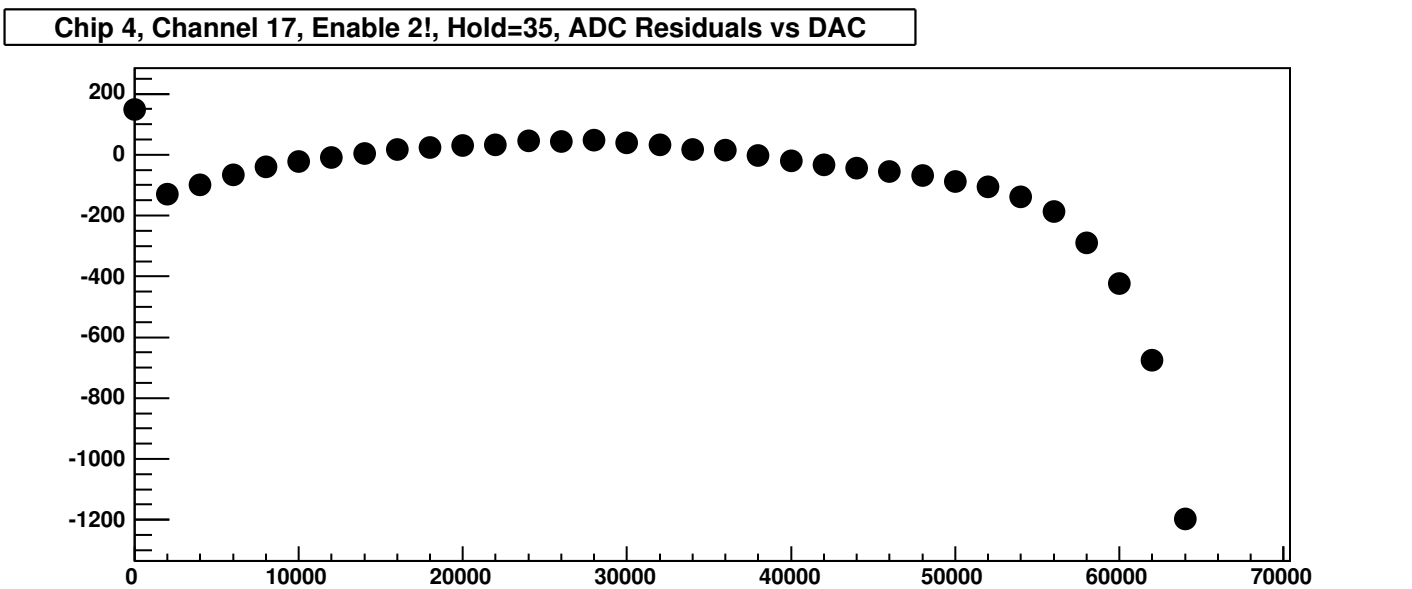


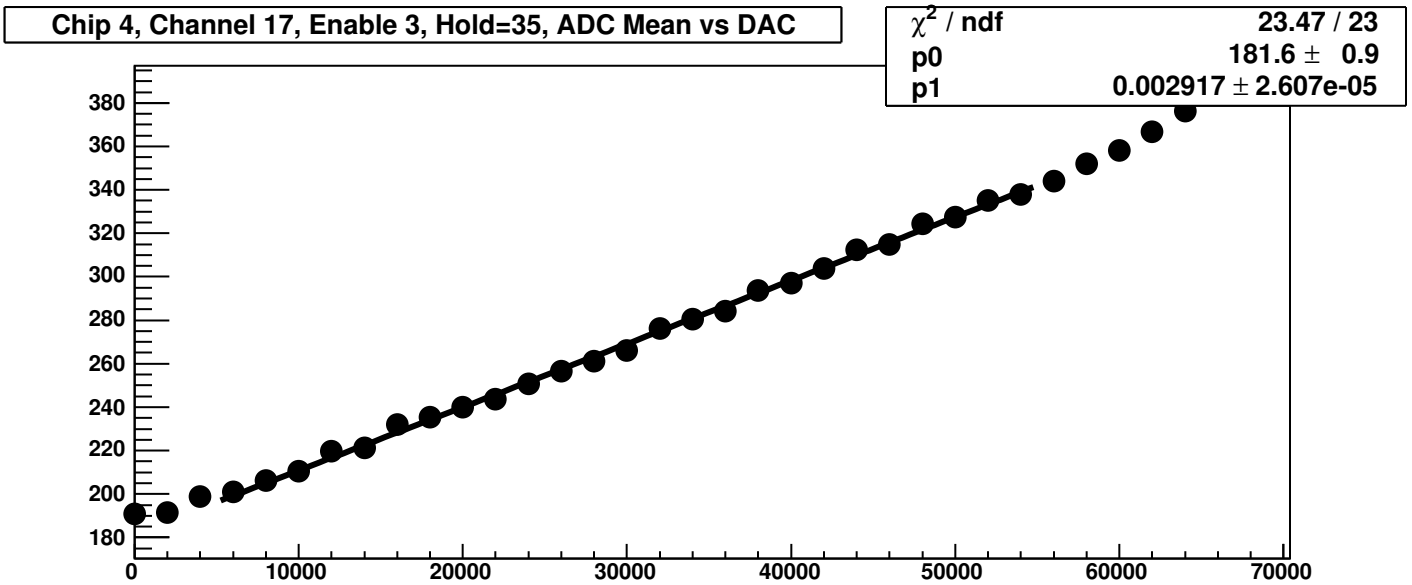
Chip 4, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

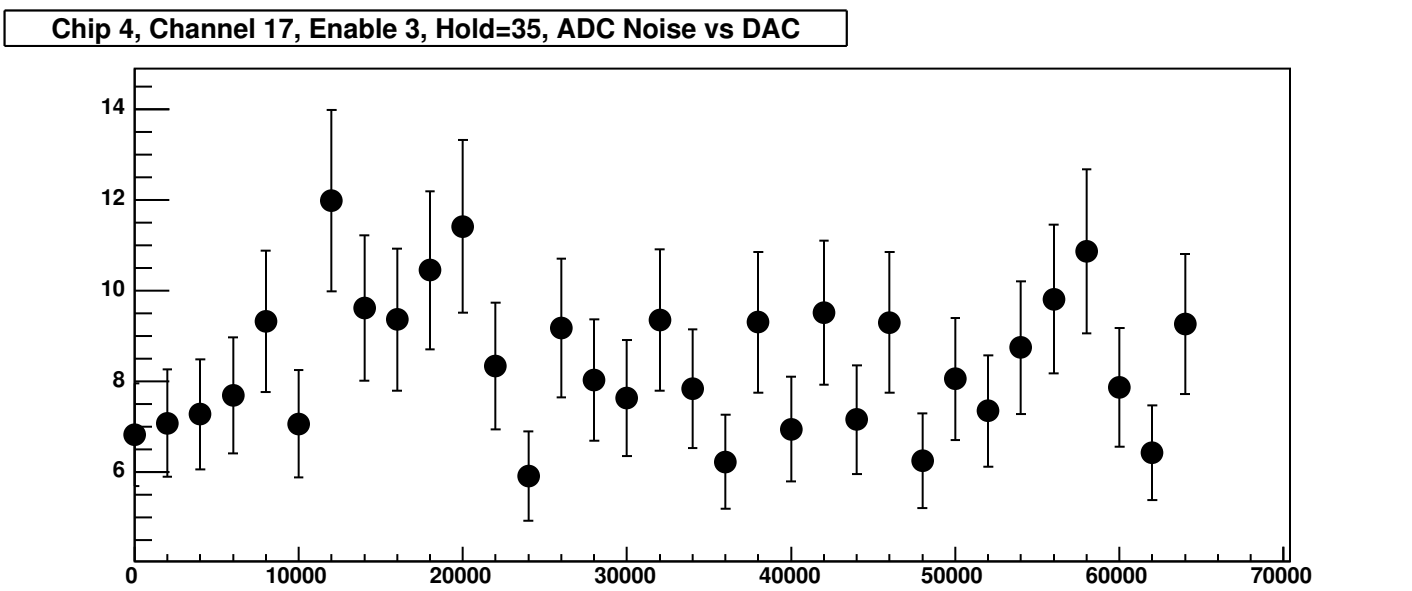


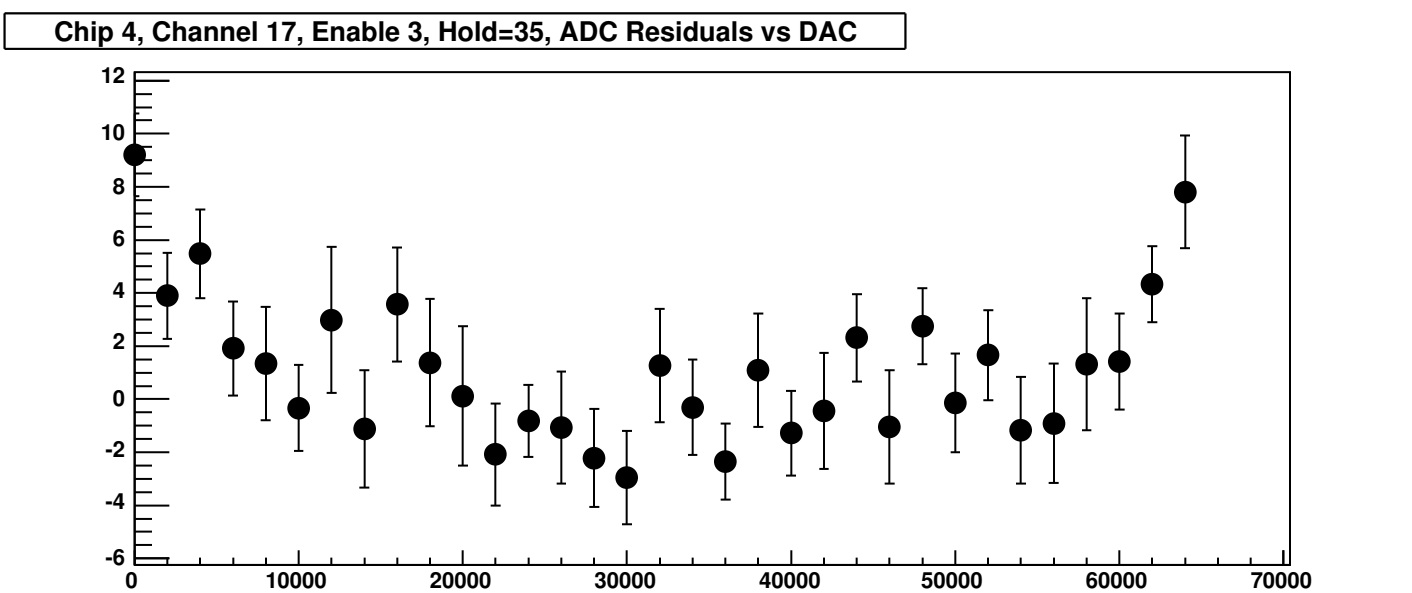

Chip 4, Channel 17, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 4465 / 23
p0 366.9 ± 1.249
p1 0.4834 ± 5.371e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

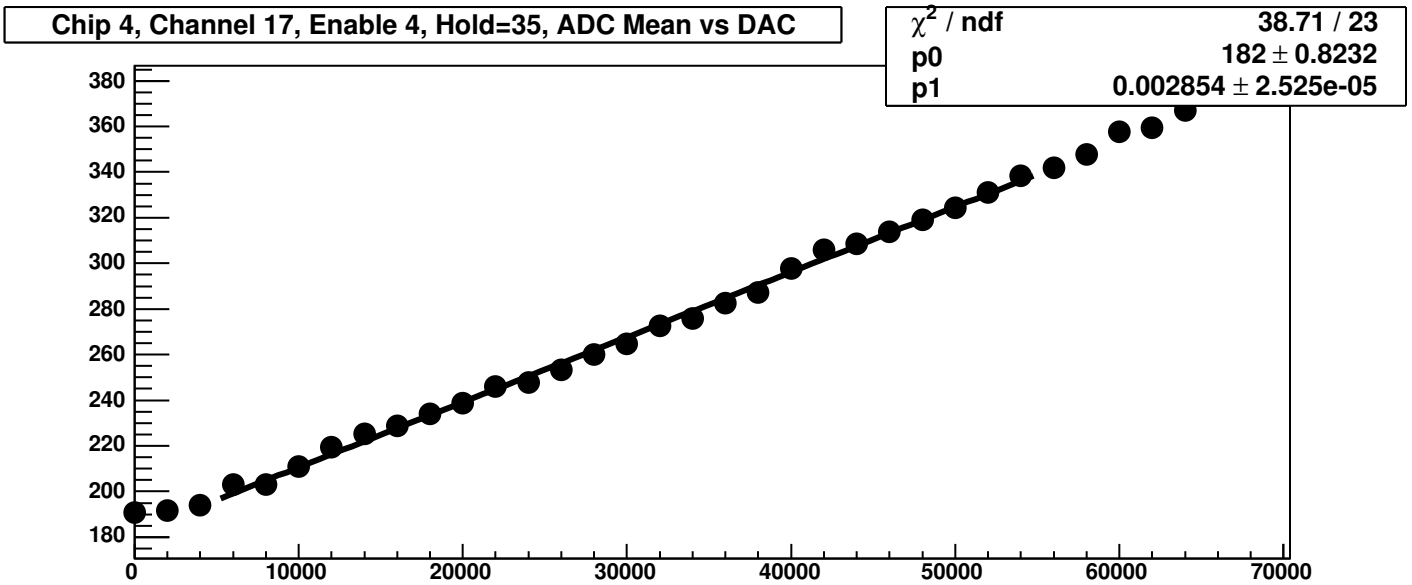

Chip 4, Channel 17, Enable 2!, Hold=35, ADC Noise vs DAC
45
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000
70000

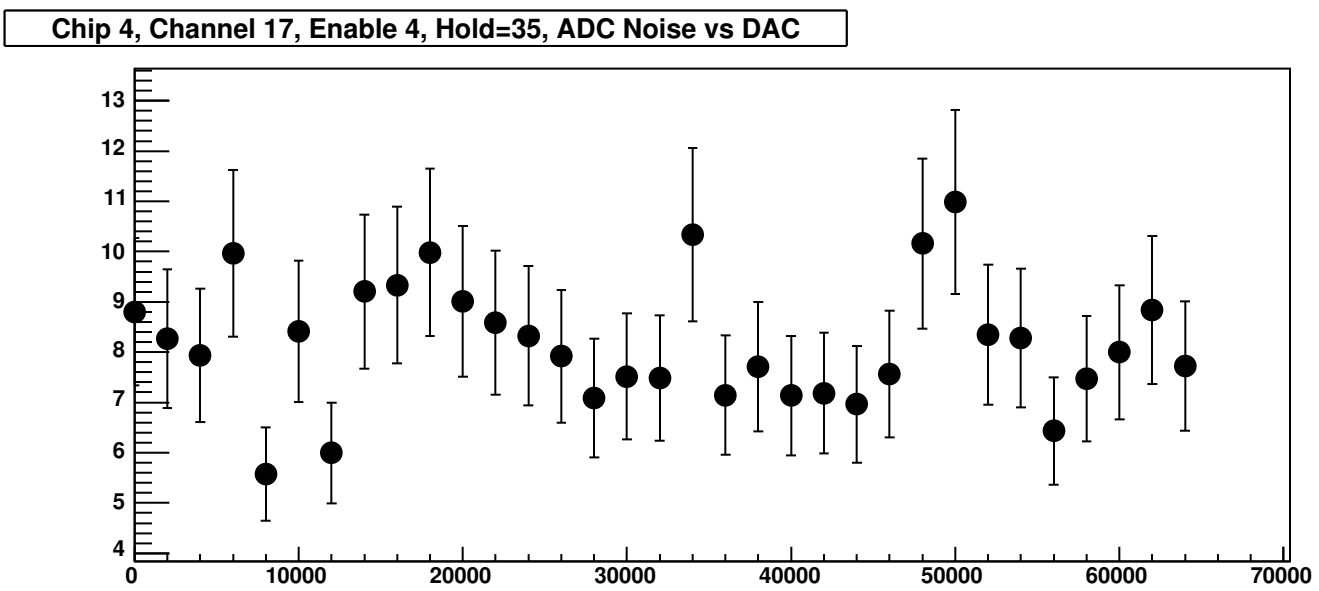

Chip 4, Channel 17, Enable 2!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
0
10000
20000
30000
40000
50000
60000
70000

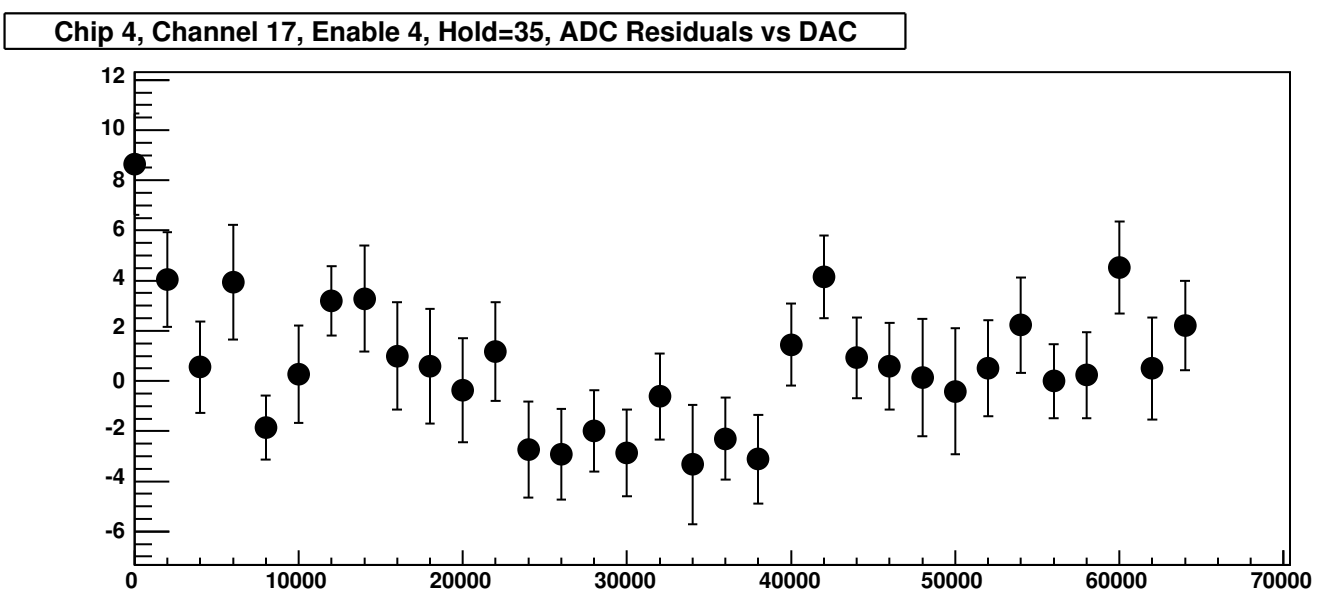

Chip 4, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.47 / 23
p0 181.6 ± 0.9
p1 0.002917 ± 2.607e-05
380
360
340
320
300
280
260
240
220
200
180
0
10000
20000
30000
40000
50000
60000
70000

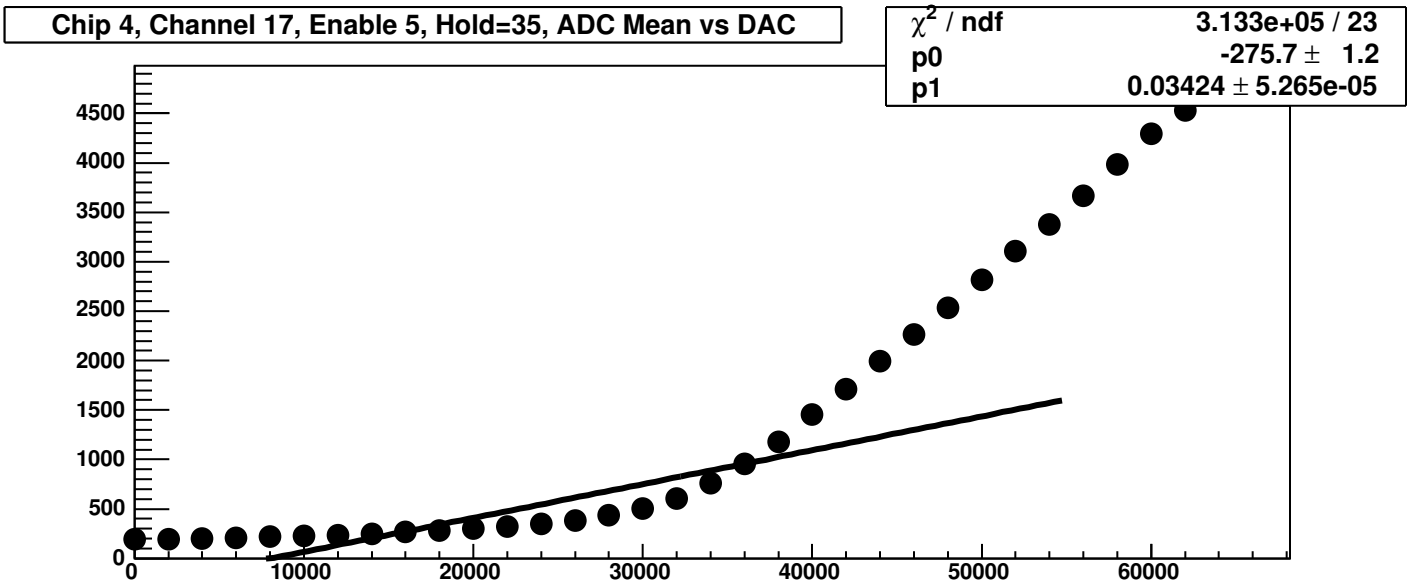

Chip 4, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
14
12
10
8
6
0
10000
20000
30000
40000
50000
60000
70000

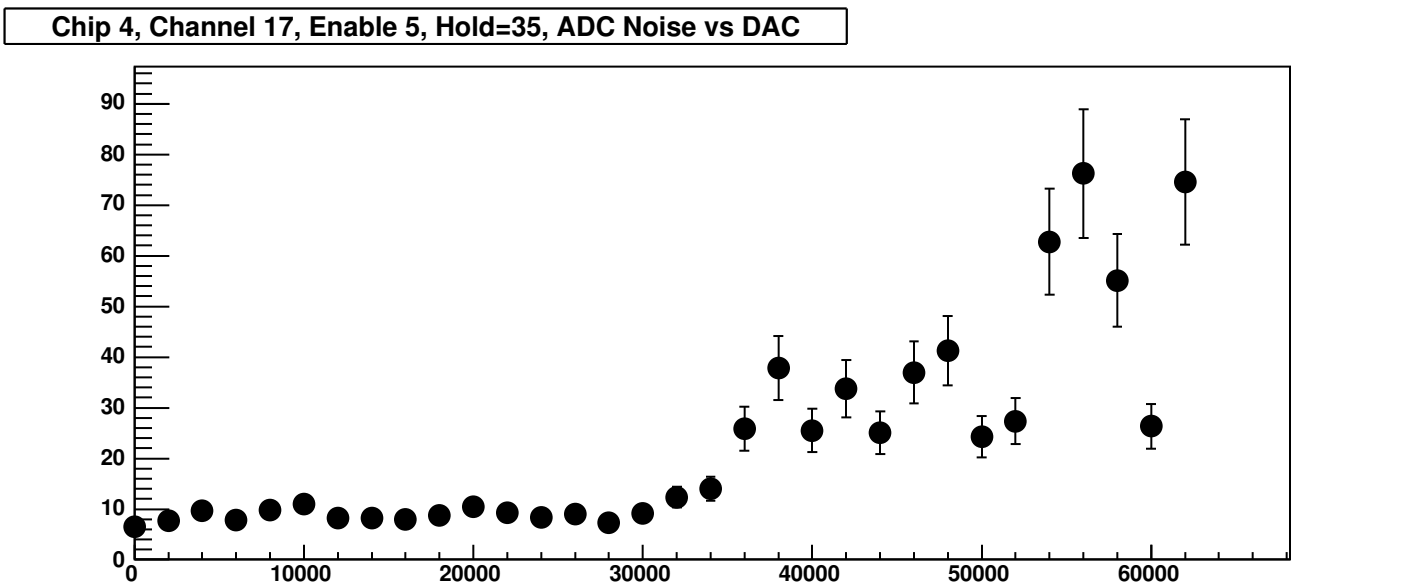

Chip 4, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000

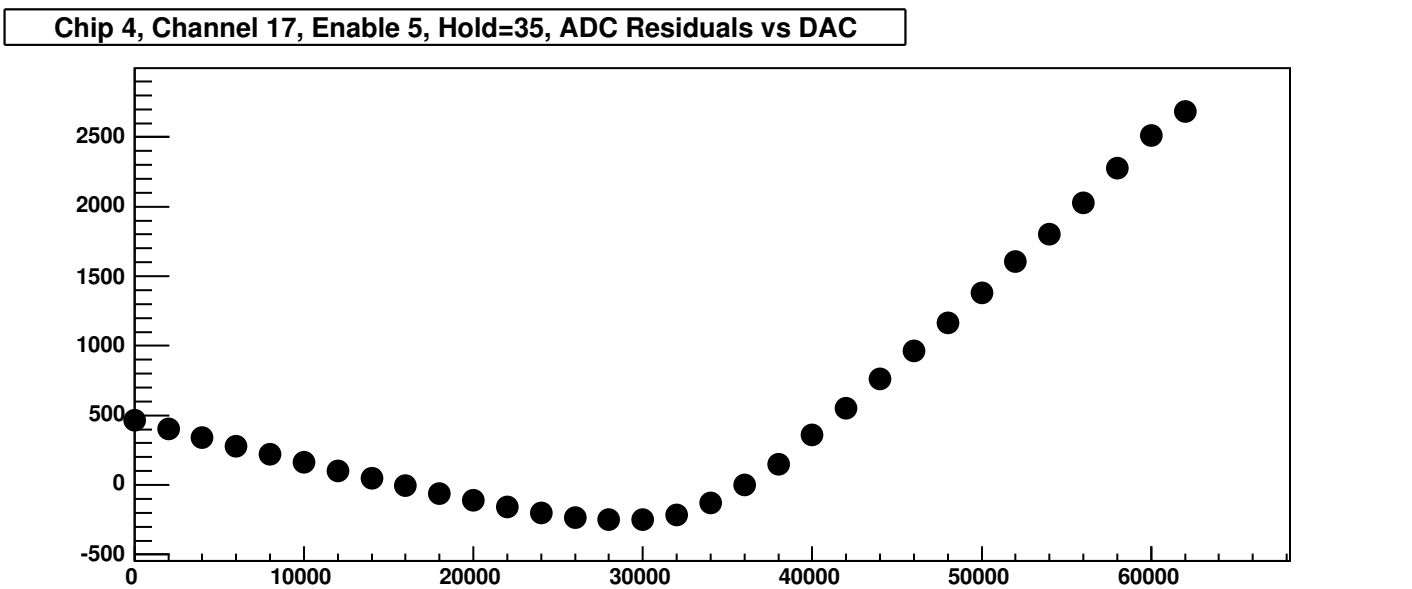

Chip 4, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 38.71 / 23
p0 182 ± 0.8232
p1 0.002854 ± 2.525e-05
380
360
340
320
300
280
260
240
220
200
180
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

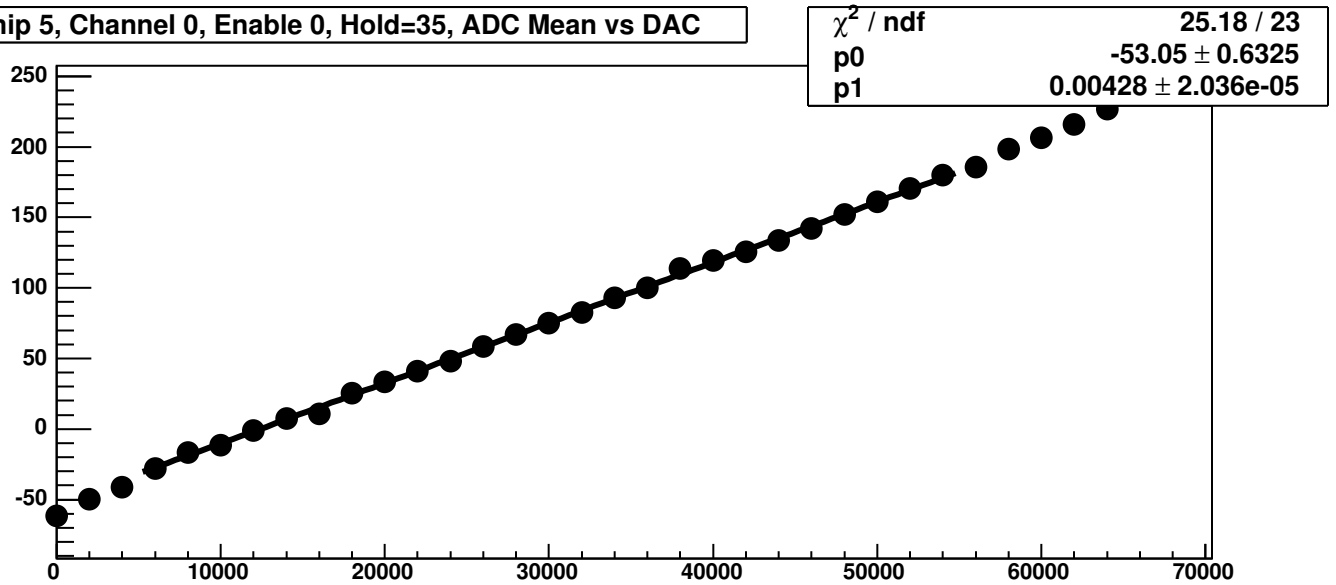

Chip 4, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC
12
10
8
6
4
2
0
-2
-4
-6
0
10000
20000
30000
40000
50000
60000
70000


Chip 4, Channel 17, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 3.133e+05 / 23
p0 -275.7 ± 1.2
p1 0.03424 ± 5.265e-05
4500
4000
3500
3000
2500
2000
1500
1000
500
0
0
10000
20000
30000
40000
50000
60000

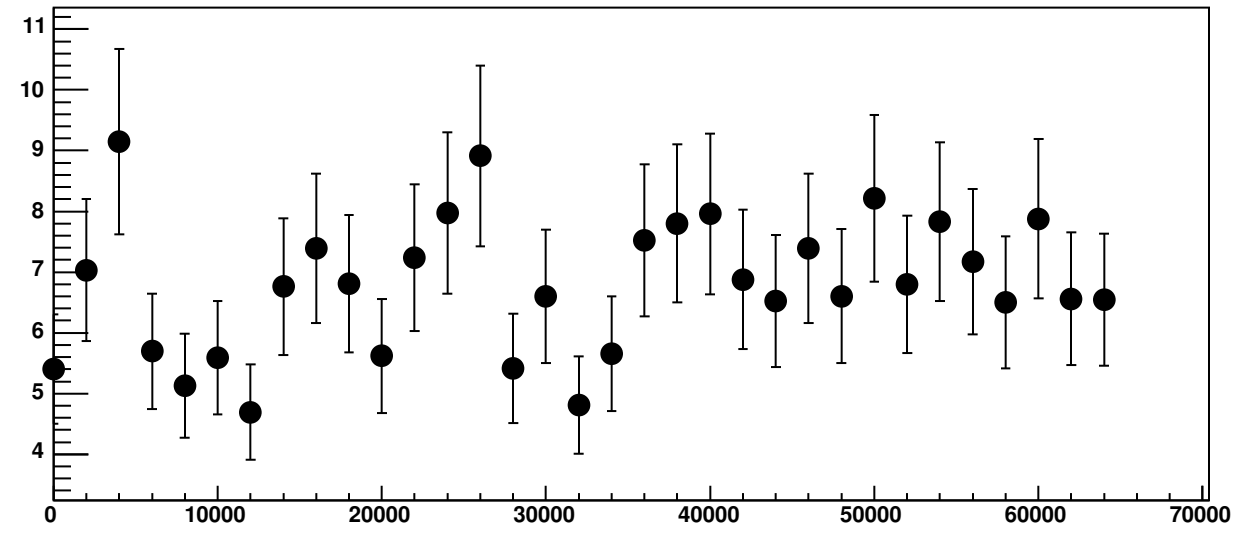

Chip 4, Channel 17, Enable 5, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000


Chip 4, Channel 17, Enable 5, Hold=35, ADC Residuals vs DAC
2500
2000
1500
1000
500
0
-500
0
10000
20000
30000
40000
50000
60000

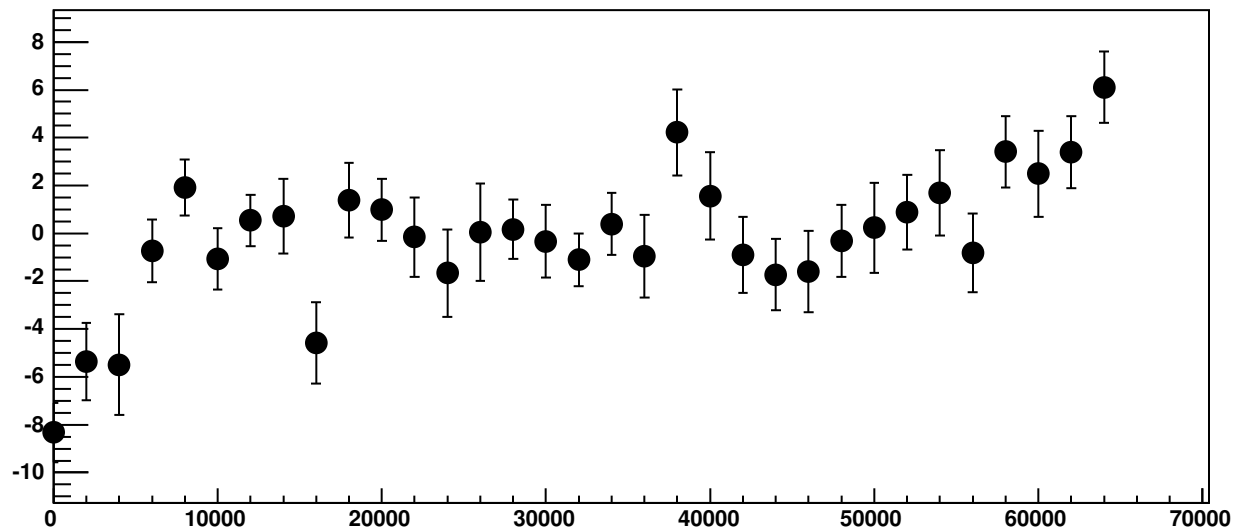
Chip 5, Channel 0, Enable 0, Hold=35, ADC Mean vs DAC

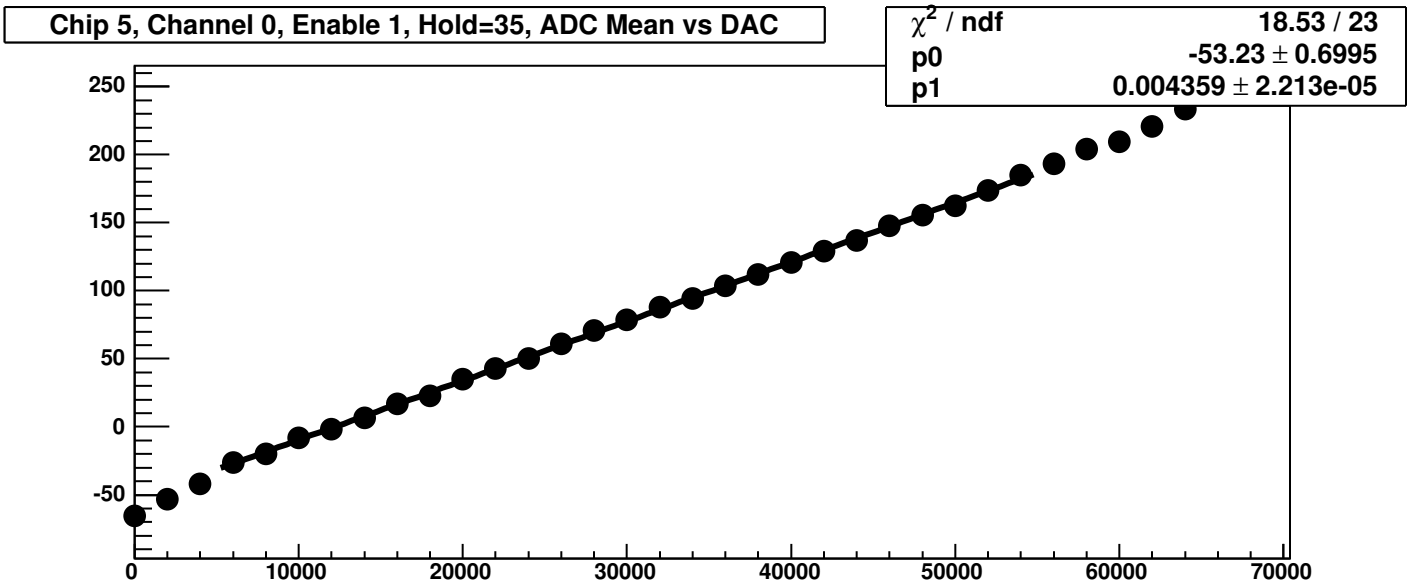


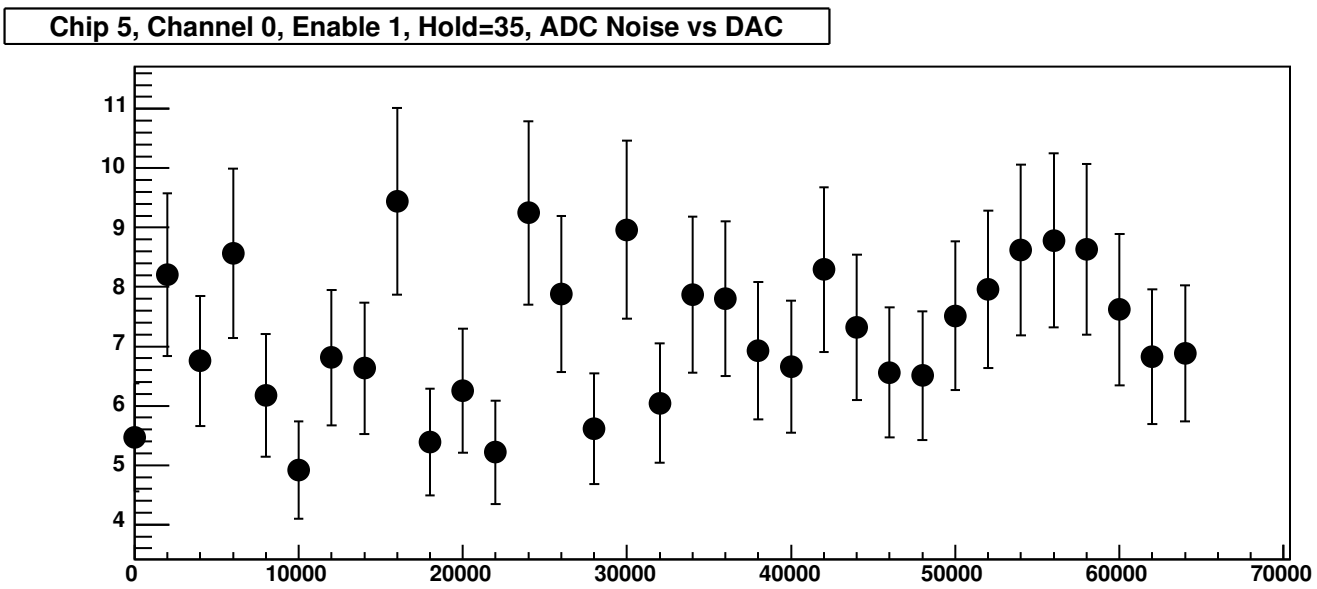
Chip 5, Channel 0, Enable 0, Hold=35, ADC Noise vs DAC

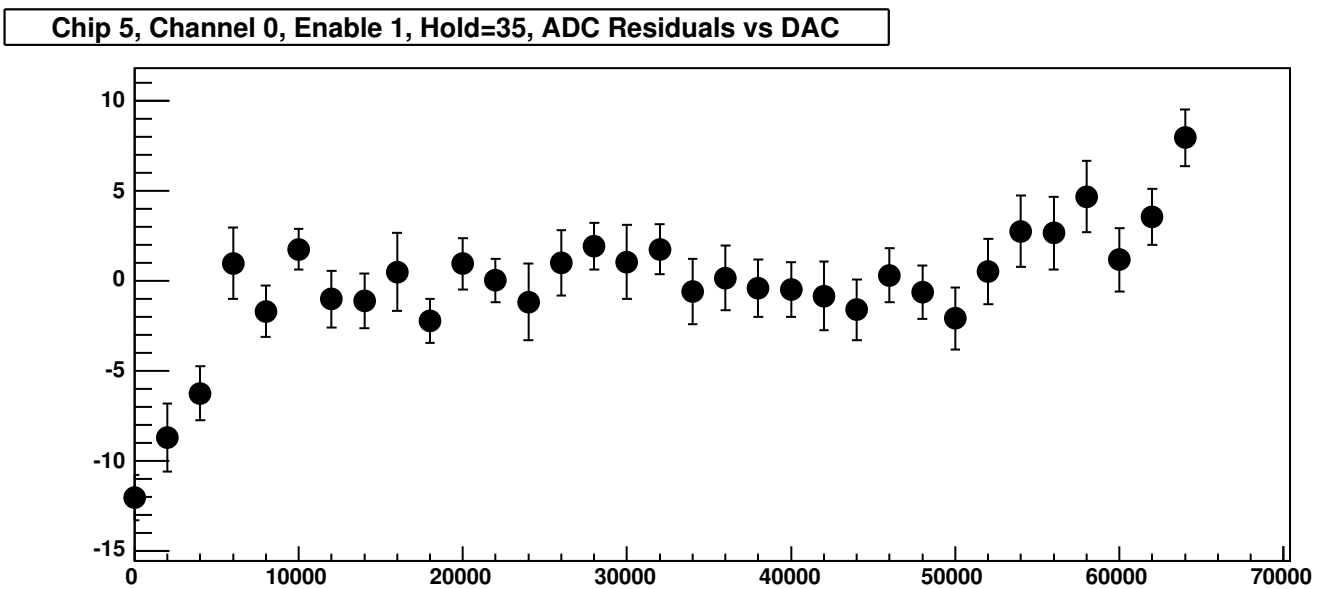


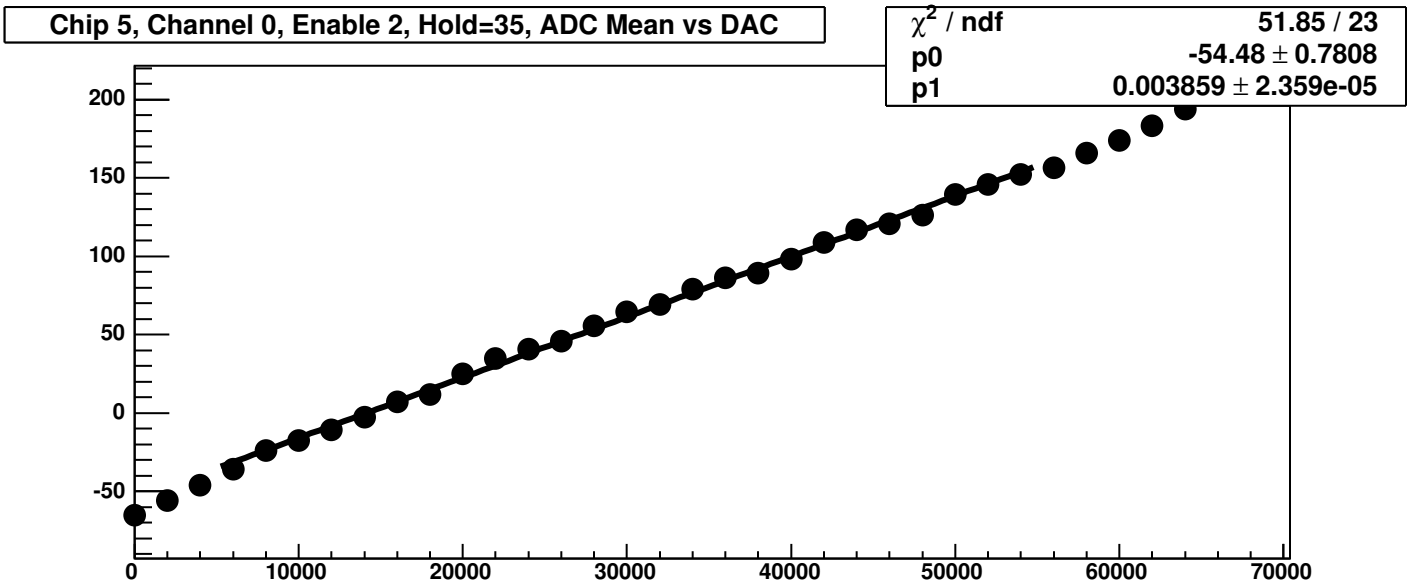
Chip 5, Channel 0, Enable 0, Hold=35, ADC Residuals vs DAC

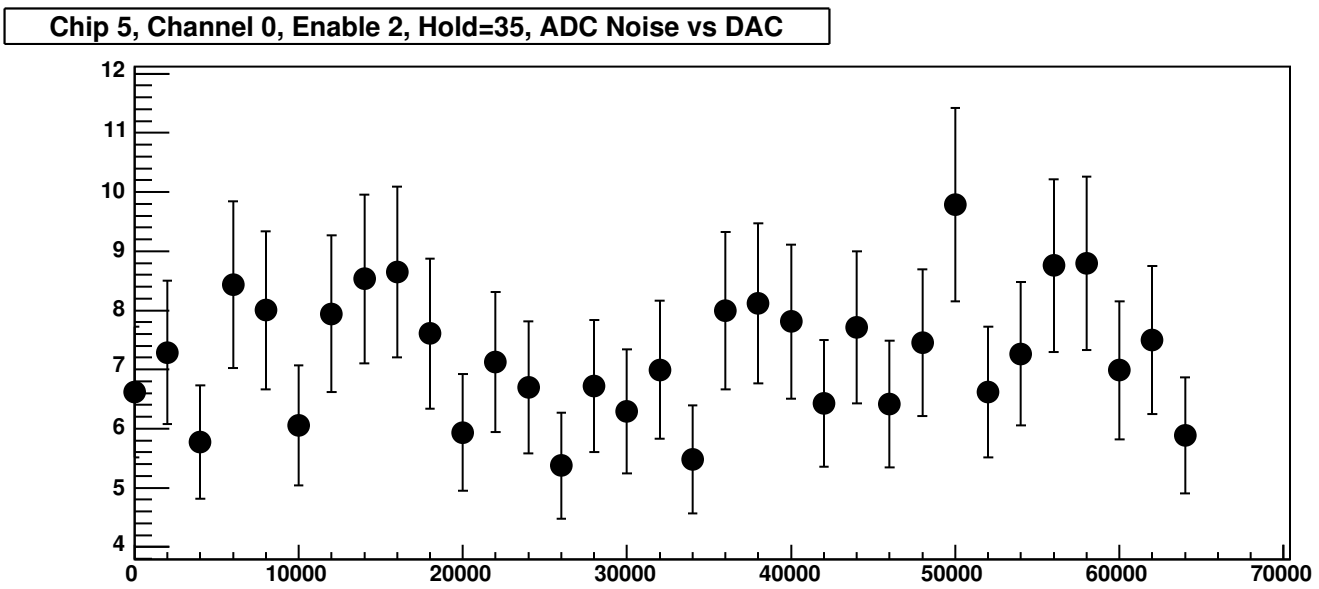


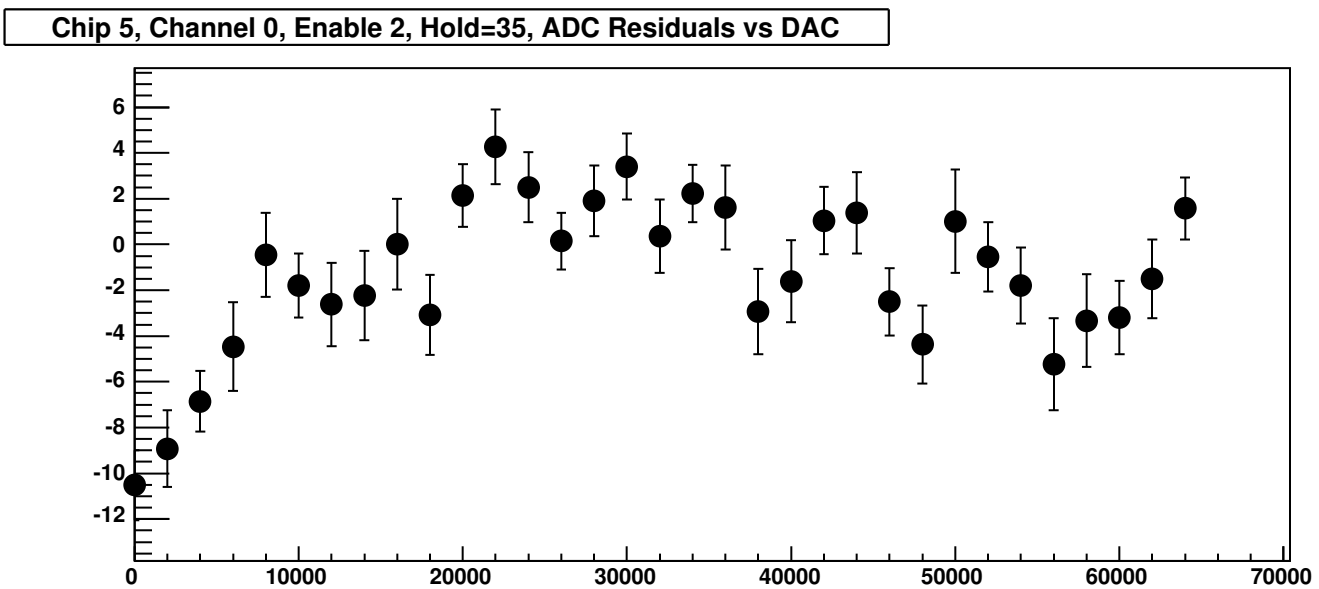

Chip 5, Channel 0, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 18.53 / 23
p0 -53.23 ± 0.6995
p1 0.004359 ± 2.213e-05
250
200
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000

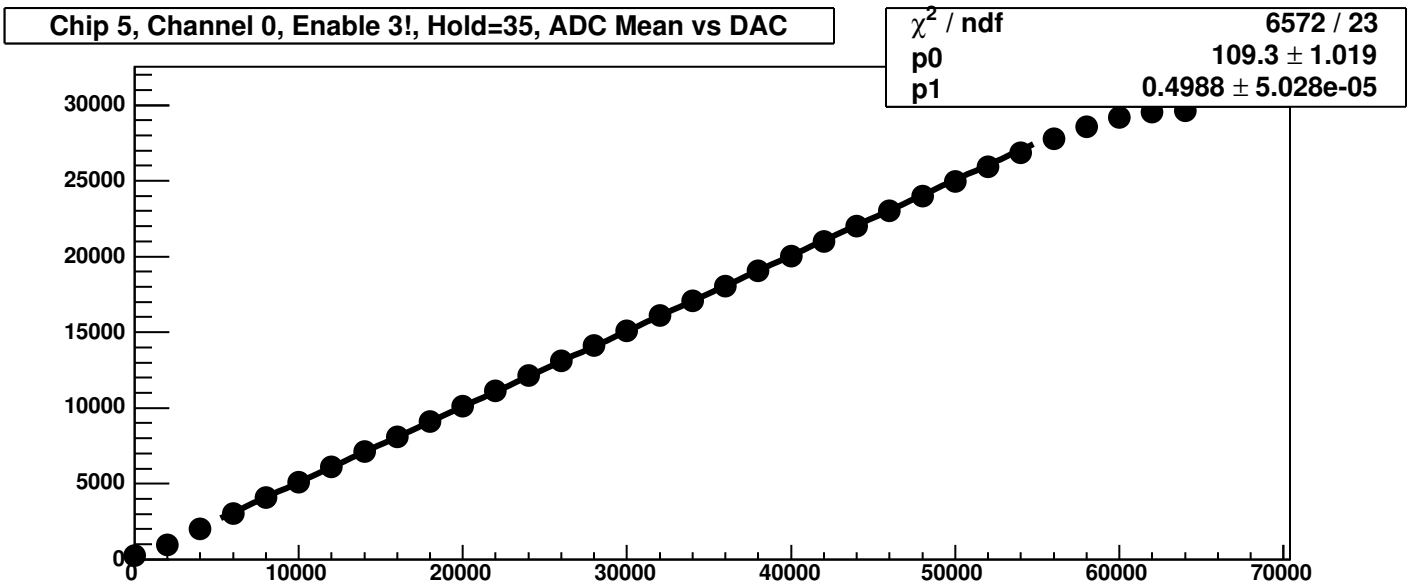

Chip 5, Channel 0, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

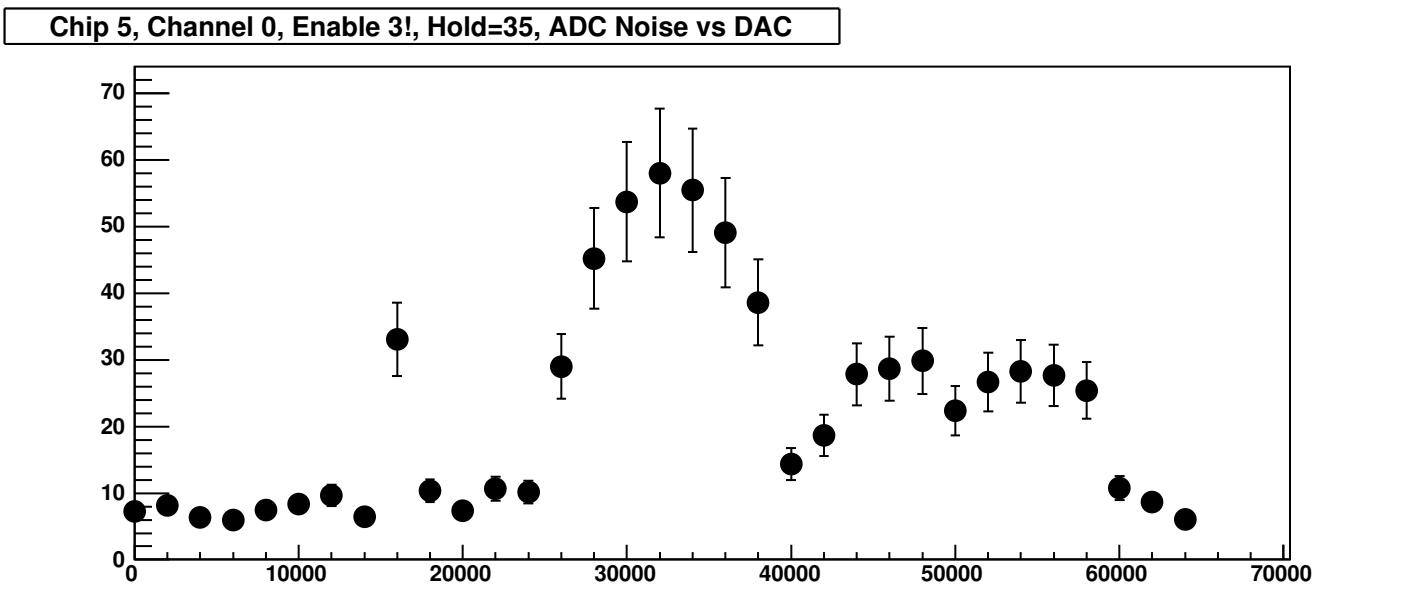

Chip 5, Channel 0, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

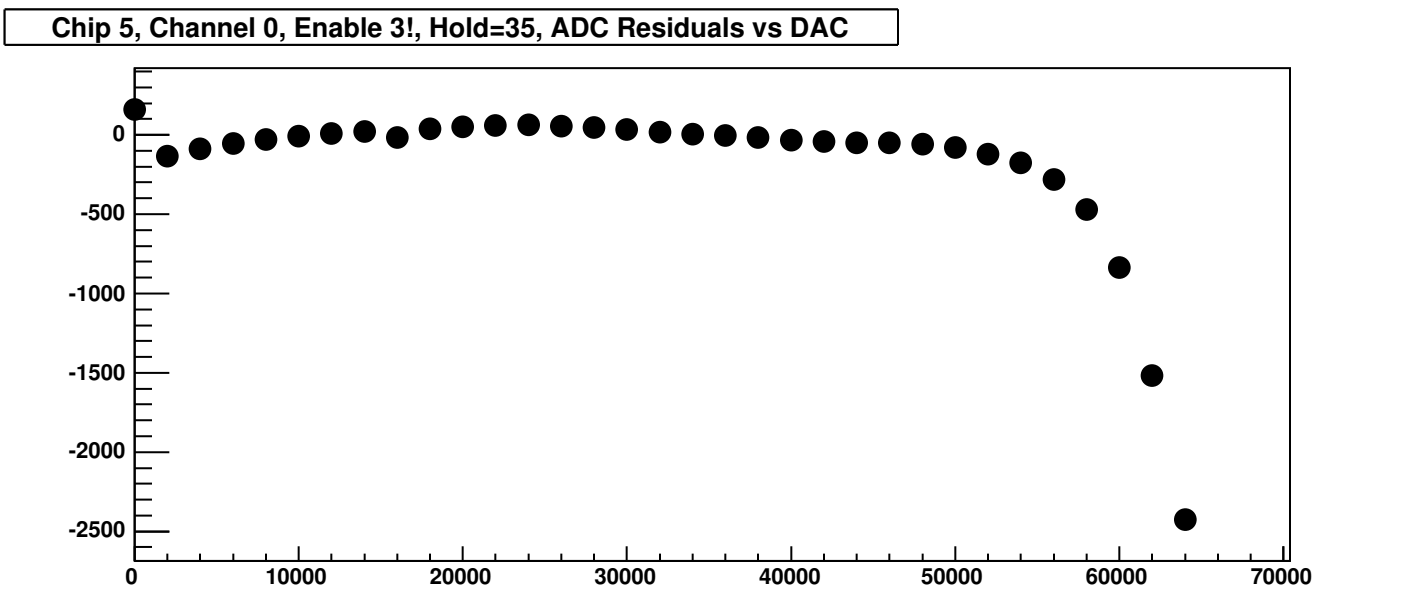

Chip 5, Channel 0, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 51.85 / 23
p0 -54.48 ± 0.7808
p1 0.003859 ± 2.359e-05
200
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 0, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

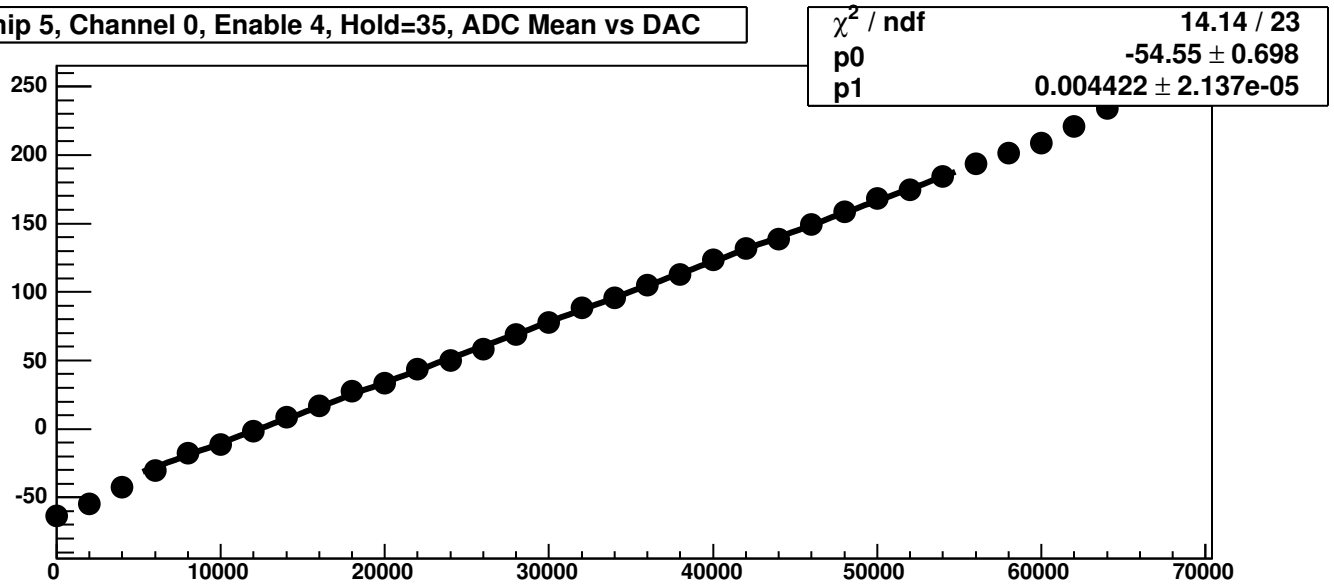

Chip 5, Channel 0, Enable 2, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 0, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 6572 / 23
p0 109.3 ± 1.019
p1 0.4988 ± 5.028e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

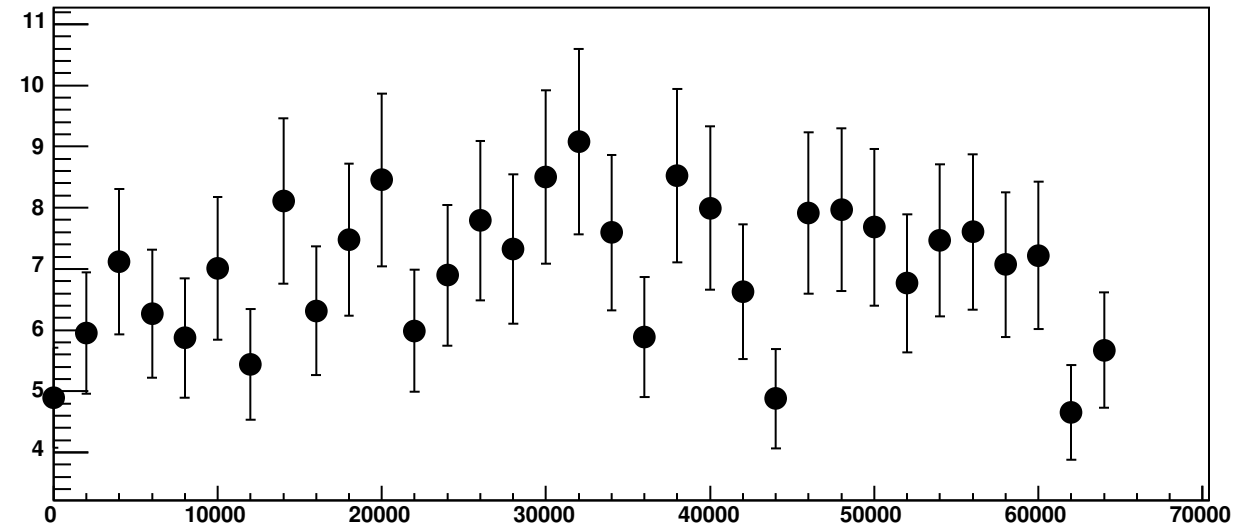

Chip 5, Channel 0, Enable 3!, Hold=35, ADC Noise vs DAC
70
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 0, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
0
10000
20000
30000
40000
50000
60000
70000

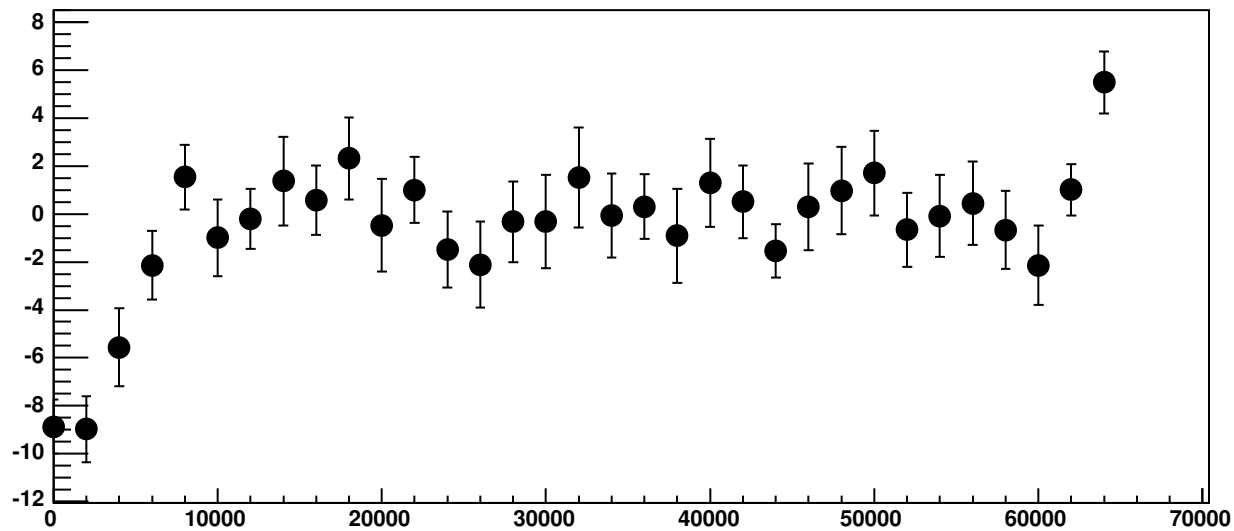
Chip 5, Channel 0, Enable 4, Hold=35, ADC Mean vs DAC

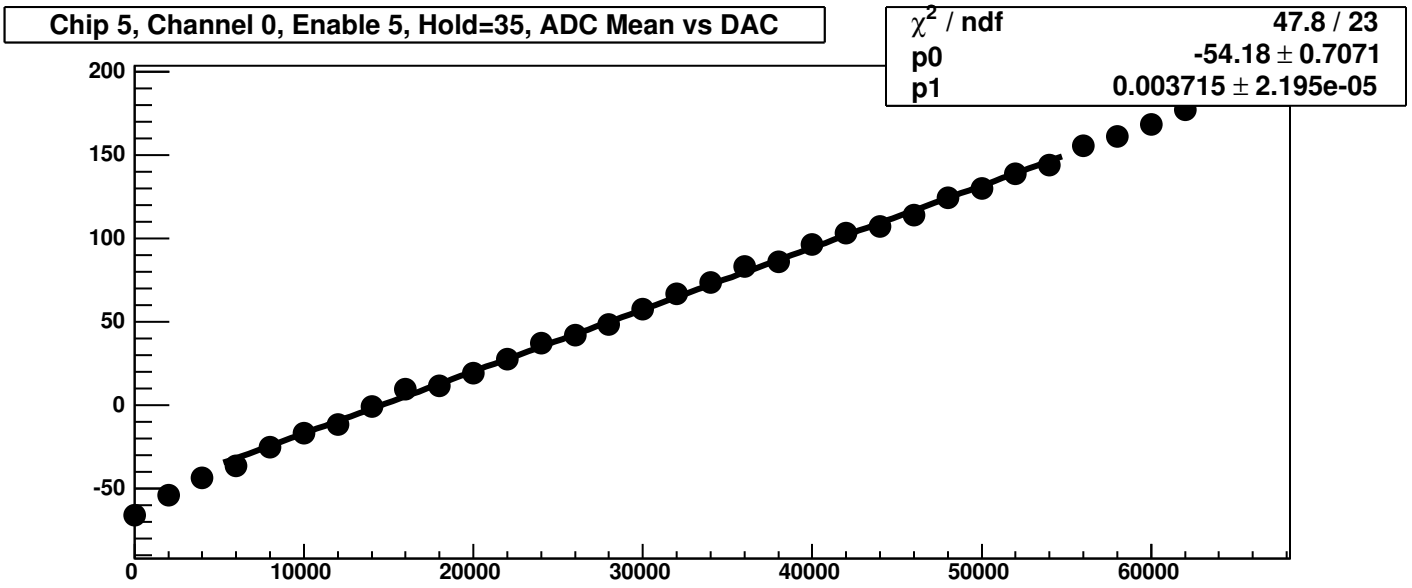


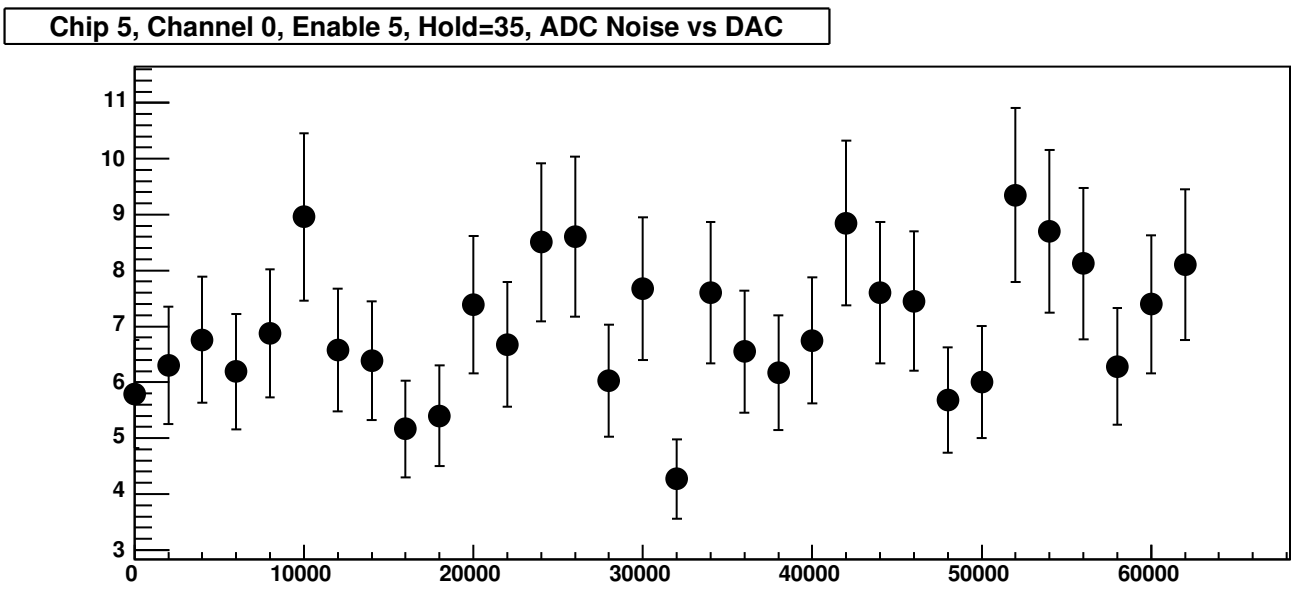
Chip 5, Channel 0, Enable 4, Hold=35, ADC Noise vs DAC

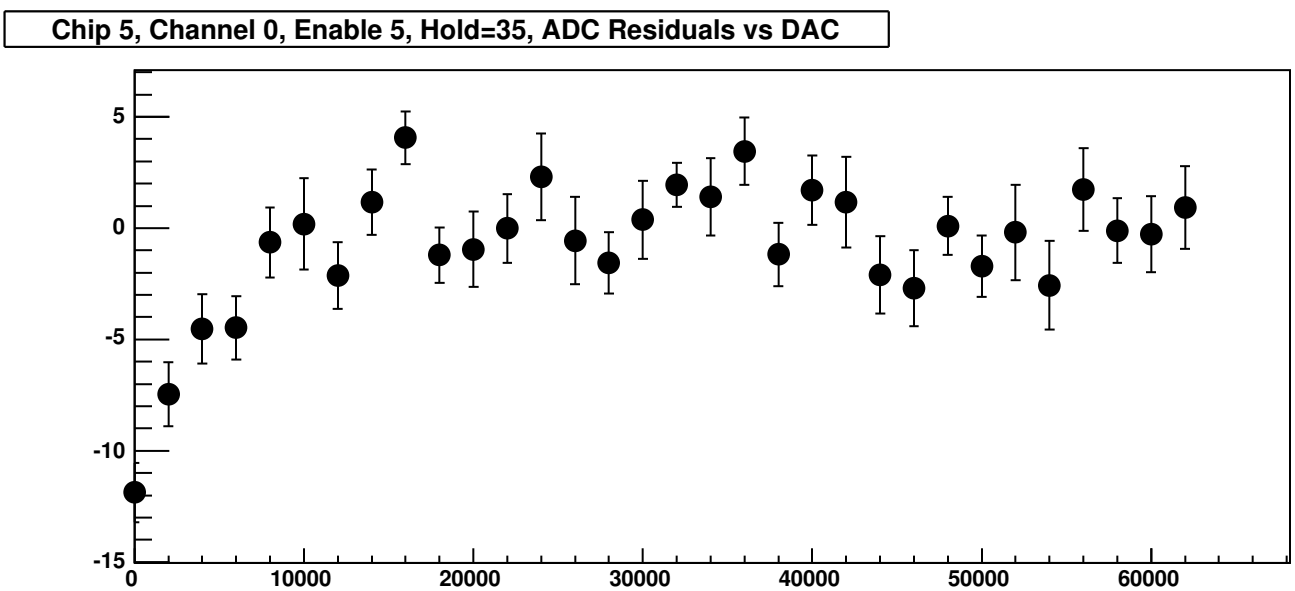


Chip 5, Channel 0, Enable 4, Hold=35, ADC Residuals vs DAC

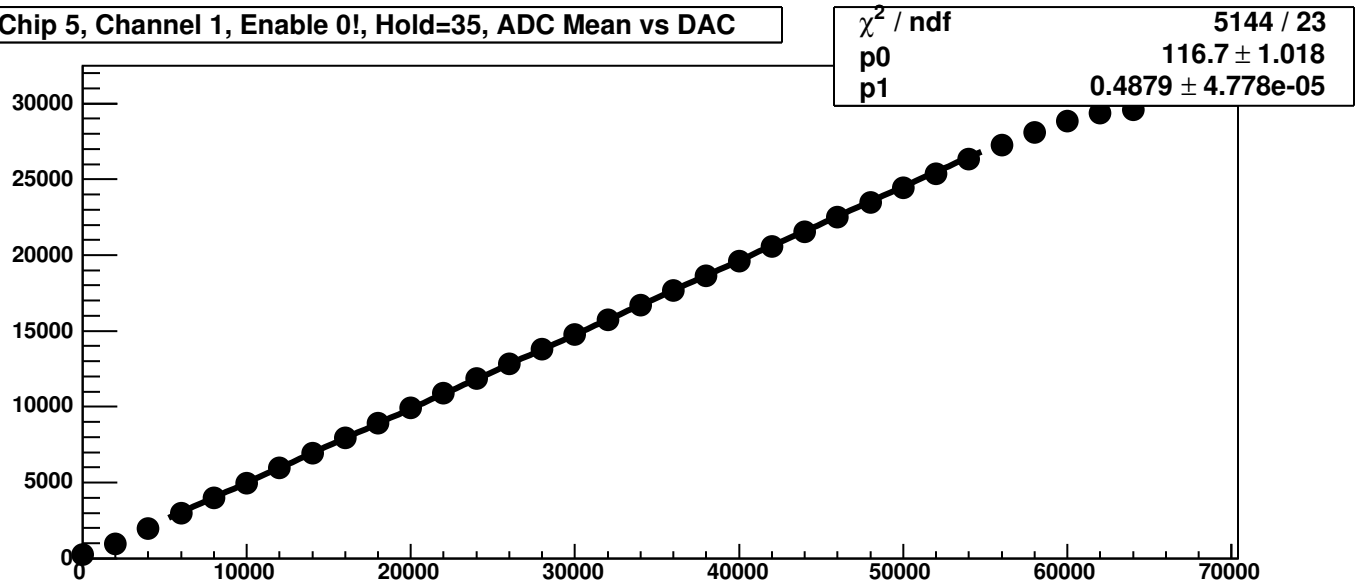



Chip 5, Channel 0, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 47.8 / 23
p0 -54.18 ± 0.7071
p1 0.003715 ± 2.195e-05
200
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000

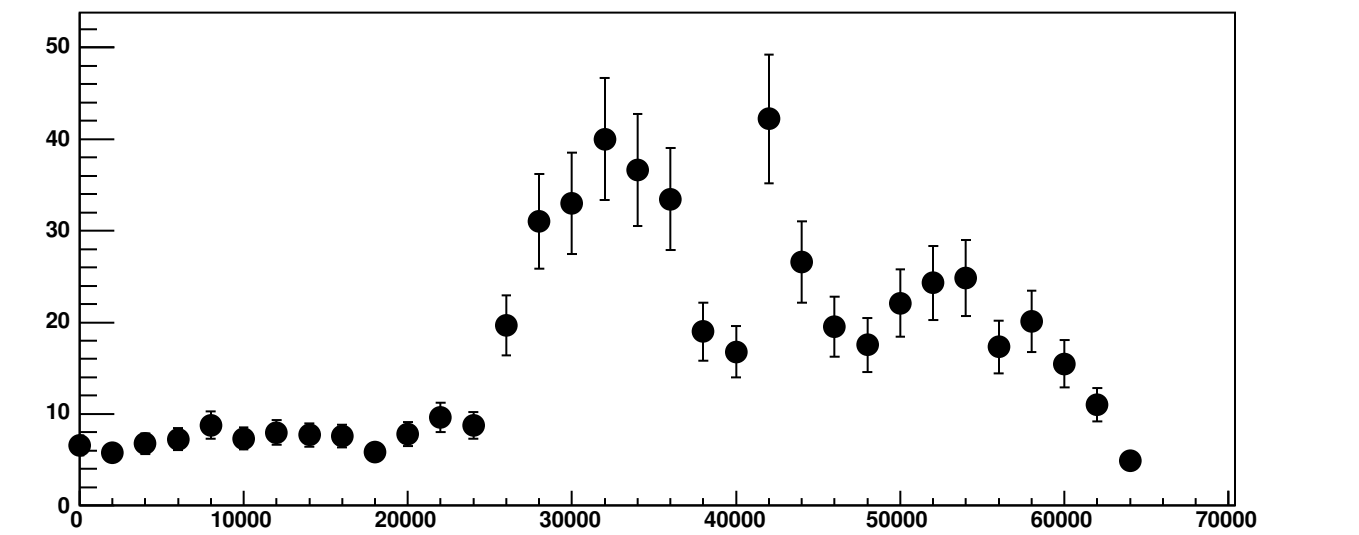

Chip 5, Channel 0, Enable 5, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 0, Enable 5, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000

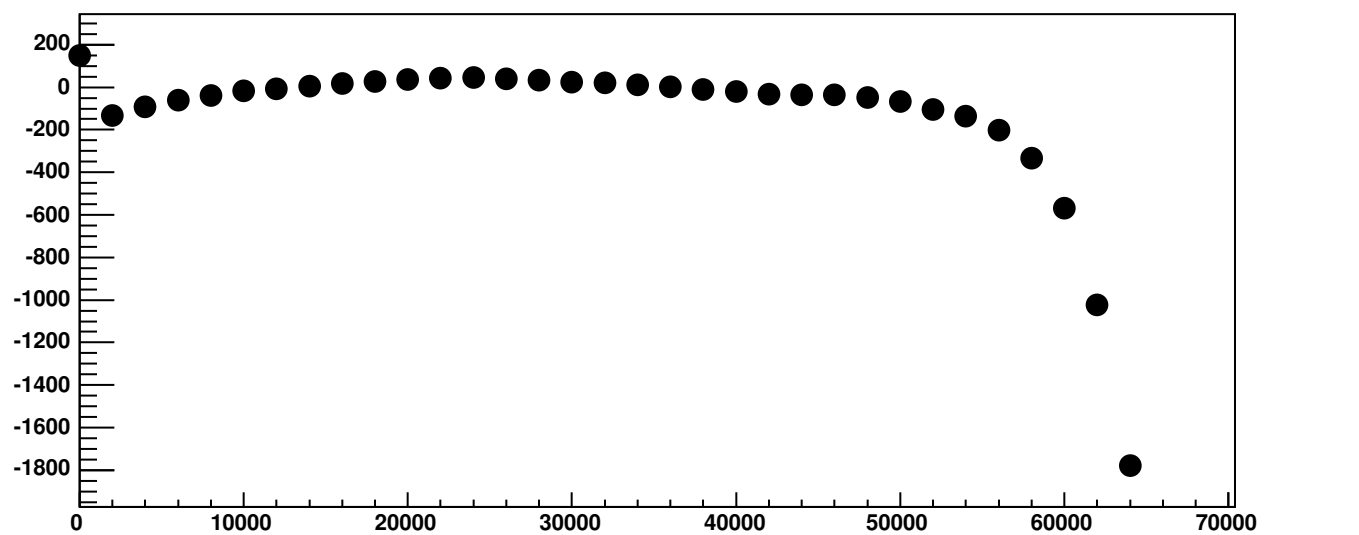
Chip 5, Channel 1, Enable 0!, Hold=35, ADC Mean vs DAC

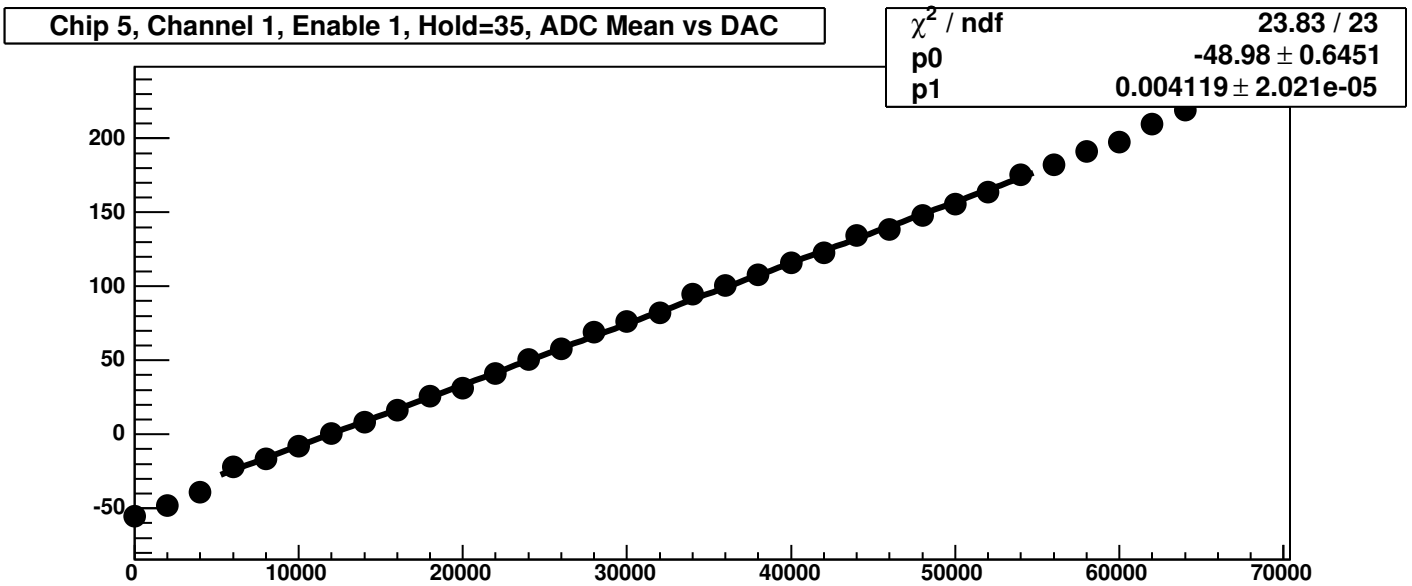


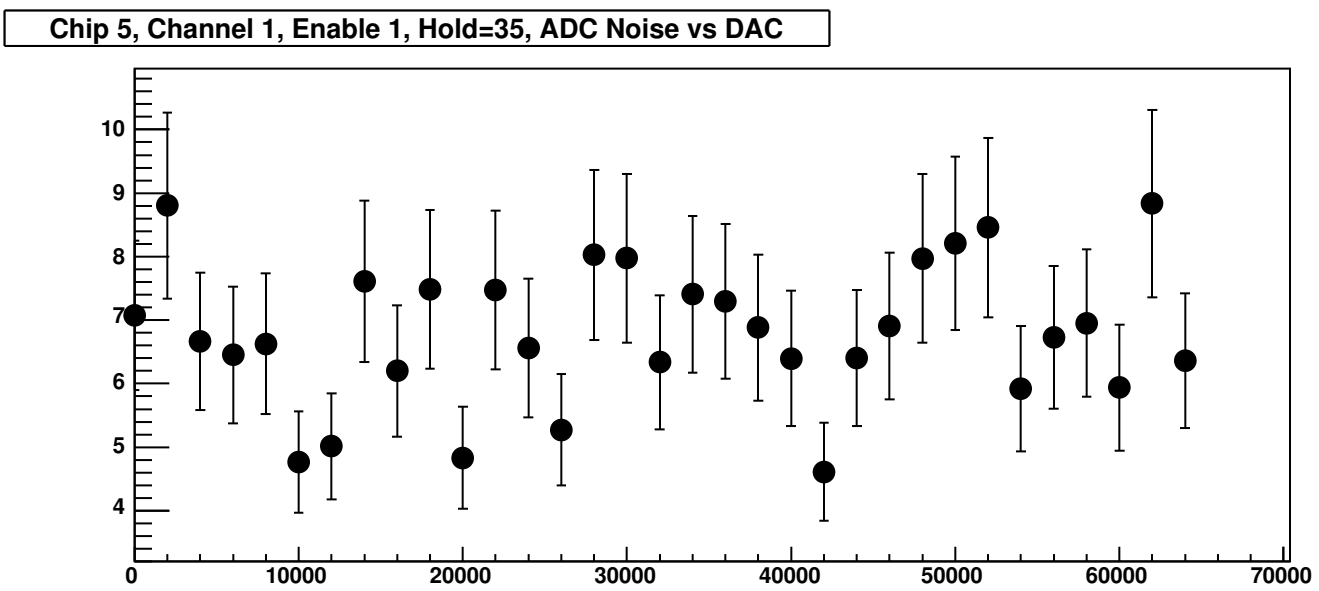
Chip 5, Channel 1, Enable 0!, Hold=35, ADC Noise vs DAC

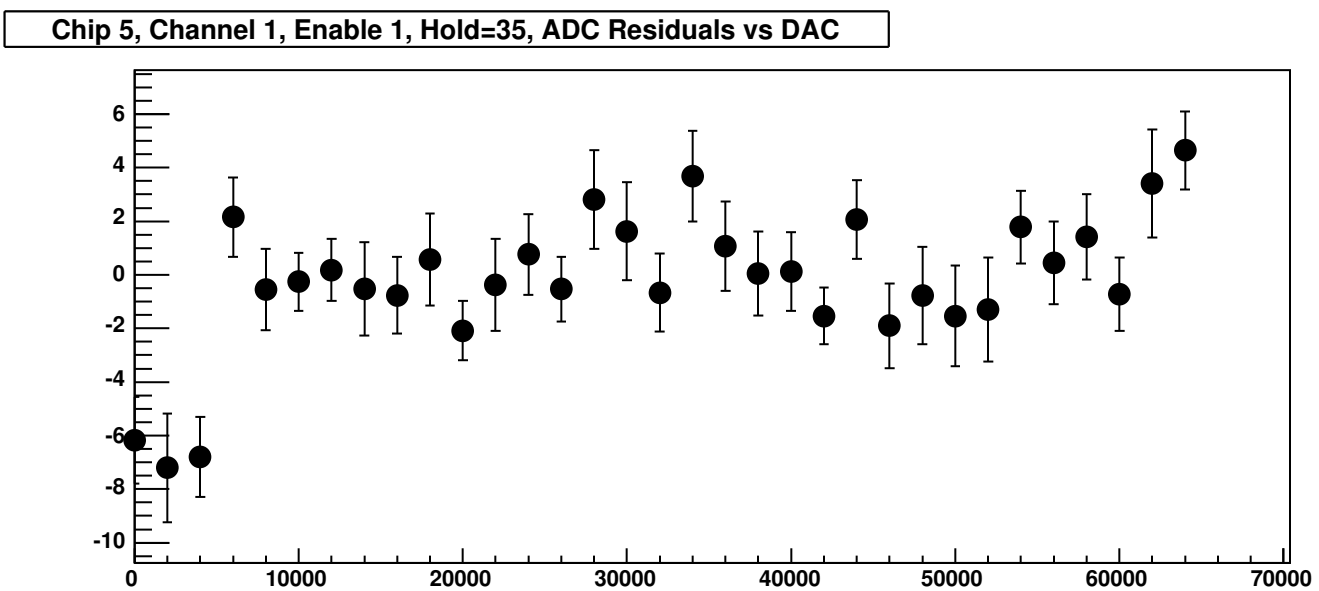


Chip 5, Channel 1, Enable 0!, Hold=35, ADC Residuals vs DAC

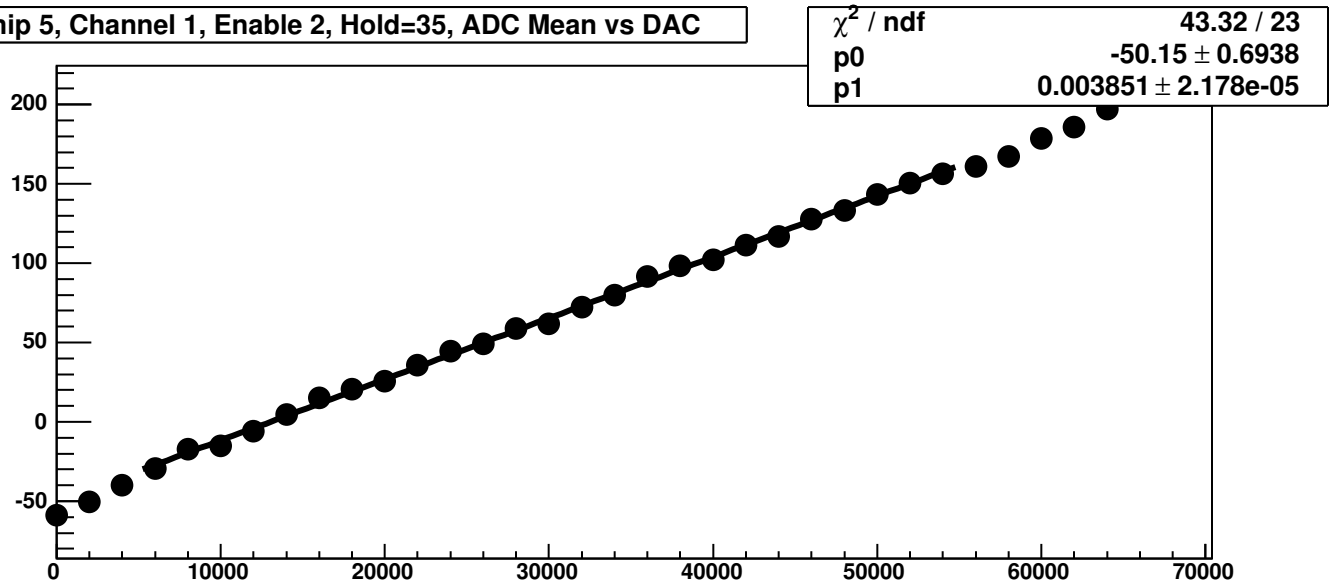



Chip 5, Channel 1, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 23.83 / 23
p0 -48.98 ± 0.6451
p1 0.004119 ± 2.021e-05
200
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000

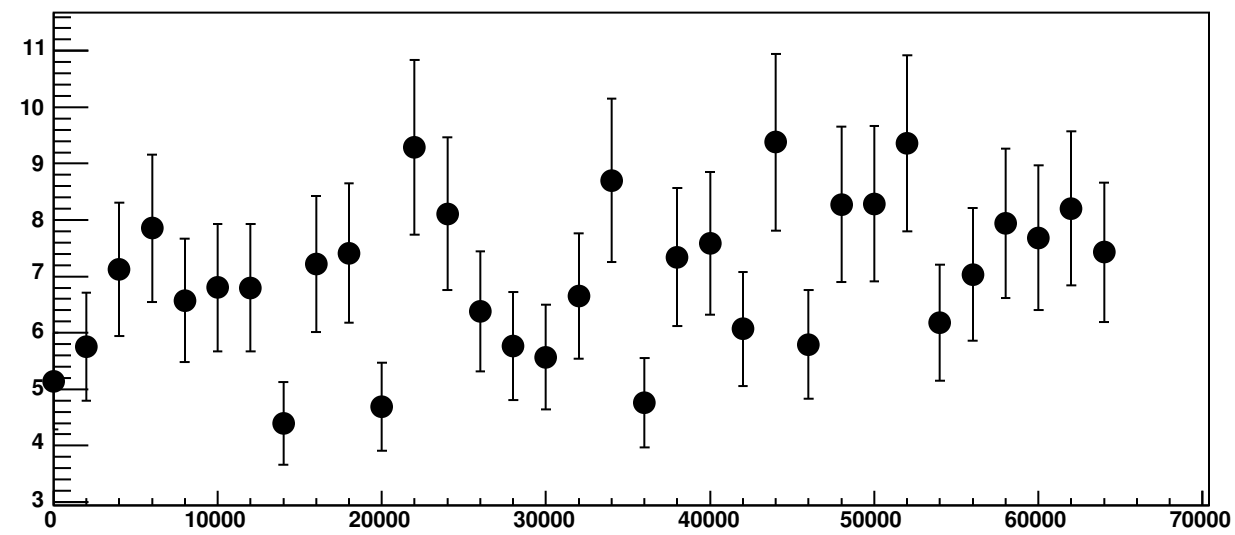

Chip 5, Channel 1, Enable 1, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 1, Enable 1, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
0
10000
20000
30000
40000
50000
60000
70000

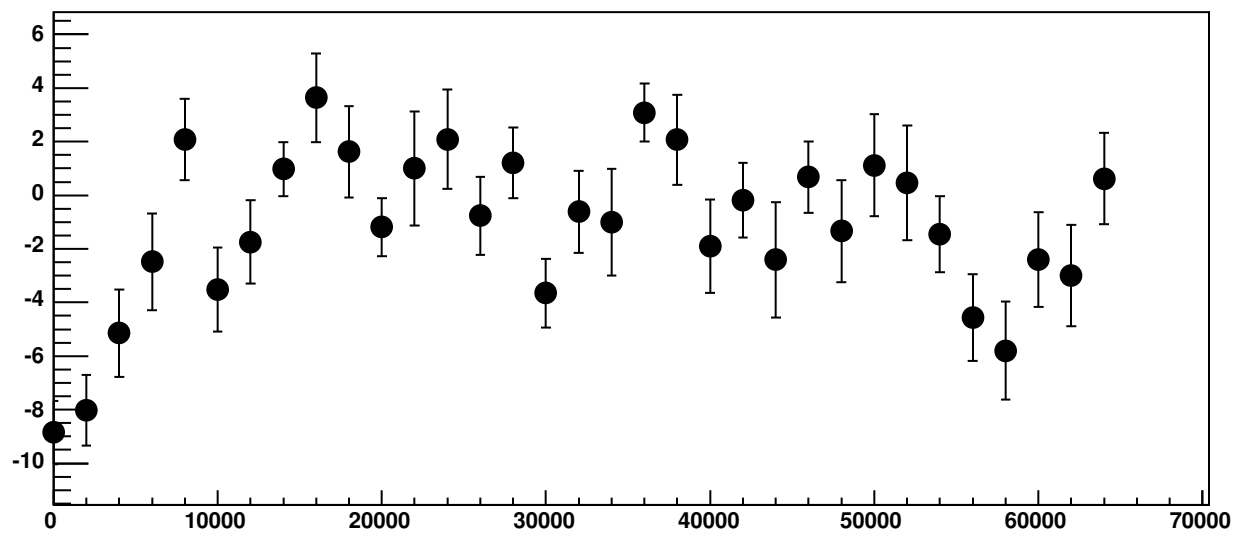
Chip 5, Channel 1, Enable 2, Hold=35, ADC Mean vs DAC

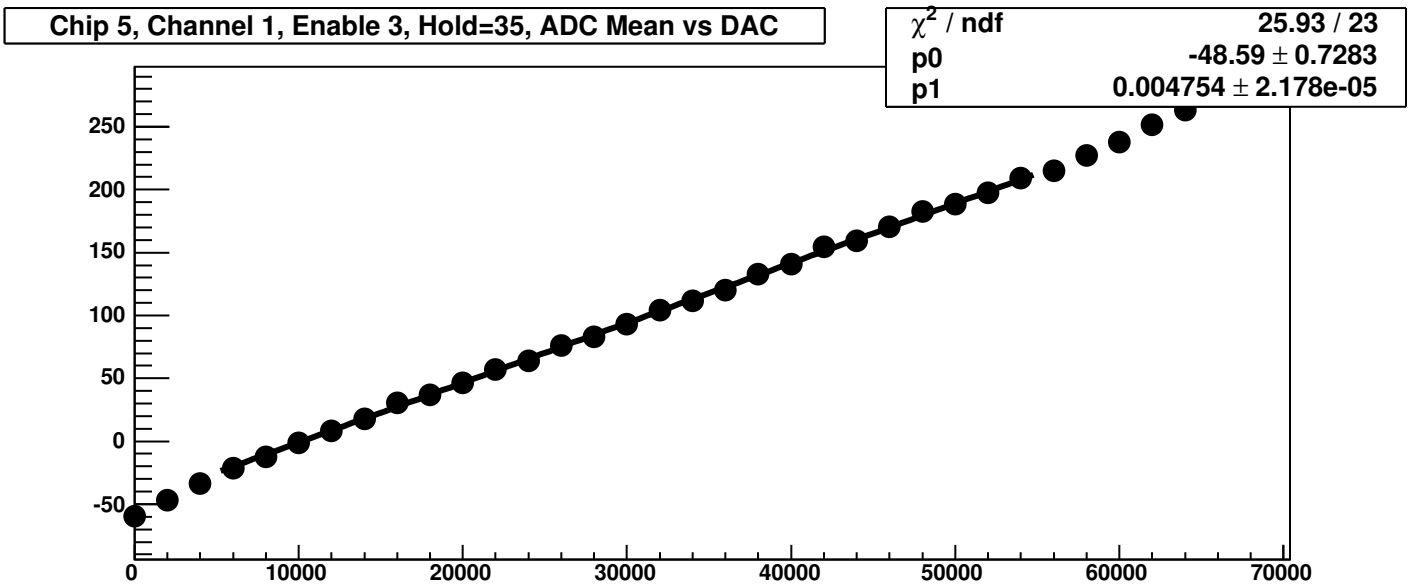


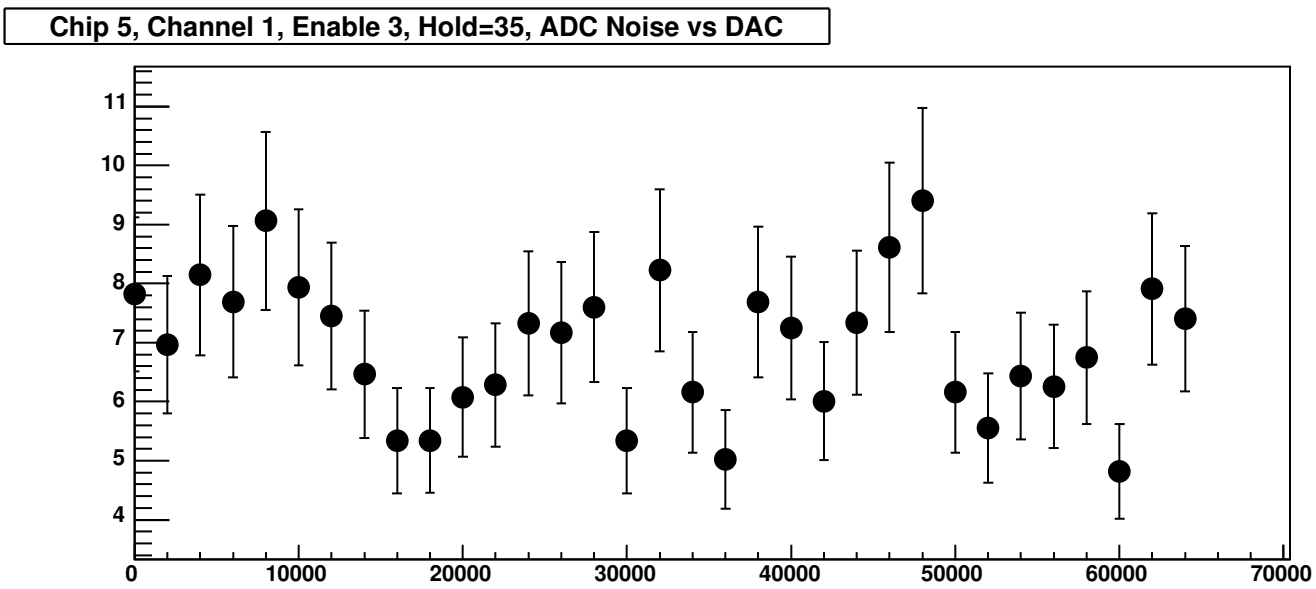
Chip 5, Channel 1, Enable 2, Hold=35, ADC Noise vs DAC

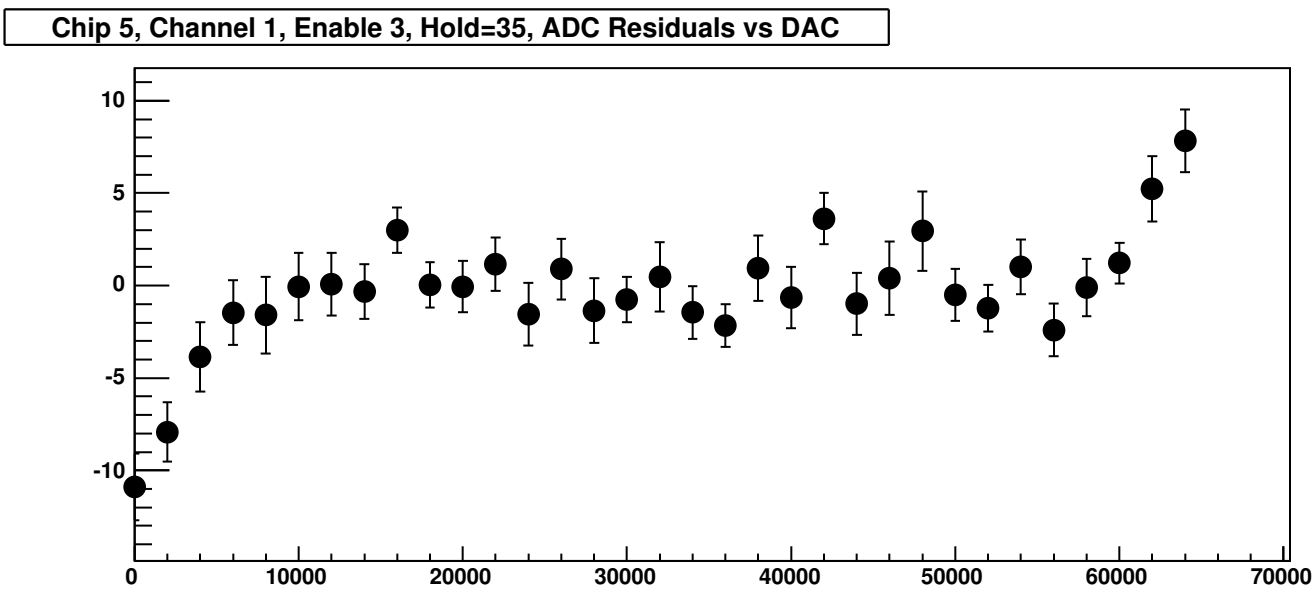


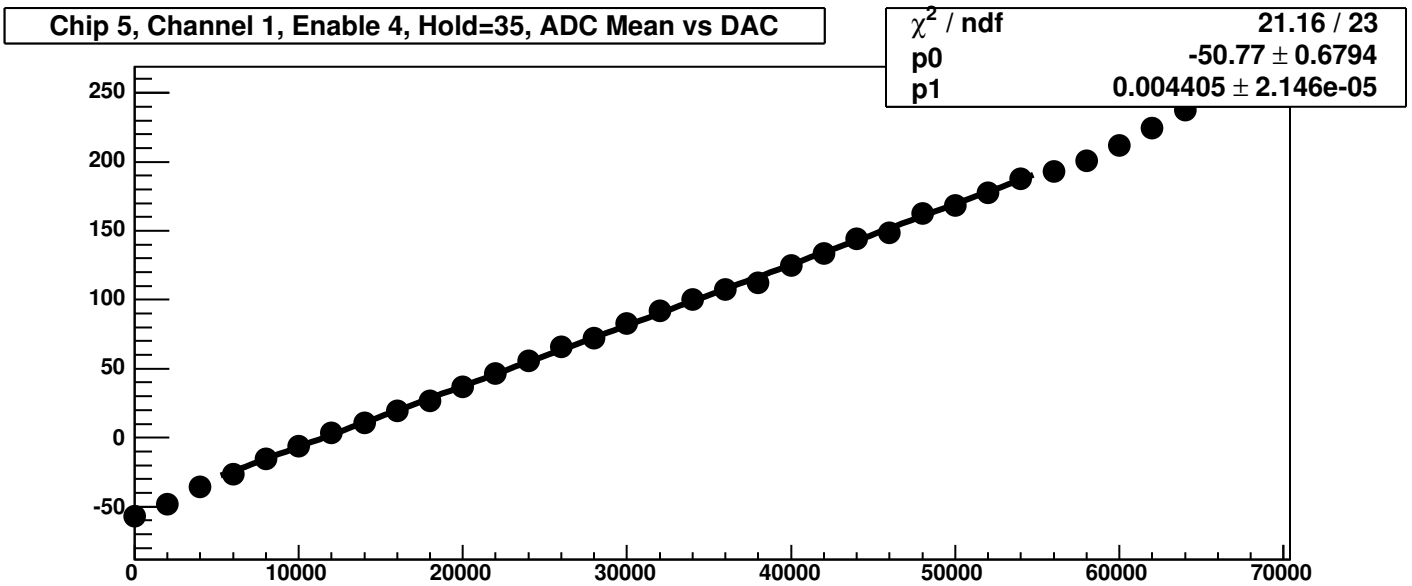
Chip 5, Channel 1, Enable 2, Hold=35, ADC Residuals vs DAC

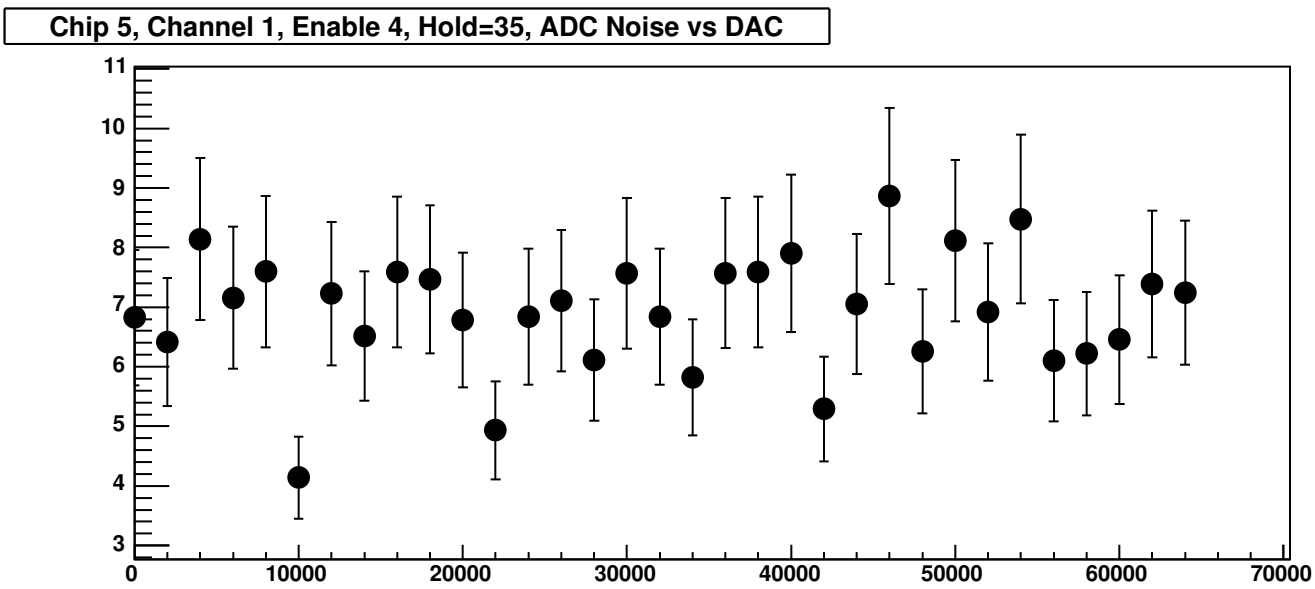


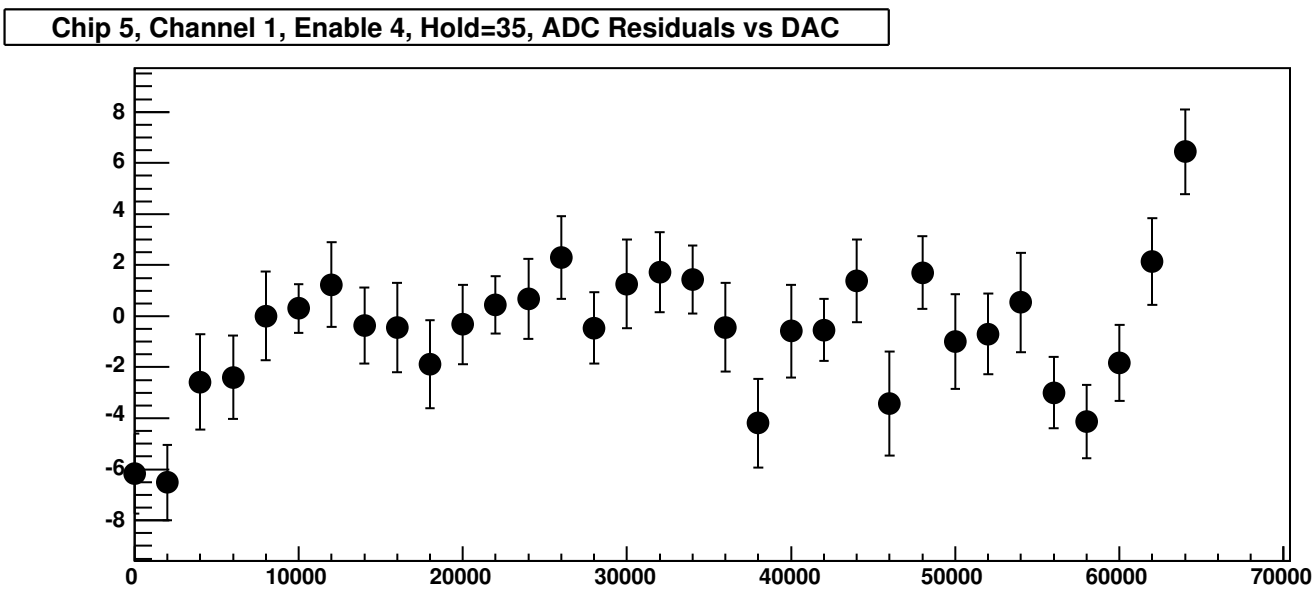

Chip 5, Channel 1, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 25.93 / 23
p0 -48.59 ± 0.7283
p1 0.004754 ± 2.178e-05
250
200
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000

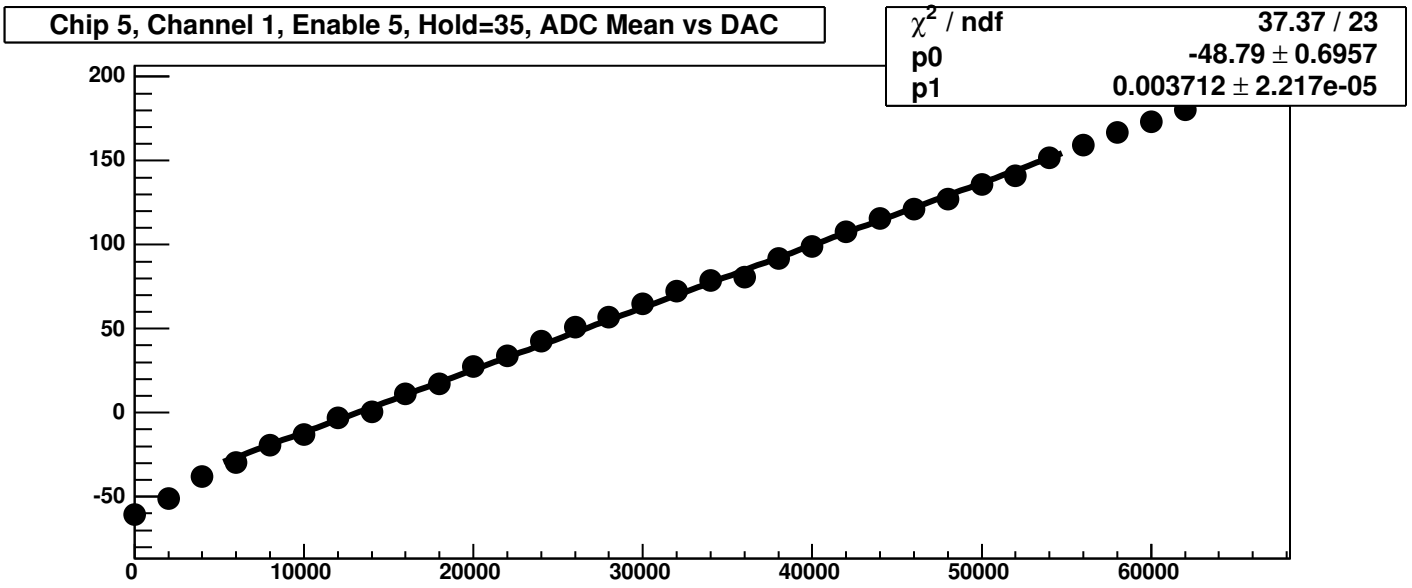

Chip 5, Channel 1, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

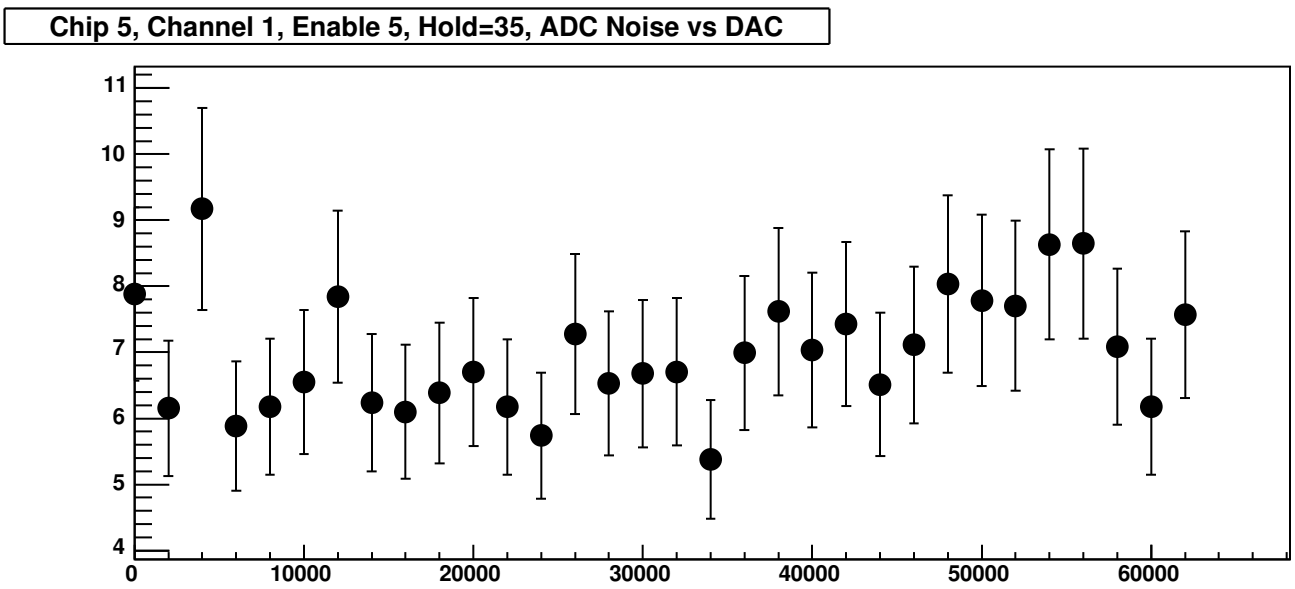

Chip 5, Channel 1, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

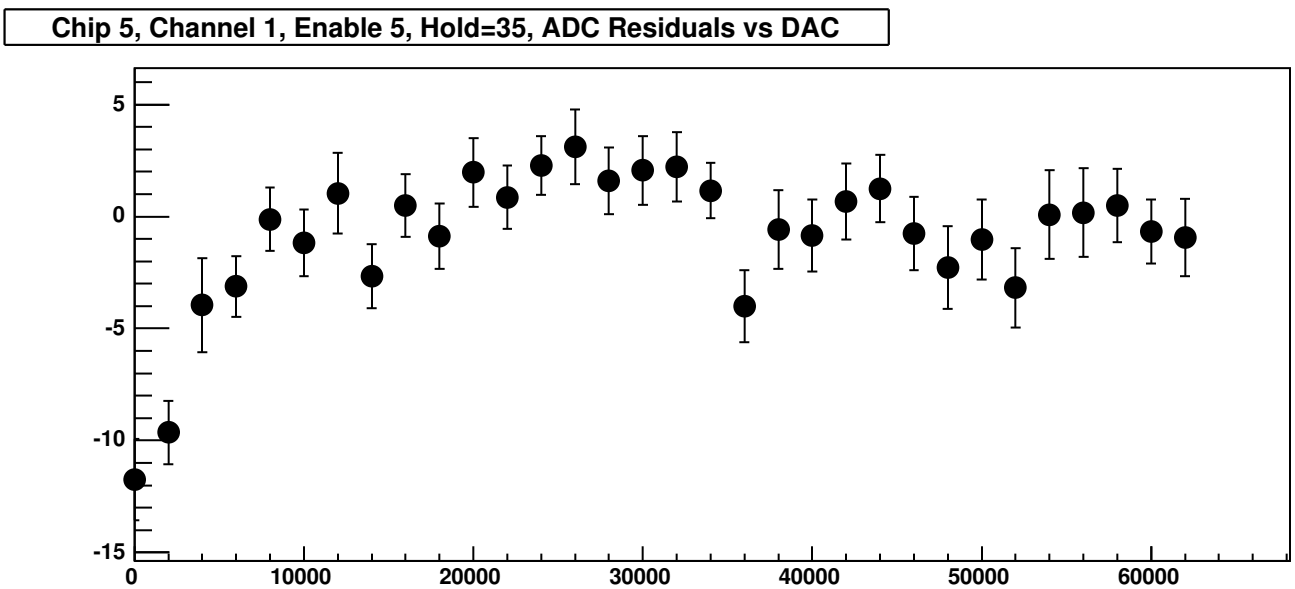

Chip 5, Channel 1, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 21.16 / 23
p0 -50.77 ± 0.6794
p1 0.004405 ± 2.146e-05
250
200
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 1, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 1, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

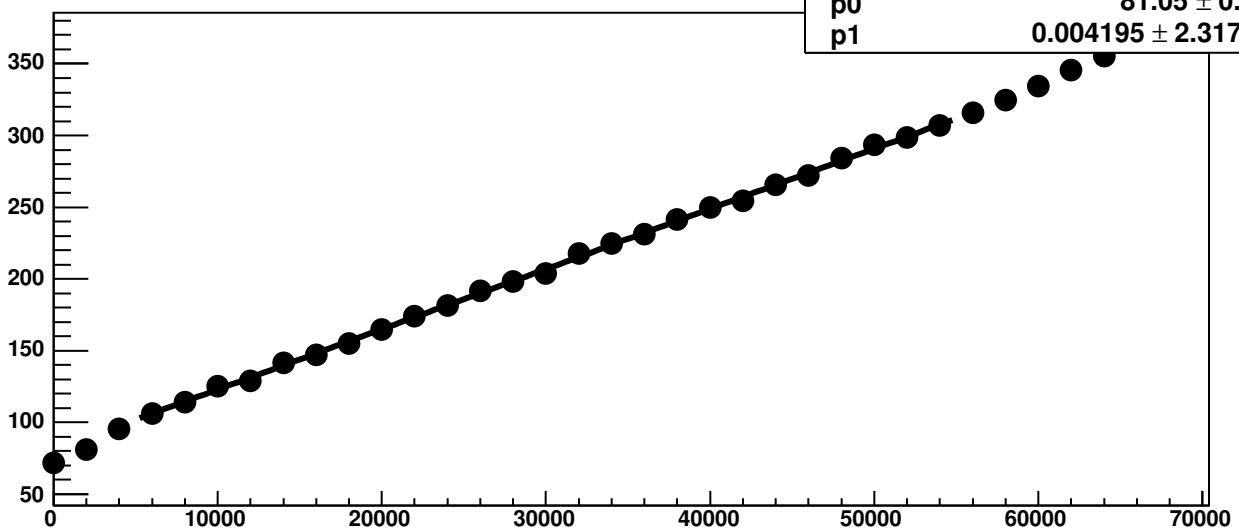

Chip 5, Channel 1, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 37.37 / 23
p0 -48.79 ± 0.6957
p1 0.003712 ± 2.217e-05
200
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 1, Enable 5, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

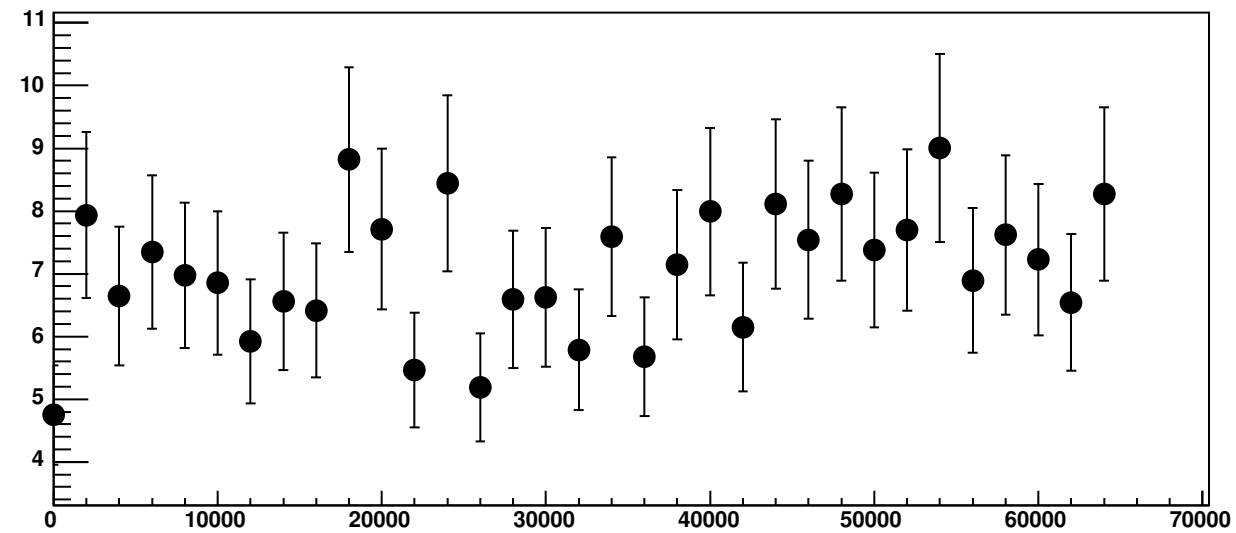

Chip 5, Channel 1, Enable 5, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000

Chip 5, Channel 2, Enable 0, Hold=35, ADC Mean vs DAC

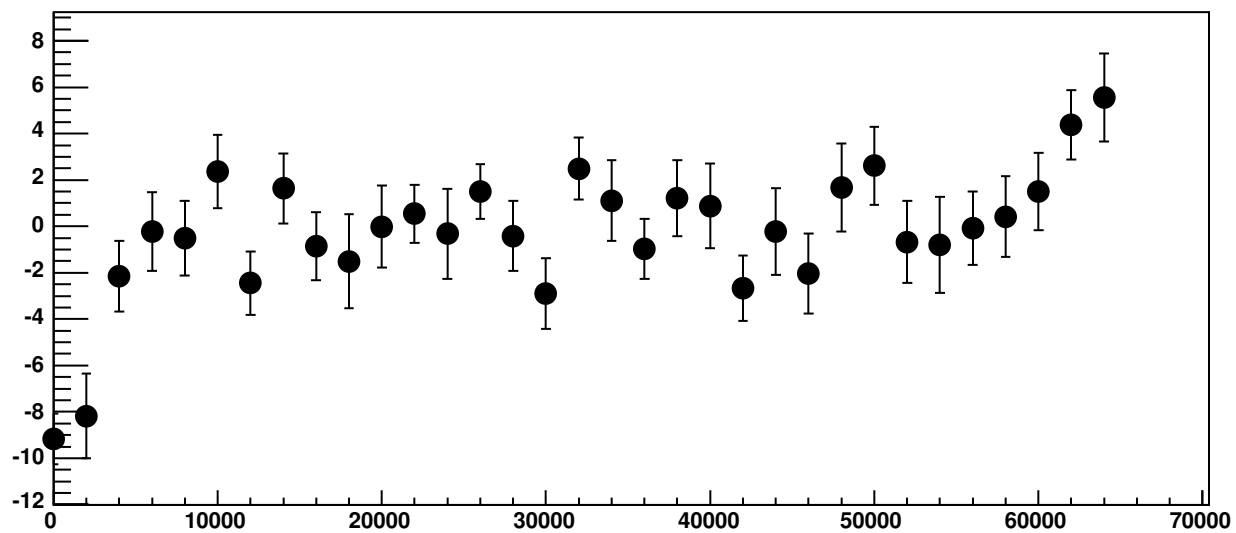
χ^2 / ndf	26.95 / 23
p0	81.05 ± 0.7361
p1	0.004195 ± 2.317e-05



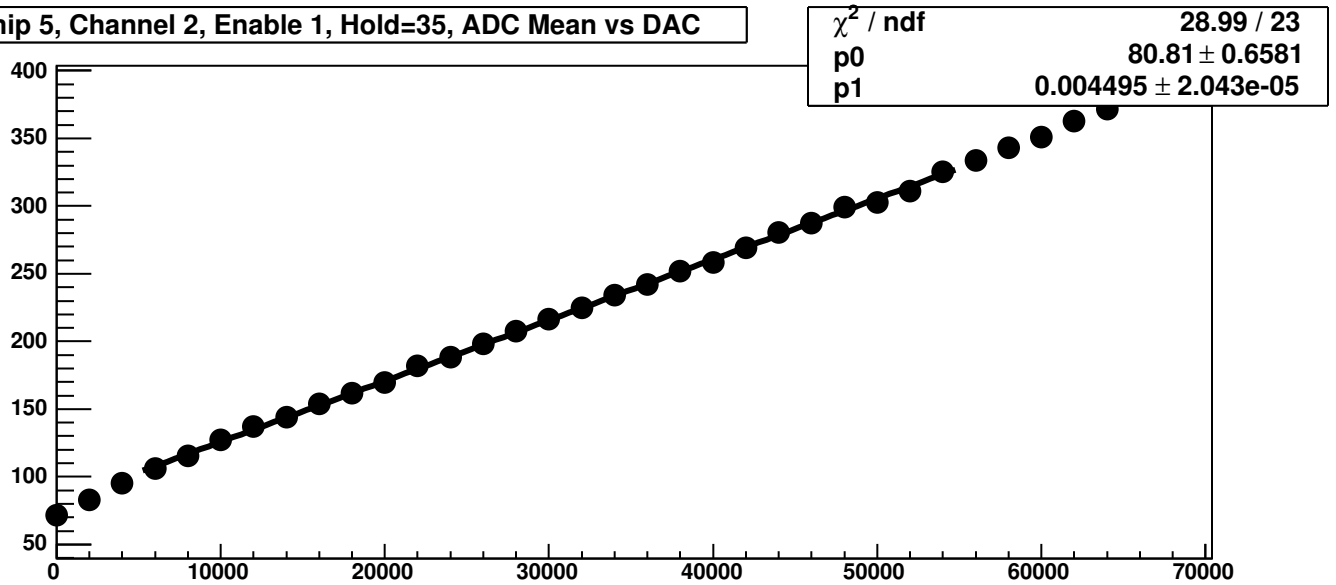
Chip 5, Channel 2, Enable 0, Hold=35, ADC Noise vs DAC



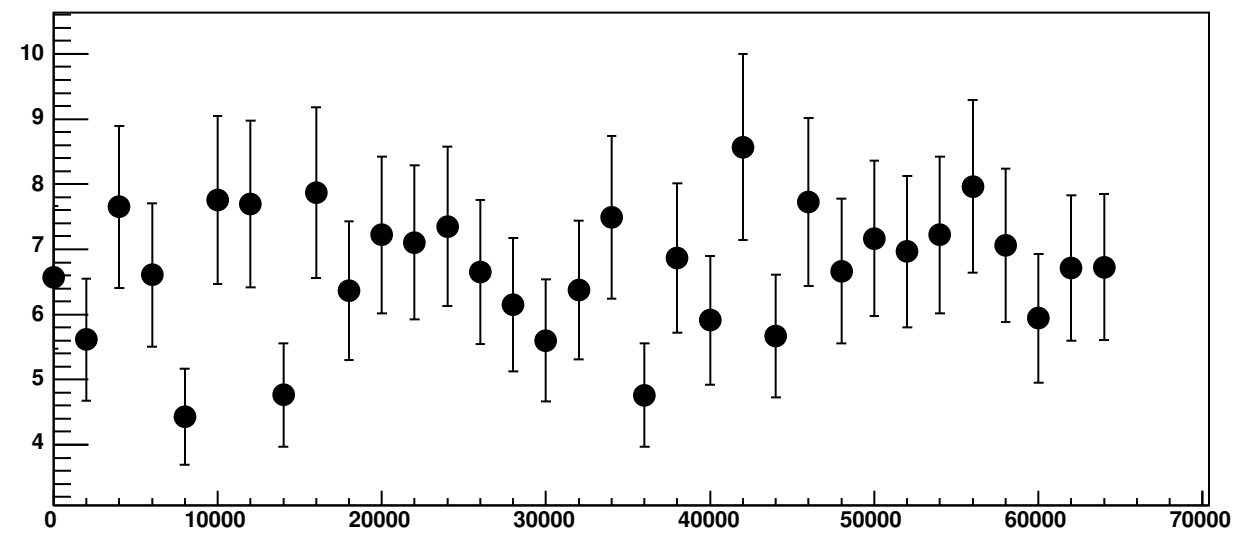
Chip 5, Channel 2, Enable 0, Hold=35, ADC Residuals vs DAC



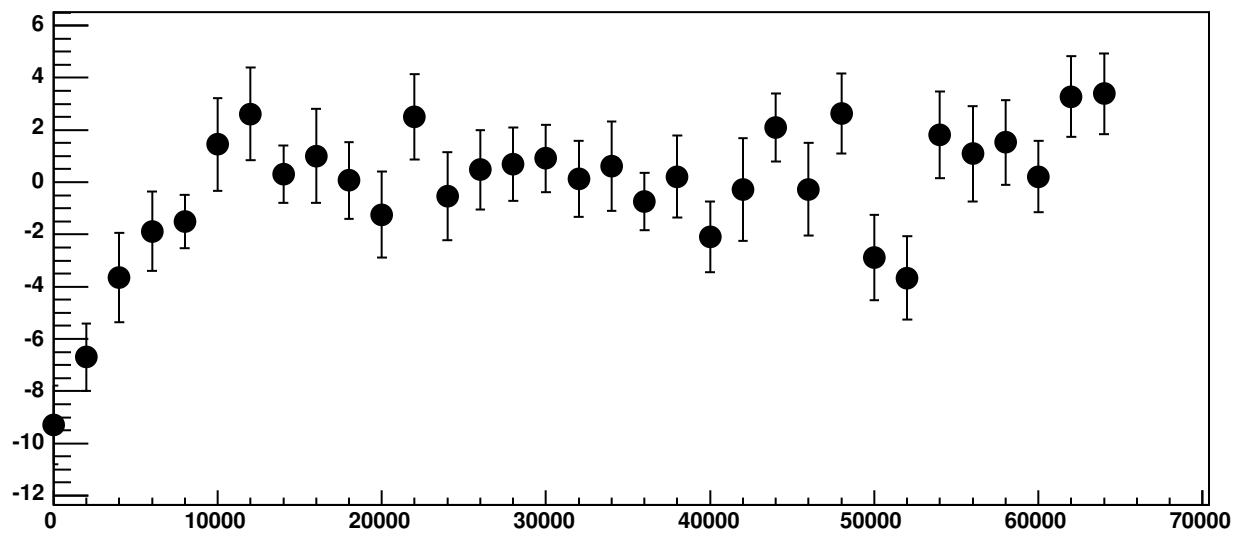
Chip 5, Channel 2, Enable 1, Hold=35, ADC Mean vs DAC

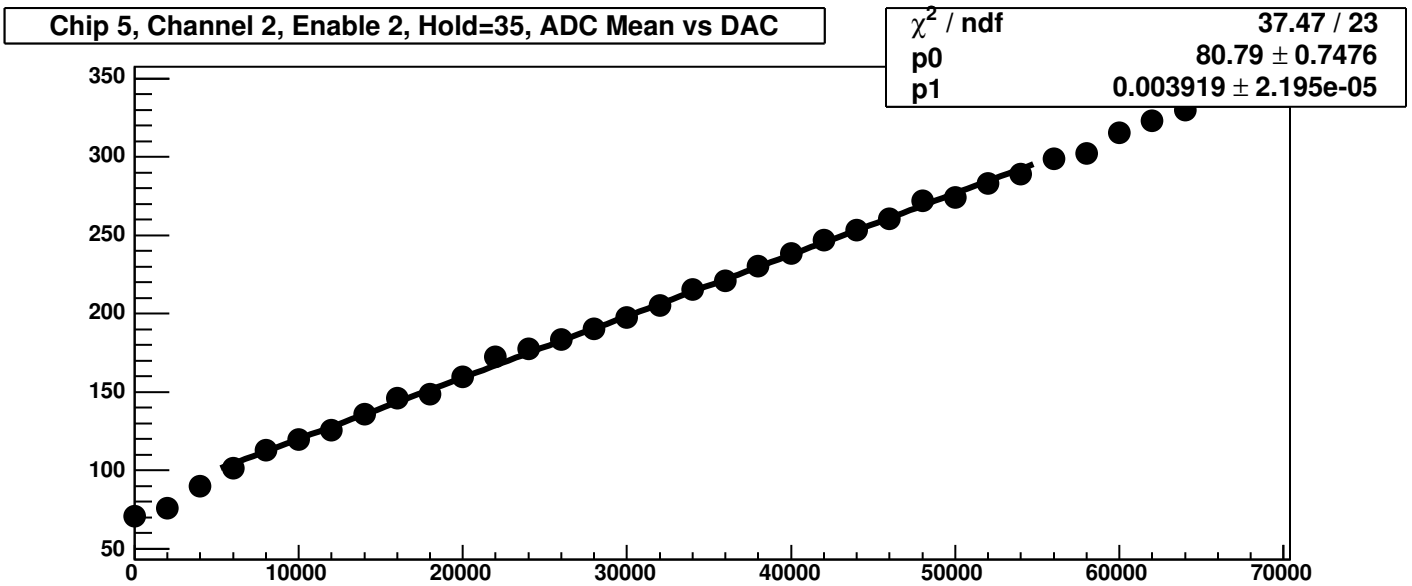


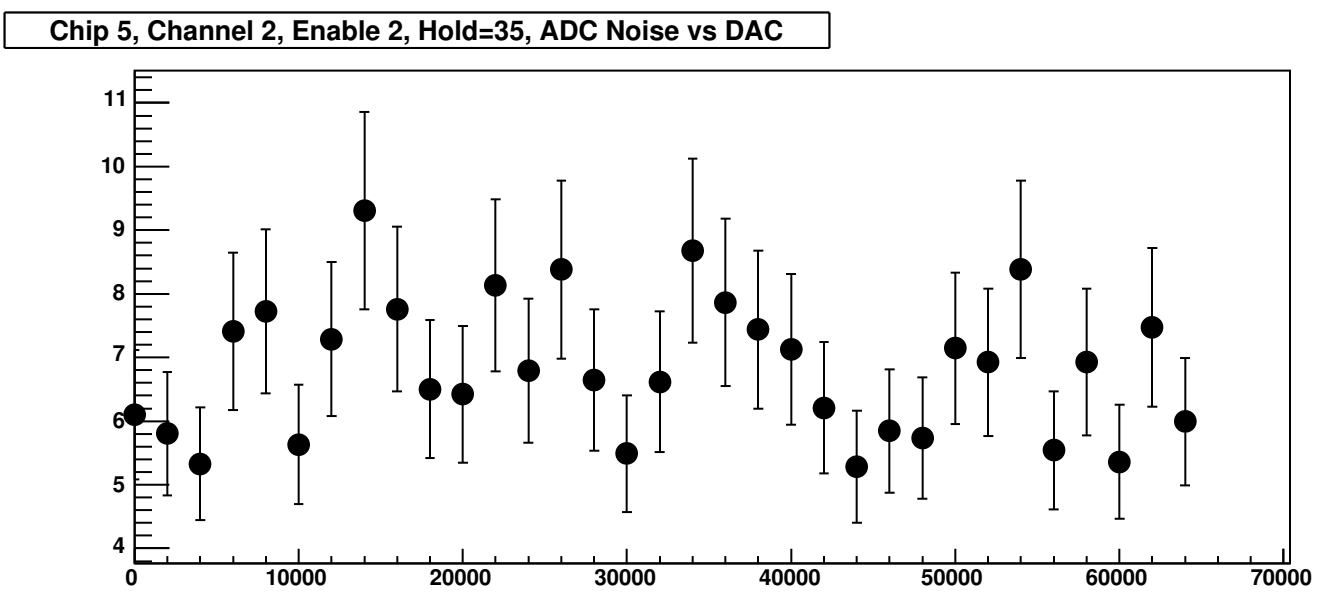
Chip 5, Channel 2, Enable 1, Hold=35, ADC Noise vs DAC

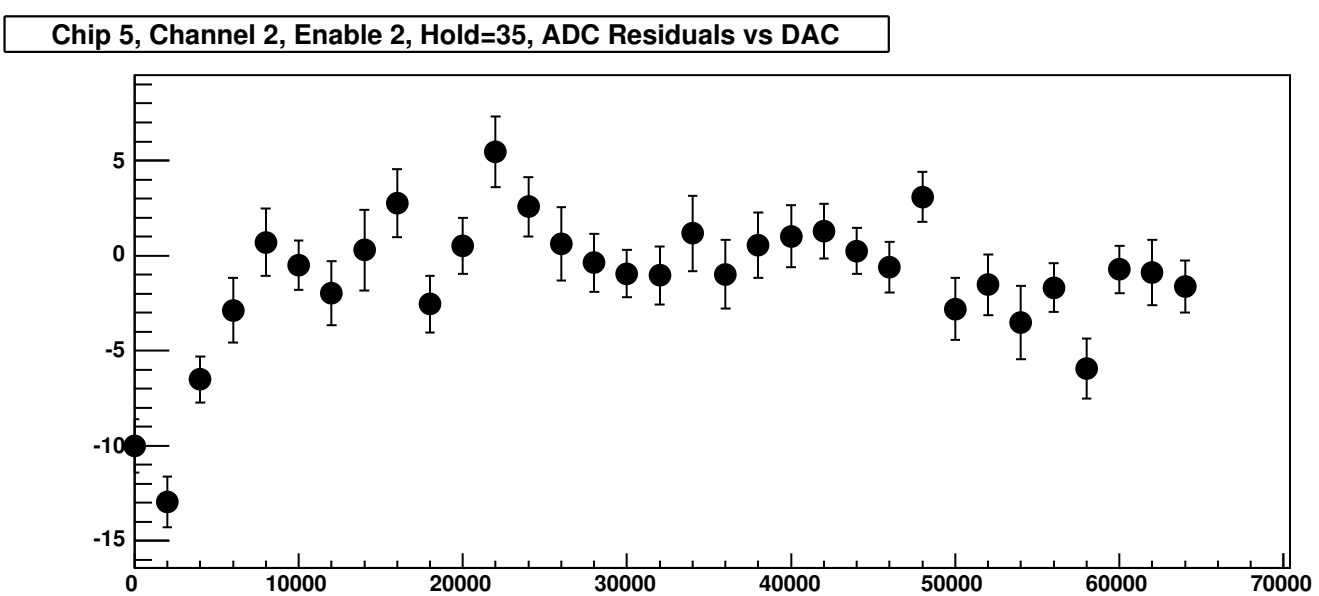


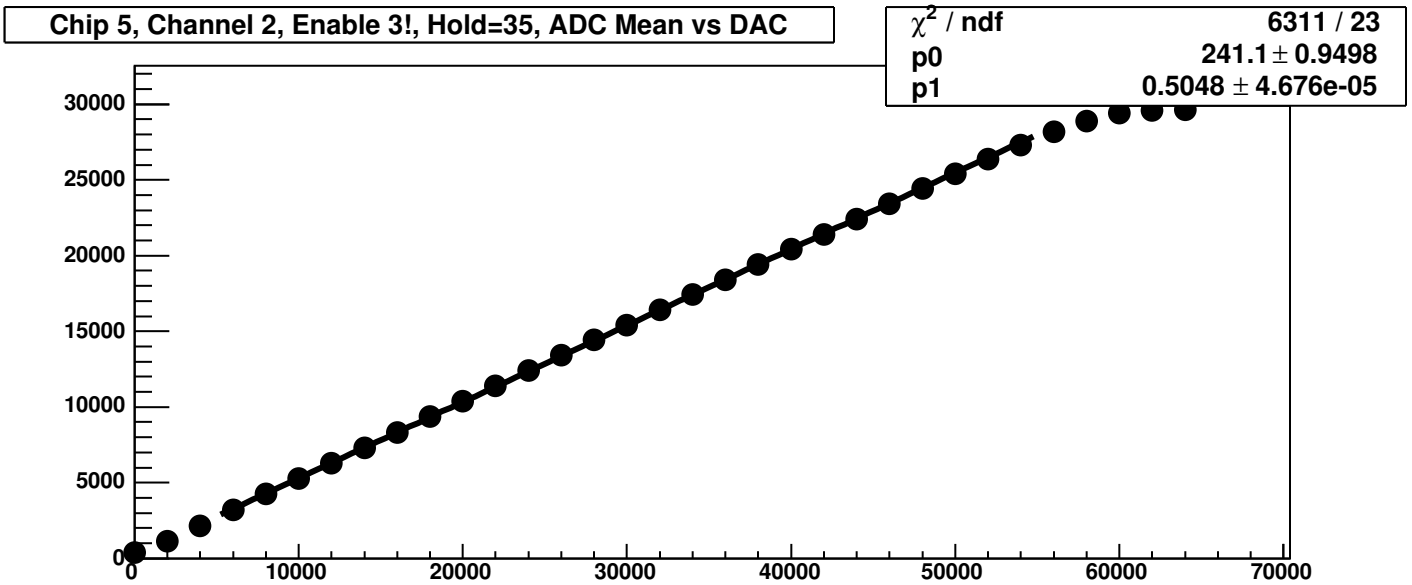
Chip 5, Channel 2, Enable 1, Hold=35, ADC Residuals vs DAC

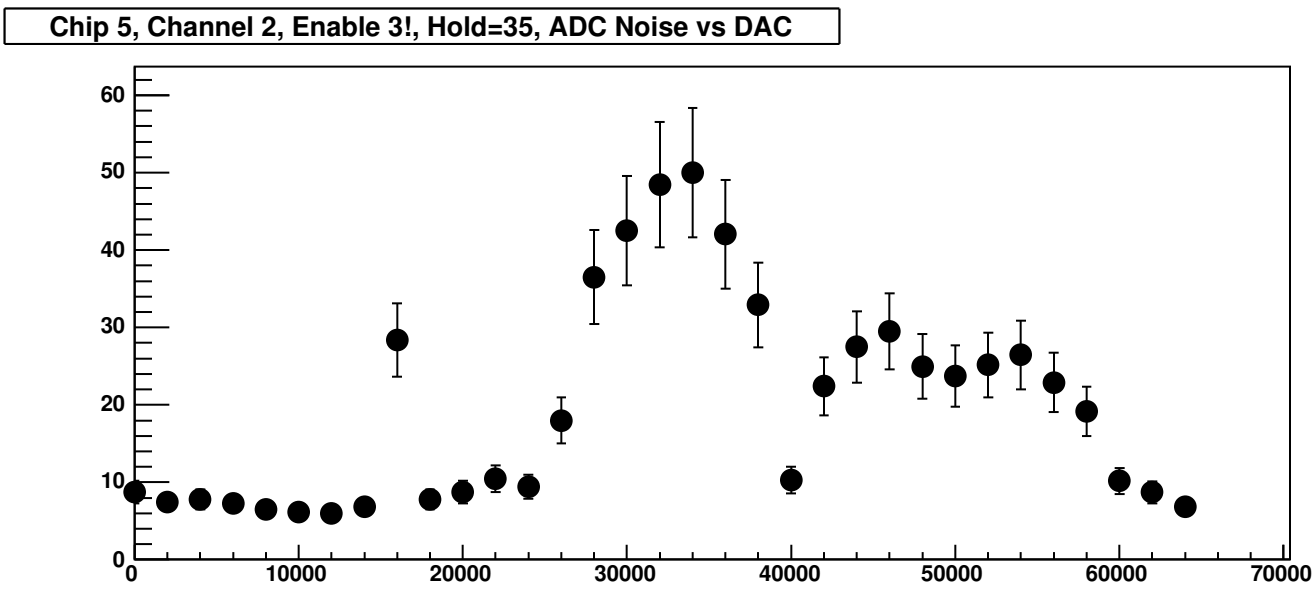


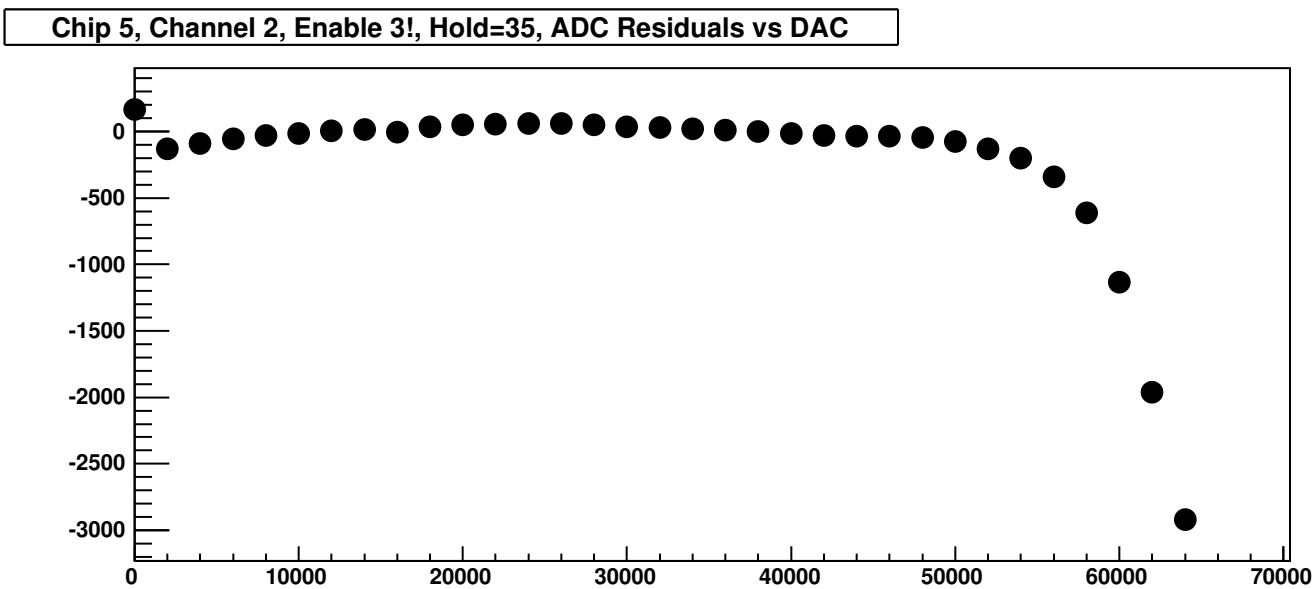

Chip 5, Channel 2, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 37.47 / 23
p0 80.79 ± 0.7476
p1 0.003919 ± 2.195e-05
350
300
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 2, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

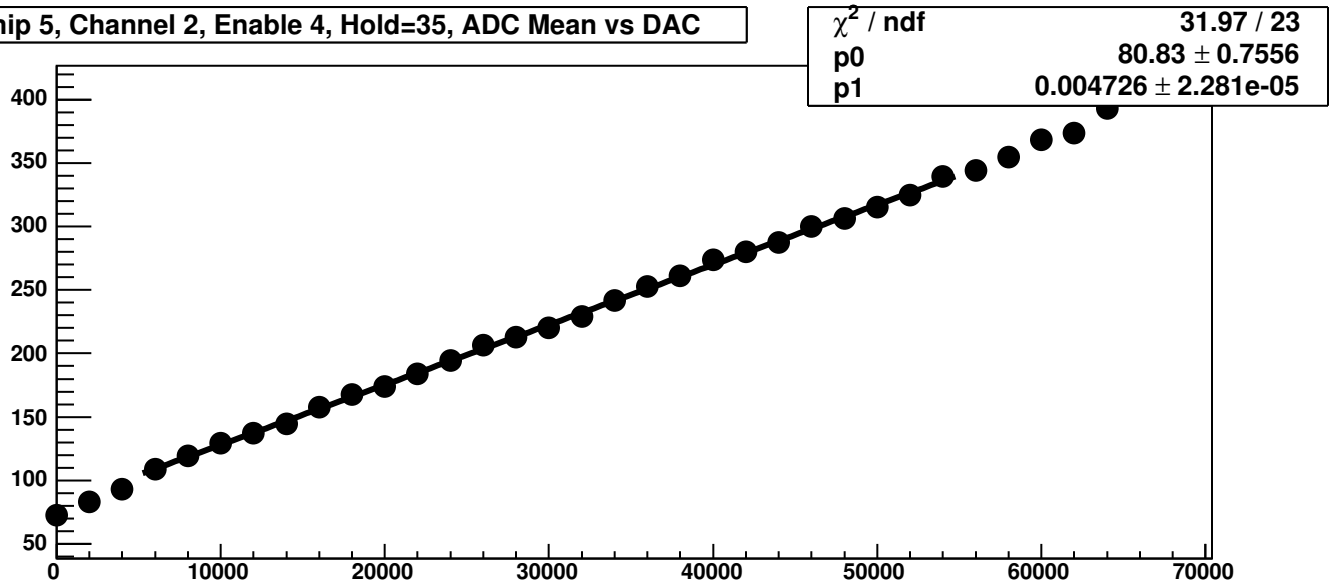

Chip 5, Channel 2, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 2, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 6311 / 23
p0 241.1 ± 0.9498
p1 0.5048 ± 4.676e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

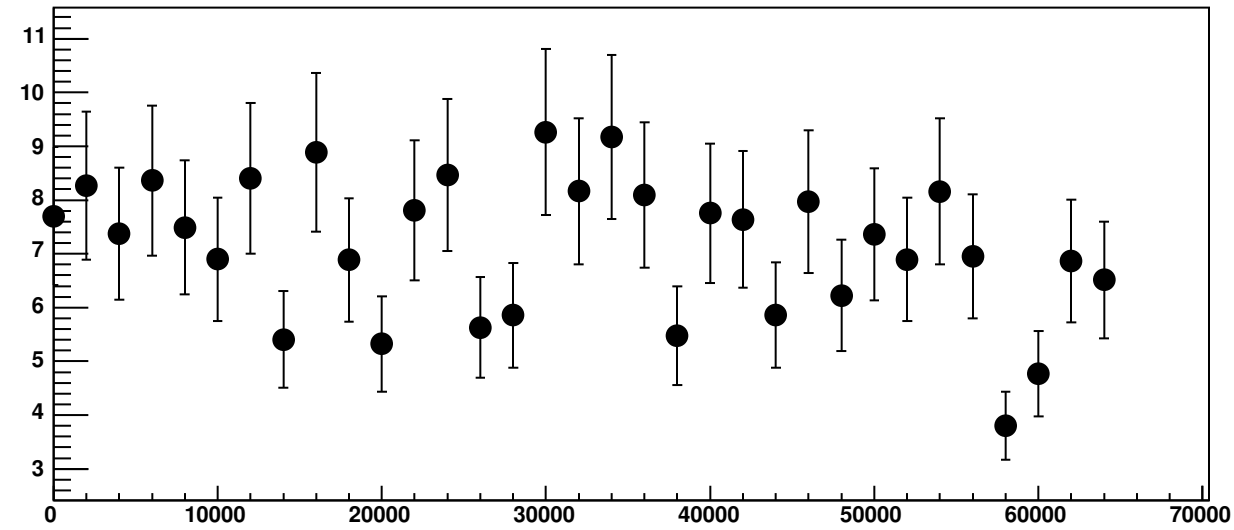

Chip 5, Channel 2, Enable 3!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 2, Enable 3!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
-2500
-3000
0
10000
20000
30000
40000
50000
60000
70000

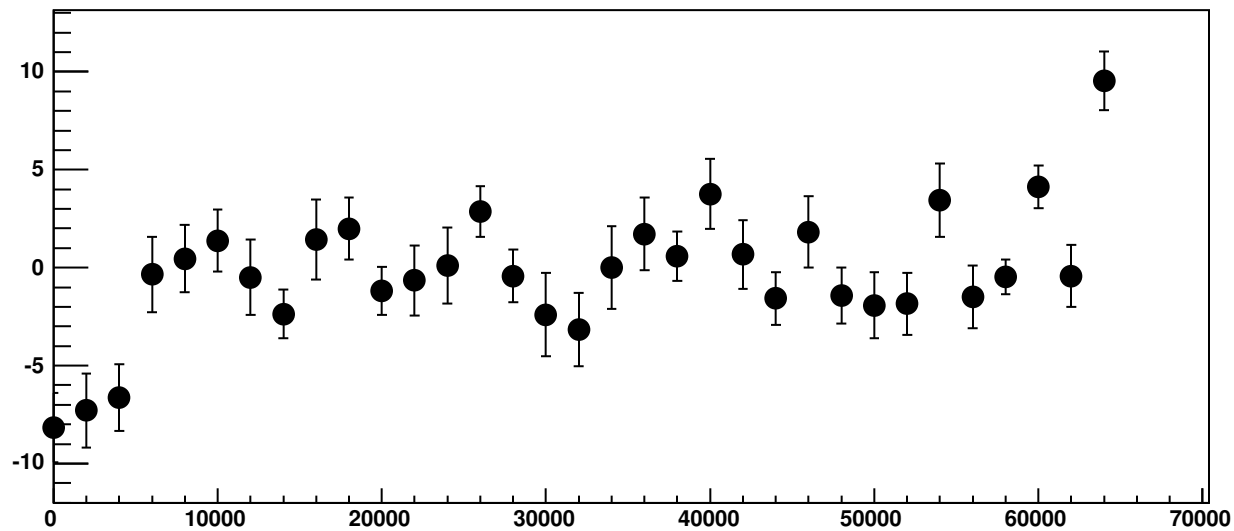
Chip 5, Channel 2, Enable 4, Hold=35, ADC Mean vs DAC

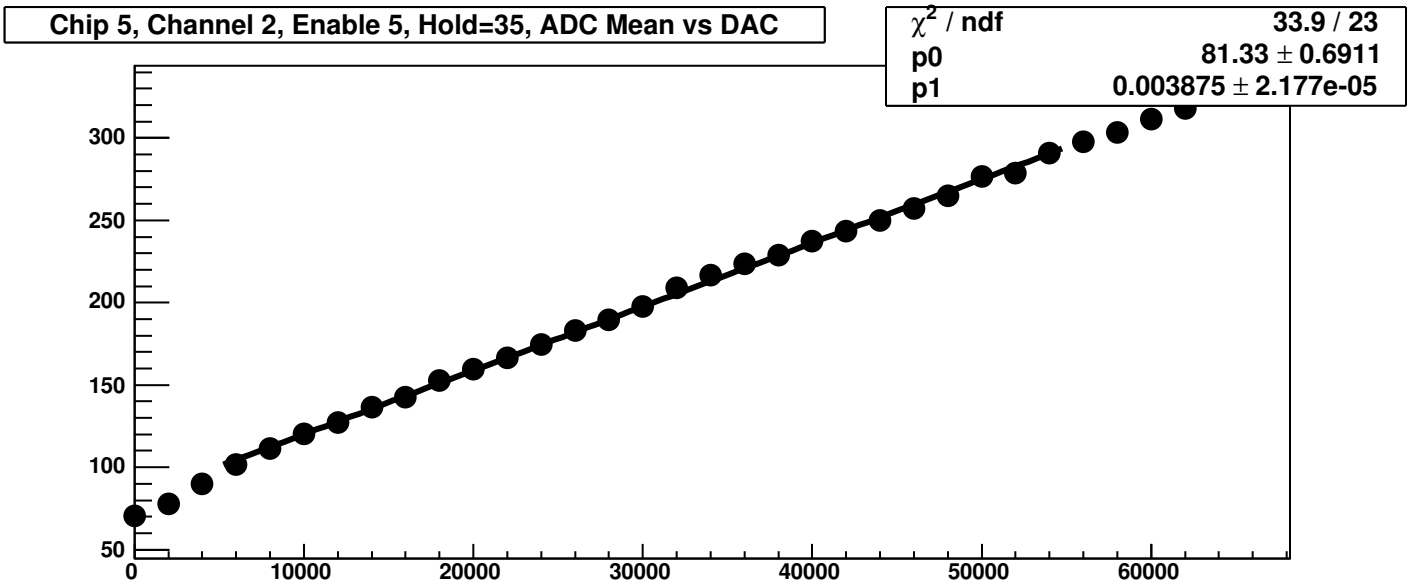


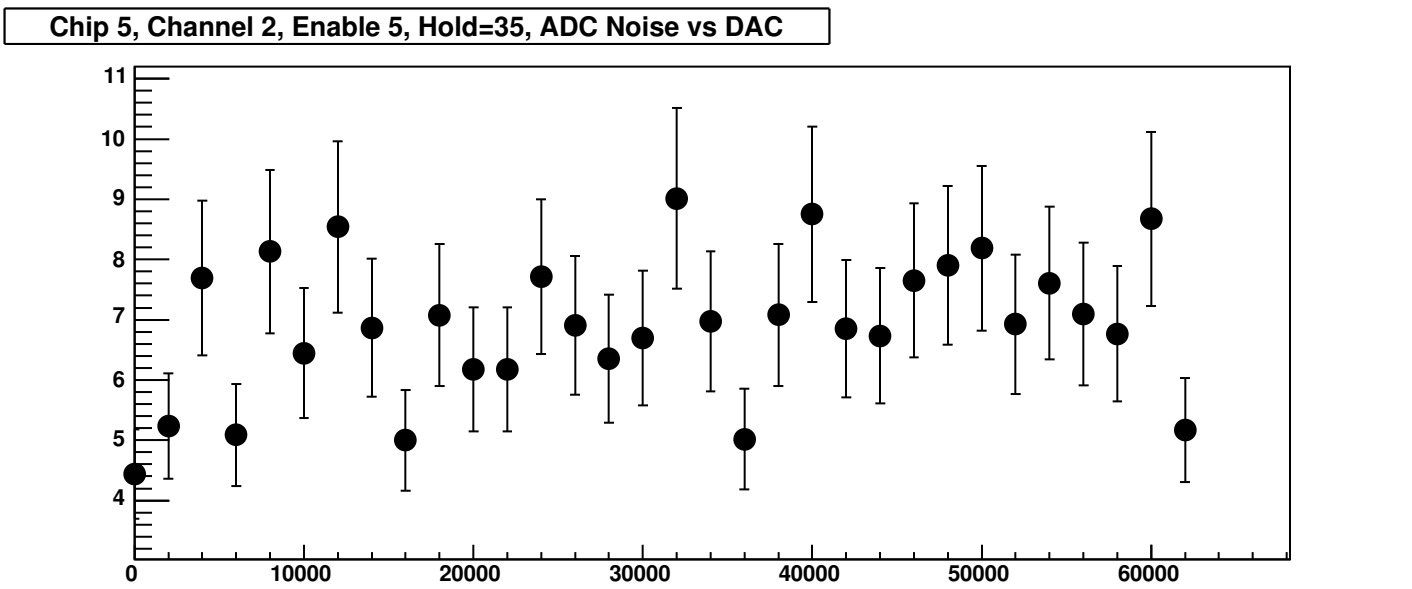
Chip 5, Channel 2, Enable 4, Hold=35, ADC Noise vs DAC

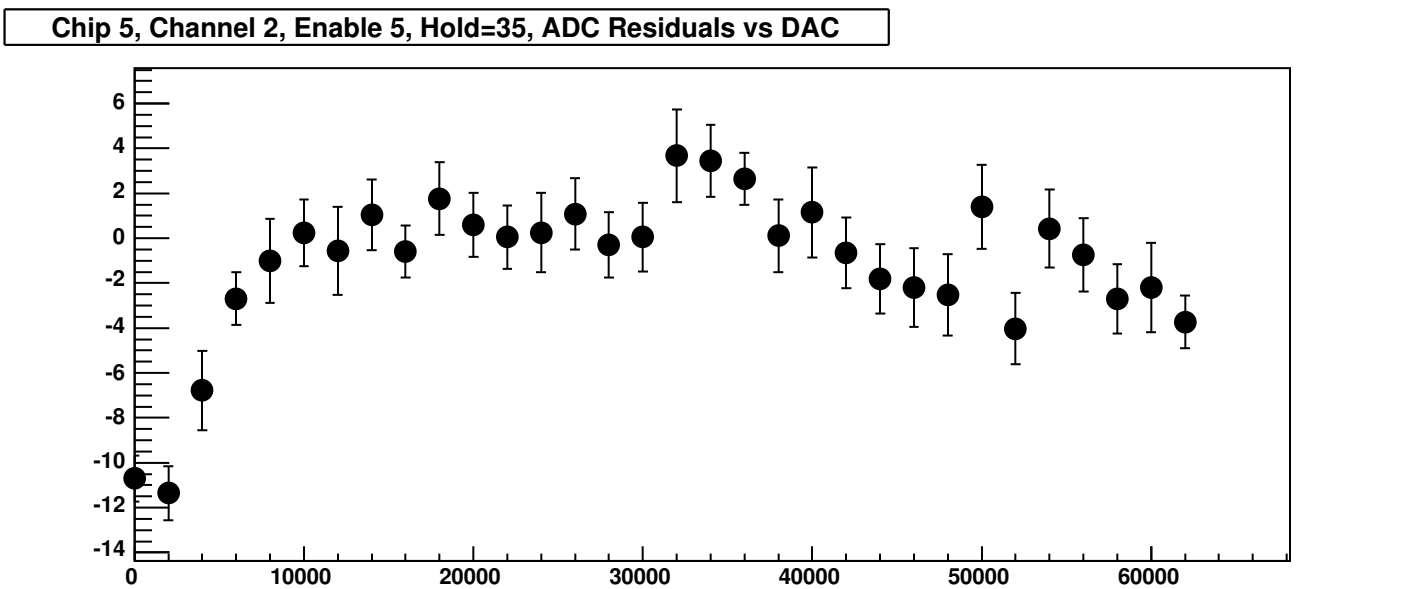


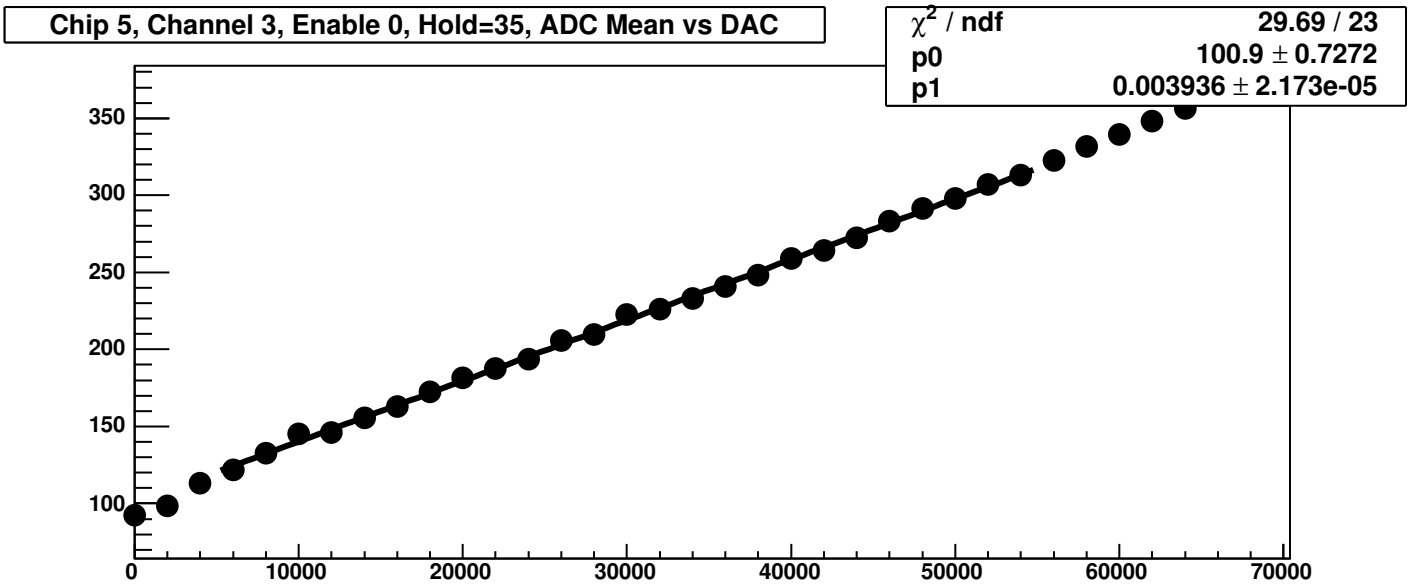
Chip 5, Channel 2, Enable 4, Hold=35, ADC Residuals vs DAC

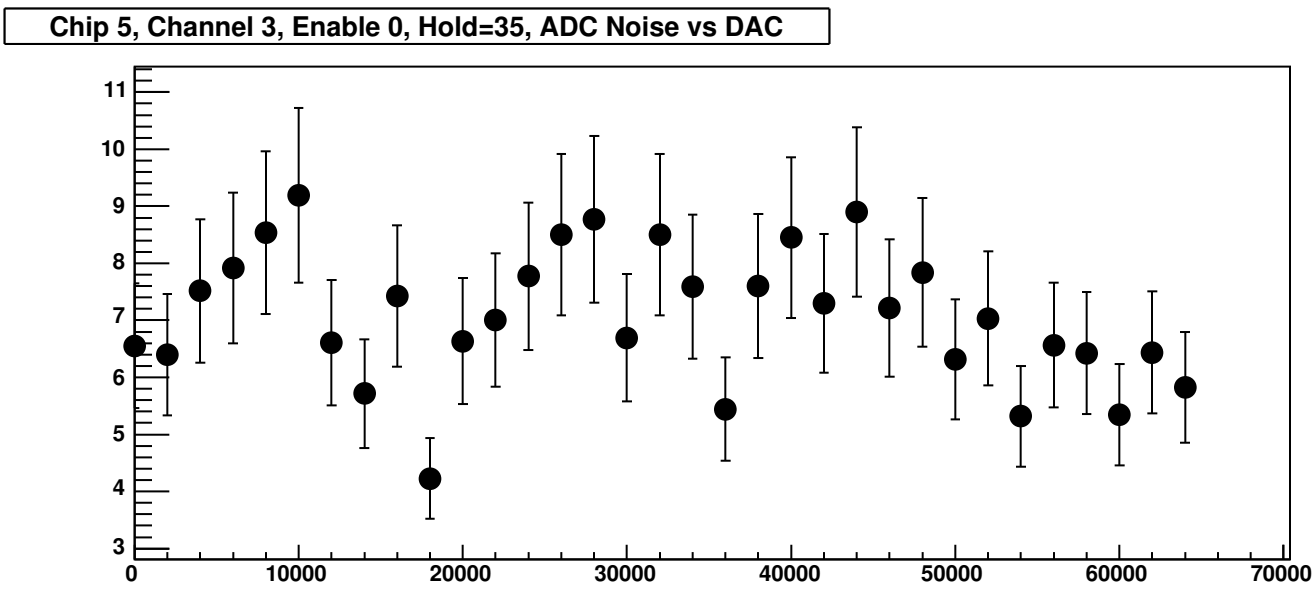


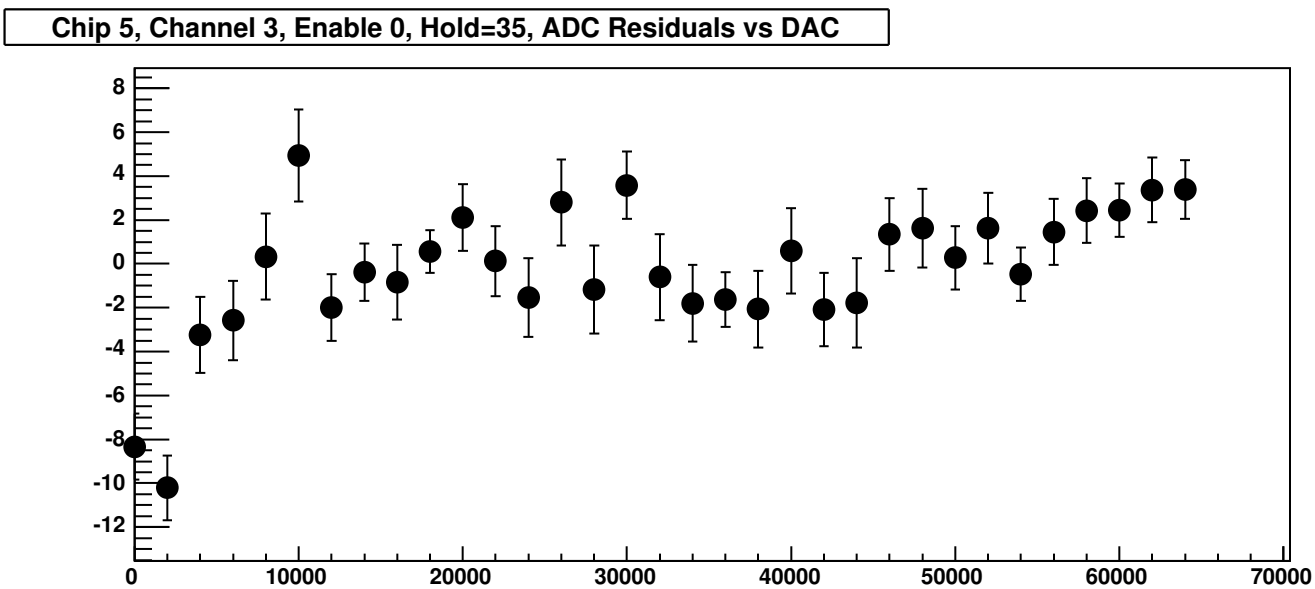

Chip 5, Channel 2, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 33.9 / 23
p0 81.33 ± 0.6911
p1 0.003875 ± 2.177e-05
300
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 2, Enable 5, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

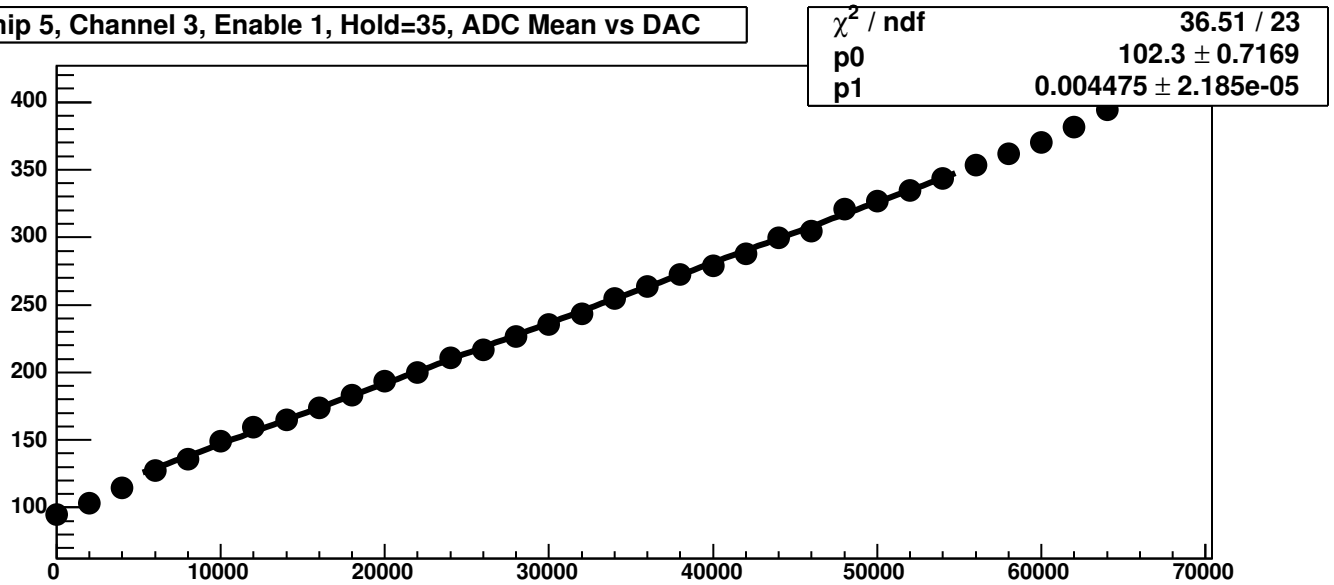

Chip 5, Channel 2, Enable 5, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
-14
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 3, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 29.69 / 23
p0 100.9 ± 0.7272
p1 0.003936 ± 2.173e-05
350
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

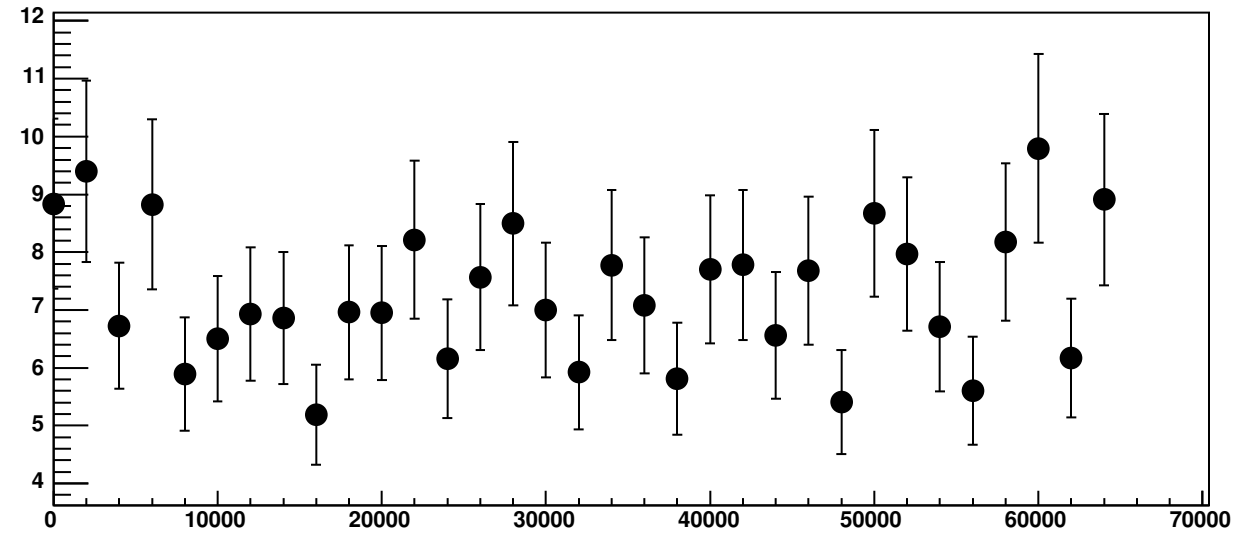

Chip 5, Channel 3, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 3, Enable 0, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
0
10000
20000
30000
40000
50000
60000
70000

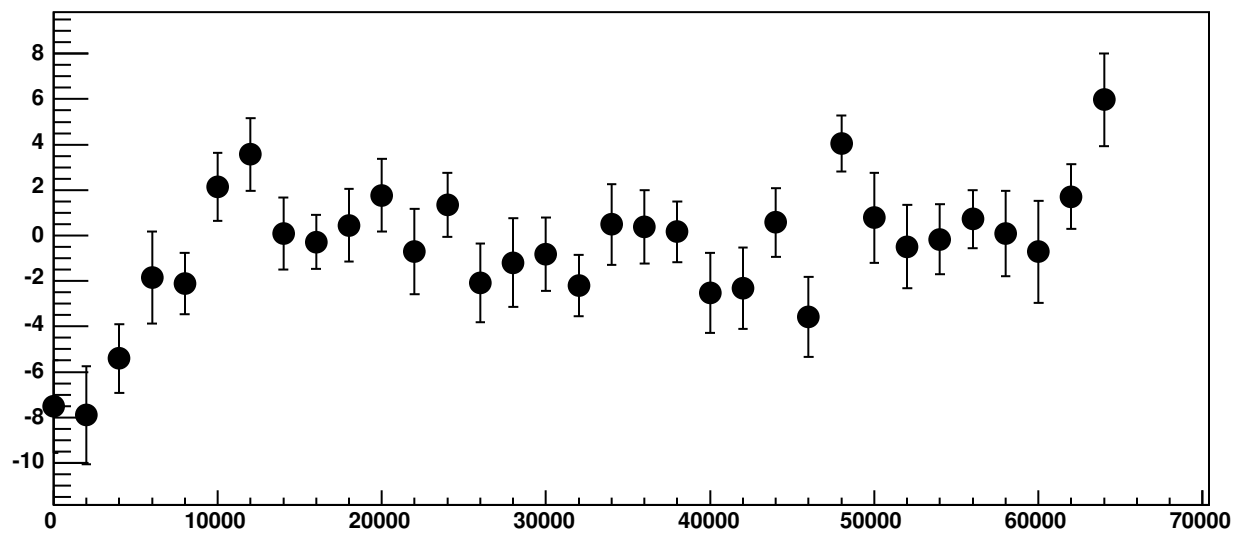
Chip 5, Channel 3, Enable 1, Hold=35, ADC Mean vs DAC

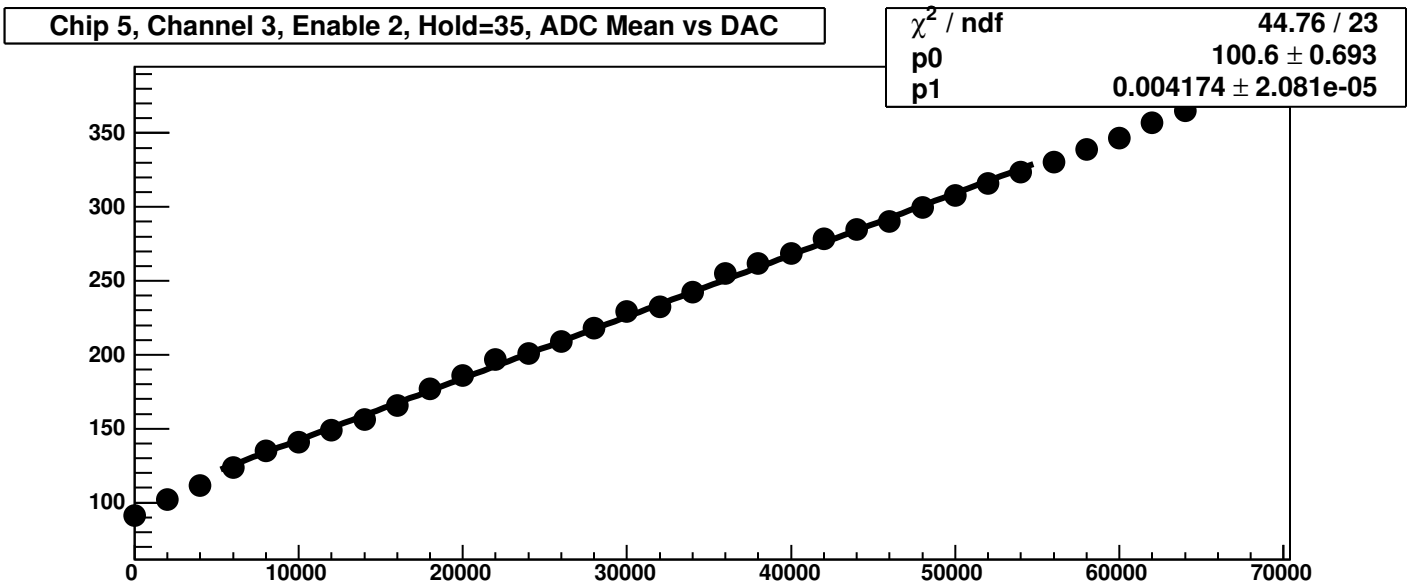


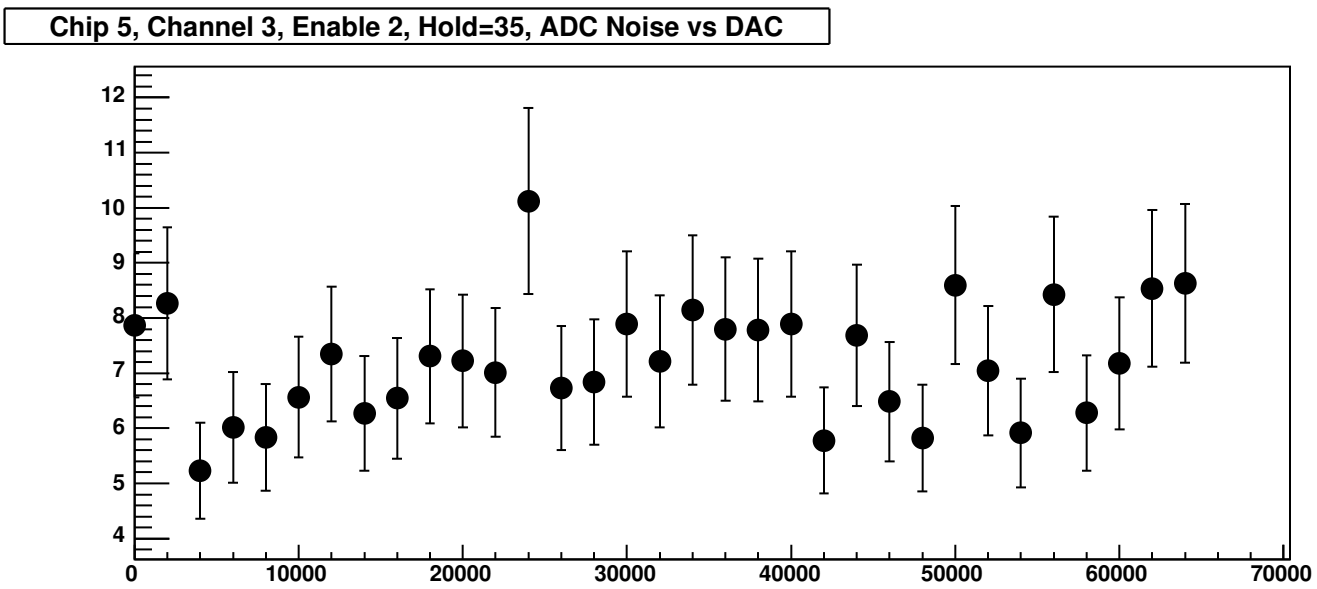
Chip 5, Channel 3, Enable 1, Hold=35, ADC Noise vs DAC

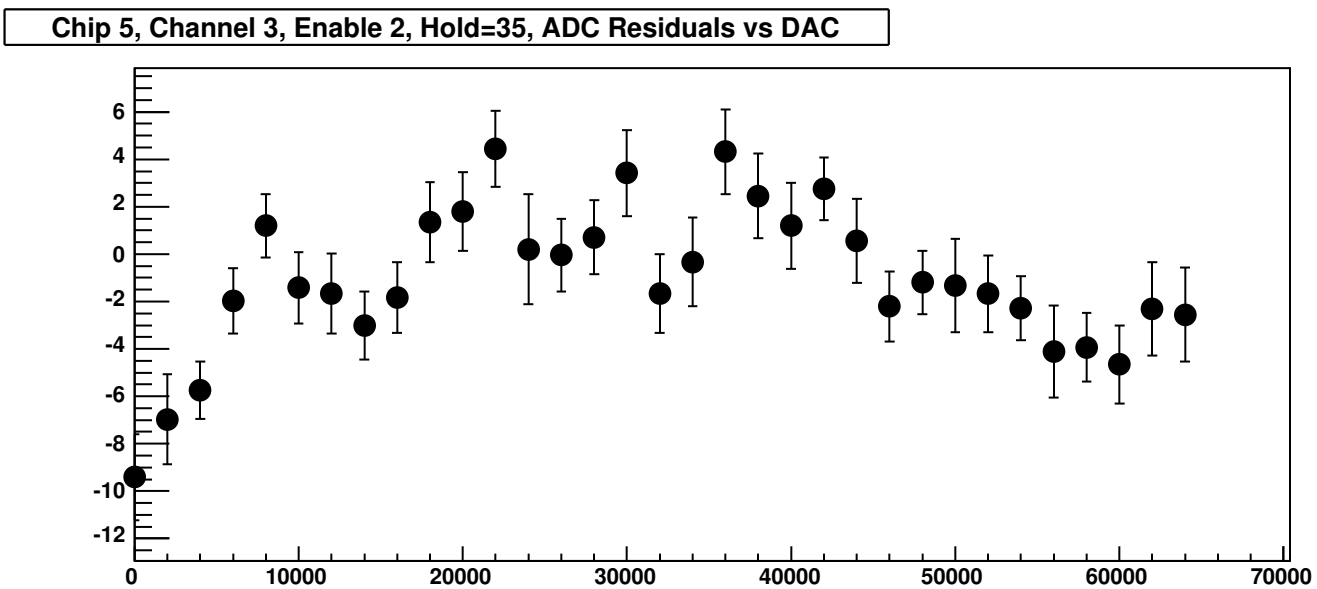


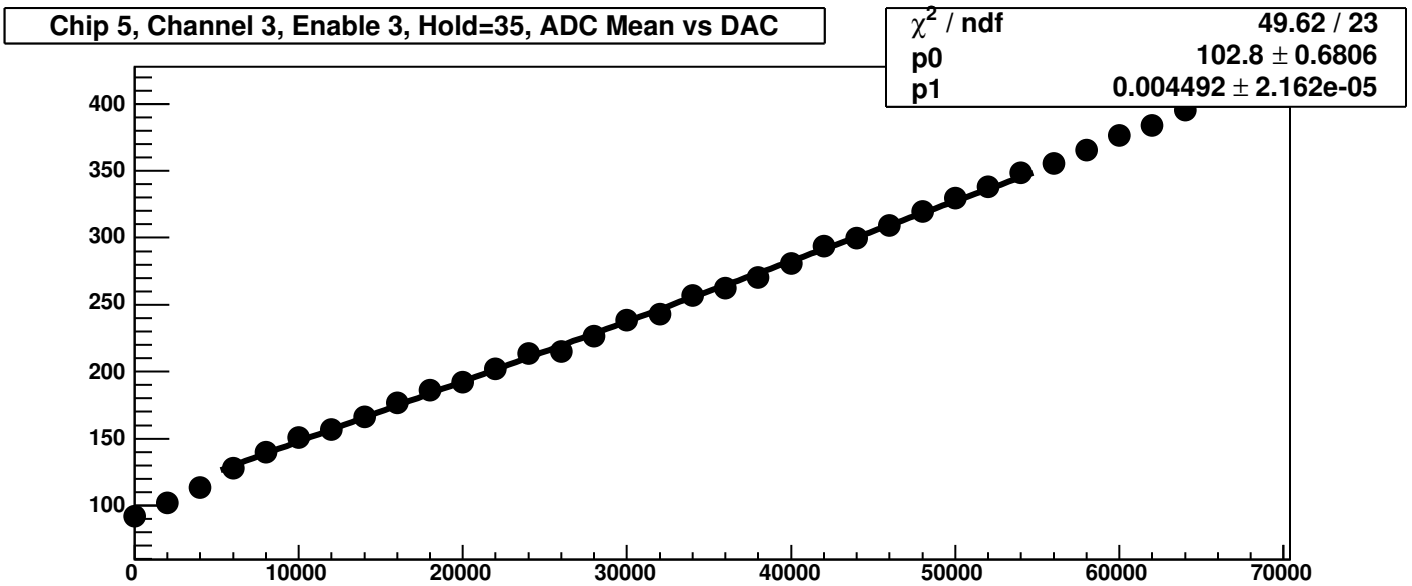
Chip 5, Channel 3, Enable 1, Hold=35, ADC Residuals vs DAC

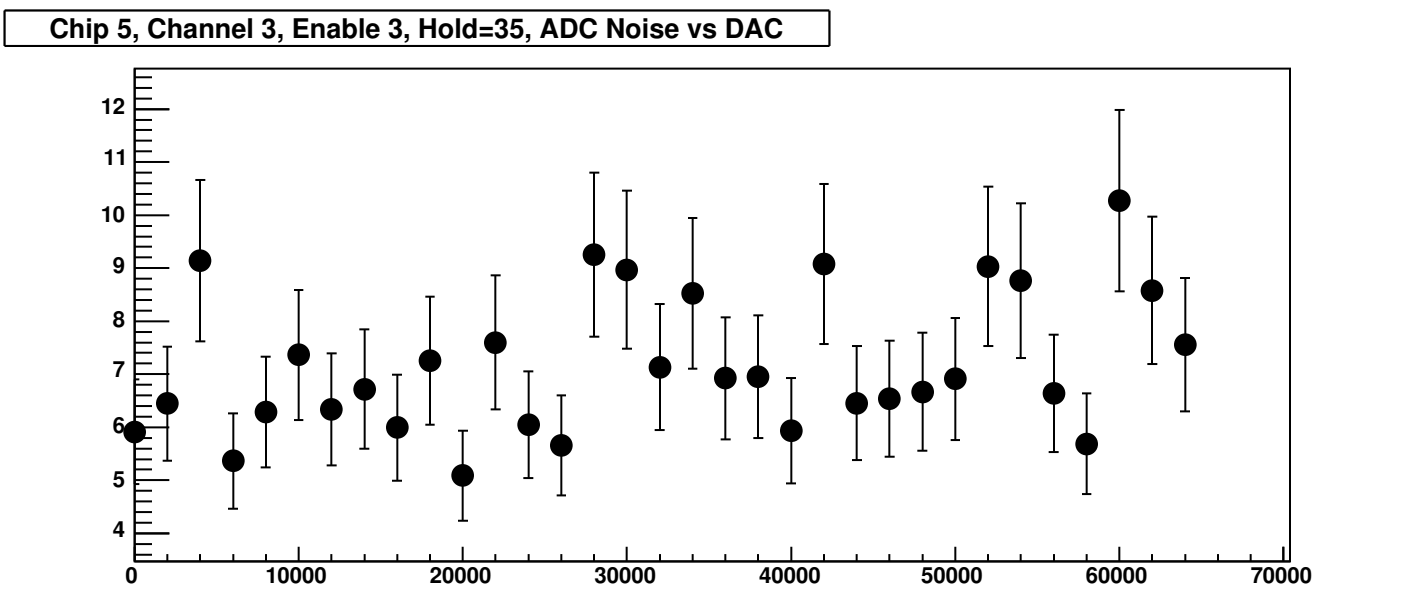


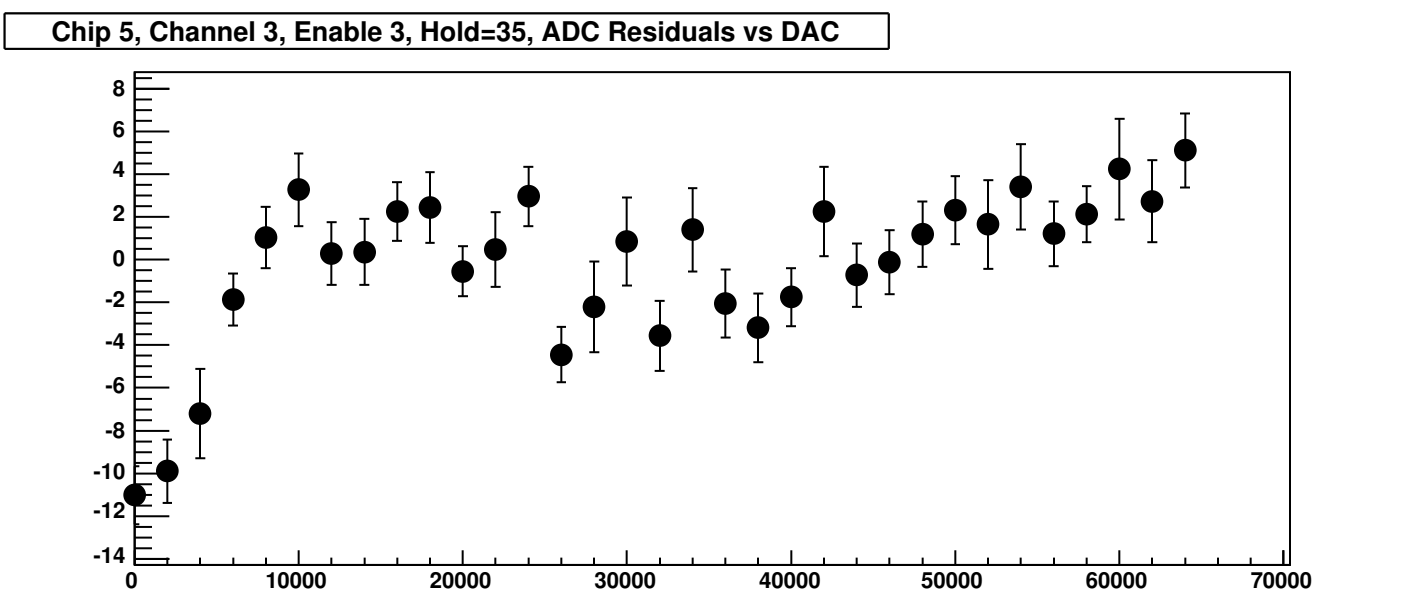

Chip 5, Channel 3, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 44.76 / 23
p0 100.6 ± 0.693
p1 0.004174 ± 2.081e-05
350
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 3, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

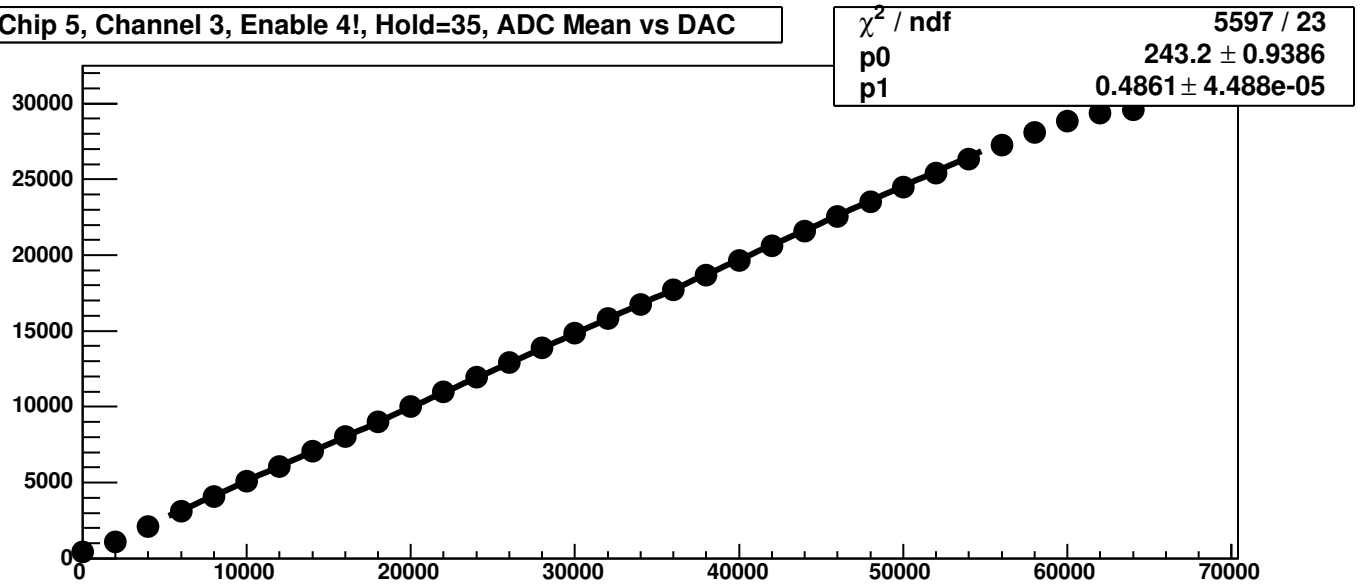

Chip 5, Channel 3, Enable 2, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 3, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 49.62 / 23
p0 102.8 ± 0.6806
p1 0.004492 ± 2.162e-05
400
350
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

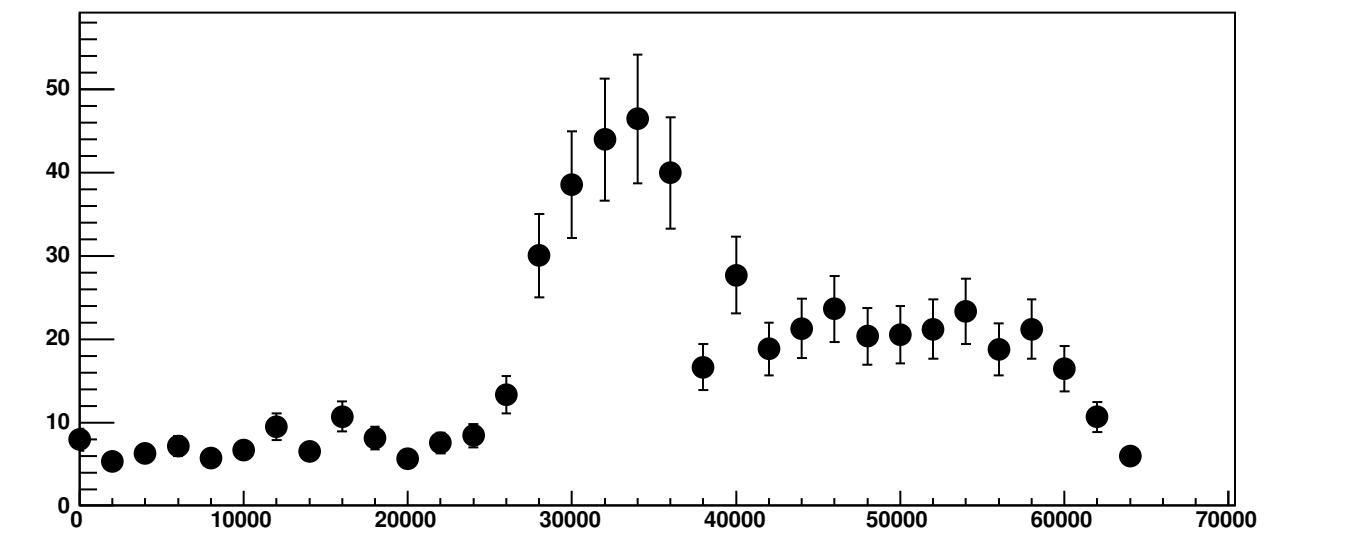

Chip 5, Channel 3, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 3, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
-14
0
10000
20000
30000
40000
50000
60000
70000

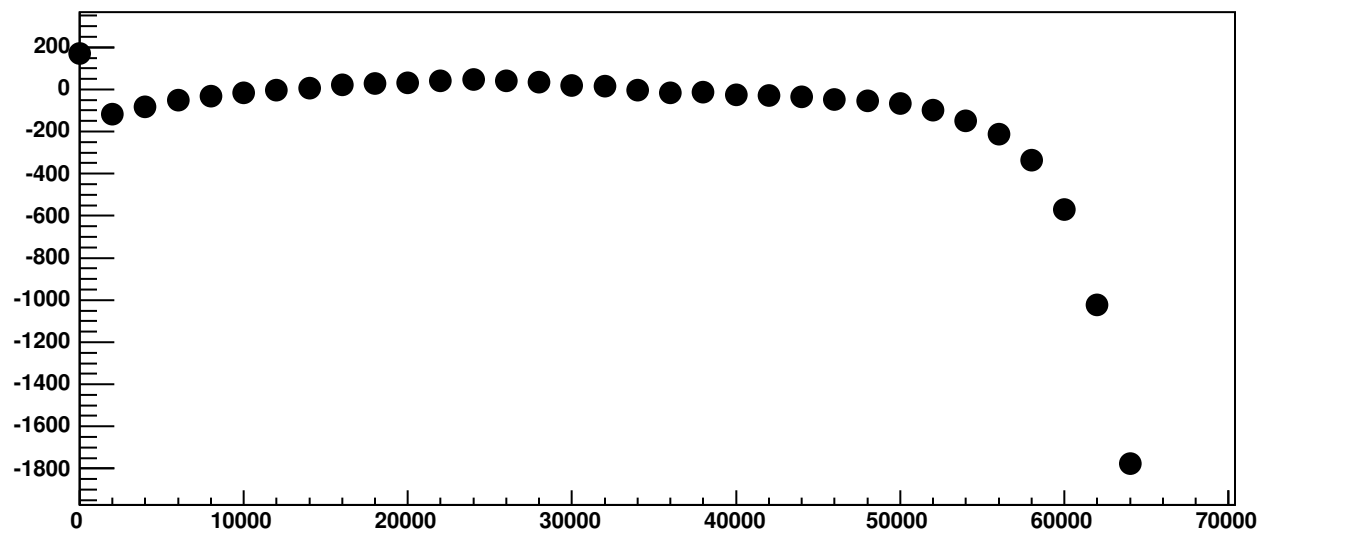
Chip 5, Channel 3, Enable 4!, Hold=35, ADC Mean vs DAC

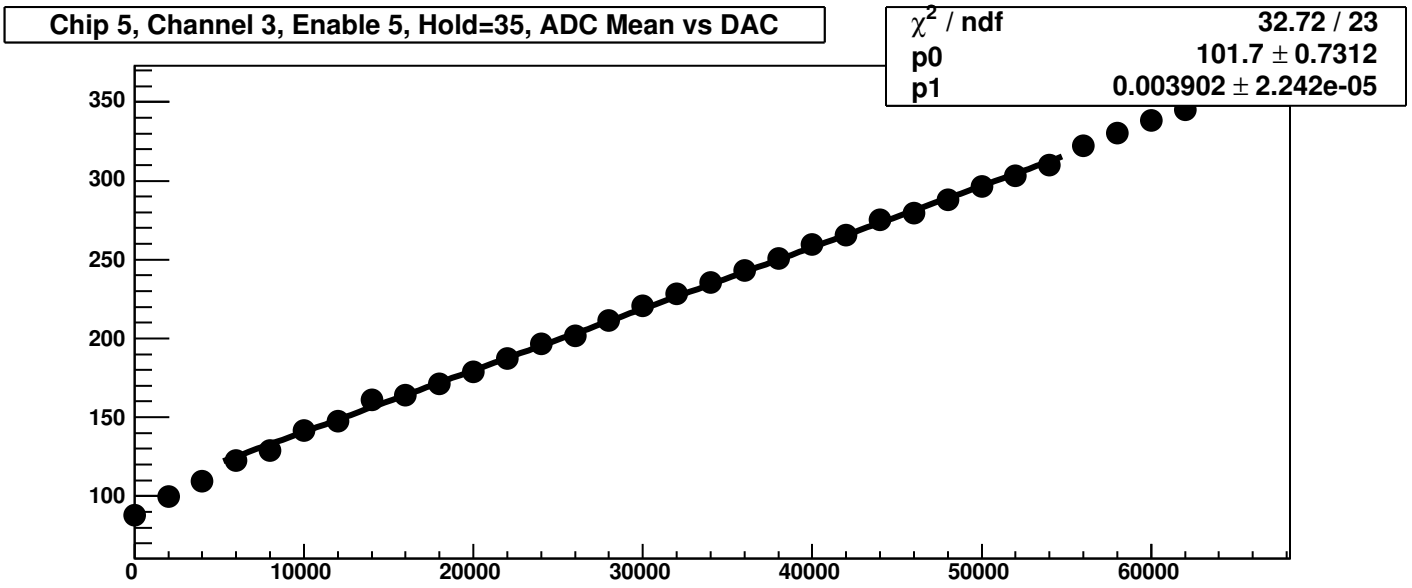


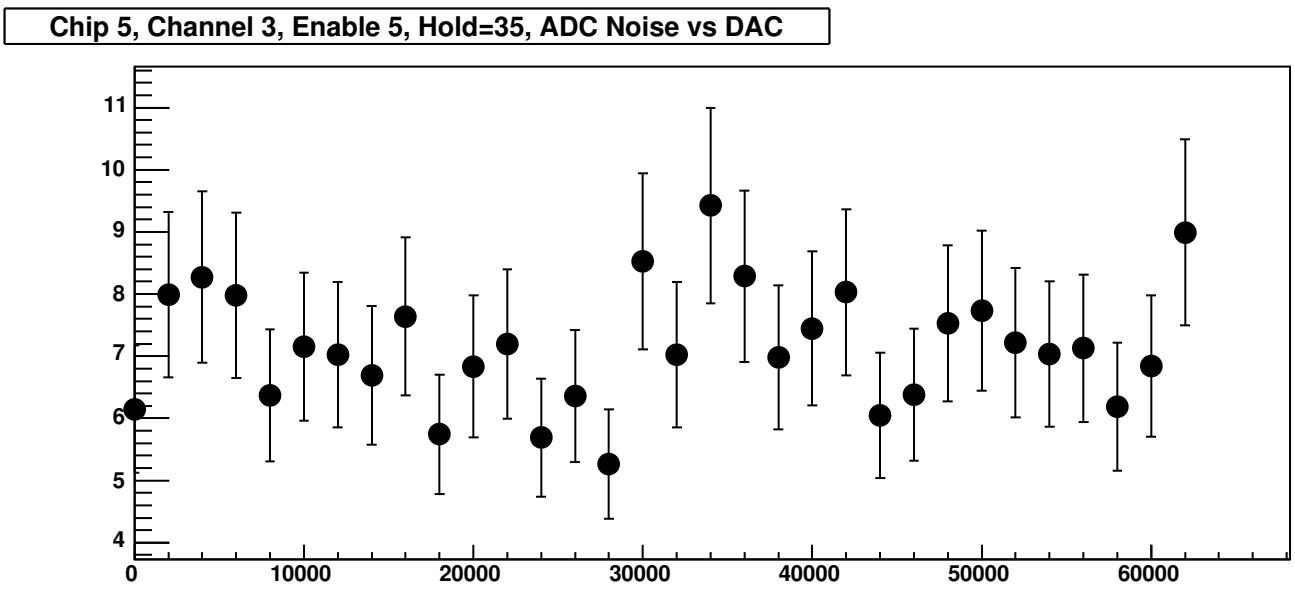
Chip 5, Channel 3, Enable 4!, Hold=35, ADC Noise vs DAC

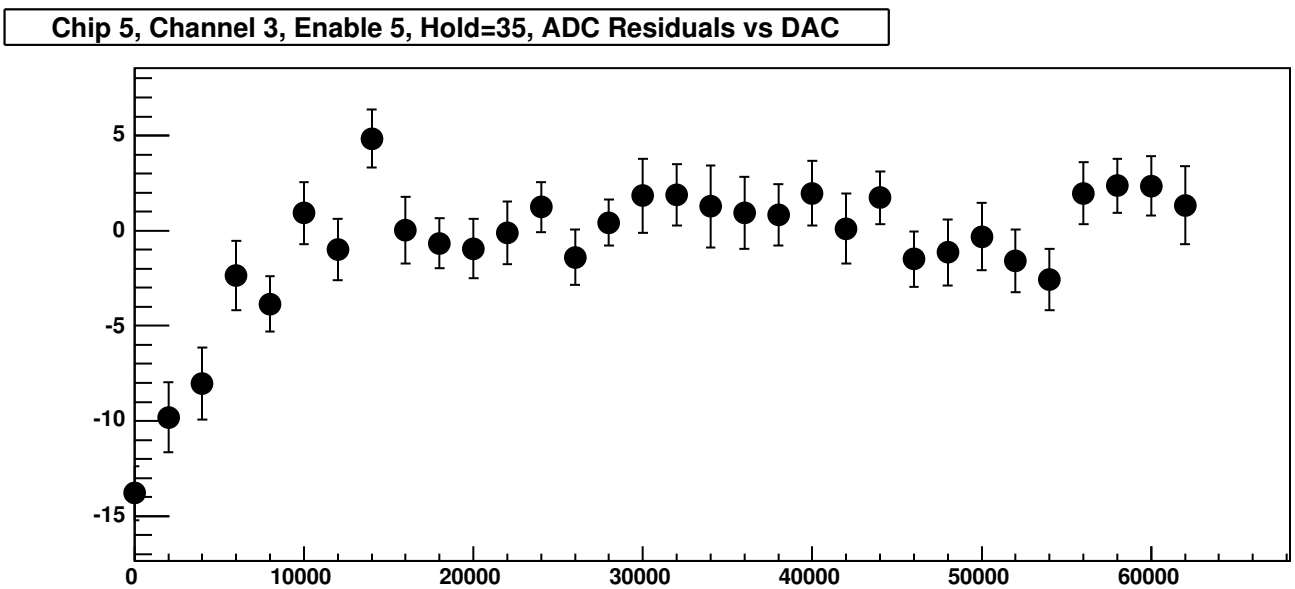


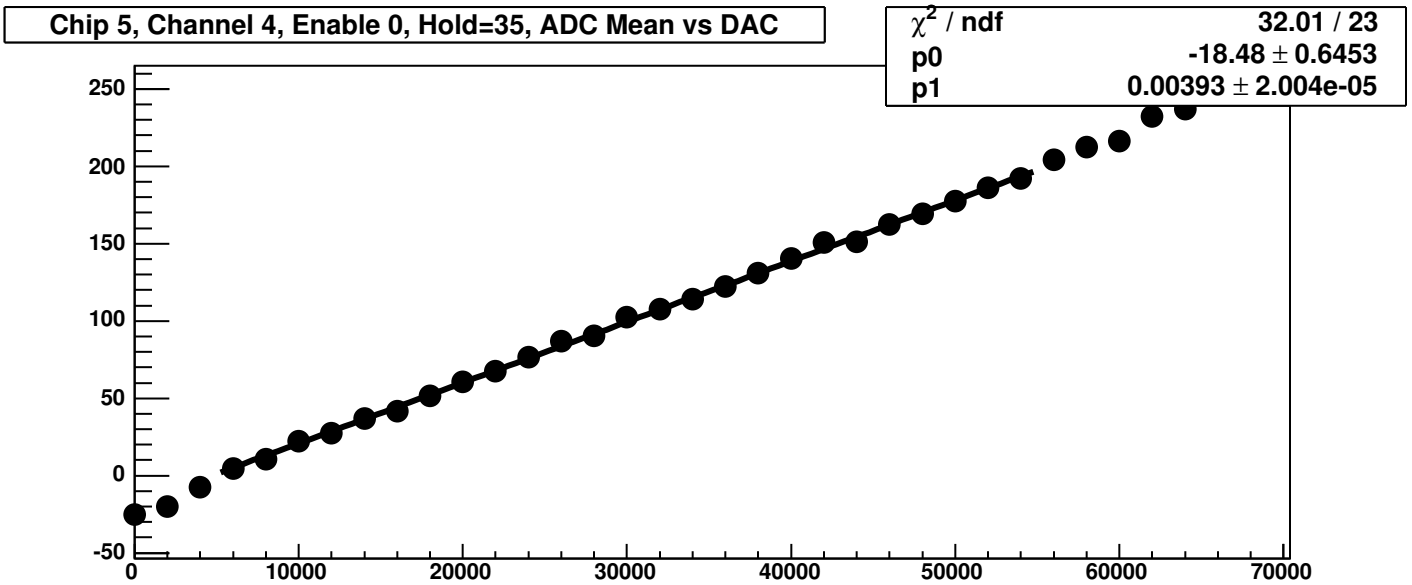
Chip 5, Channel 3, Enable 4!, Hold=35, ADC Residuals vs DAC

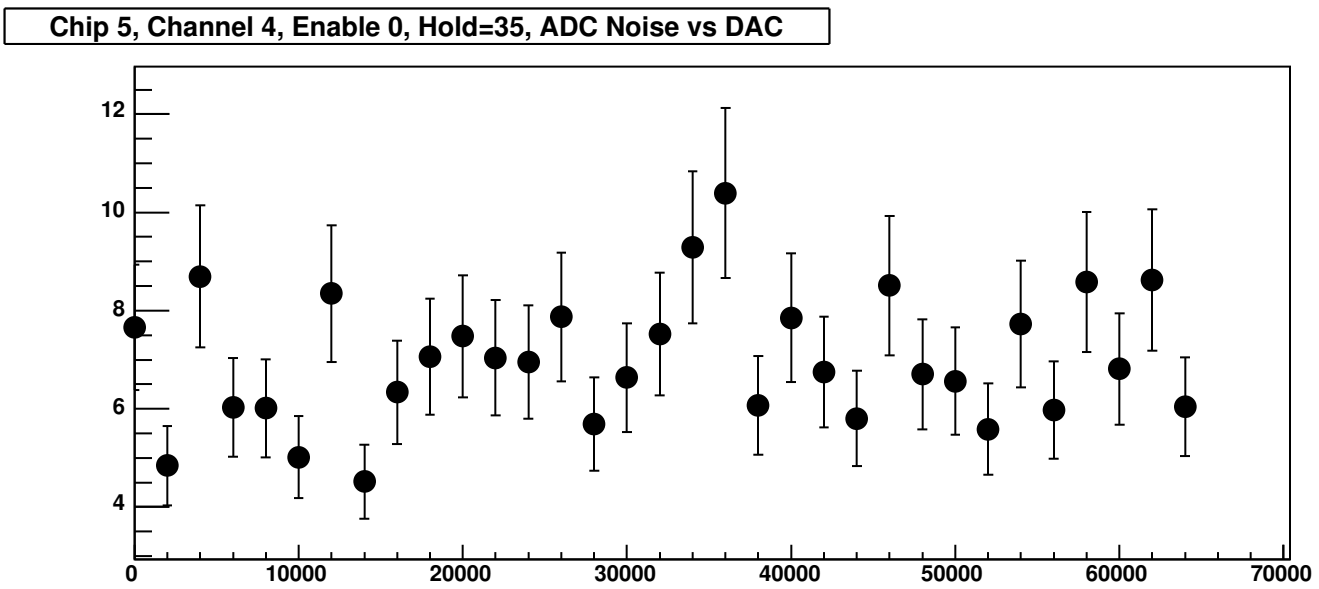


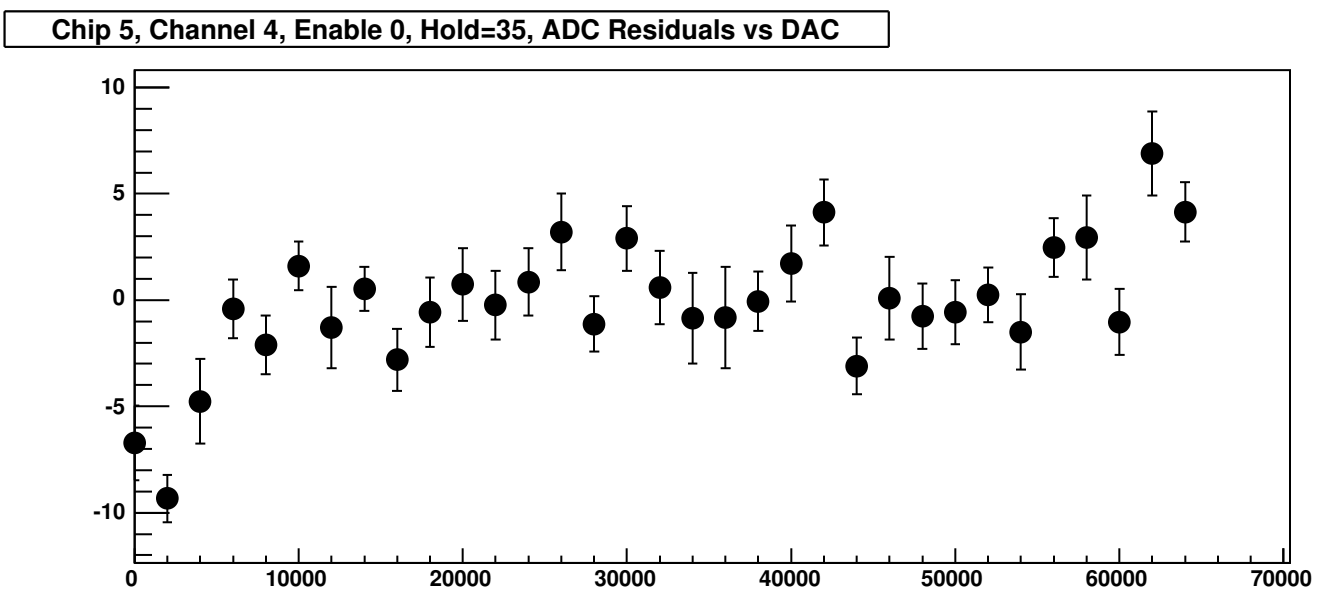

Chip 5, Channel 3, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 32.72 / 23
p0 101.7 ± 0.7312
p1 0.003902 ± 2.242e-05
350
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000

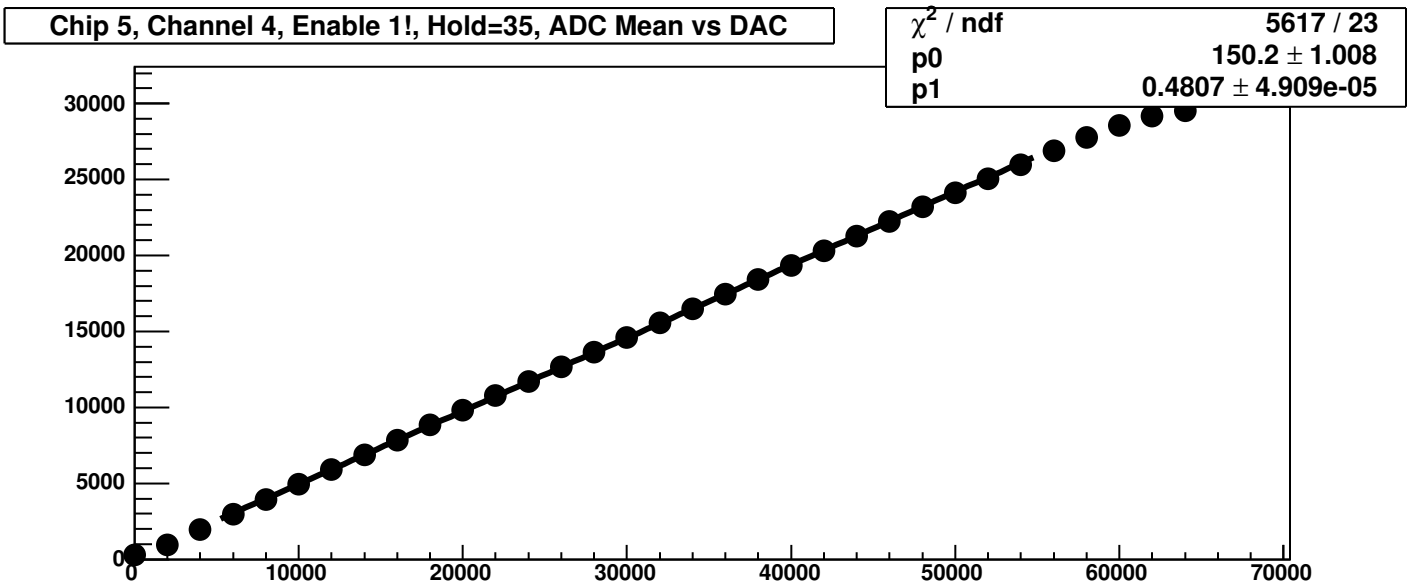

Chip 5, Channel 3, Enable 5, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000

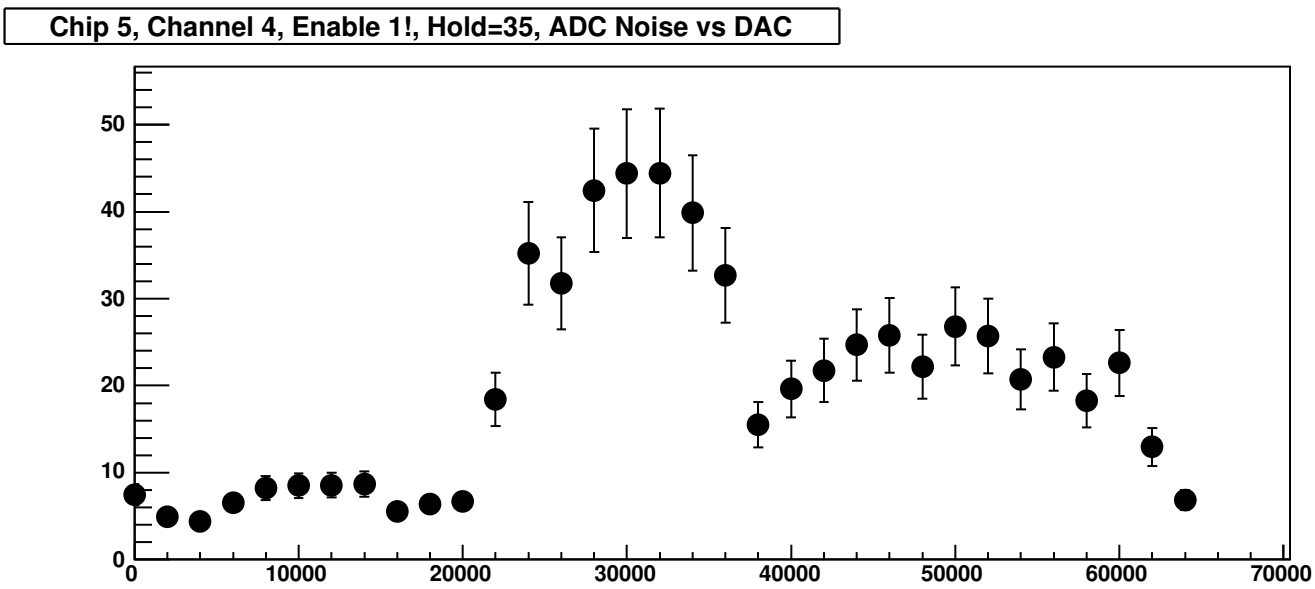

Chip 5, Channel 3, Enable 5, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000

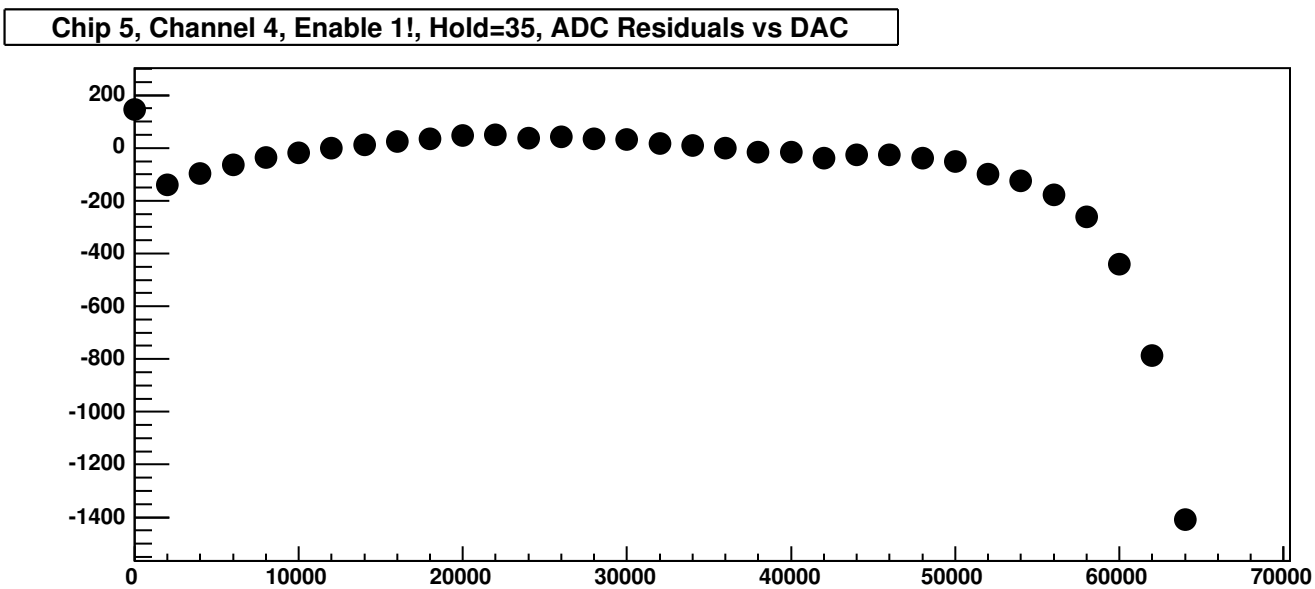

Chip 5, Channel 4, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 32.01 / 23
p0 -18.48 ± 0.6453
p1 0.00393 ± 2.004e-05
250
200
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000

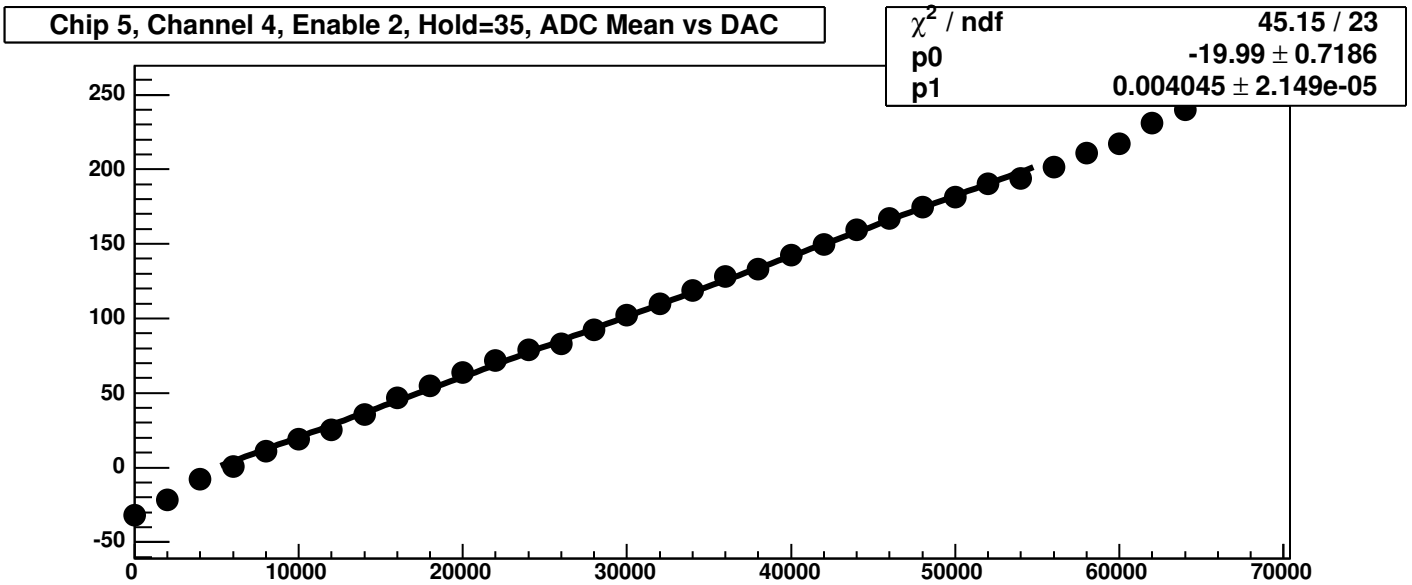

Chip 5, Channel 4, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

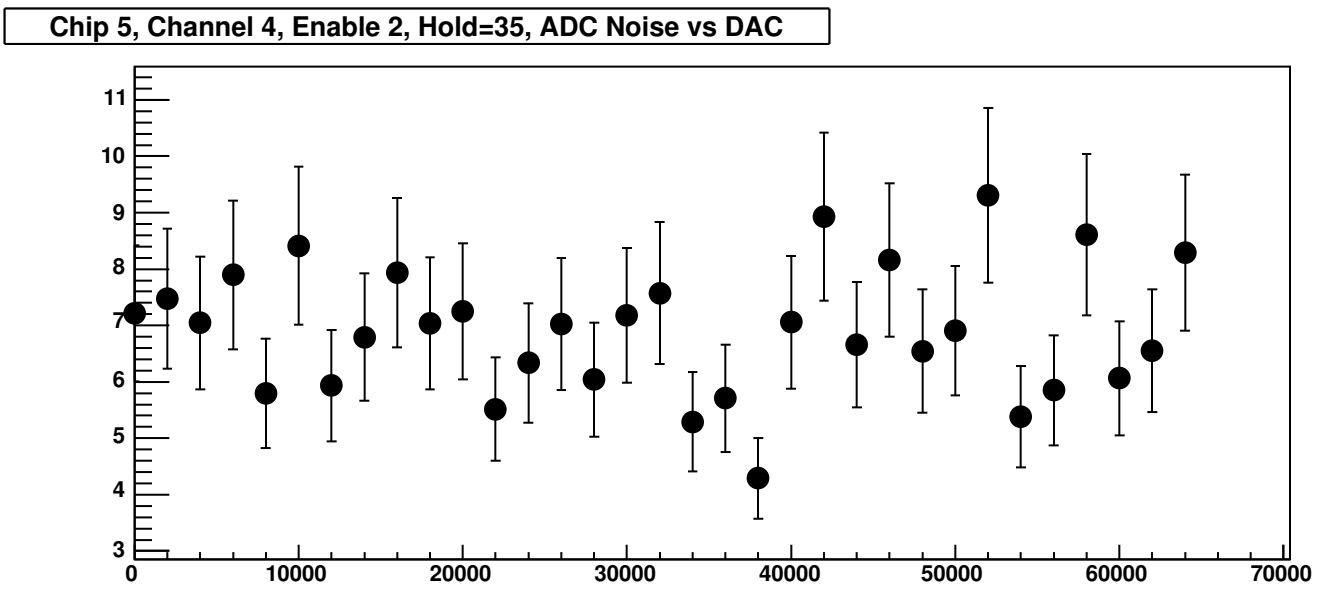

Chip 5, Channel 4, Enable 0, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

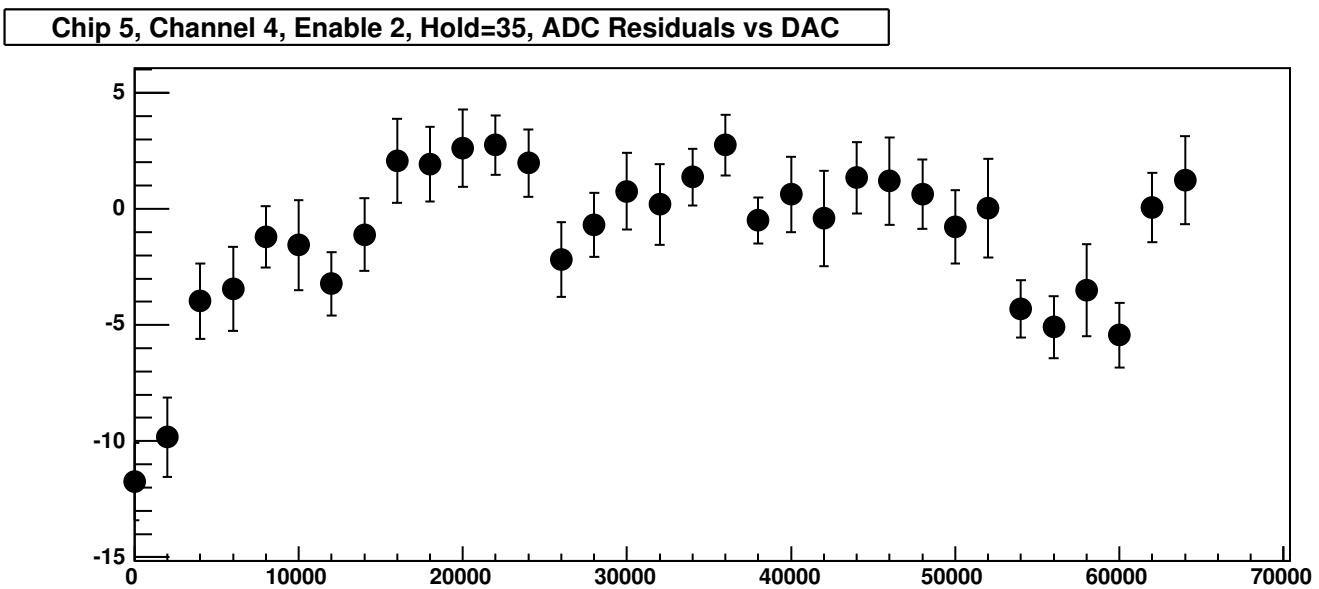

Chip 5, Channel 4, Enable 1!, Hold=35, ADC Mean vs DAC
χ² / ndf 5617 / 23
p0 150.2 ± 1.008
p1 0.4807 ± 4.909e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

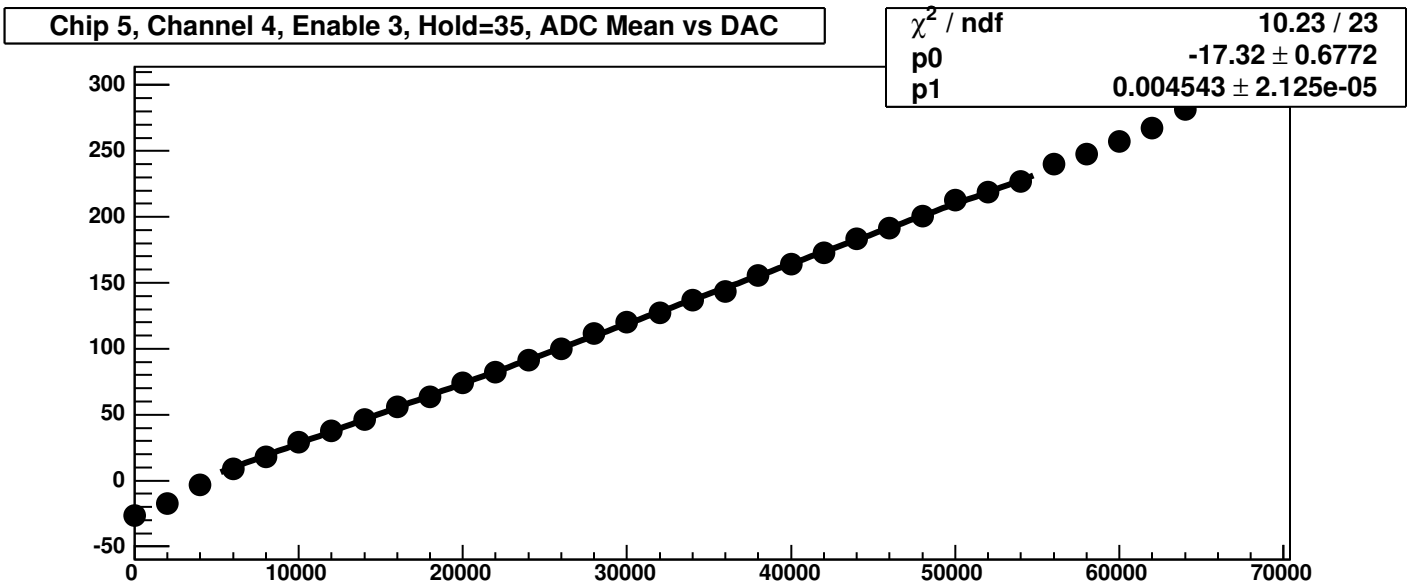

Chip 5, Channel 4, Enable 1!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

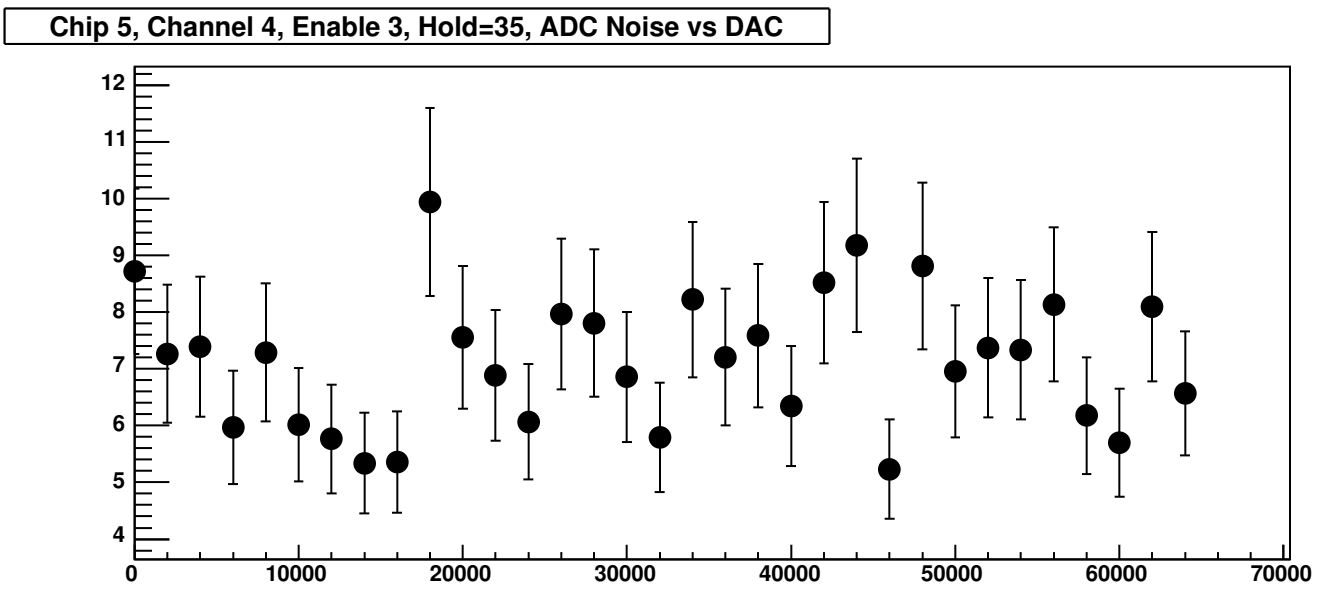

Chip 5, Channel 4, Enable 1!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
0
10000
20000
30000
40000
50000
60000
70000

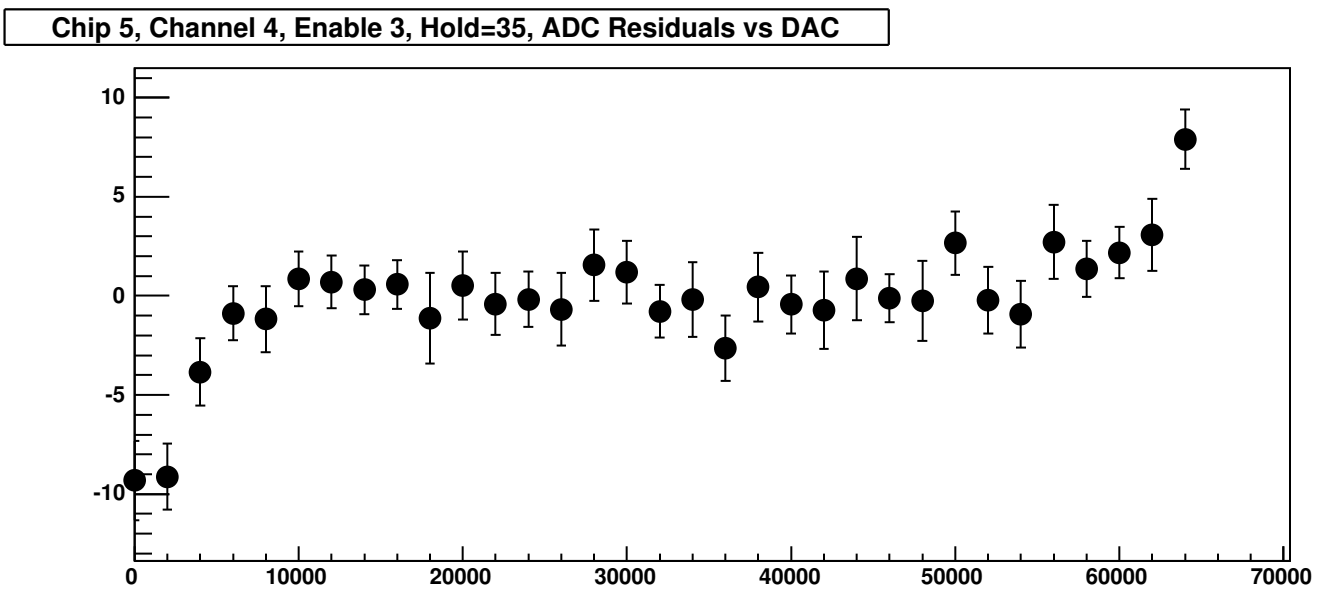

Chip 5, Channel 4, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 45.15 / 23
p0 -19.99 ± 0.7186
p1 0.004045 ± 2.149e-05
250
200
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000

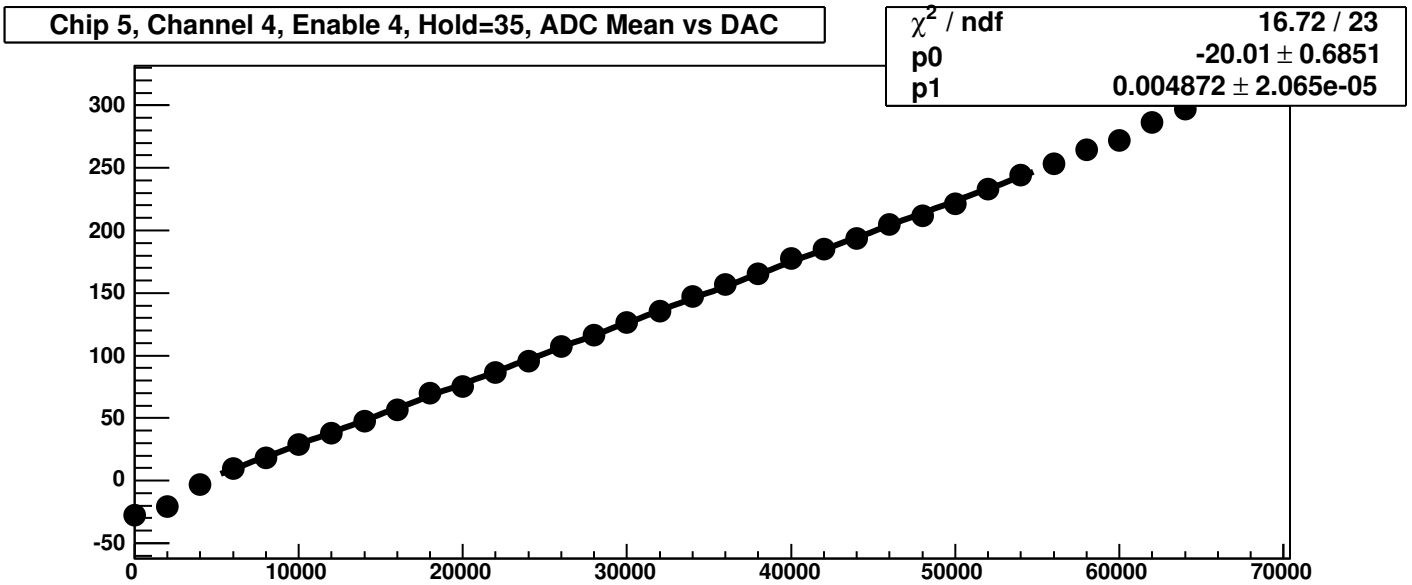

Chip 5, Channel 4, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

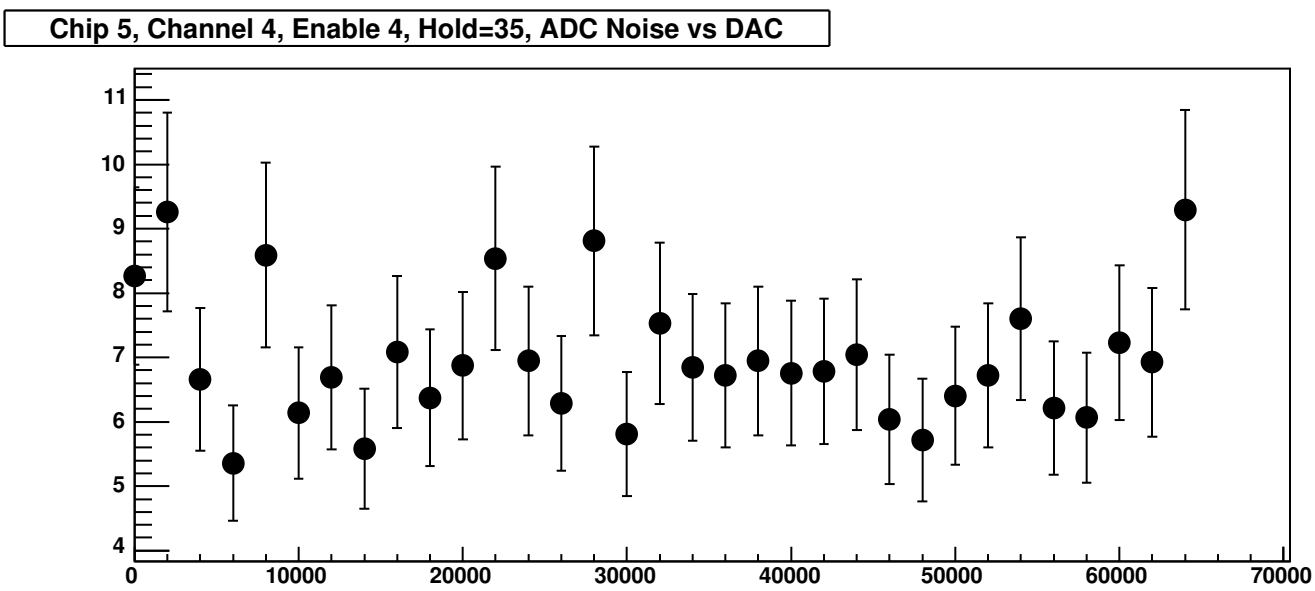

Chip 5, Channel 4, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

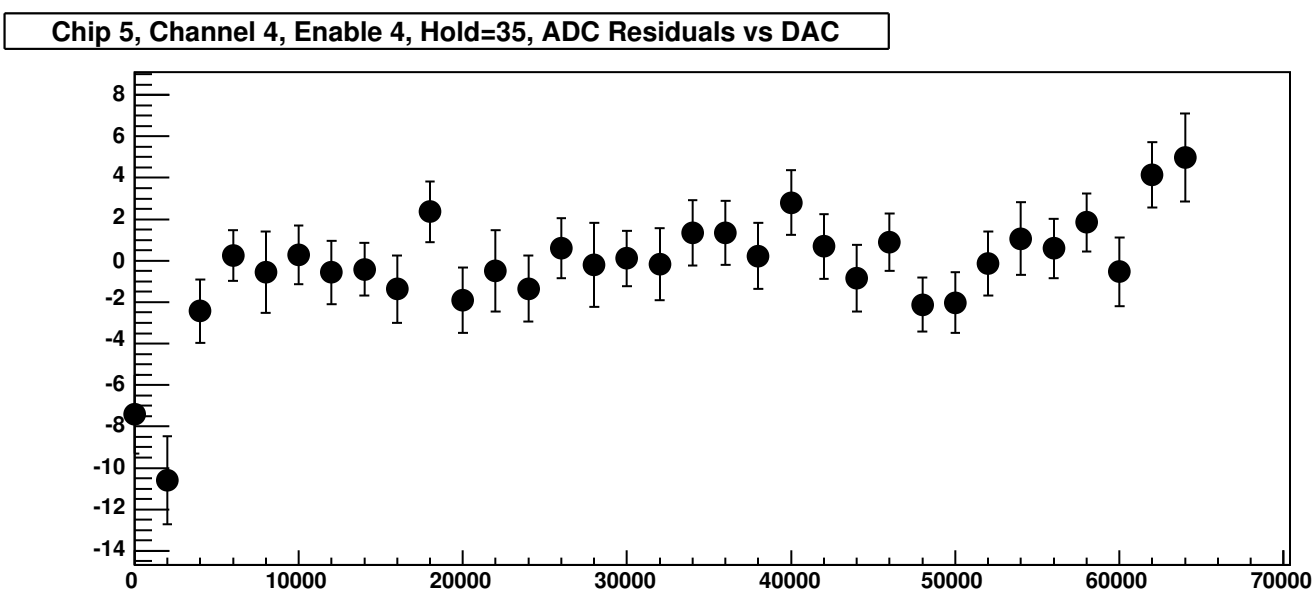

Chip 5, Channel 4, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 10.23 / 23
p0 -17.32 ± 0.6772
p1 0.004543 ± 2.125e-05
300
250
200
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000

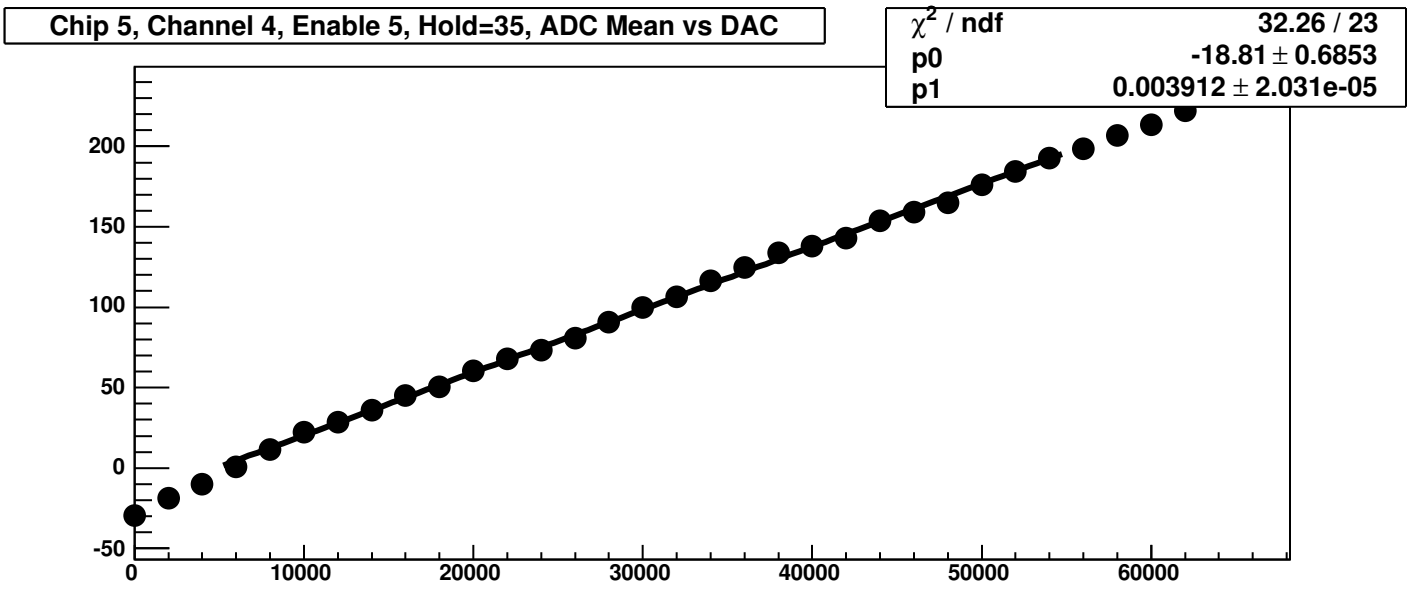

Chip 5, Channel 4, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

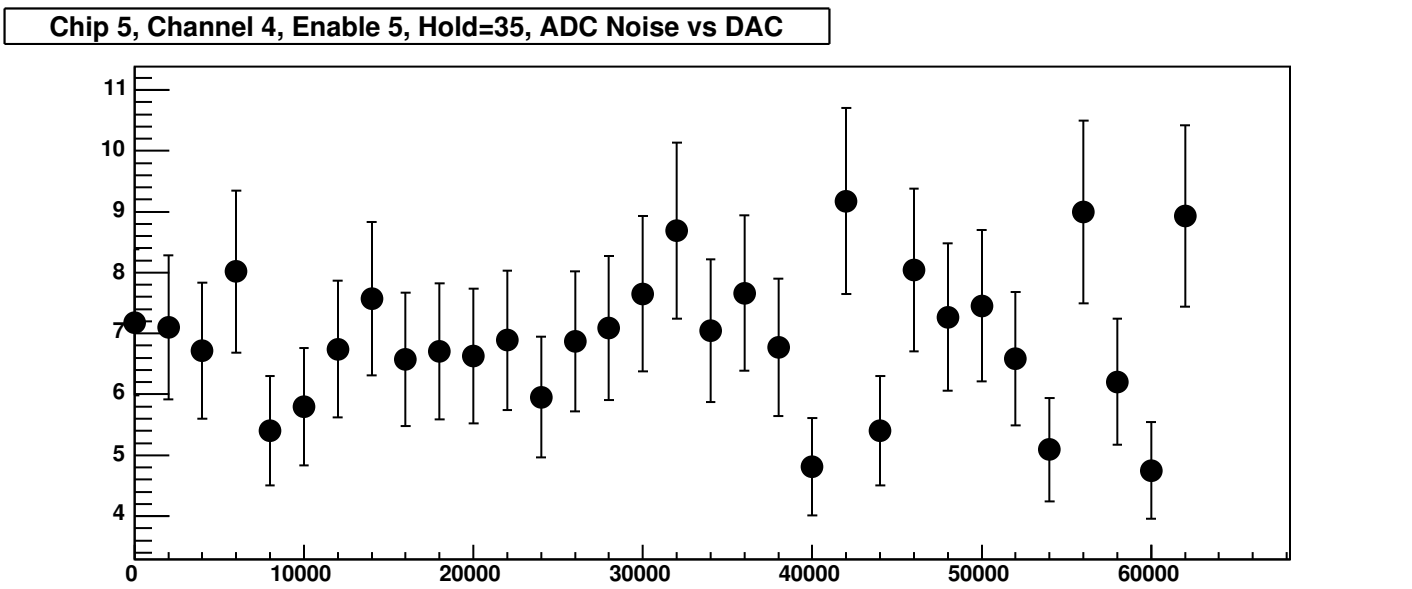

Chip 5, Channel 4, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

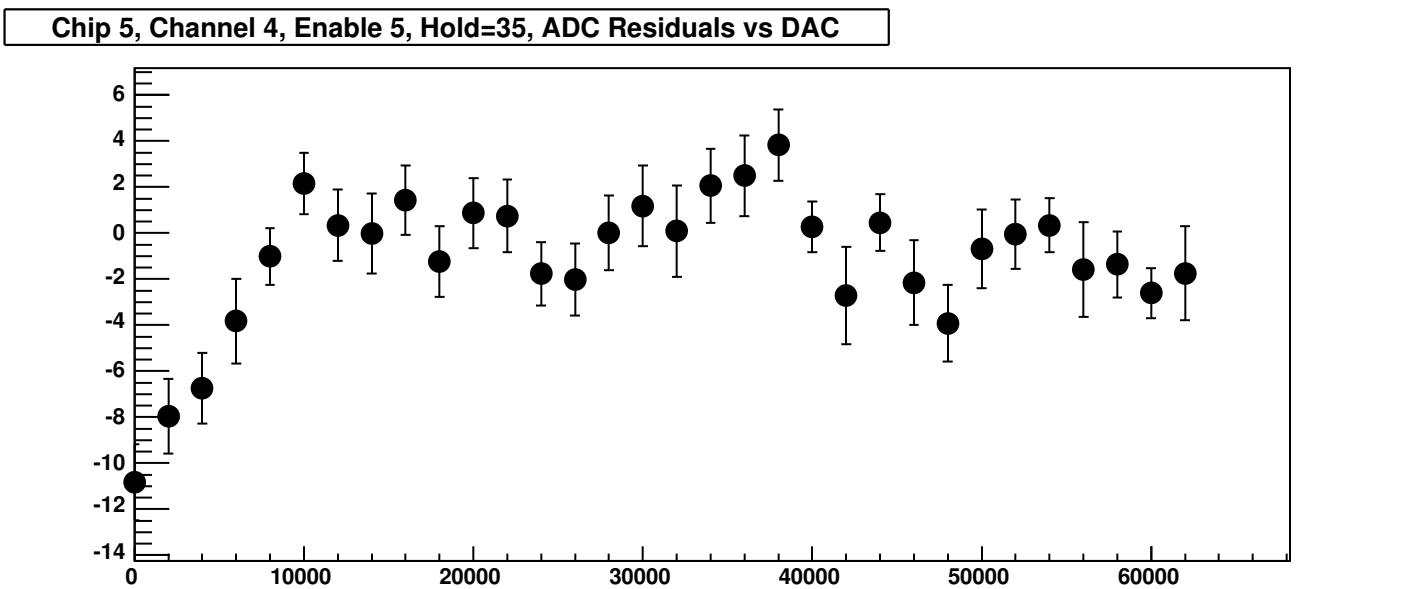

Chip 5, Channel 4, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 16.72 / 23
p0 -20.01 ± 0.6851
p1 0.004872 ± 2.065e-05
300
250
200
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 4, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

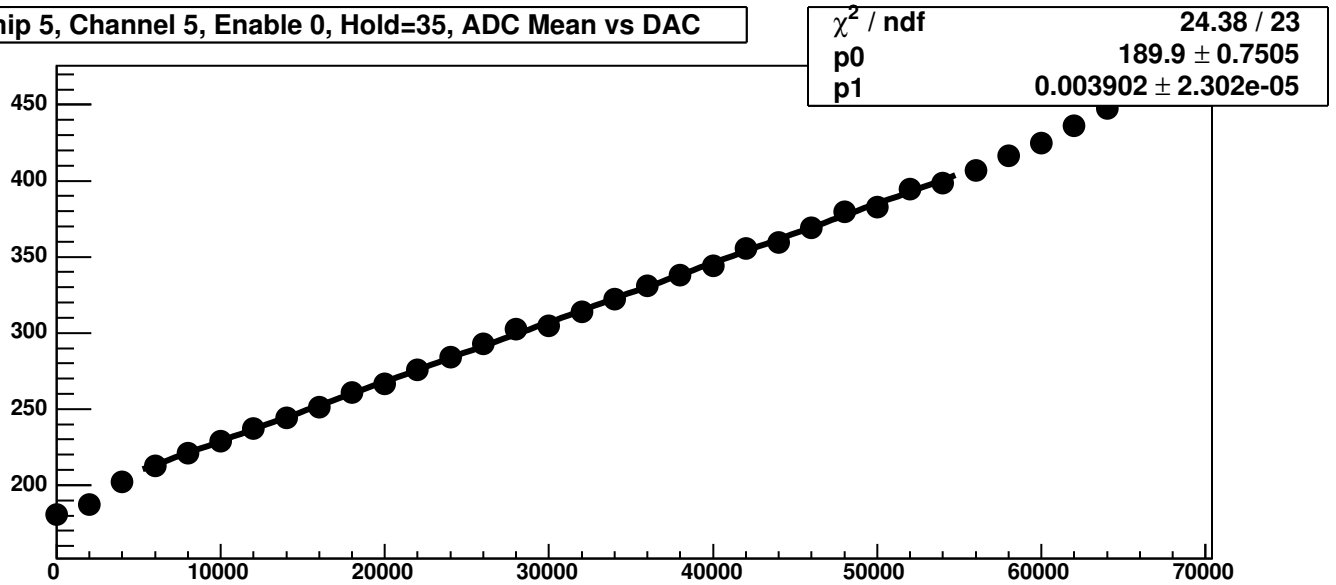

Chip 5, Channel 4, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
-14
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 4, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 32.26 / 23
p0 -18.81 ± 0.6853
p1 0.003912 ± 2.031e-05
200
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000

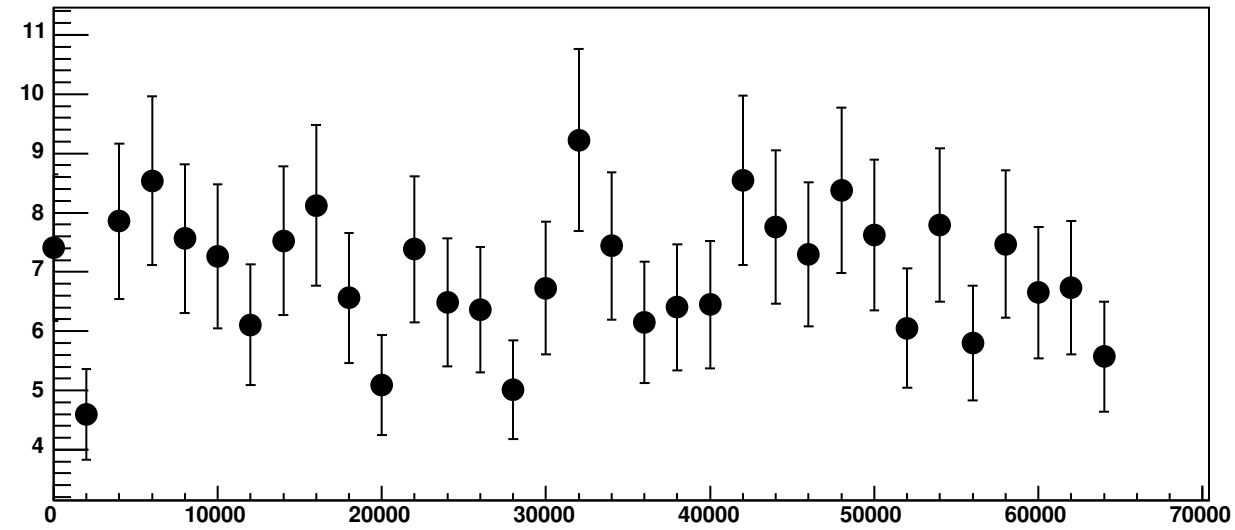

Chip 5, Channel 4, Enable 5, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 4, Enable 5, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
-14
0
10000
20000
30000
40000
50000
60000

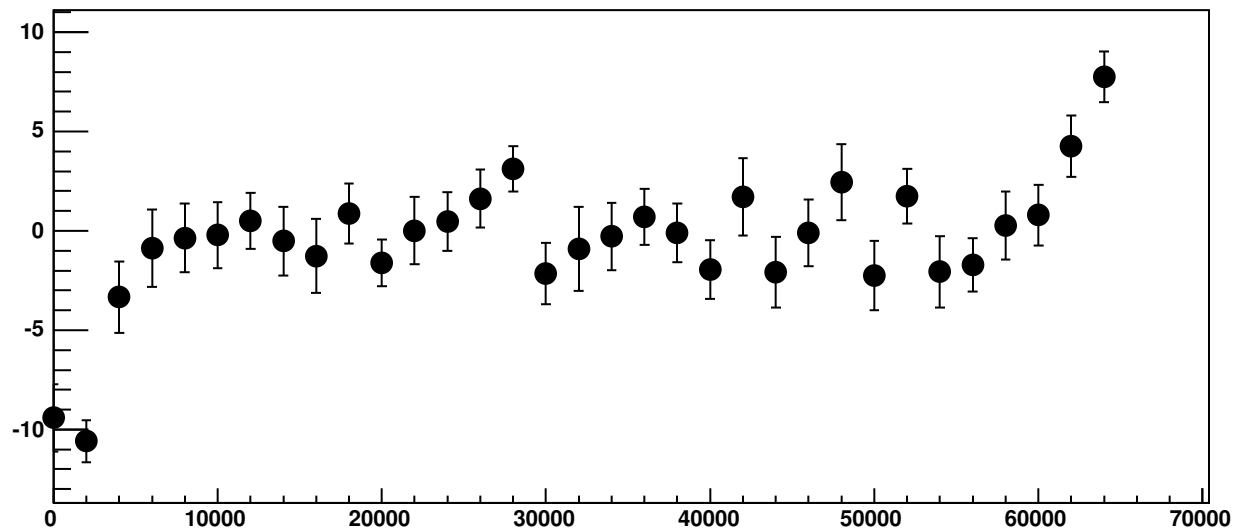
Chip 5, Channel 5, Enable 0, Hold=35, ADC Mean vs DAC

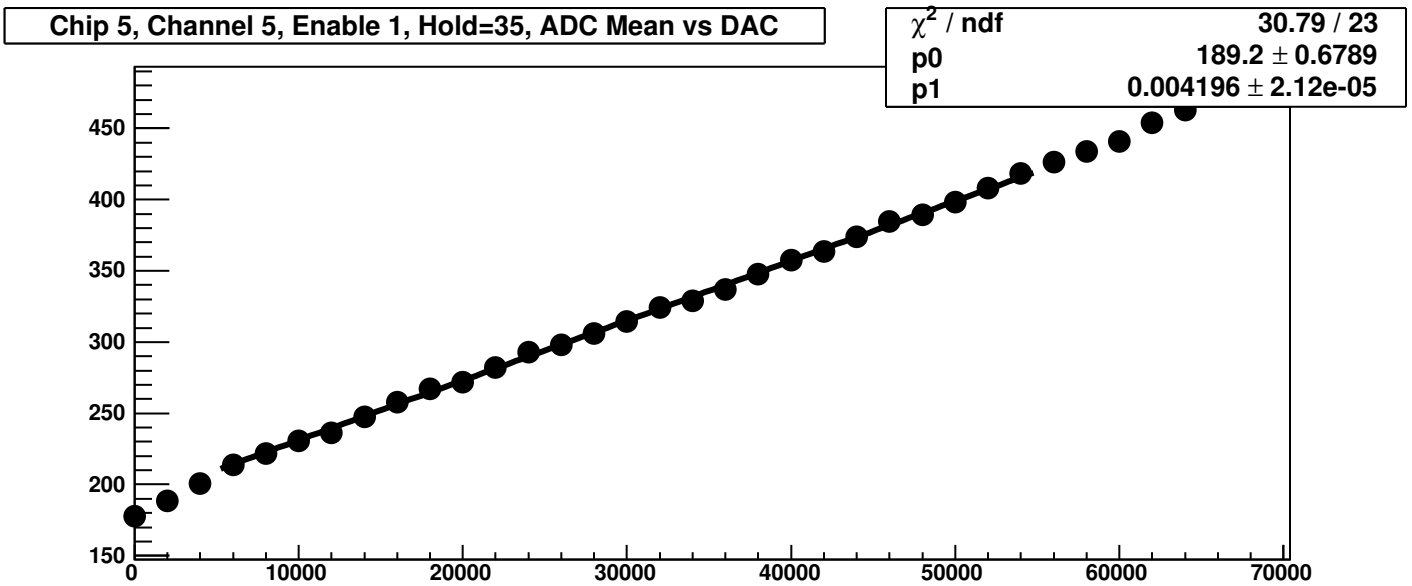


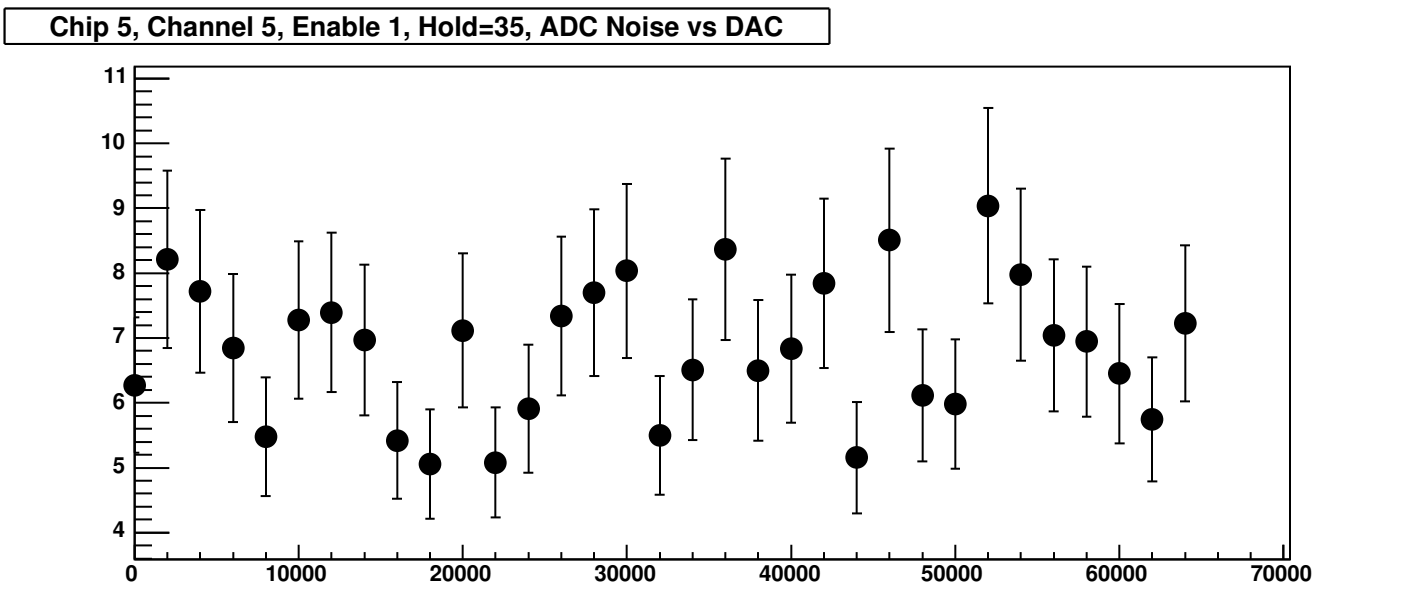
Chip 5, Channel 5, Enable 0, Hold=35, ADC Noise vs DAC

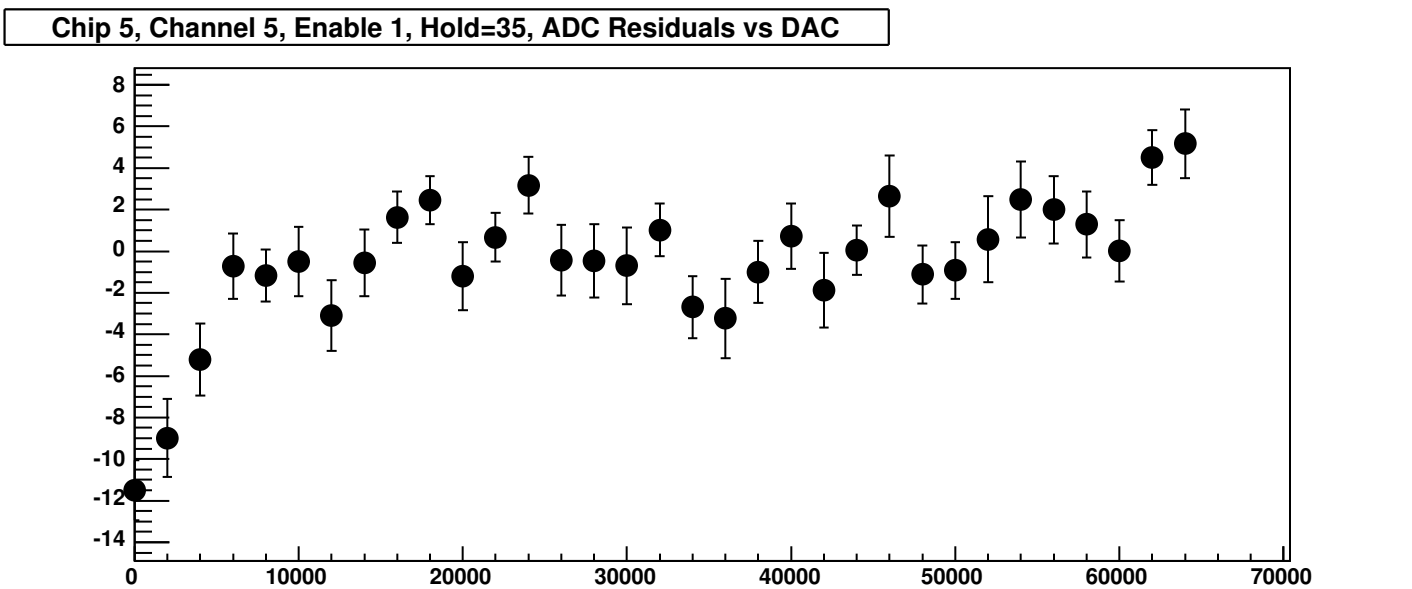


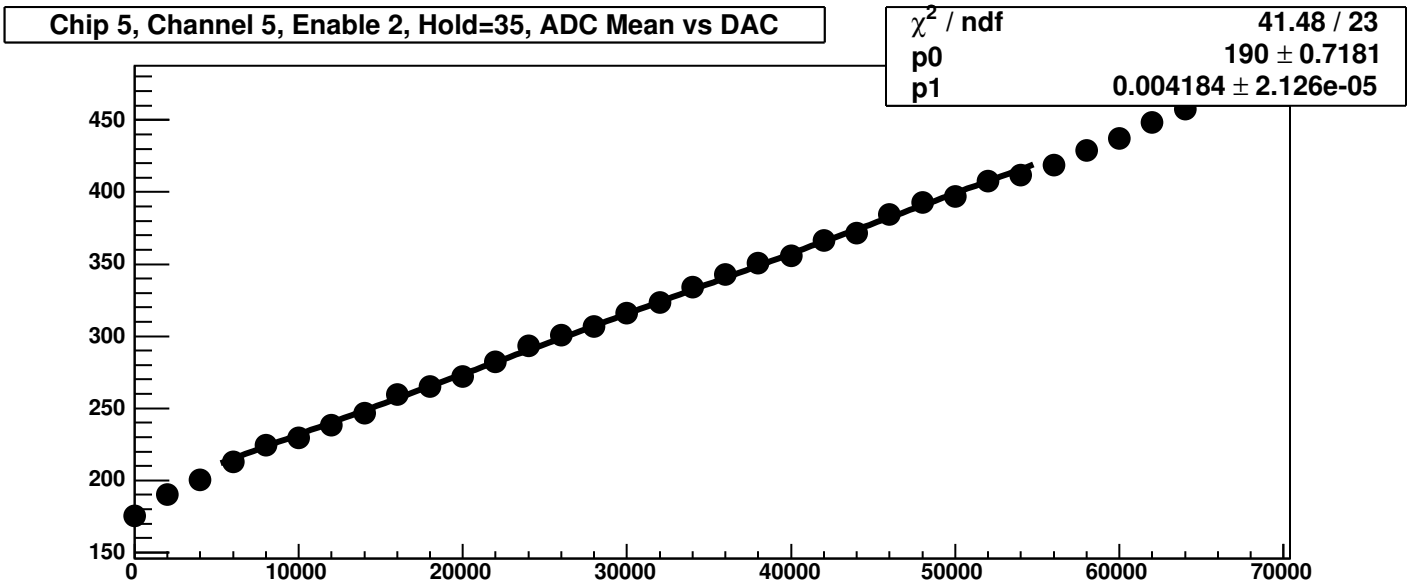
Chip 5, Channel 5, Enable 0, Hold=35, ADC Residuals vs DAC

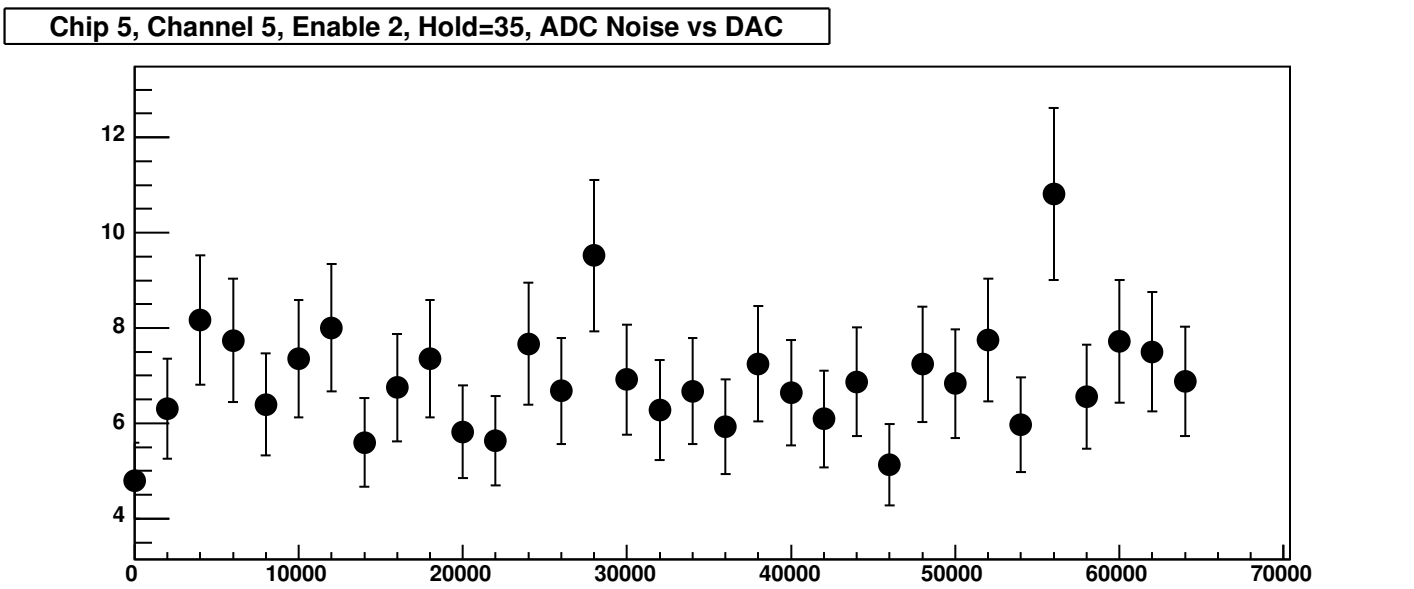


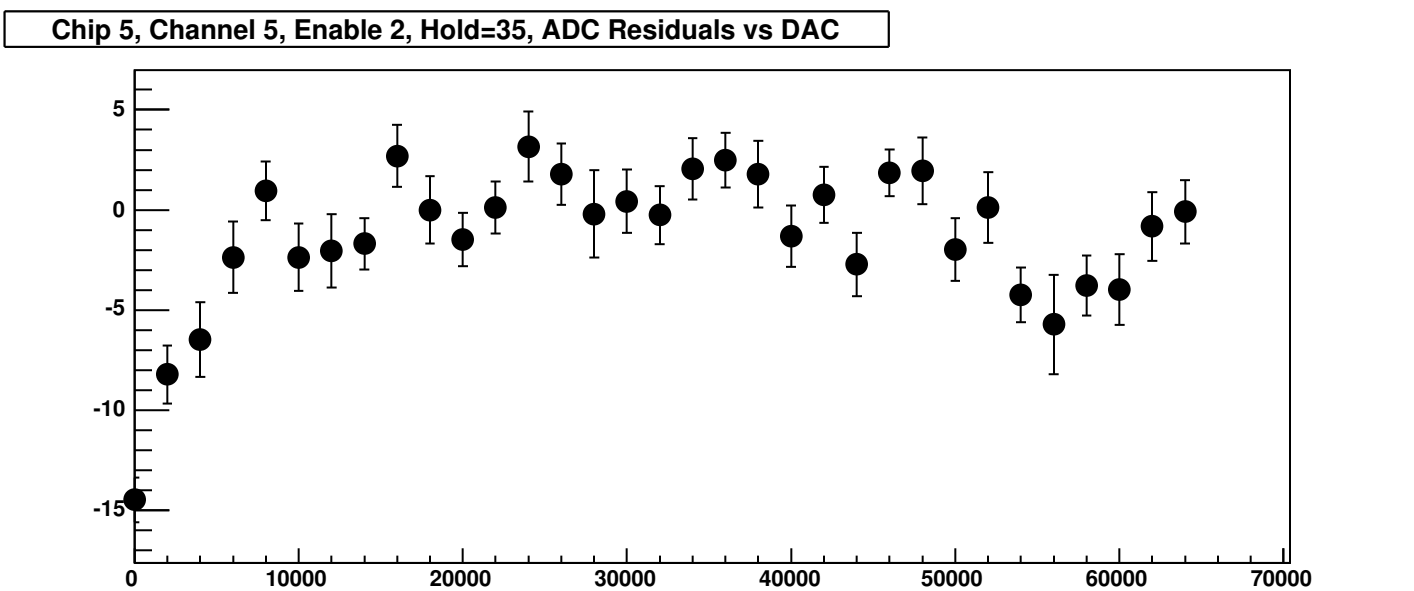

Chip 5, Channel 5, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 30.79 / 23
p0 189.2 ± 0.6789
p1 0.004196 ± 2.12e-05
450
400
350
300
250
200
150
0
10000
20000
30000
40000
50000
60000
70000

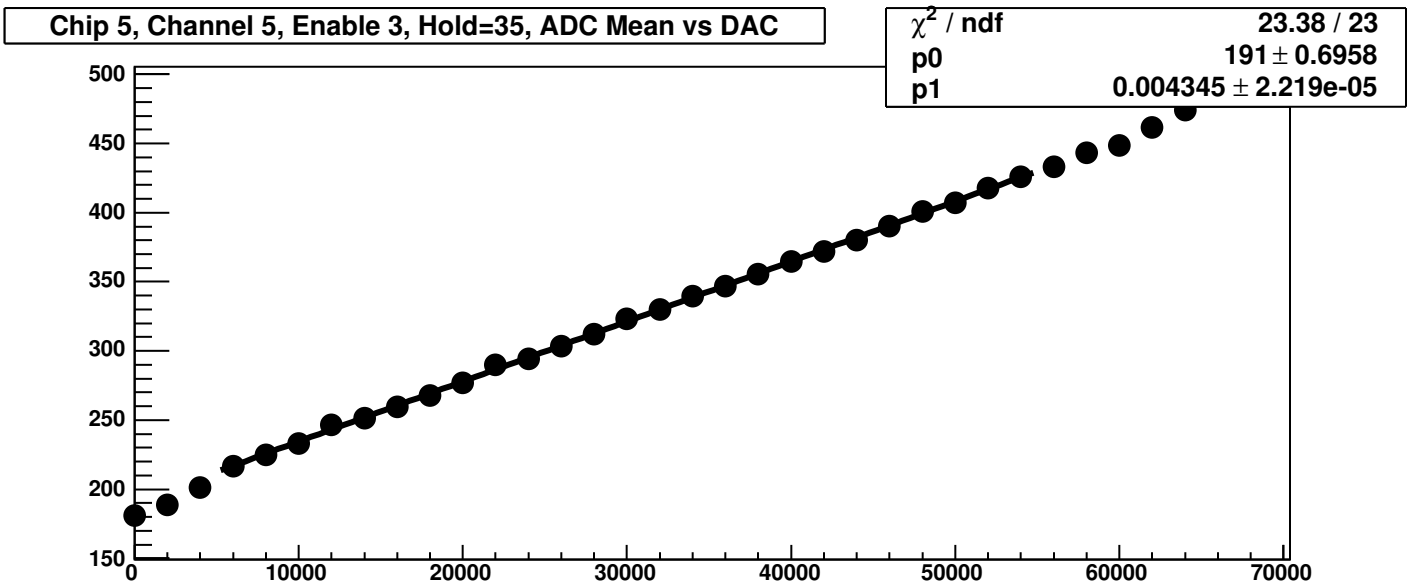

Chip 5, Channel 5, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

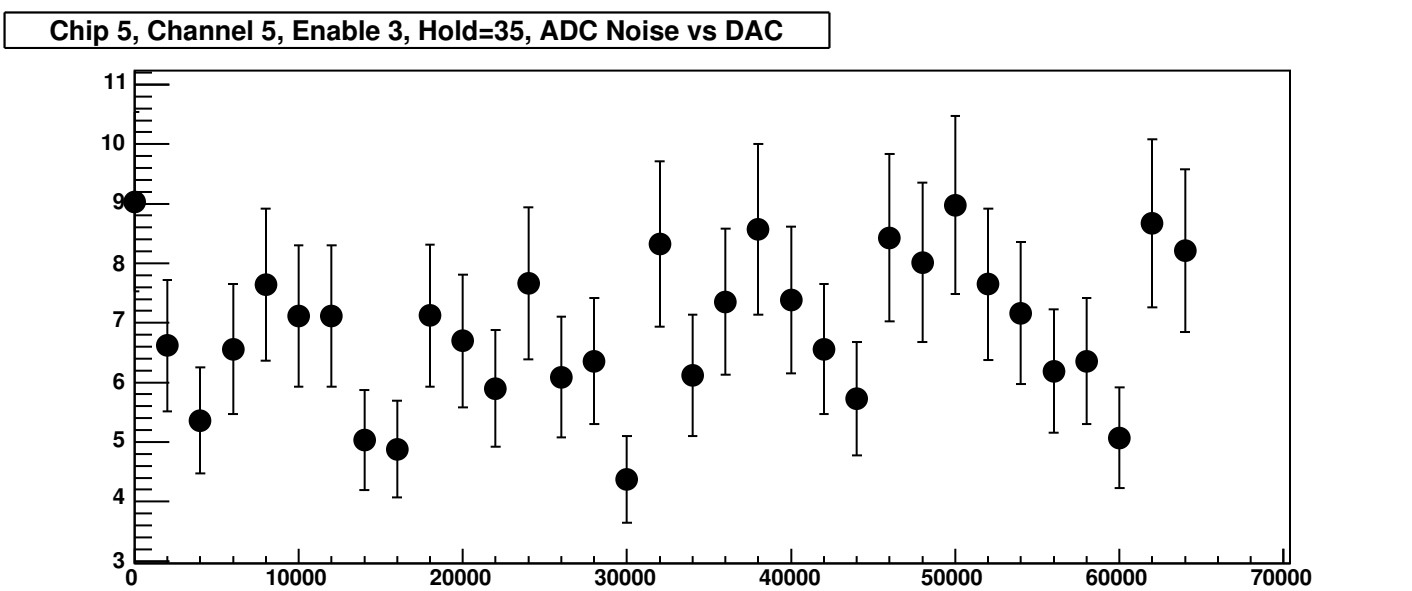

Chip 5, Channel 5, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
-14
0
10000
20000
30000
40000
50000
60000
70000

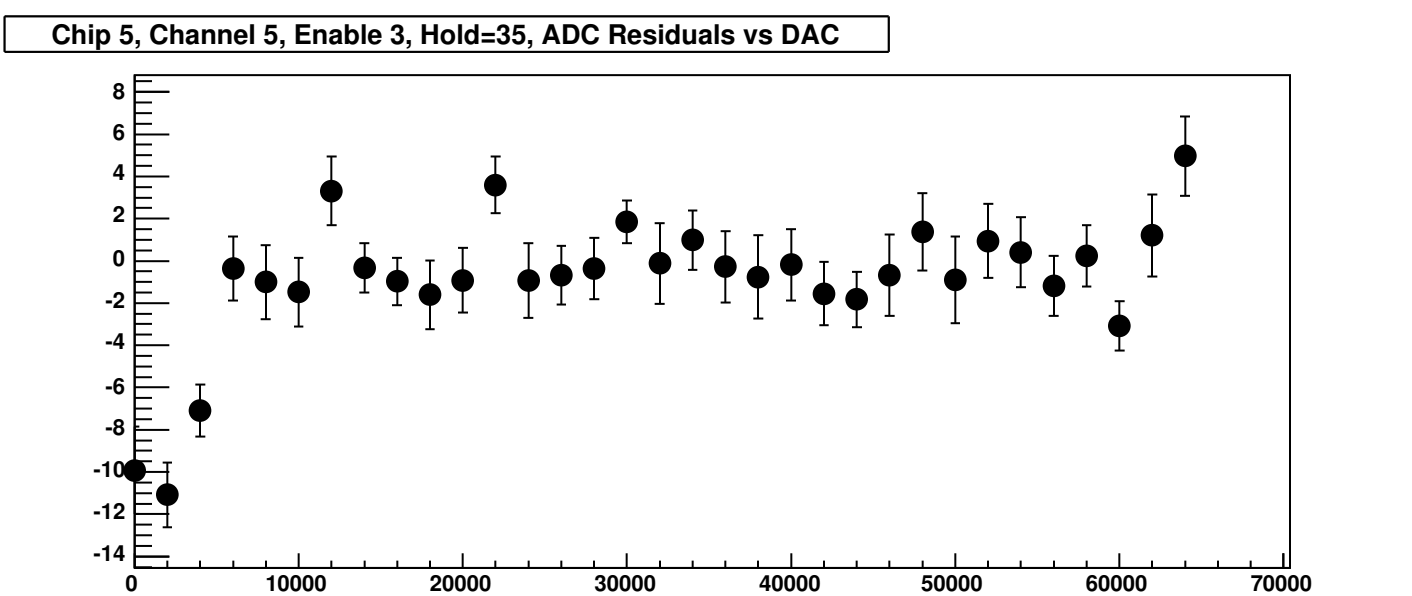

Chip 5, Channel 5, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 41.48 / 23
p0 190 ± 0.7181
p1 0.004184 ± 2.126e-05
450
400
350
300
250
200
150
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 5, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

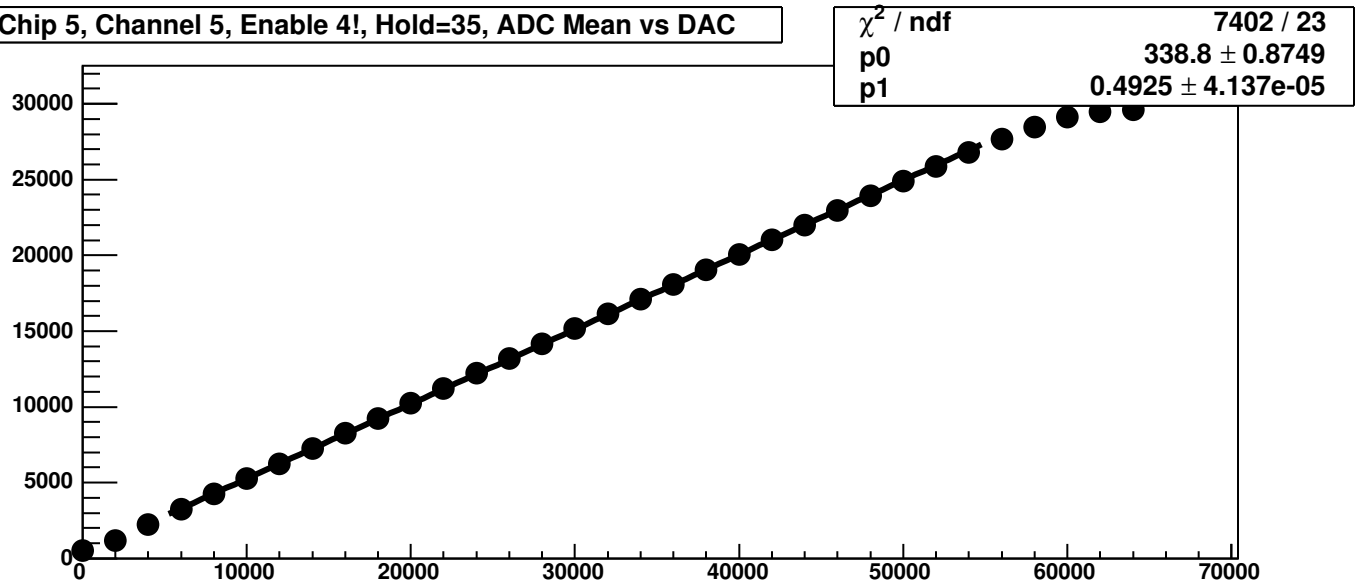

Chip 5, Channel 5, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 5, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 23.38 / 23
p0 191 ± 0.6958
p1 0.004345 ± 2.219e-05
500
450
400
350
300
250
200
150
0
10000
20000
30000
40000
50000
60000
70000

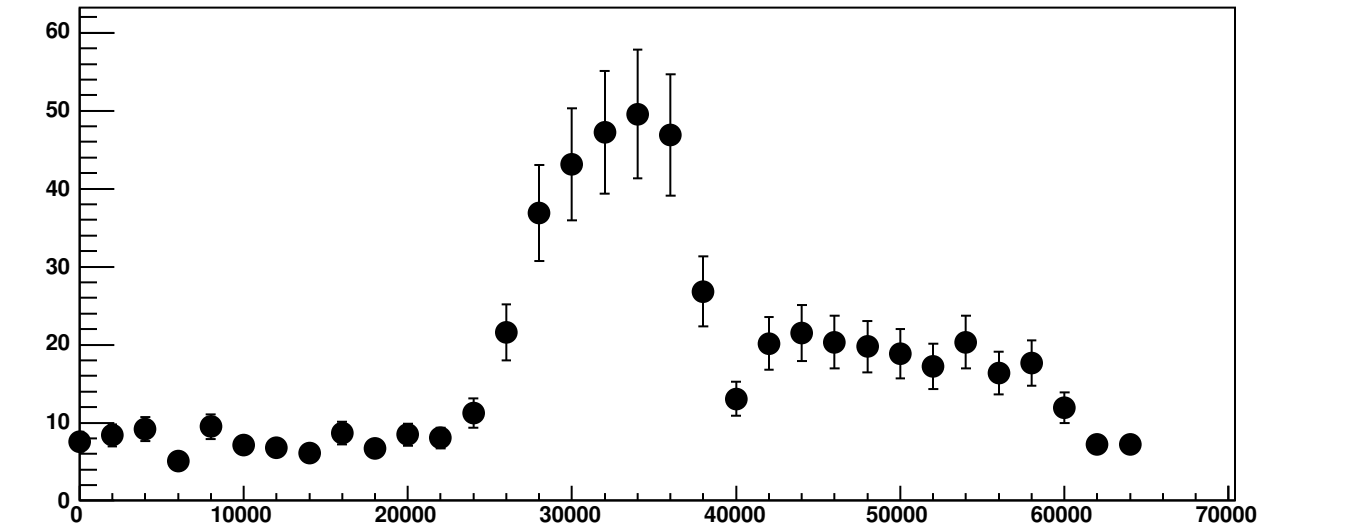

Chip 5, Channel 5, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 5, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
-14
0
10000
20000
30000
40000
50000
60000
70000

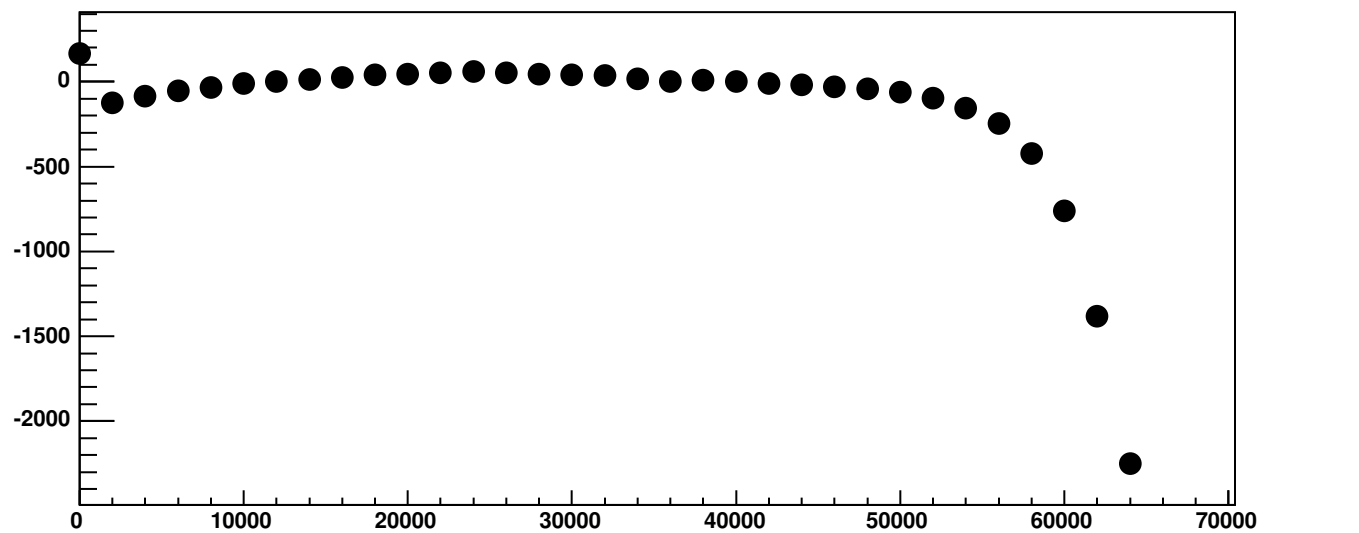
Chip 5, Channel 5, Enable 4!, Hold=35, ADC Mean vs DAC

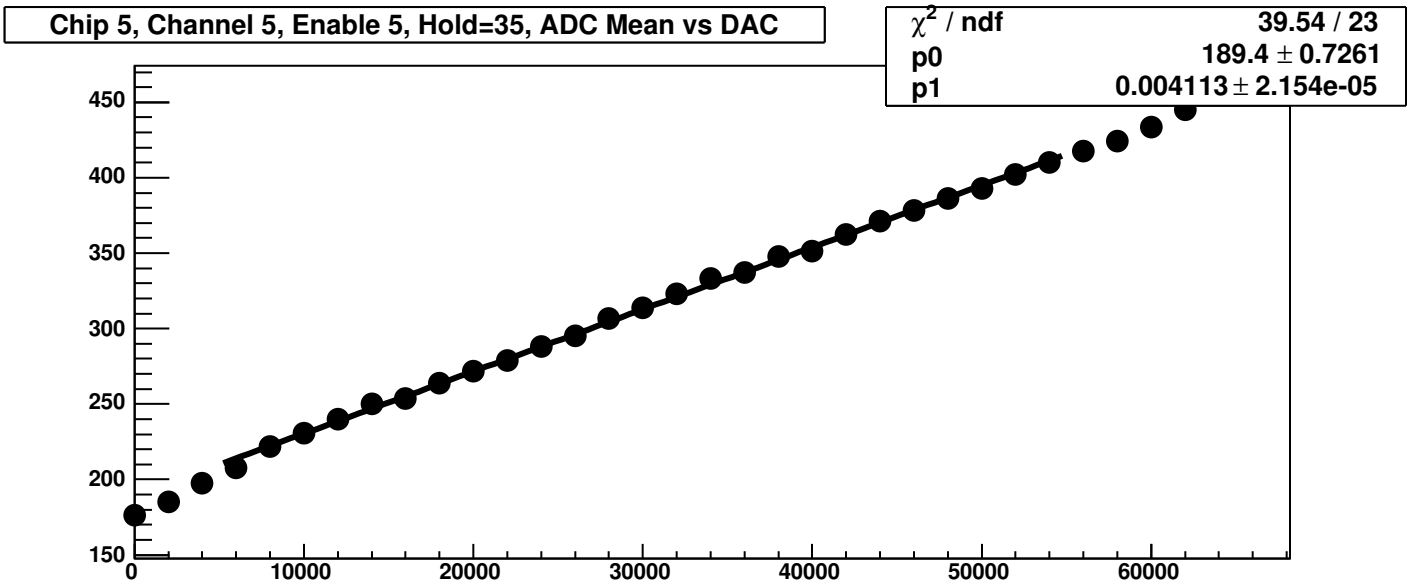


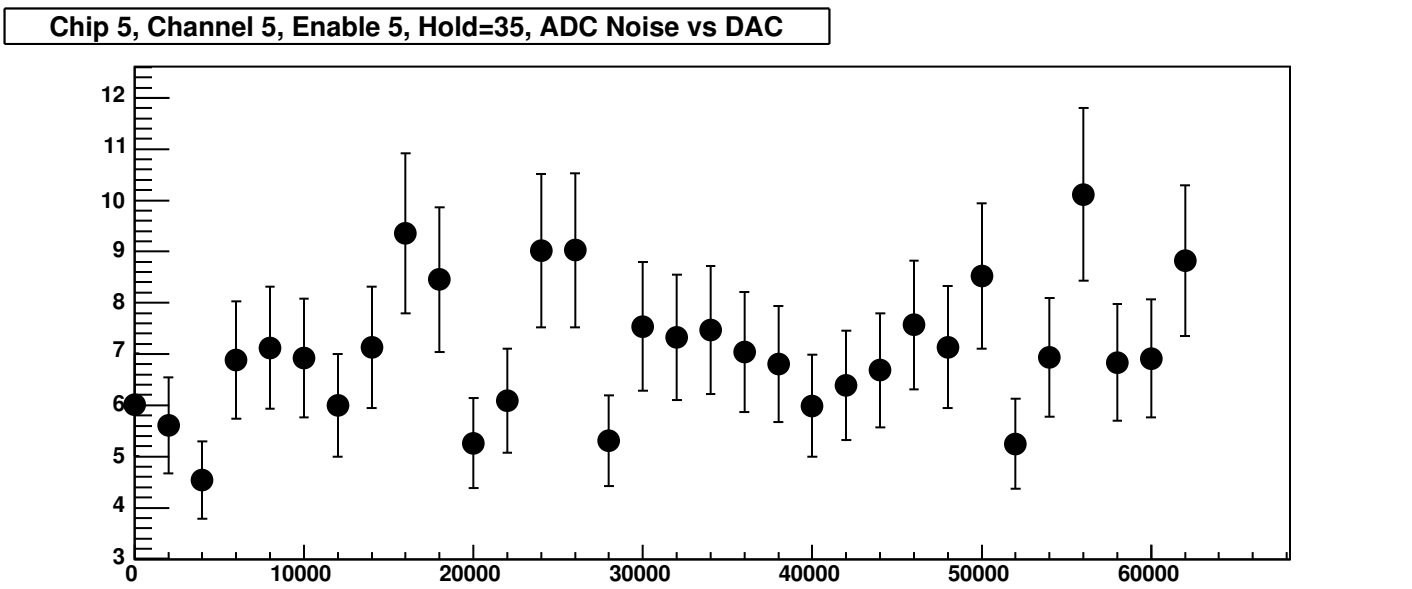
Chip 5, Channel 5, Enable 4!, Hold=35, ADC Noise vs DAC

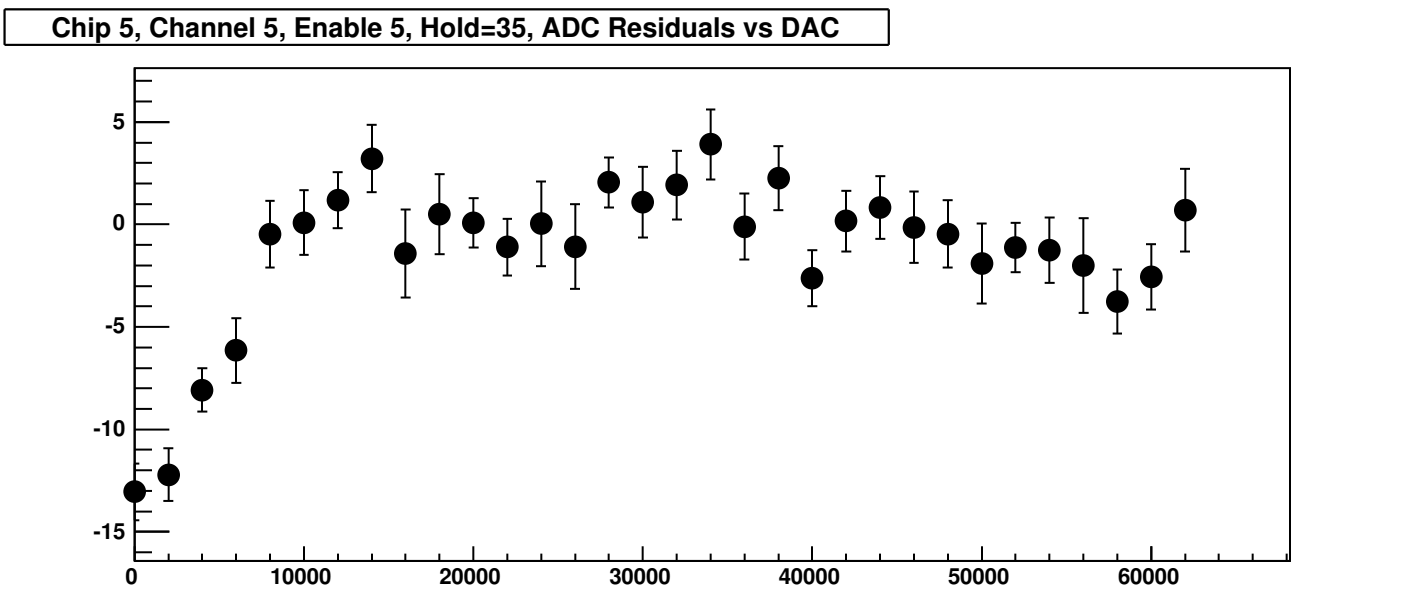


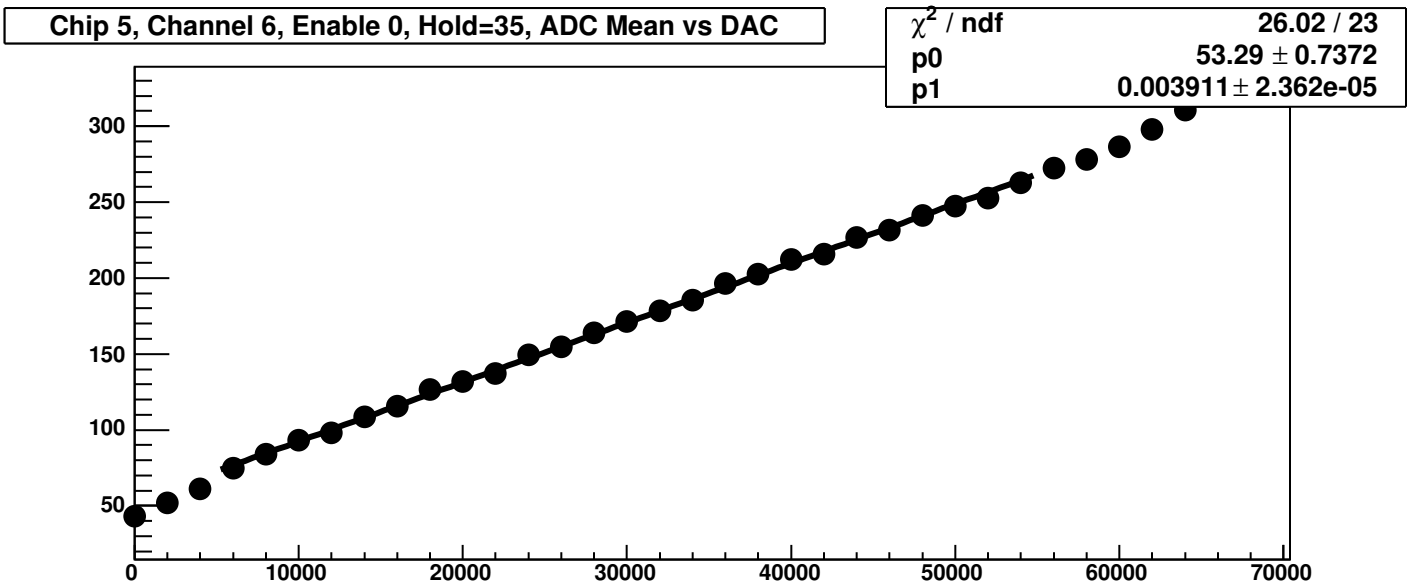
Chip 5, Channel 5, Enable 4!, Hold=35, ADC Residuals vs DAC

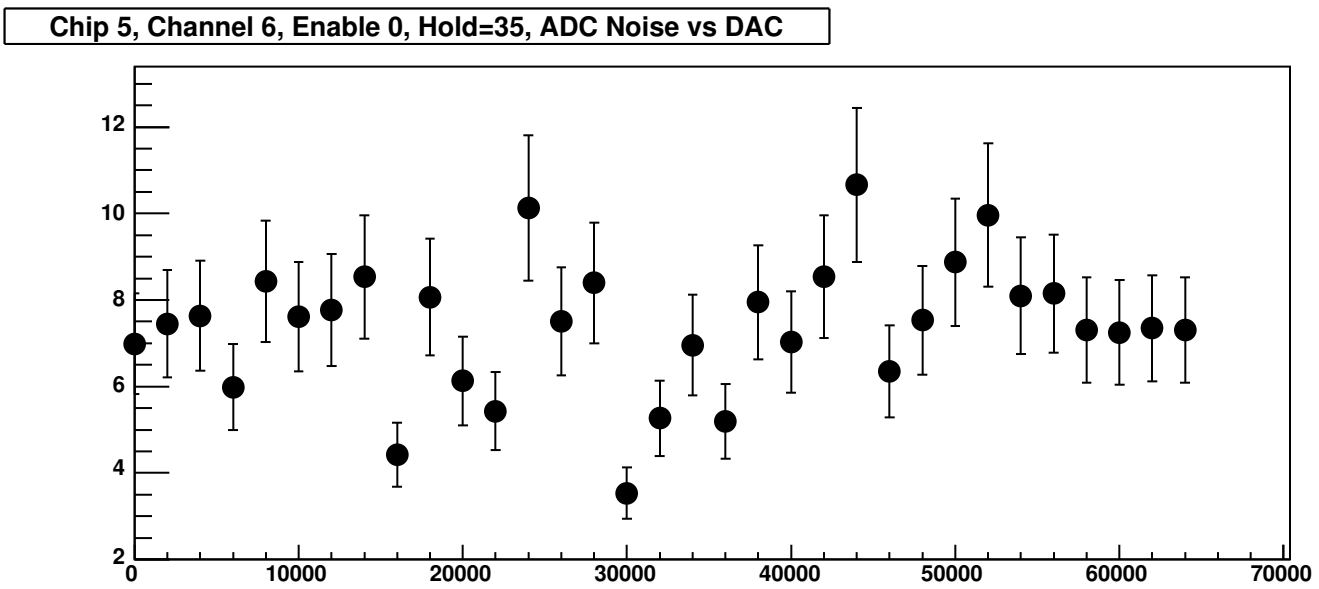


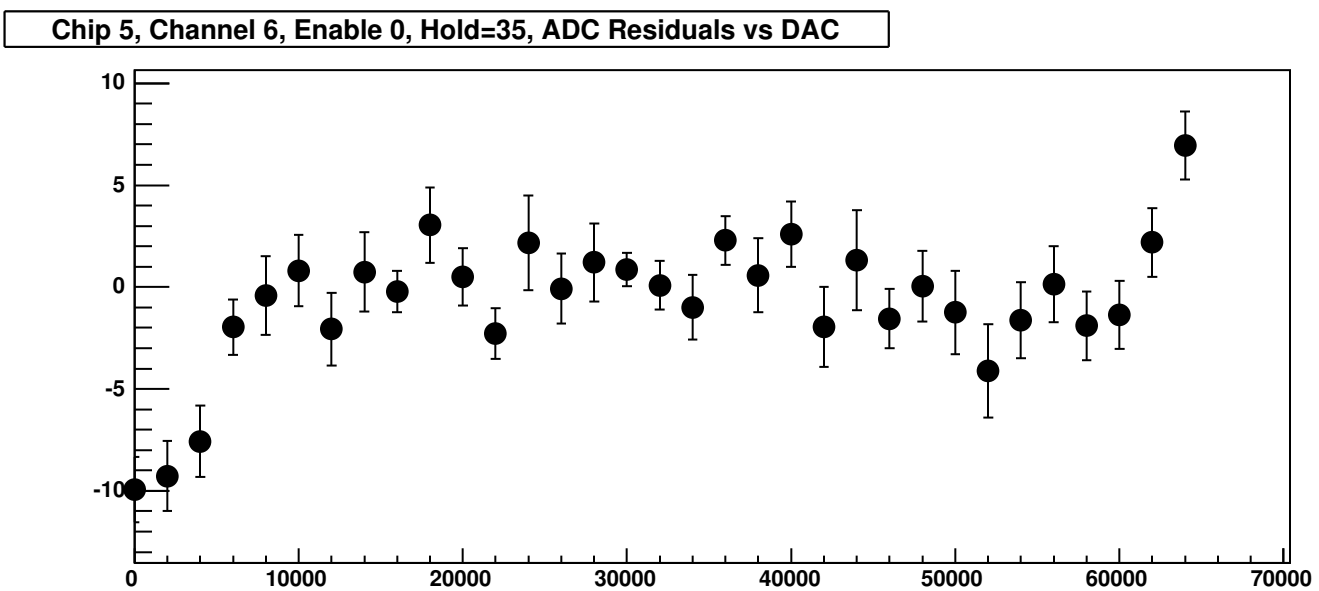

Chip 5, Channel 5, Enable 5, Hold=35, ADC Mean vs DAC
χ^2 / ndf 39.54 / 23
p0 189.4 ± 0.7261
p1 0.004113 ± 2.154e-05
450
400
350
300
250
200
150
0
10000
20000
30000
40000
50000
60000

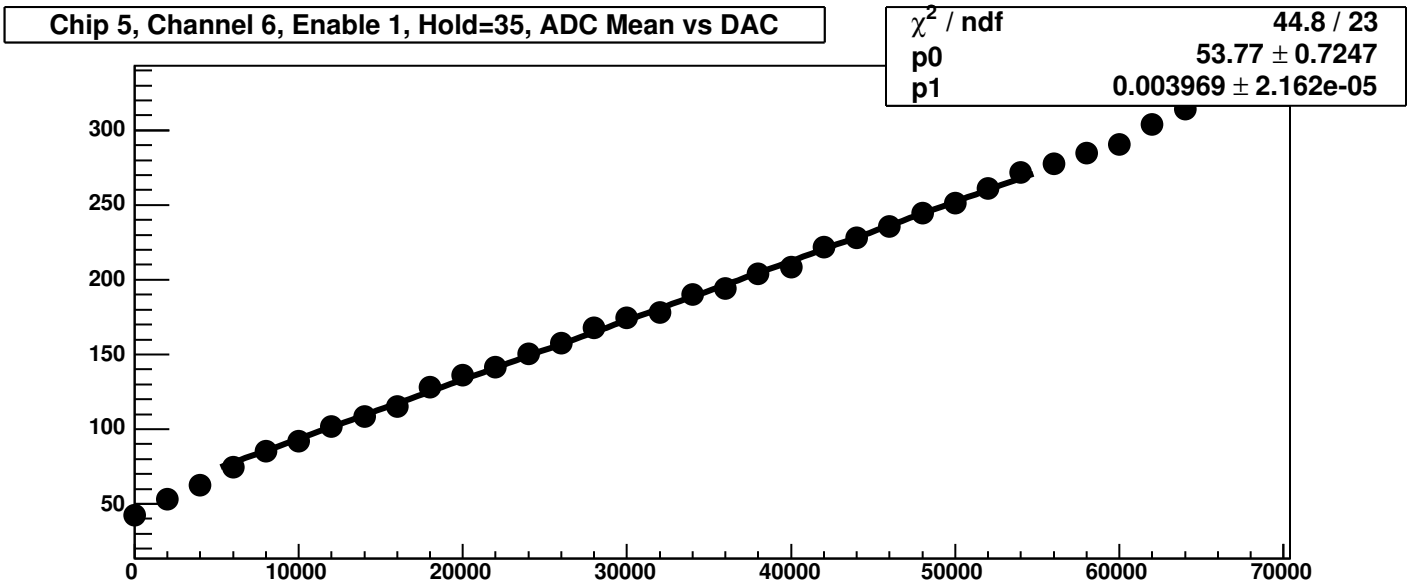

Chip 5, Channel 5, Enable 5, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000

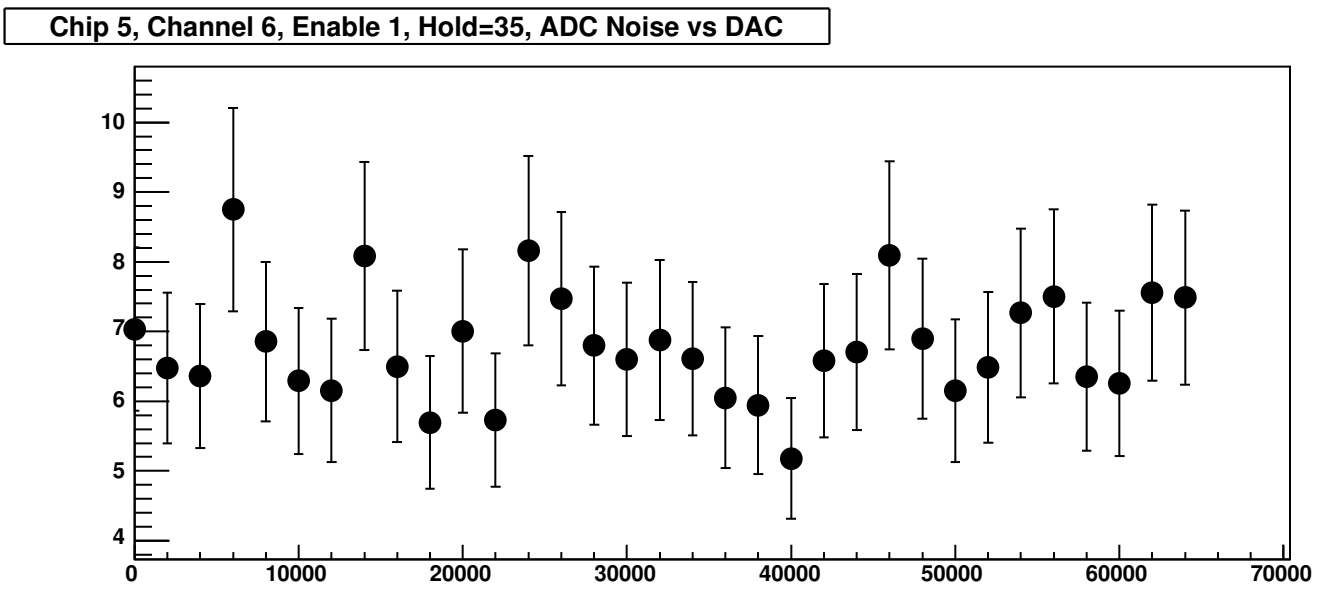

Chip 5, Channel 5, Enable 5, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000

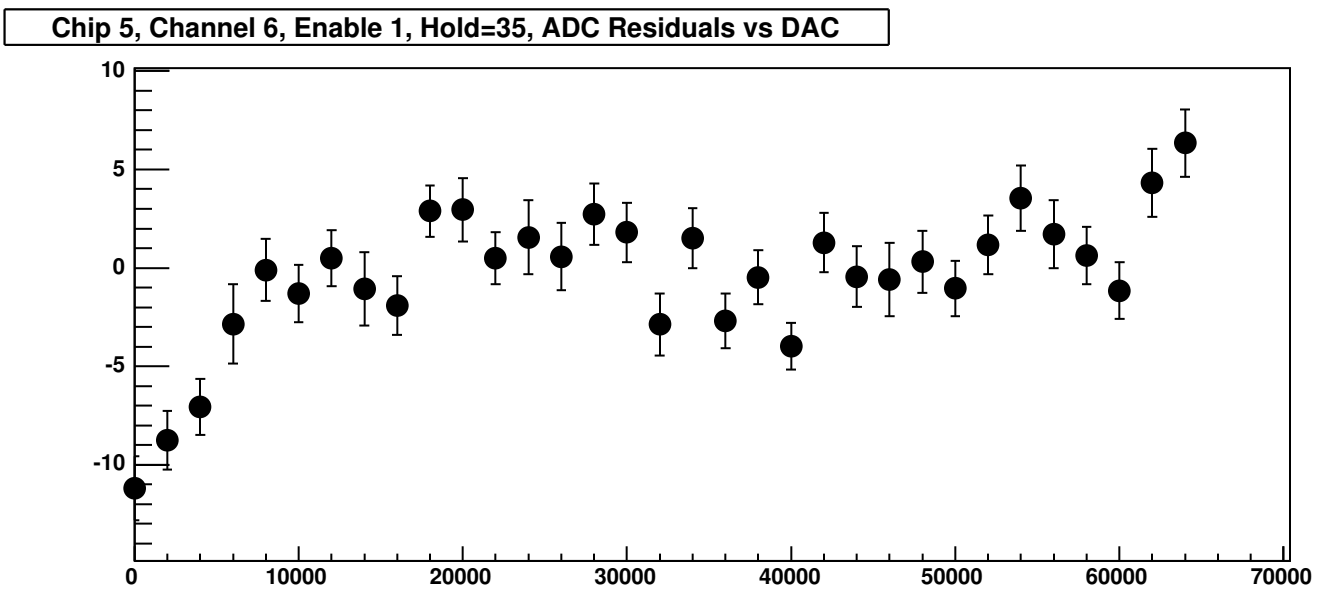

Chip 5, Channel 6, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 26.02 / 23
p0 53.29 ± 0.7372
p1 0.003911 ± 2.362e-05
300
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

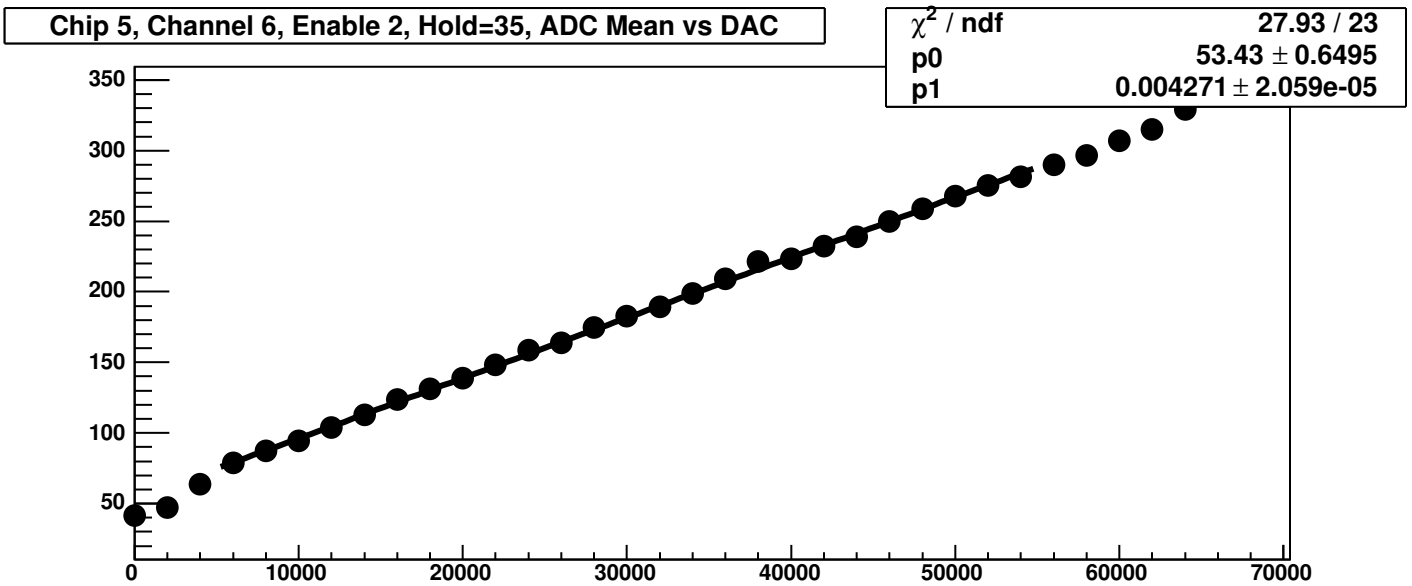

Chip 5, Channel 6, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
2
0
10000
20000
30000
40000
50000
60000
70000

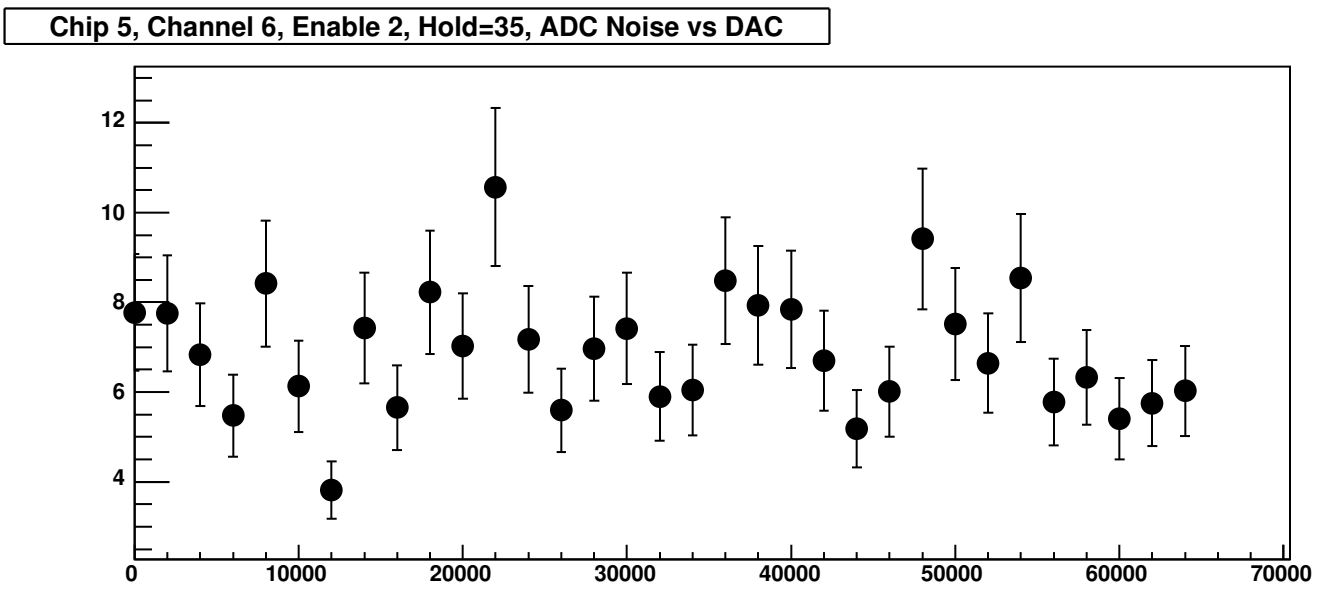

Chip 5, Channel 6, Enable 0, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

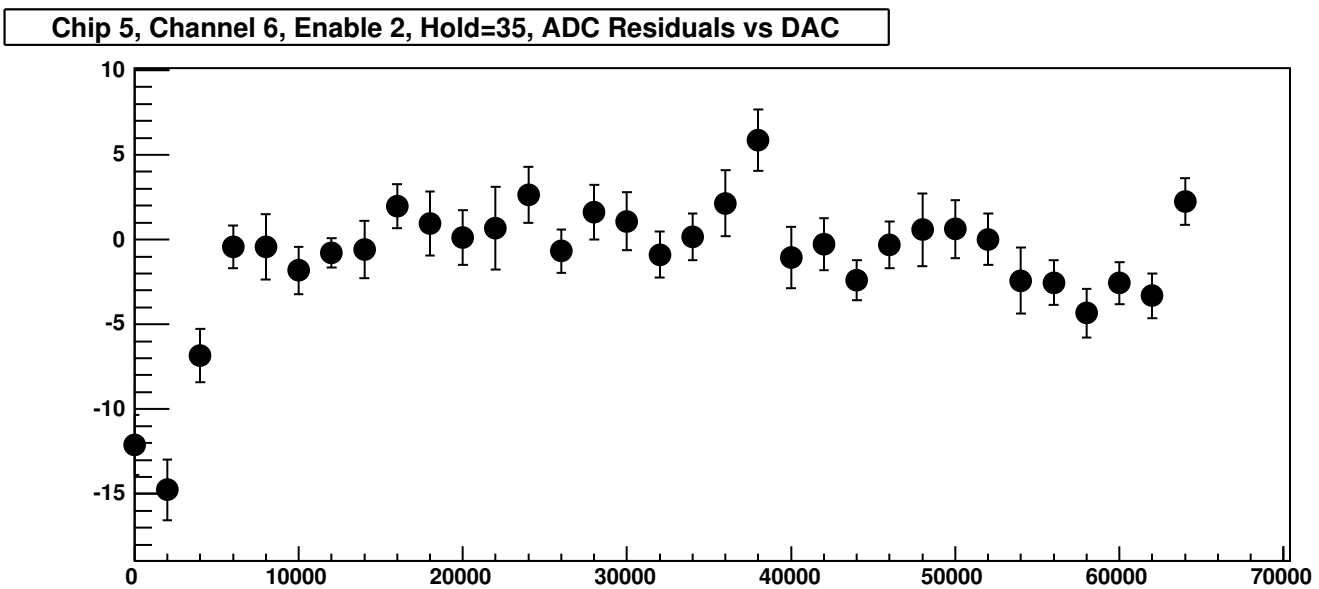

Chip 5, Channel 6, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 44.8 / 23
p0 53.77 ± 0.7247
p1 0.003969 ± 2.162e-05
300
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

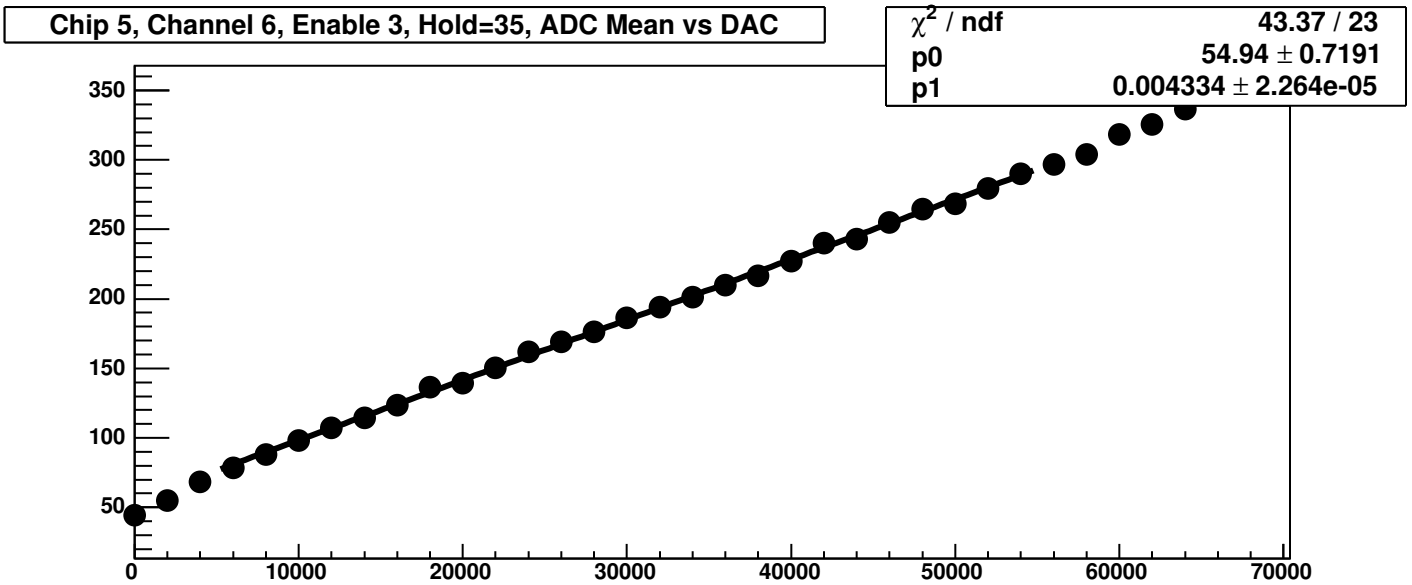

Chip 5, Channel 6, Enable 1, Hold=35, ADC Noise vs DAC
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

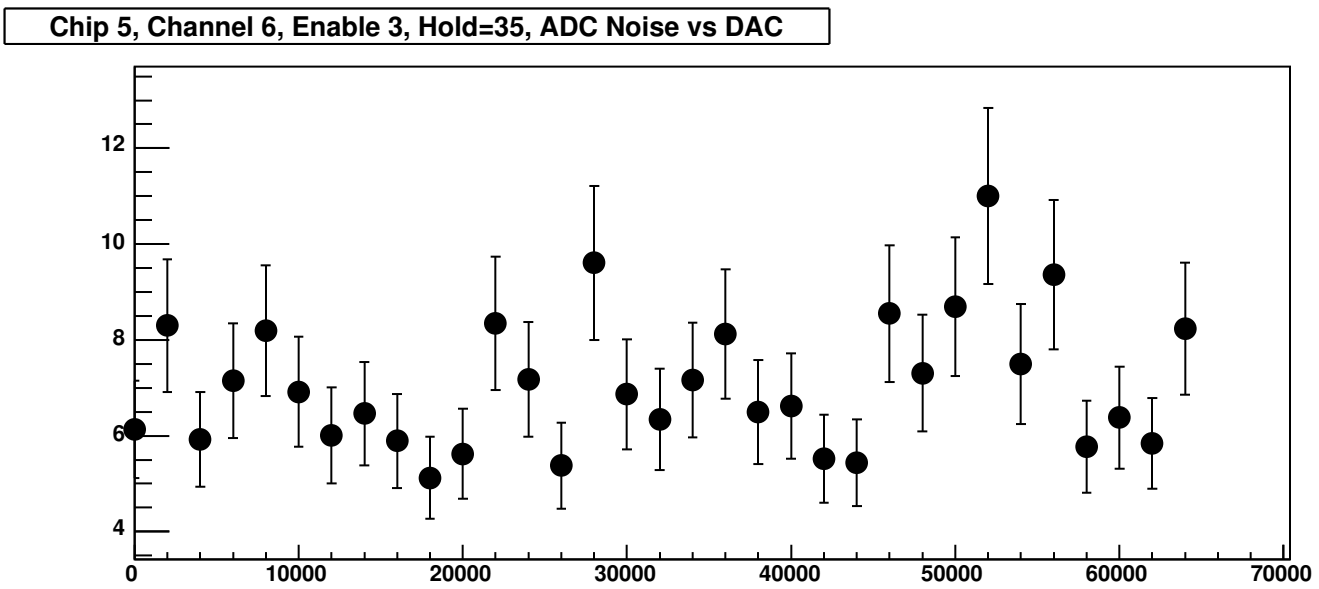

Chip 5, Channel 6, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

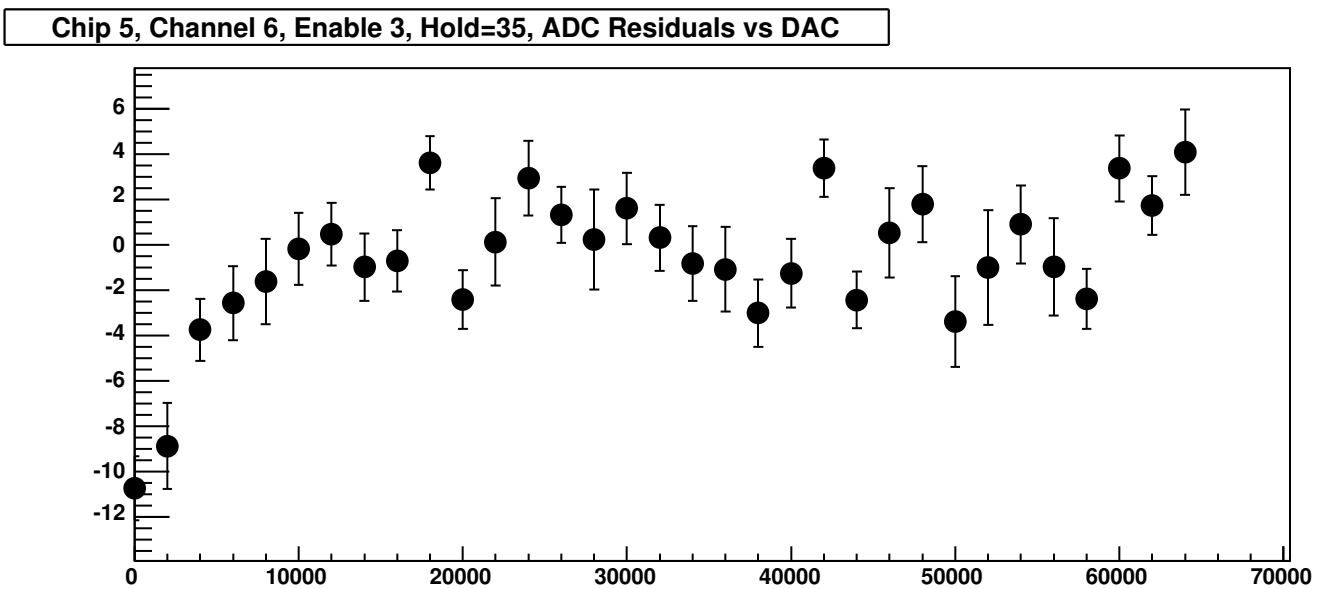

Chip 5, Channel 6, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 27.93 / 23
p0 53.43 ± 0.6495
p1 0.004271 ± 2.059e-05
350
300
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 6, Enable 2, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

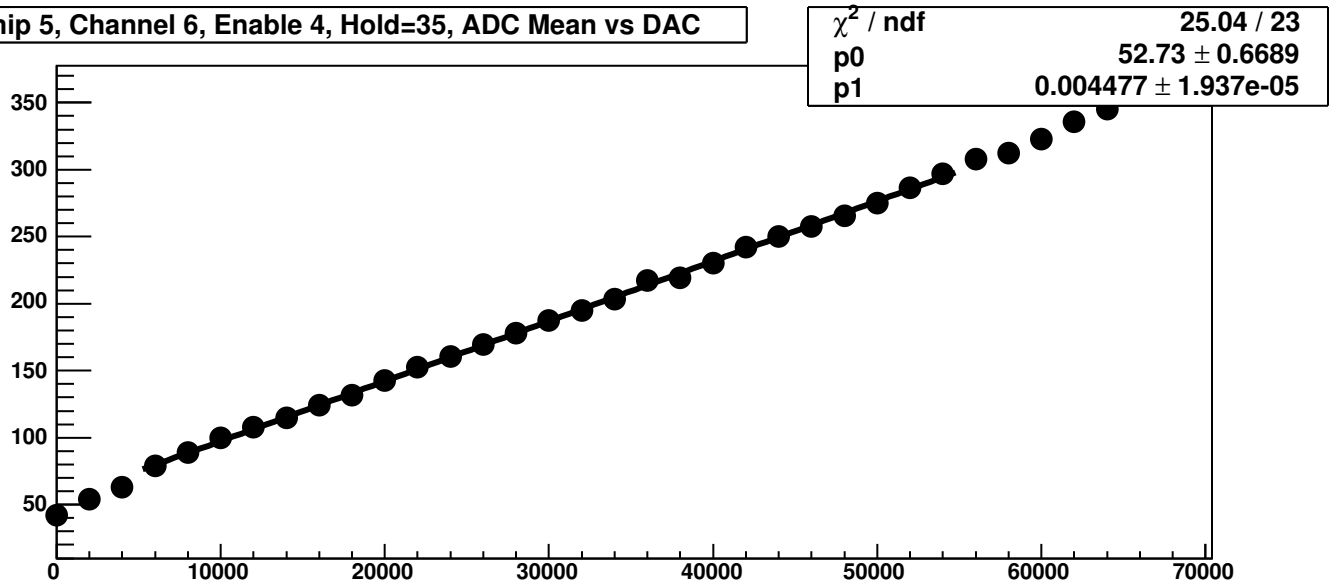

Chip 5, Channel 6, Enable 2, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 6, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 43.37 / 23
p0 54.94 ± 0.7191
p1 0.004334 ± 2.264e-05
350
300
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

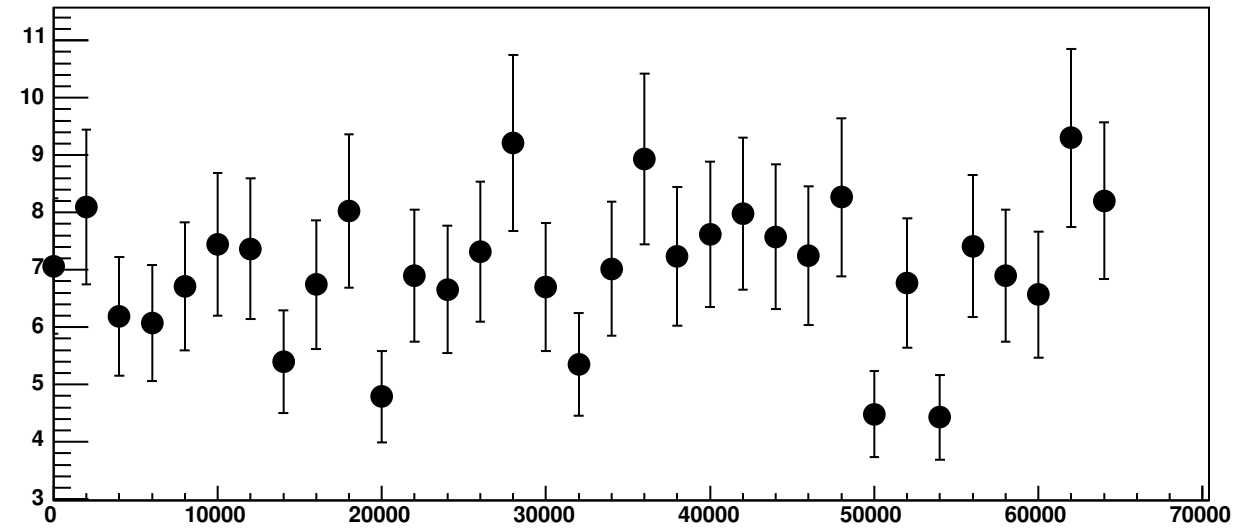

Chip 5, Channel 6, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 6, Enable 3, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
0
10000
20000
30000
40000
50000
60000
70000

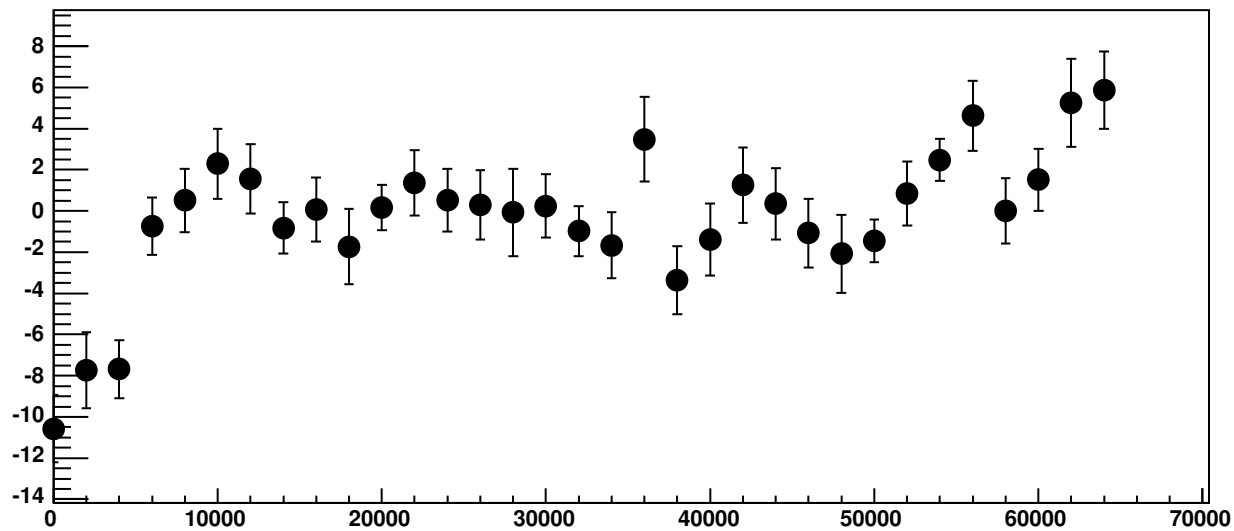
Chip 5, Channel 6, Enable 4, Hold=35, ADC Mean vs DAC

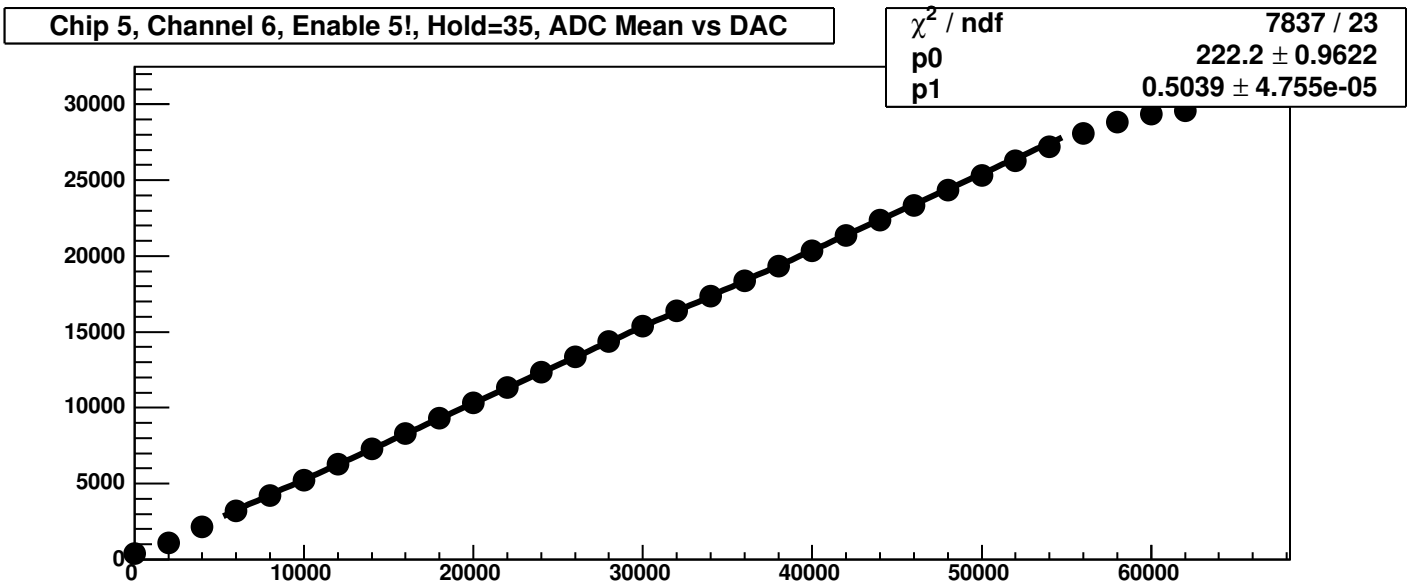


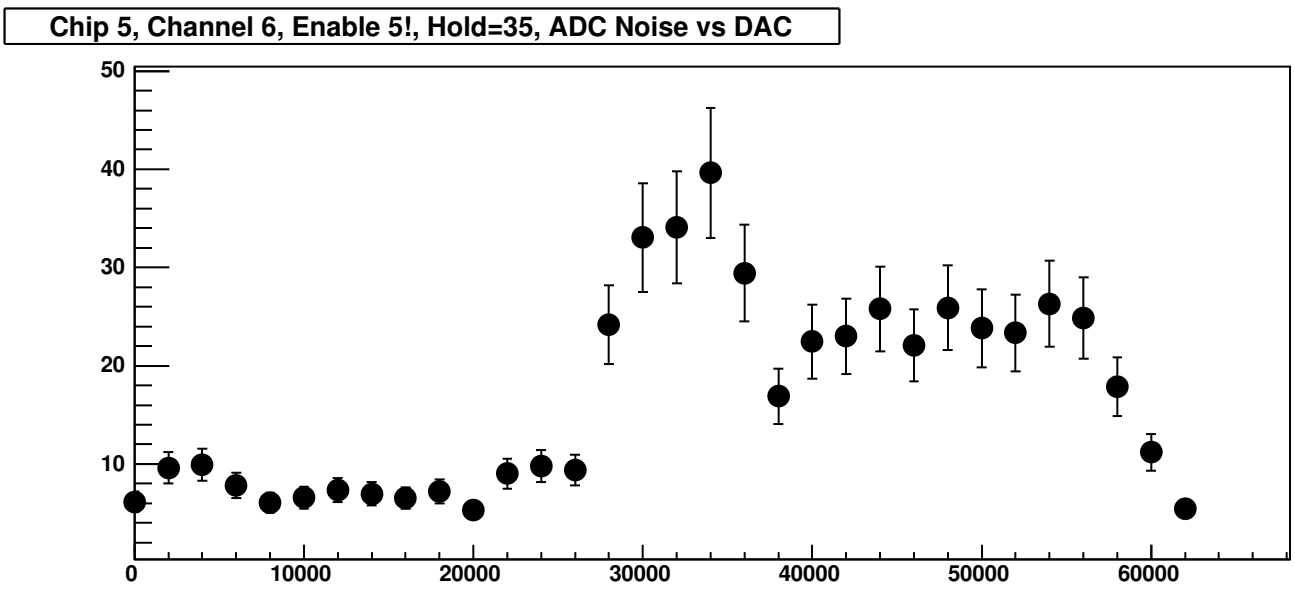
Chip 5, Channel 6, Enable 4, Hold=35, ADC Noise vs DAC

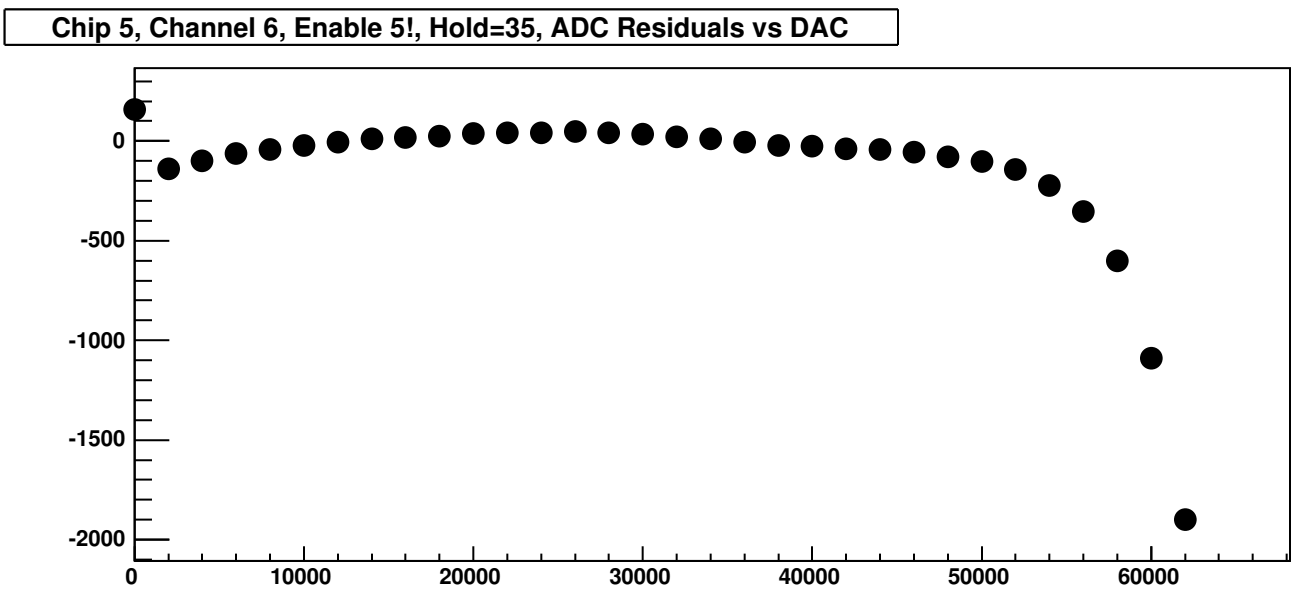


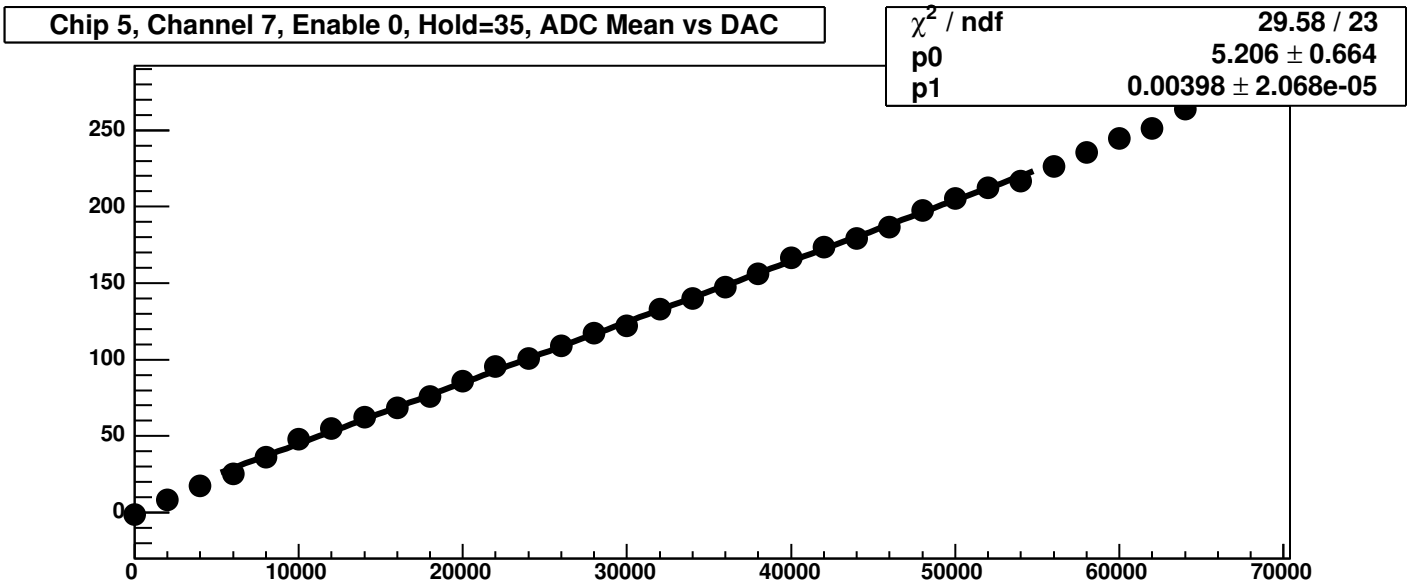
Chip 5, Channel 6, Enable 4, Hold=35, ADC Residuals vs DAC

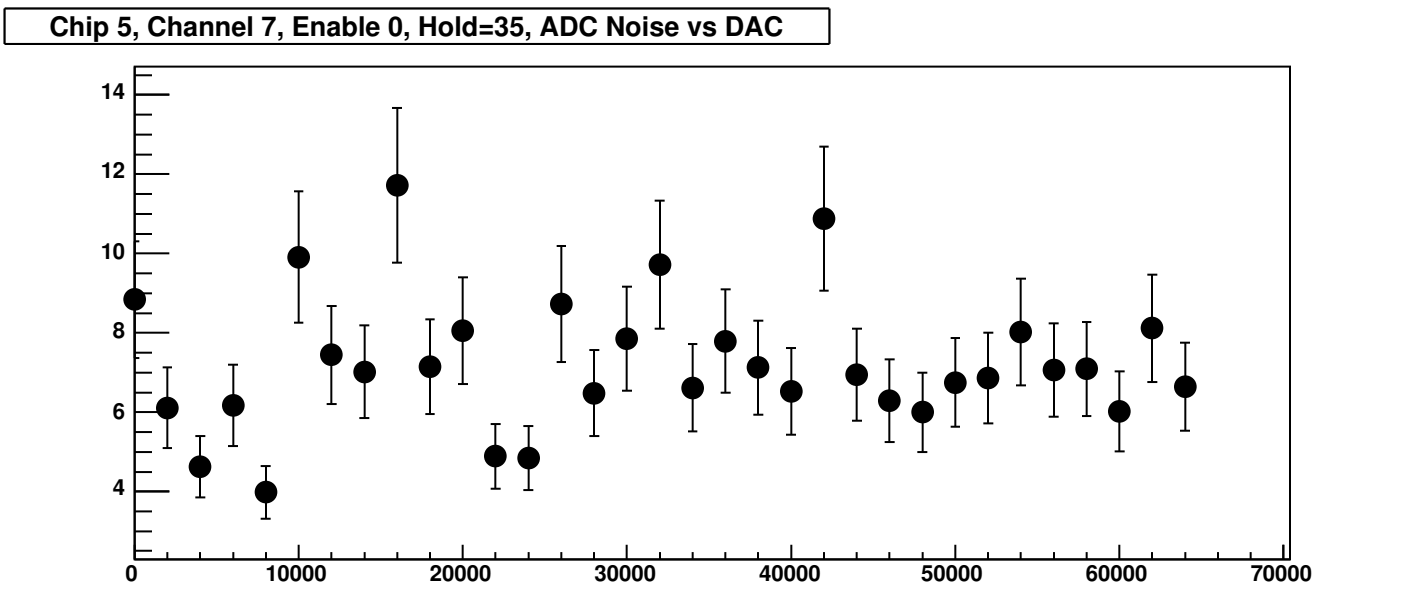


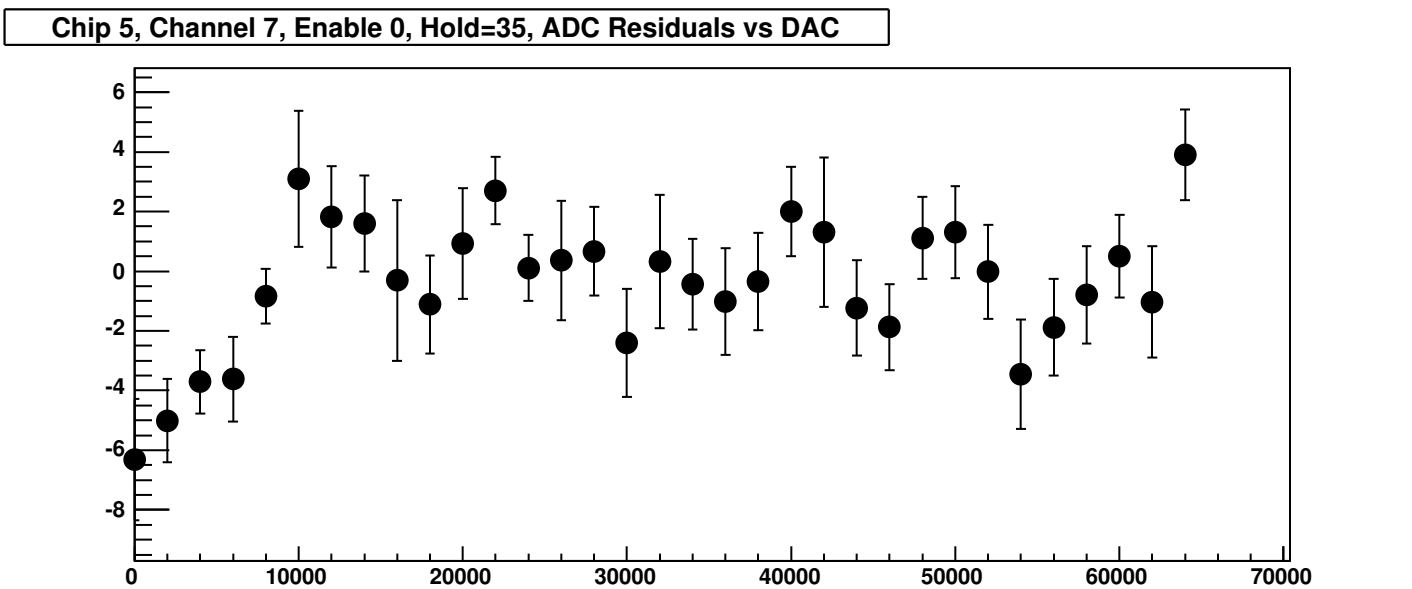

Chip 5, Channel 6, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 7837 / 23
p0 222.2 ± 0.9622
p1 0.5039 ± 4.755e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 6, Enable 5!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000

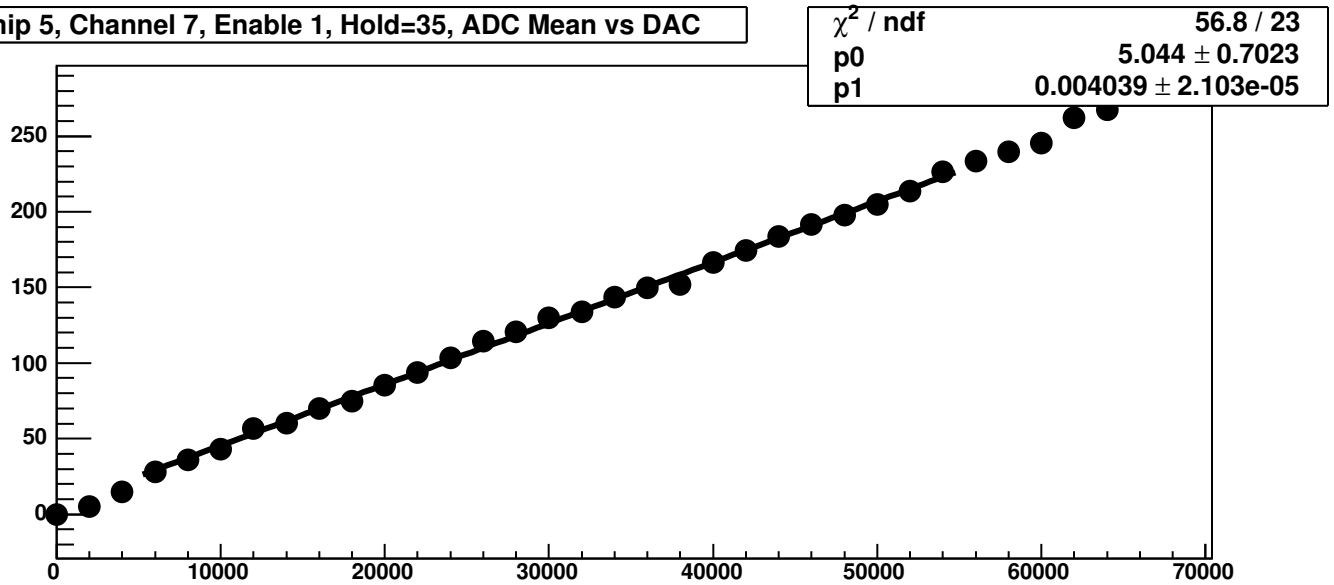

Chip 5, Channel 6, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 7, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 29.58 / 23
p0 5.206 ± 0.664
p1 0.00398 ± 2.068e-05
250
200
150
100
50
0
0
10000
20000
30000
40000
50000
60000
70000

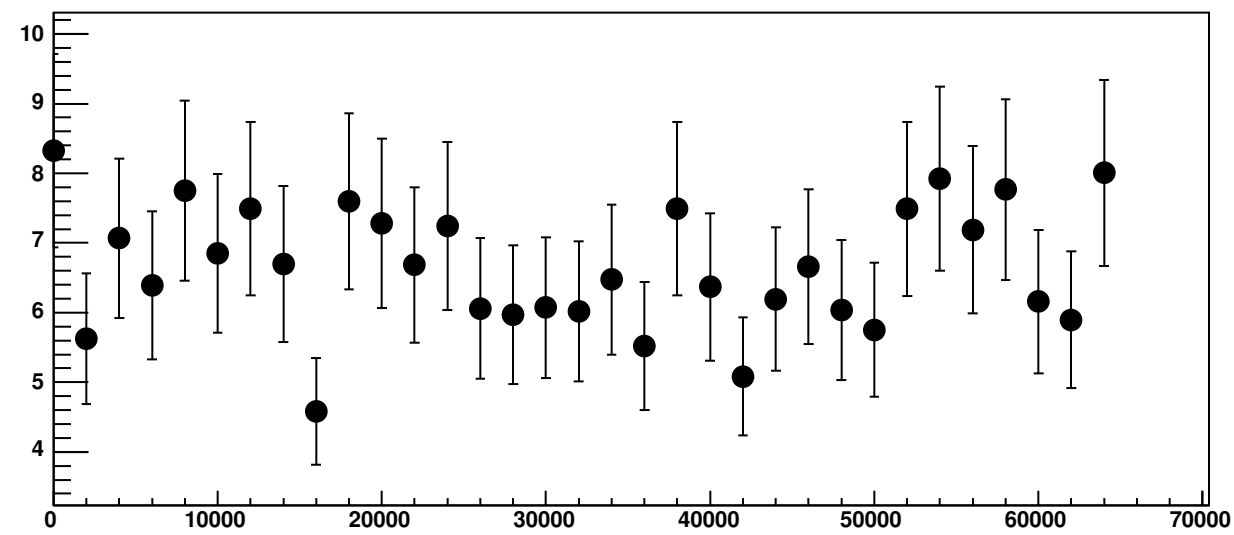

Chip 5, Channel 7, Enable 0, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 7, Enable 0, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000

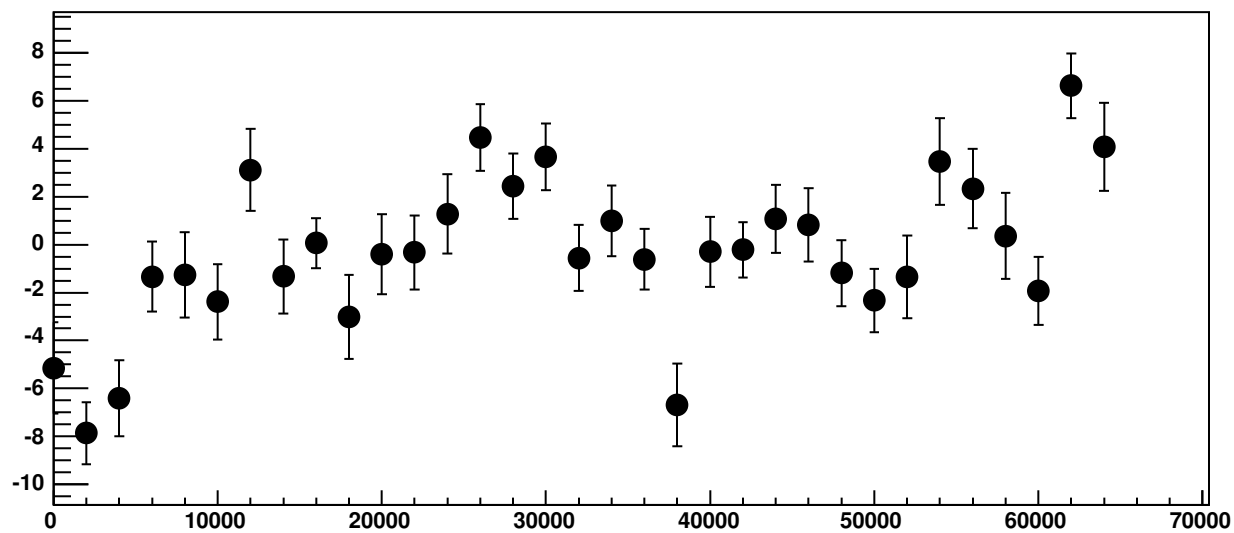
Chip 5, Channel 7, Enable 1, Hold=35, ADC Mean vs DAC

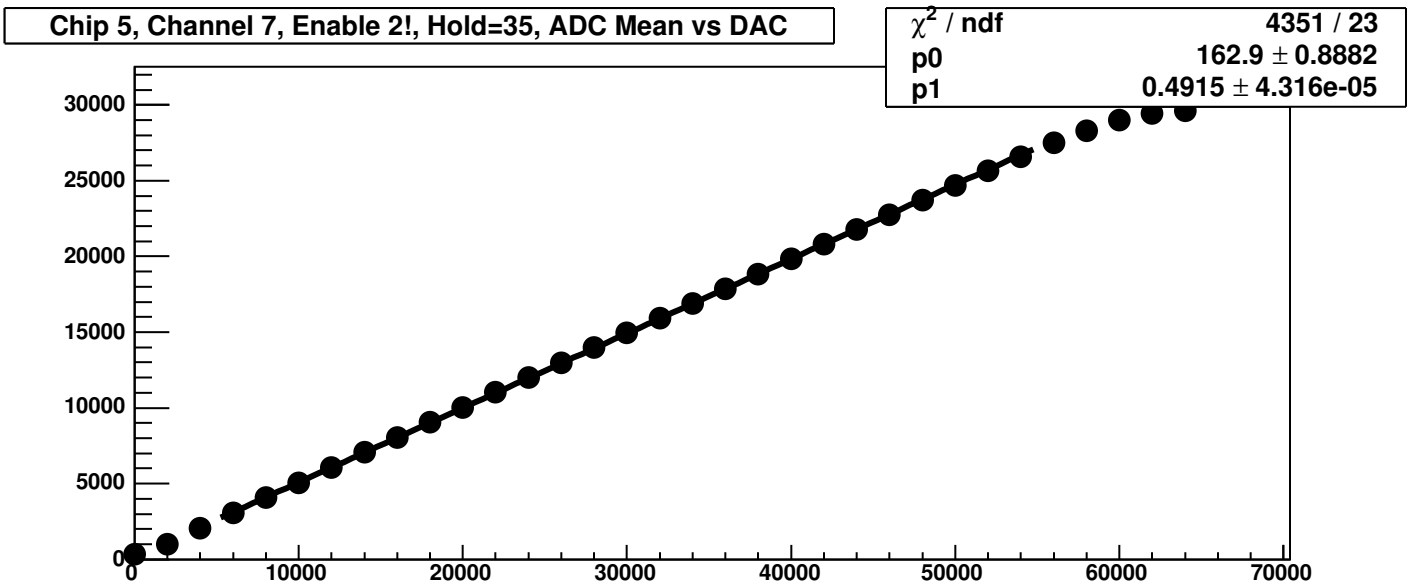


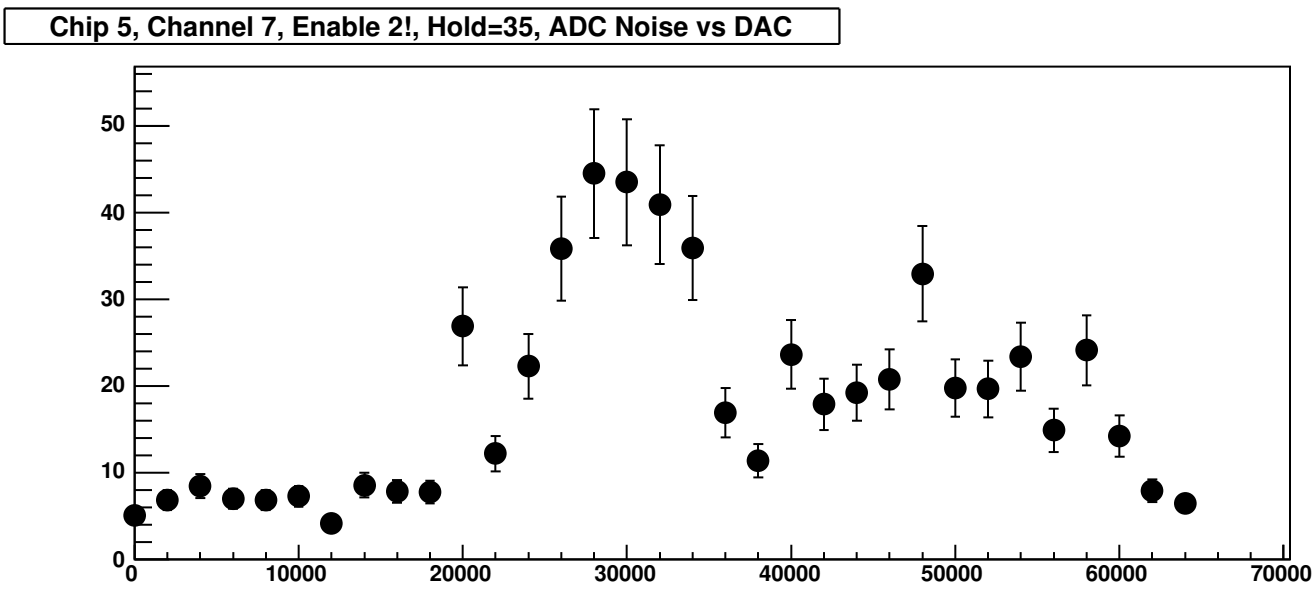
Chip 5, Channel 7, Enable 1, Hold=35, ADC Noise vs DAC

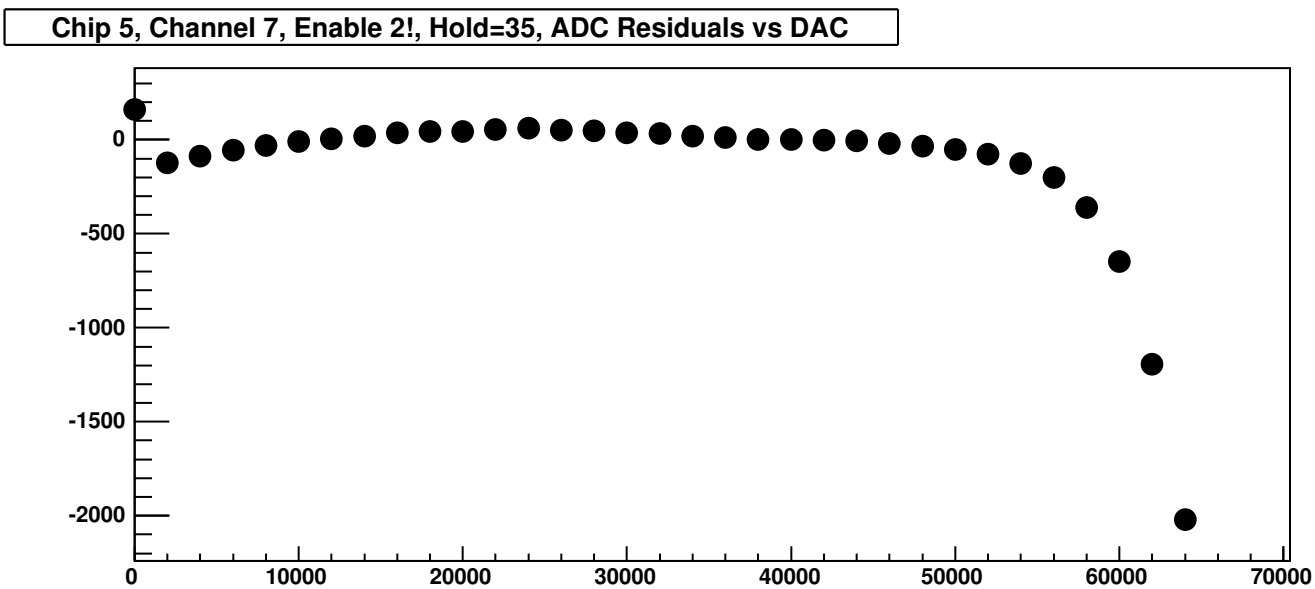


Chip 5, Channel 7, Enable 1, Hold=35, ADC Residuals vs DAC

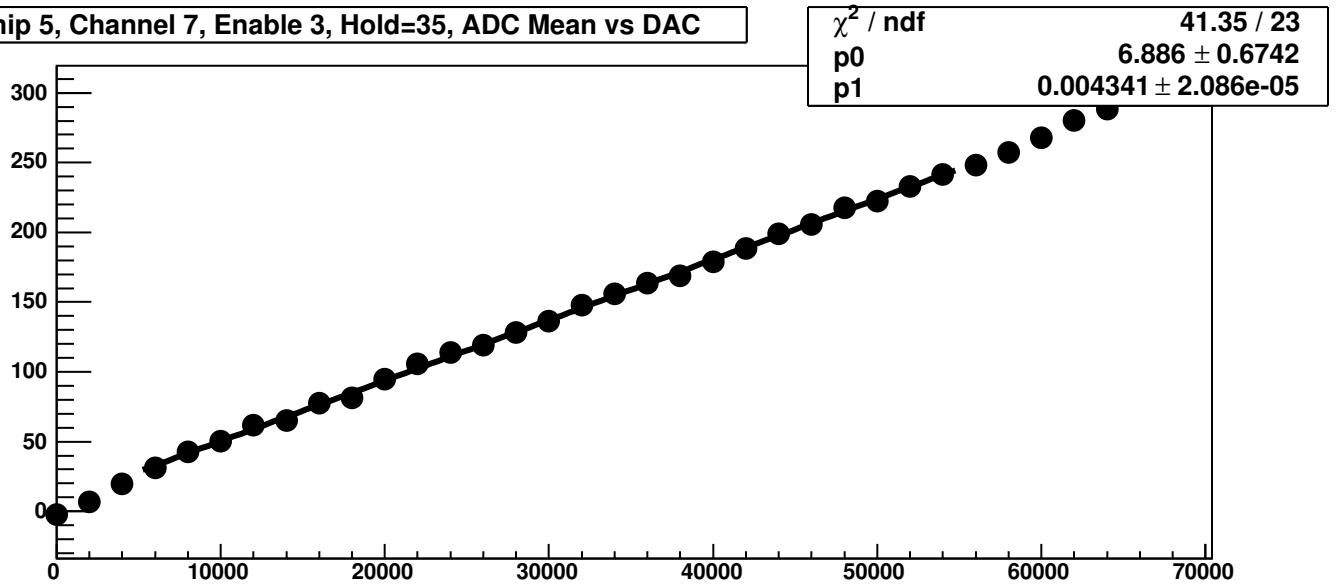



Chip 5, Channel 7, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 4351 / 23
p0 162.9 ± 0.8882
p1 0.4915 ± 4.316e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

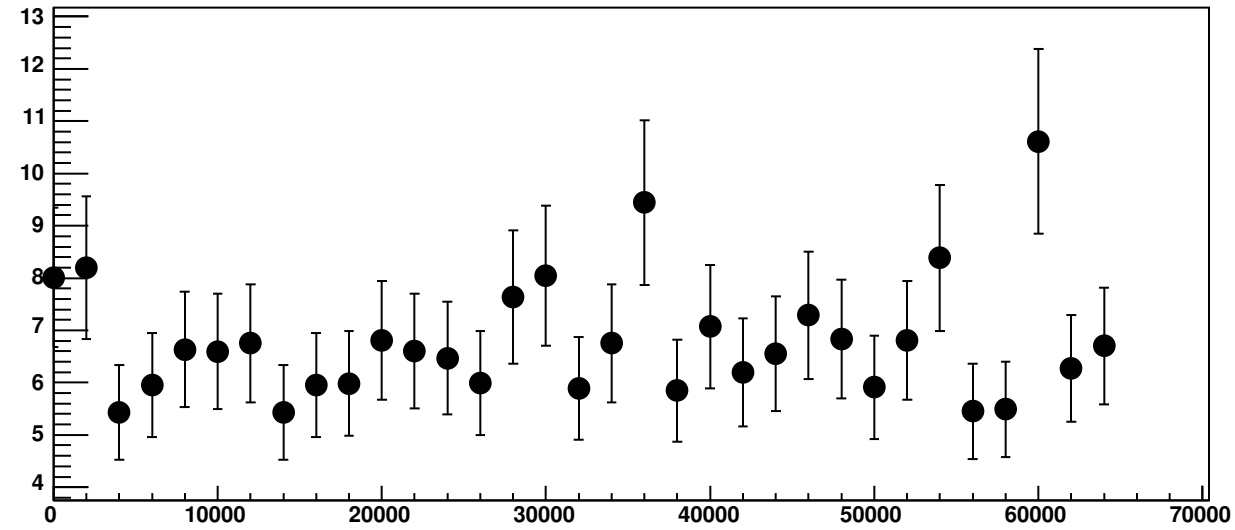

Chip 5, Channel 7, Enable 2!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 7, Enable 2!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000

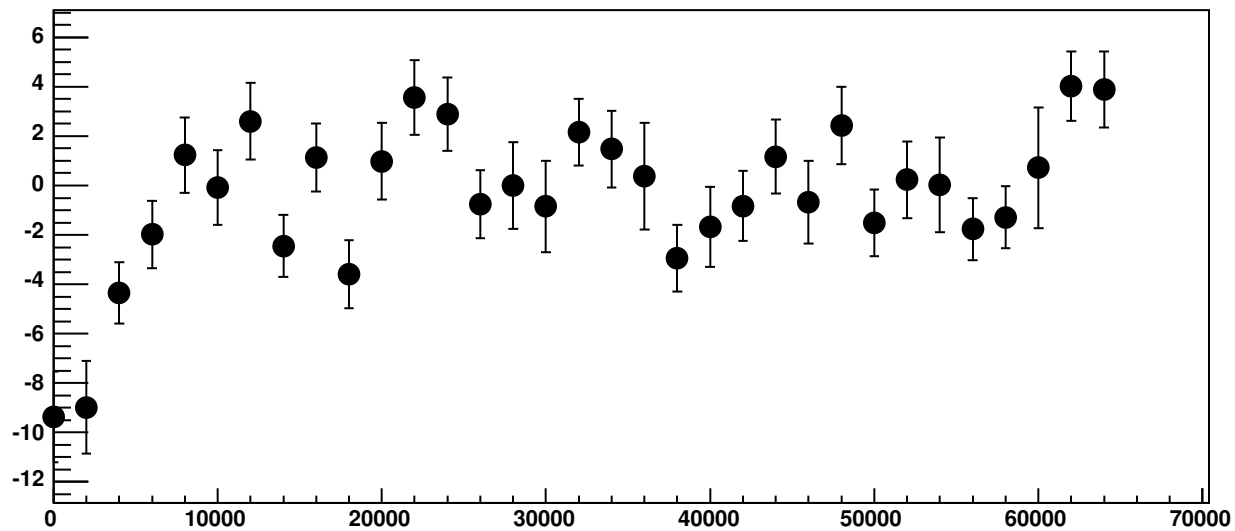
Chip 5, Channel 7, Enable 3, Hold=35, ADC Mean vs DAC



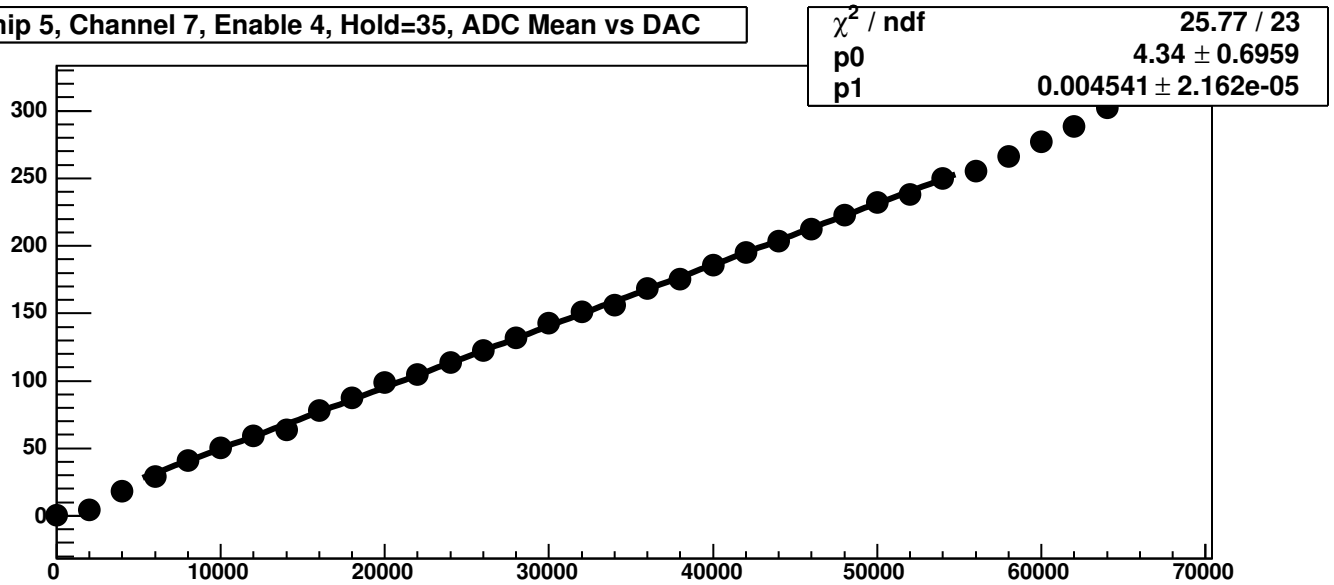
Chip 5, Channel 7, Enable 3, Hold=35, ADC Noise vs DAC



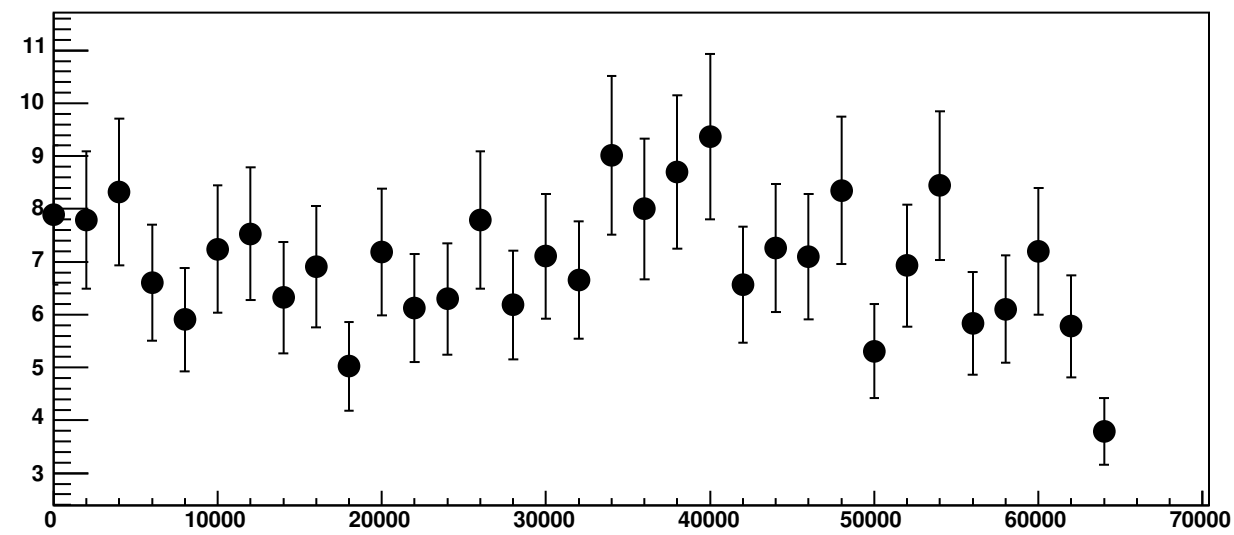
Chip 5, Channel 7, Enable 3, Hold=35, ADC Residuals vs DAC



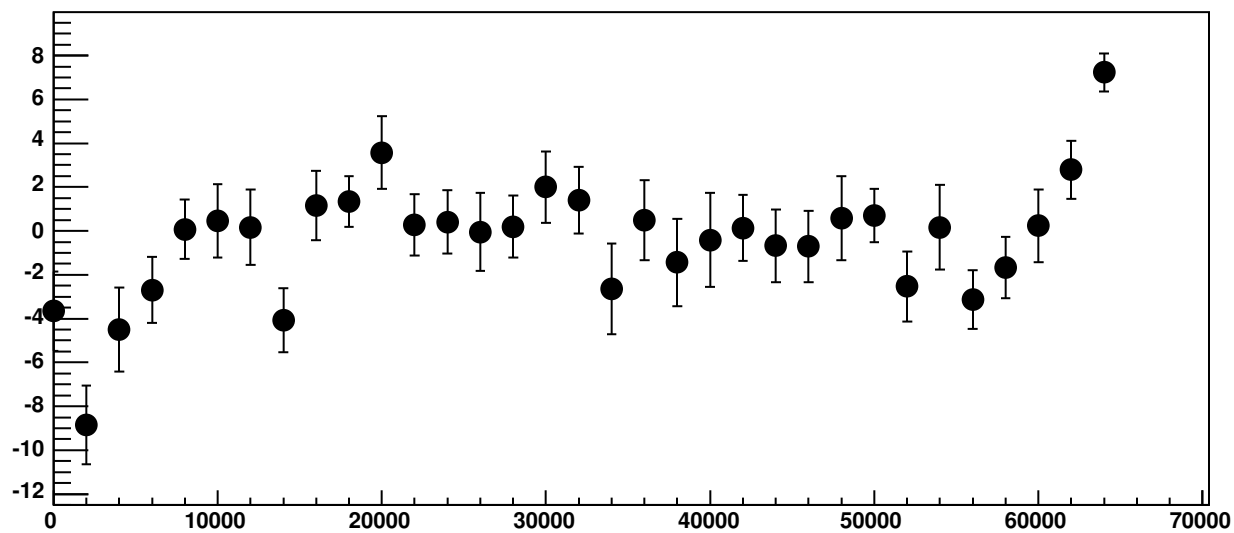
Chip 5, Channel 7, Enable 4, Hold=35, ADC Mean vs DAC

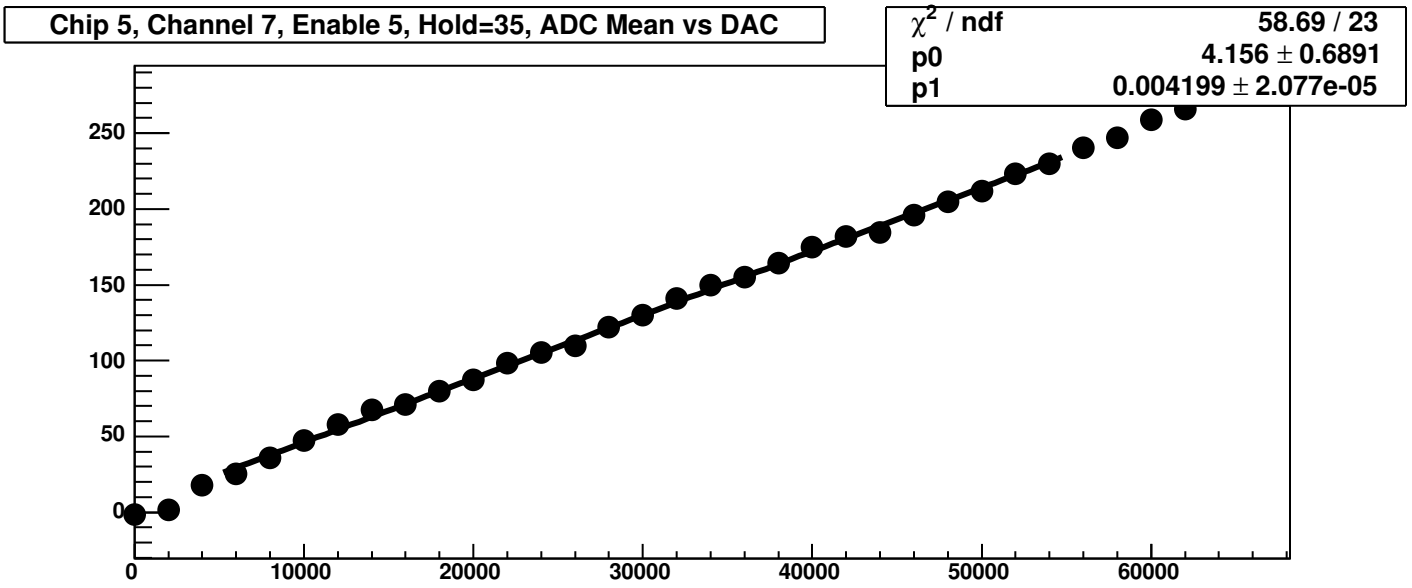


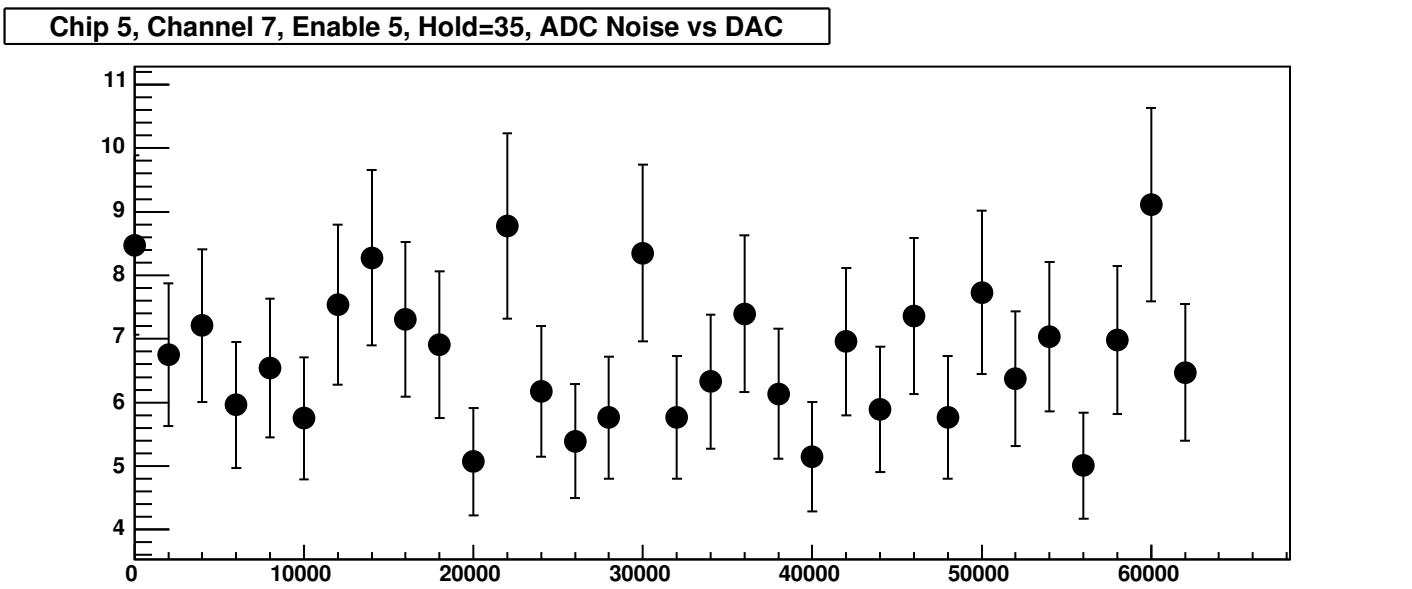
Chip 5, Channel 7, Enable 4, Hold=35, ADC Noise vs DAC

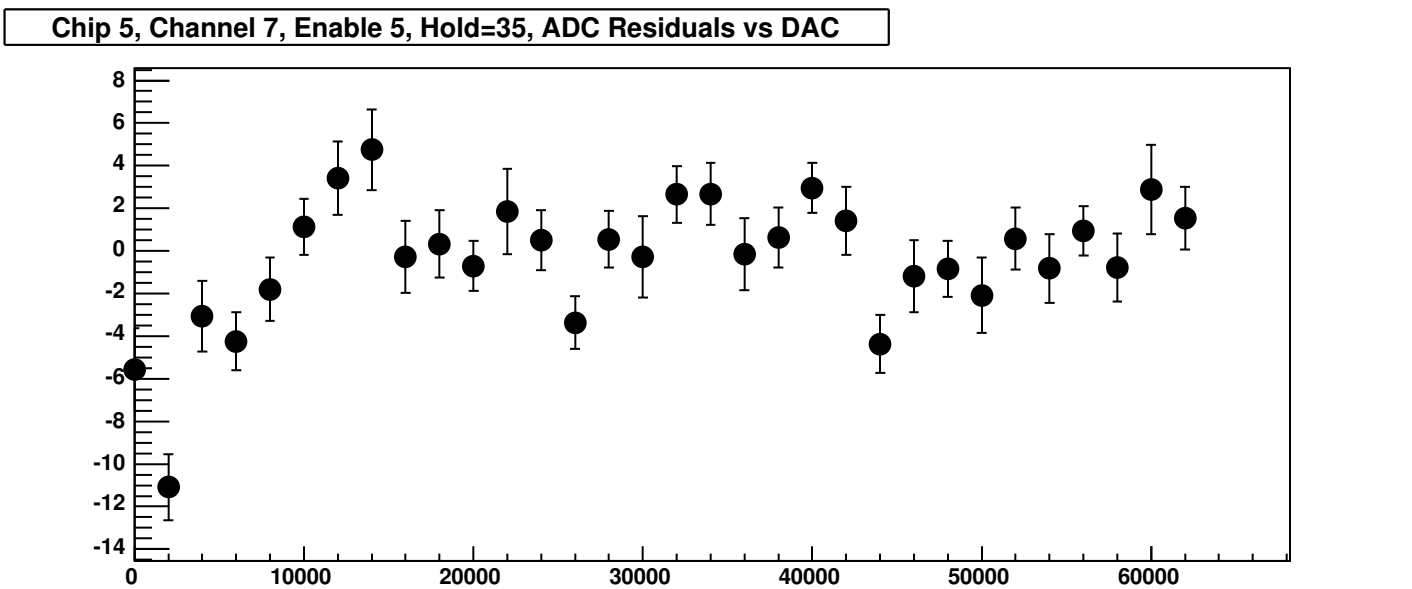


Chip 5, Channel 7, Enable 4, Hold=35, ADC Residuals vs DAC

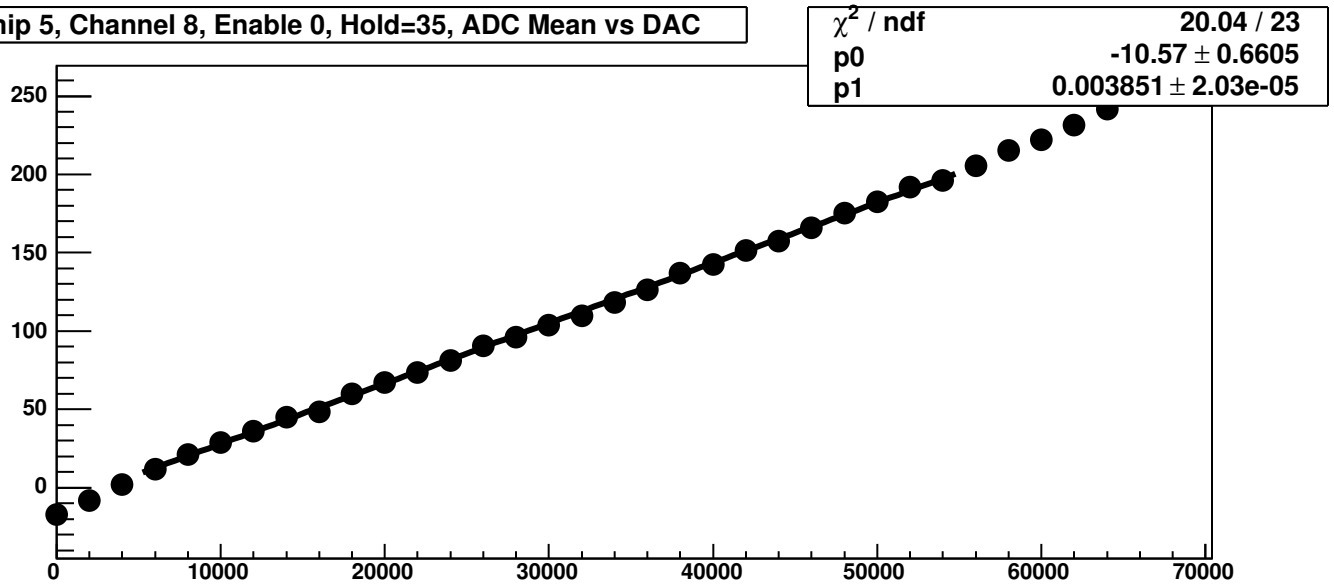



Chip 5, Channel 7, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 58.69 / 23
p0 4.156 ± 0.6891
p1 0.004199 ± 2.077e-05
250
200
150
100
50
0
0
10000
20000
30000
40000
50000
60000

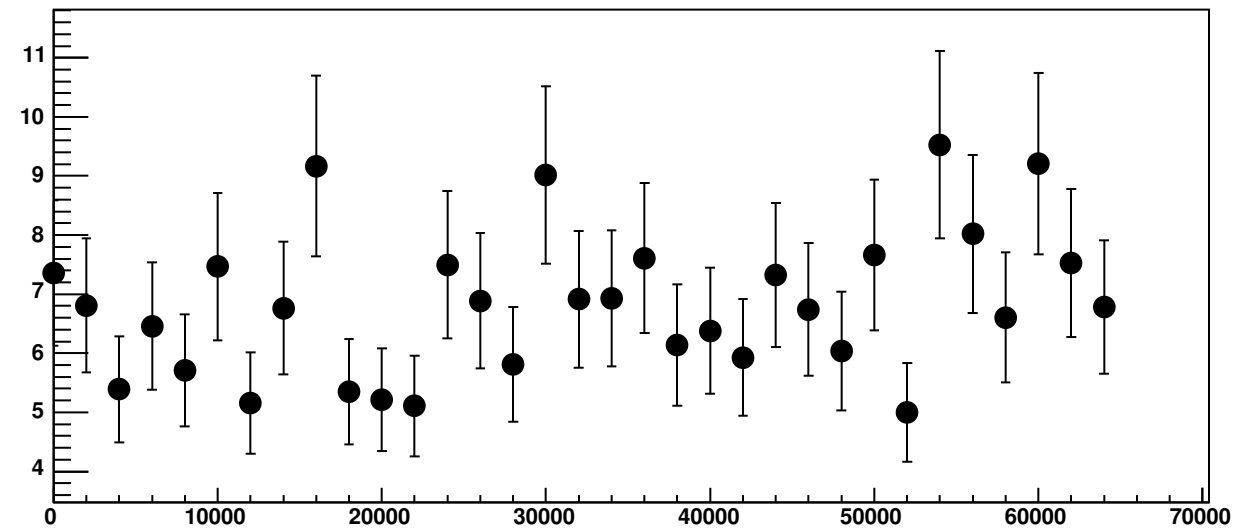

Chip 5, Channel 7, Enable 5, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 7, Enable 5, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
-14
0
10000
20000
30000
40000
50000
60000

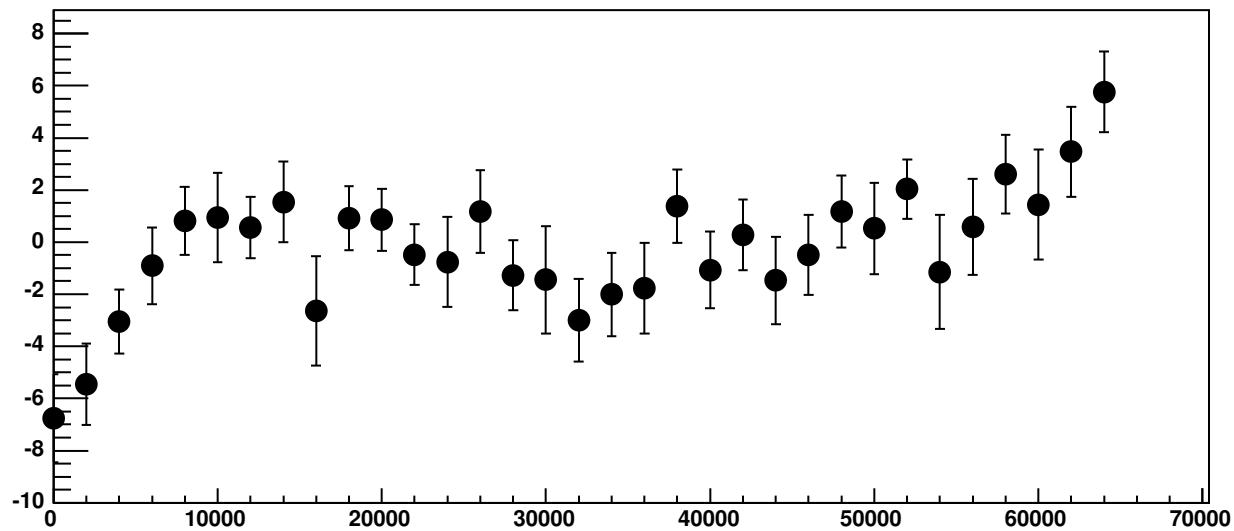
Chip 5, Channel 8, Enable 0, Hold=35, ADC Mean vs DAC



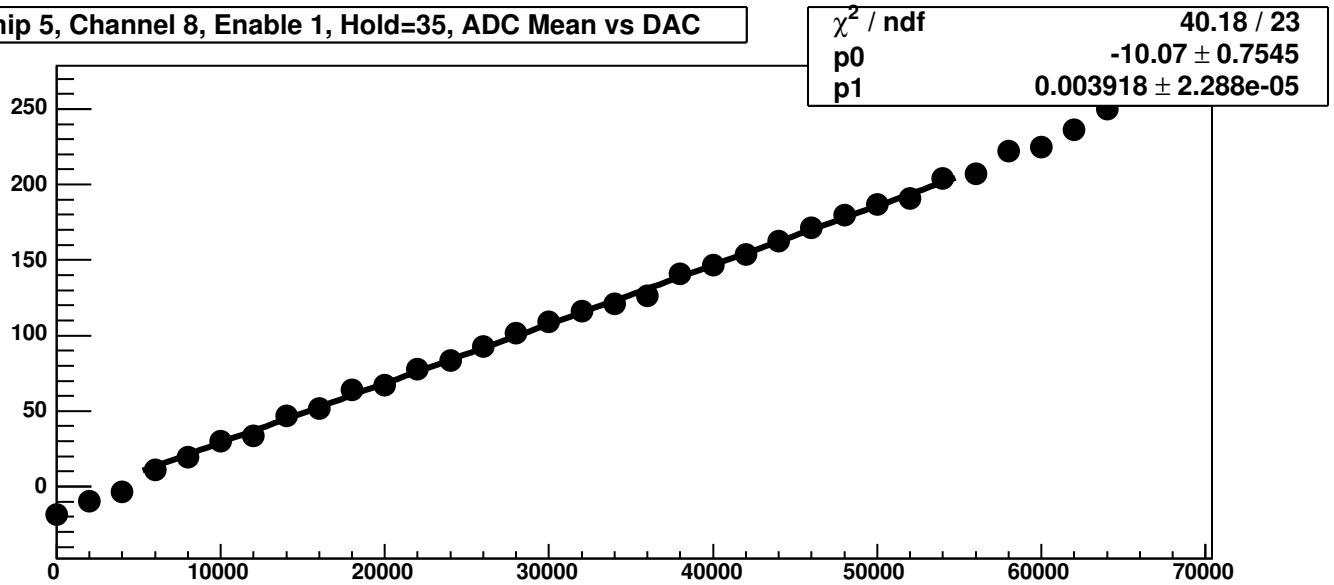
Chip 5, Channel 8, Enable 0, Hold=35, ADC Noise vs DAC



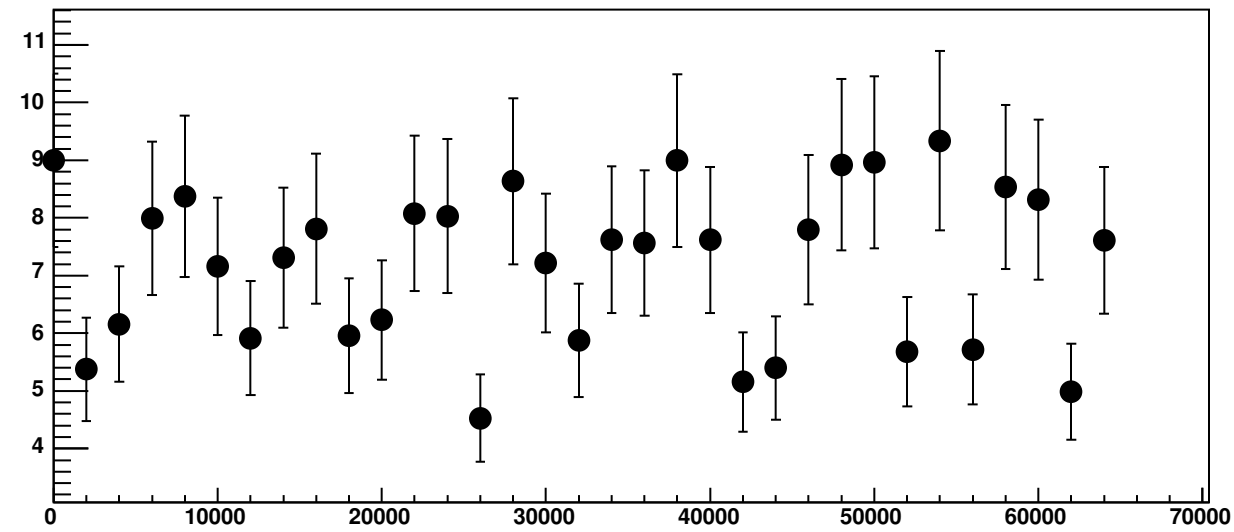
Chip 5, Channel 8, Enable 0, Hold=35, ADC Residuals vs DAC



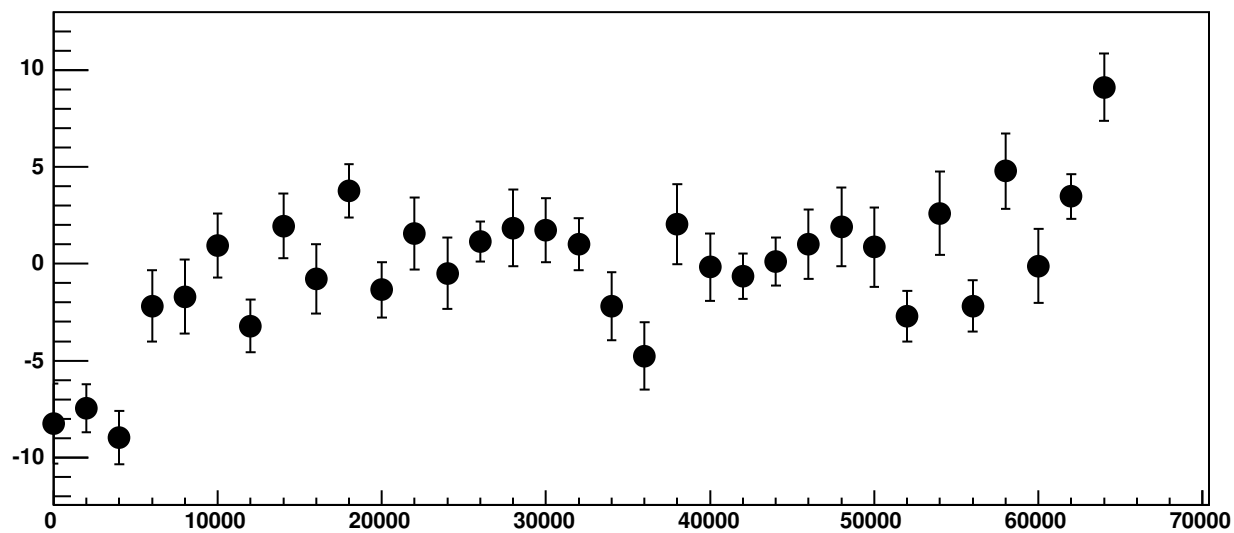
Chip 5, Channel 8, Enable 1, Hold=35, ADC Mean vs DAC

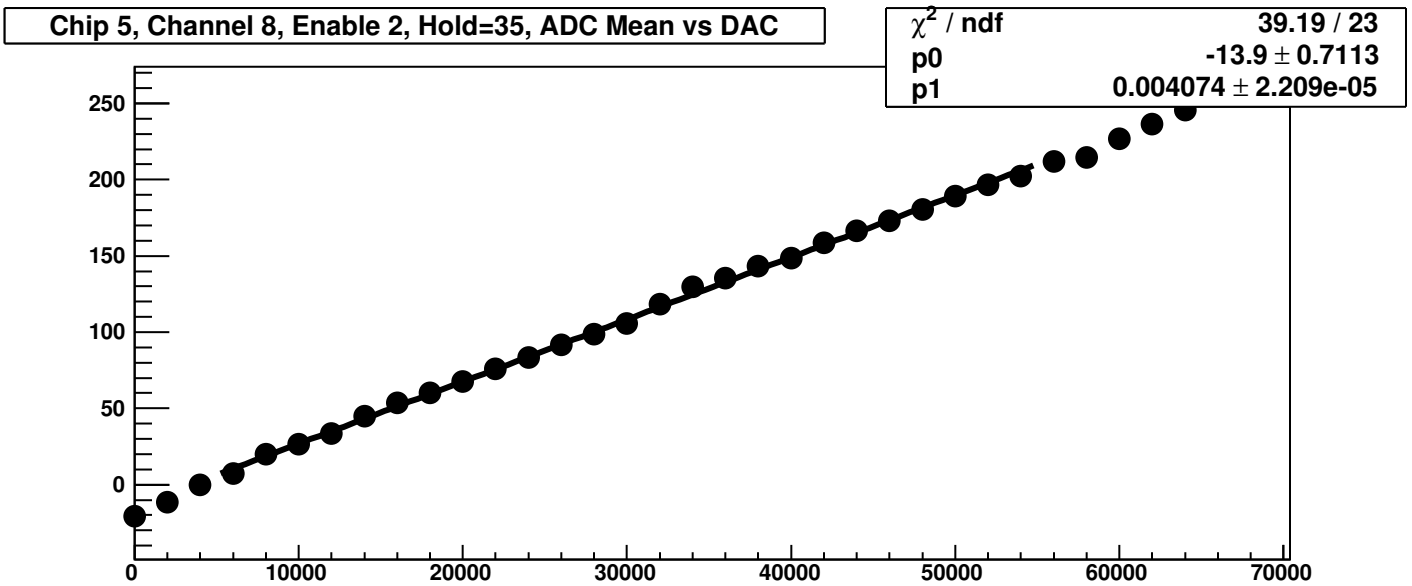


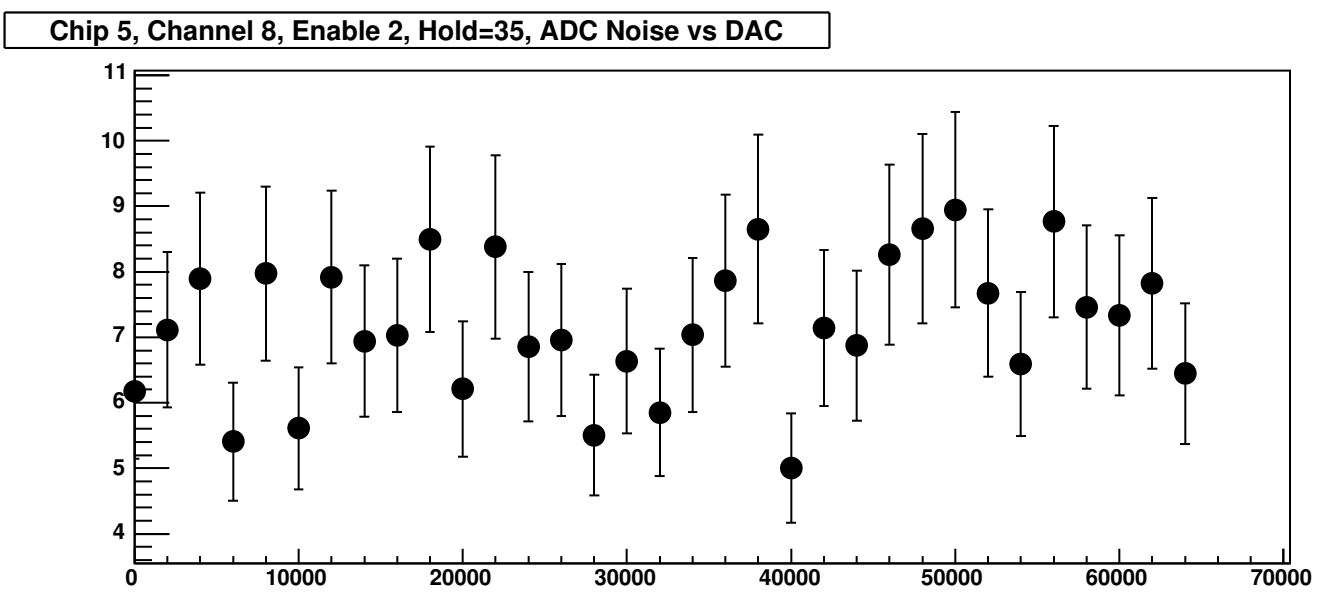
Chip 5, Channel 8, Enable 1, Hold=35, ADC Noise vs DAC

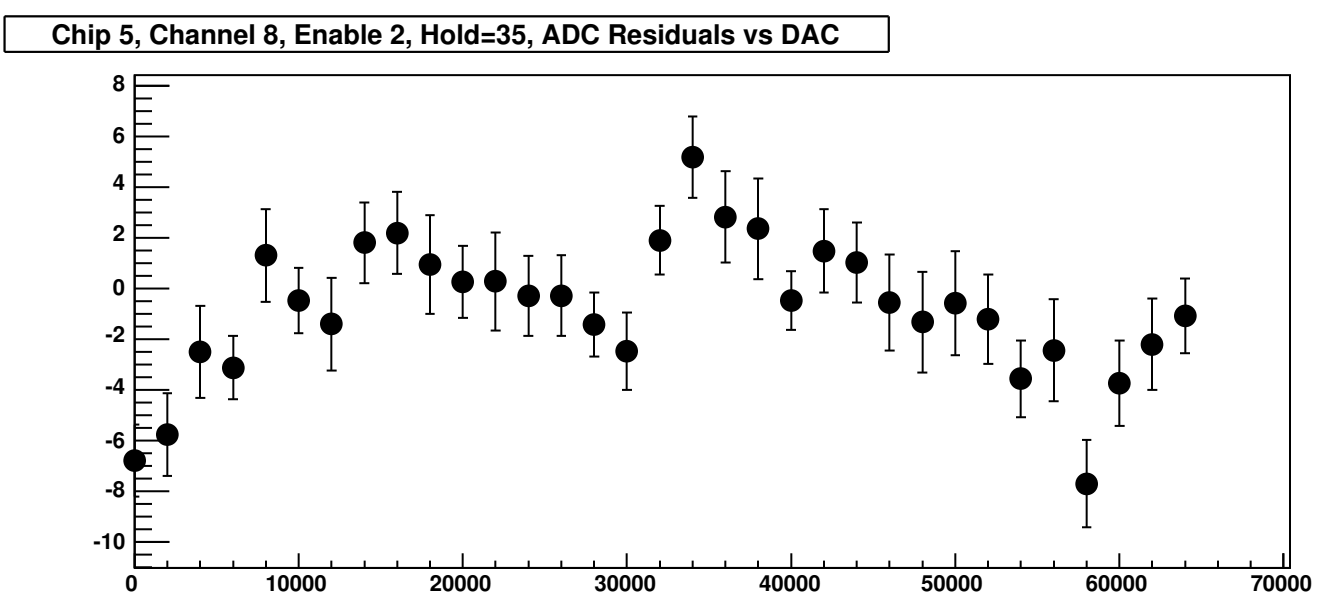


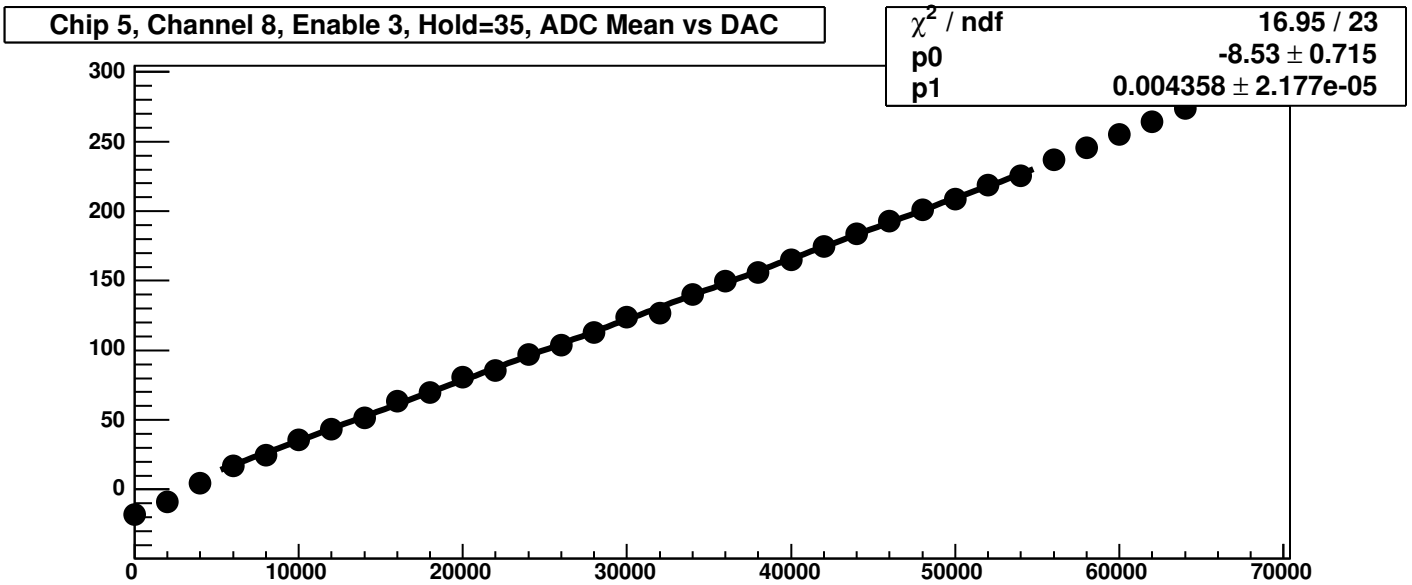
Chip 5, Channel 8, Enable 1, Hold=35, ADC Residuals vs DAC

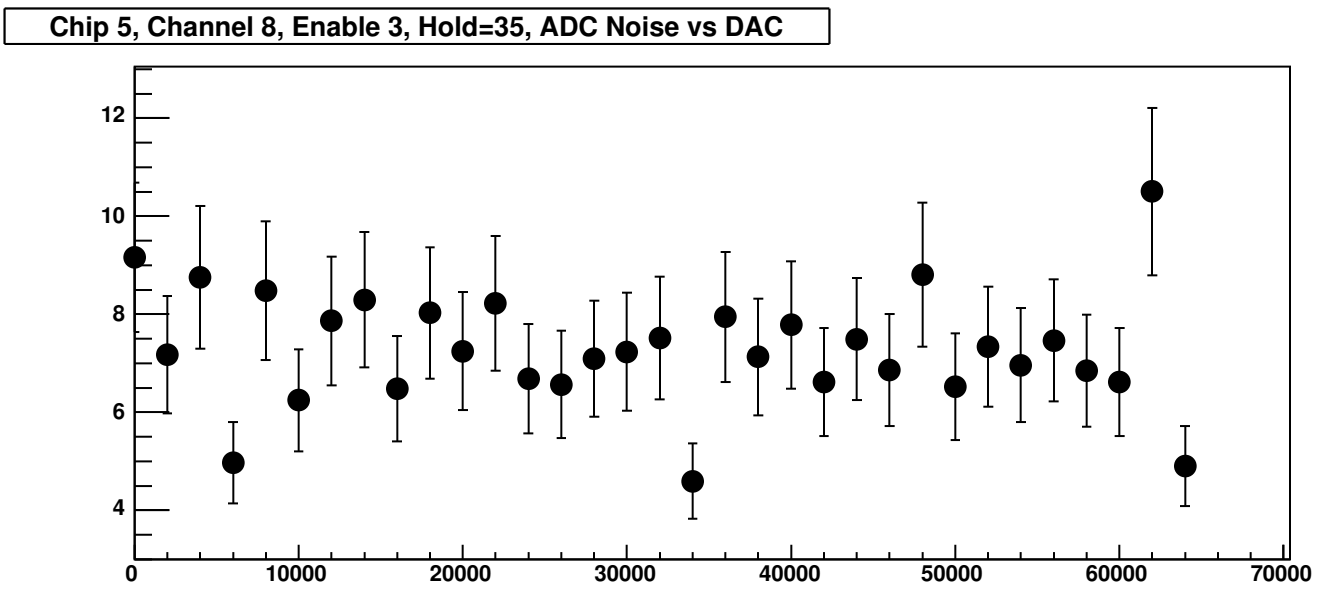


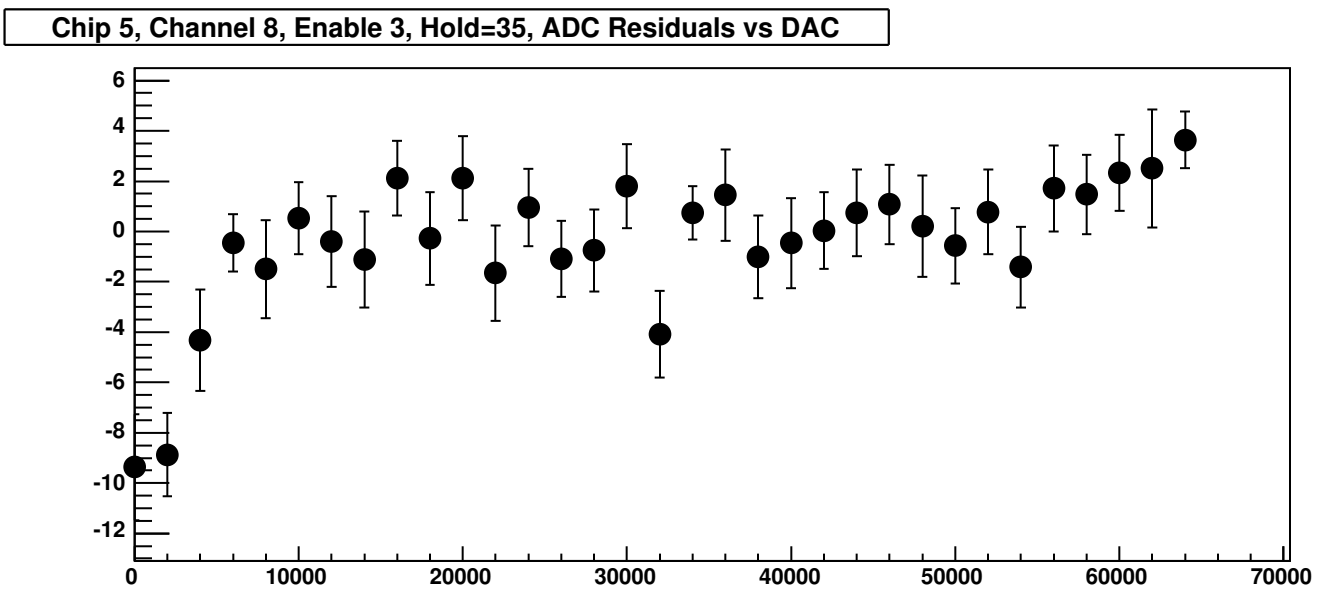

Chip 5, Channel 8, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 39.19 / 23
p0 -13.9 ± 0.7113
p1 0.004074 ± 2.209e-05
250
200
150
100
50
0
0
10000
20000
30000
40000
50000
60000
70000

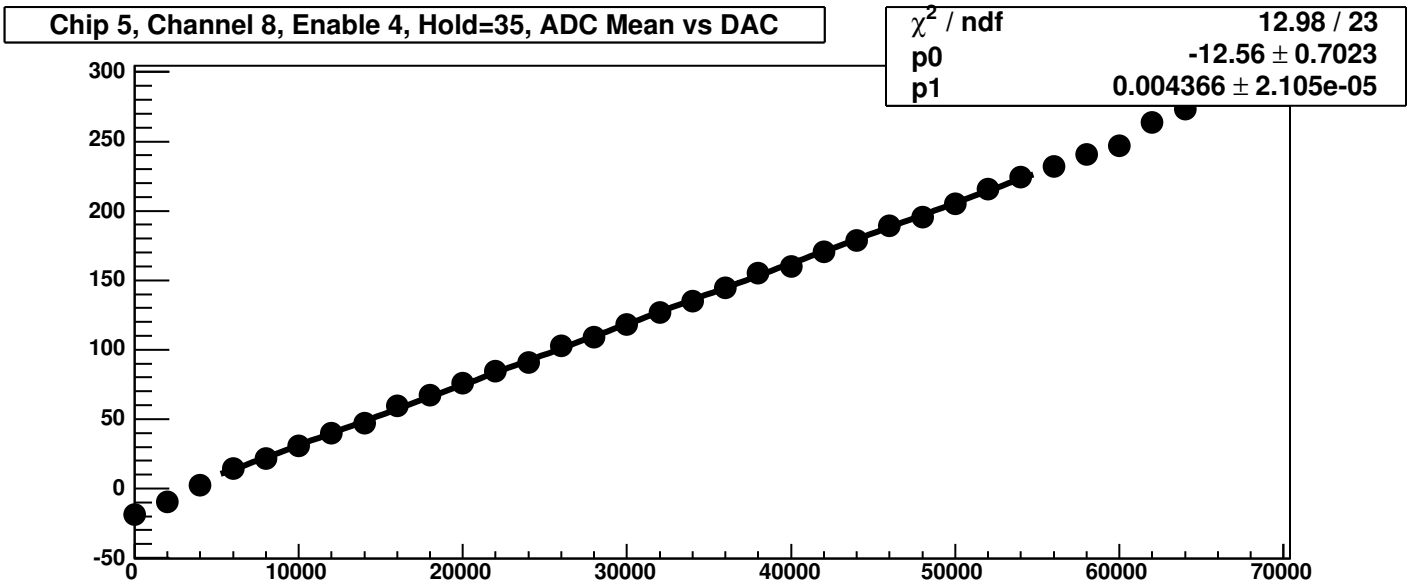

Chip 5, Channel 8, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

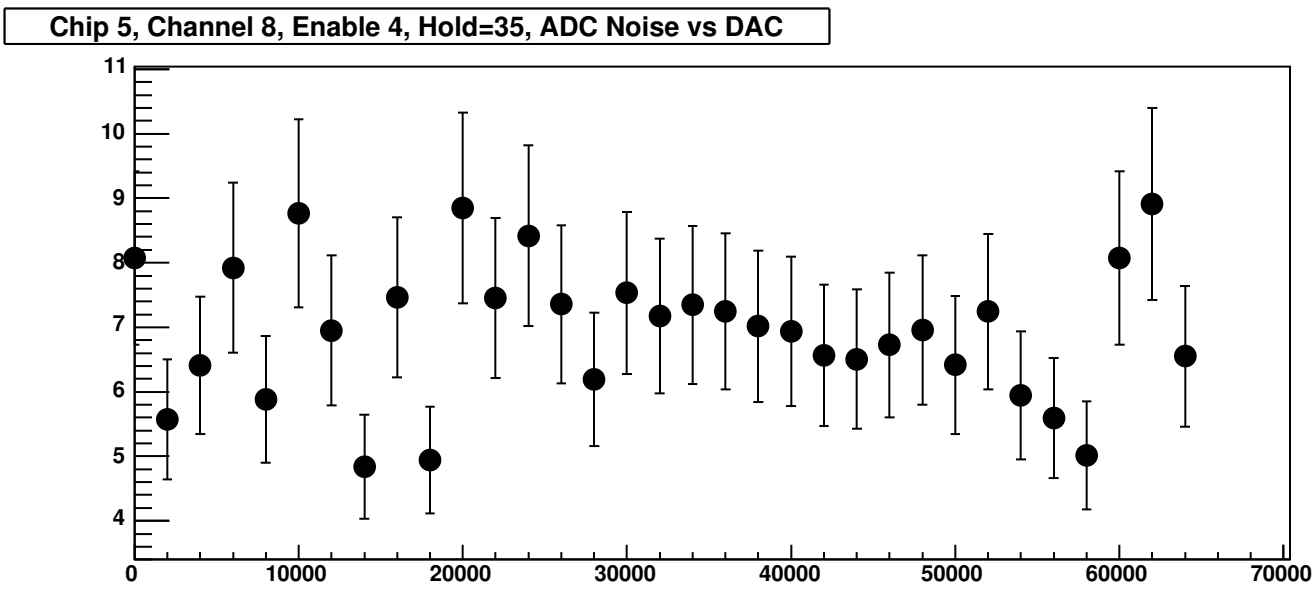

Chip 5, Channel 8, Enable 2, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
0
10000
20000
30000
40000
50000
60000
70000

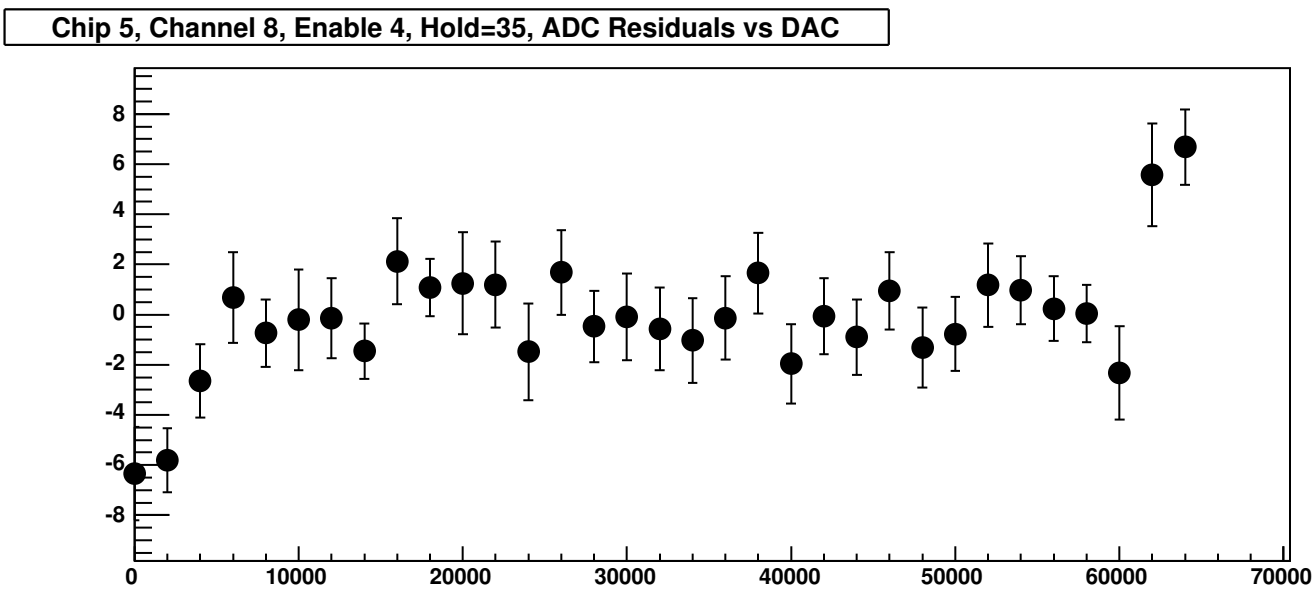

Chip 5, Channel 8, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 16.95 / 23
p0 -8.53 ± 0.715
p1 0.004358 ± 2.177e-05
300
250
200
150
100
50
0
0
10000
20000
30000
40000
50000
60000
70000

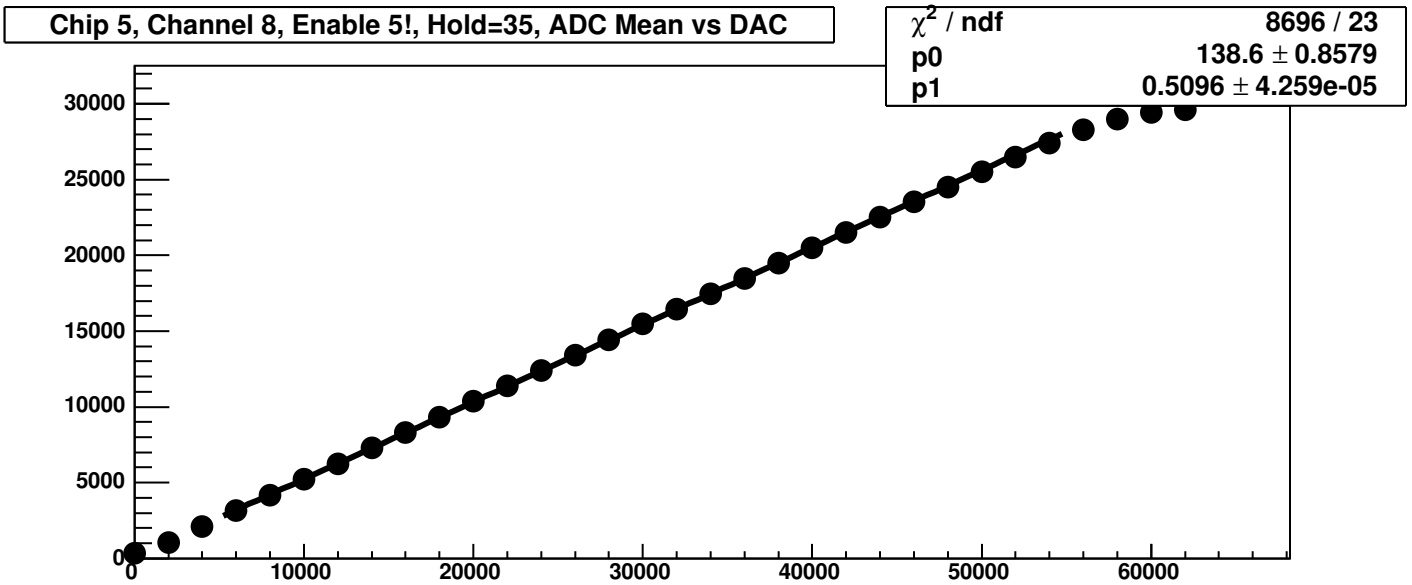

Chip 5, Channel 8, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

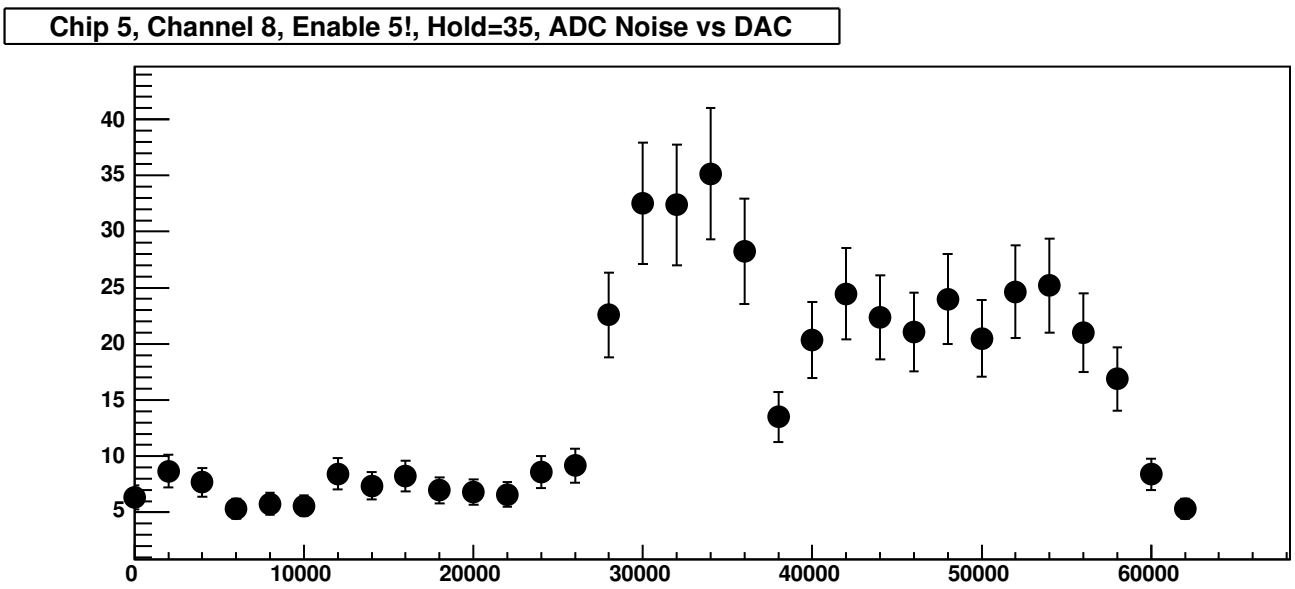

Chip 5, Channel 8, Enable 3, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
0
10000
20000
30000
40000
50000
60000
70000

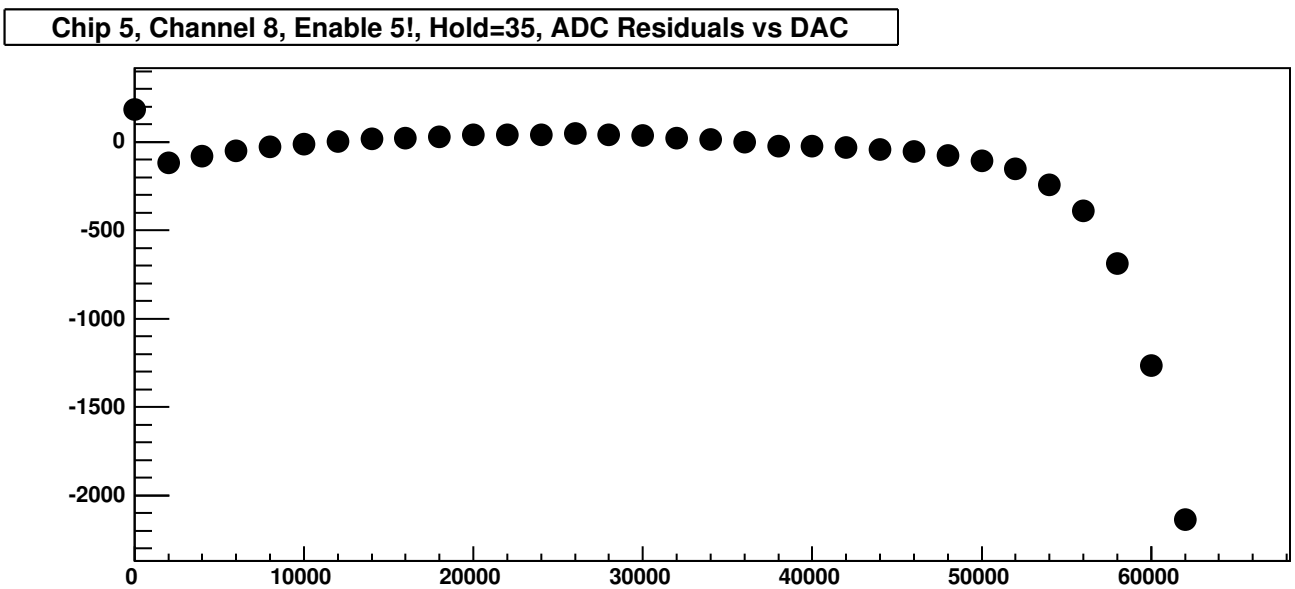

Chip 5, Channel 8, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 12.98 / 23
p0 -12.56 ± 0.7023
p1 0.004366 ± 2.105e-05
300
250
200
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 8, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

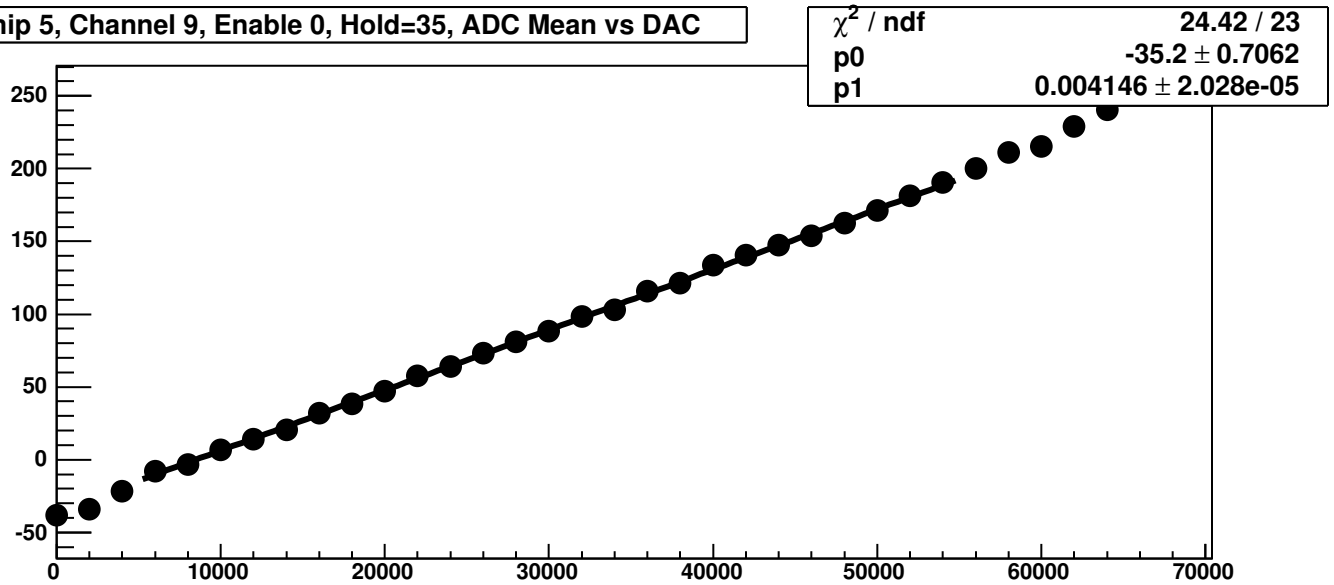

Chip 5, Channel 8, Enable 4, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 8, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 8696 / 23
p0 138.6 ± 0.8579
p1 0.5096 ± 4.259e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000

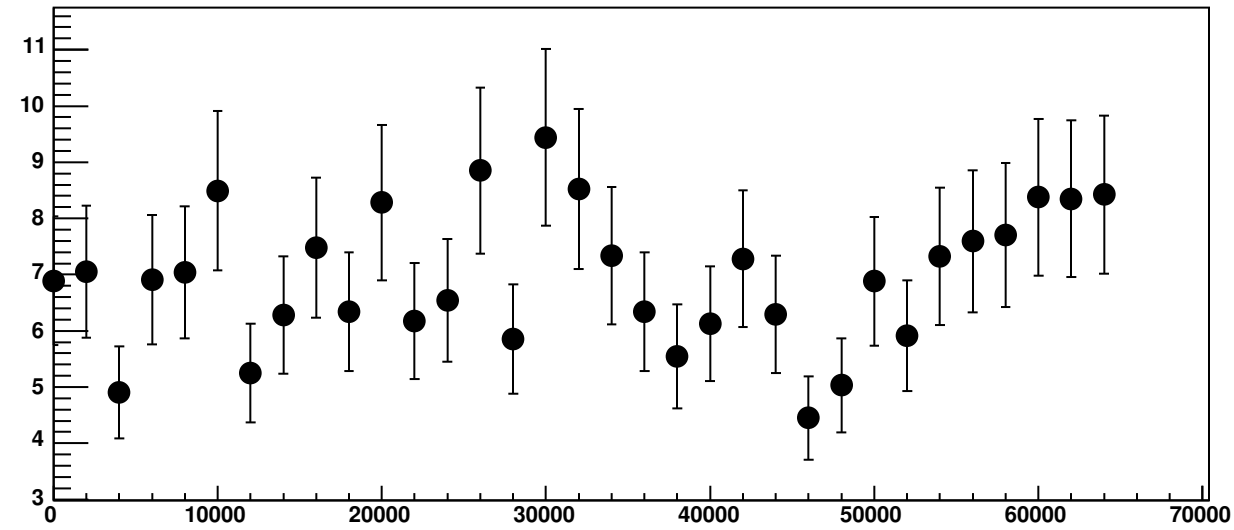

Chip 5, Channel 8, Enable 5!, Hold=35, ADC Noise vs DAC
40
35
30
25
20
15
10
5
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 8, Enable 5!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000

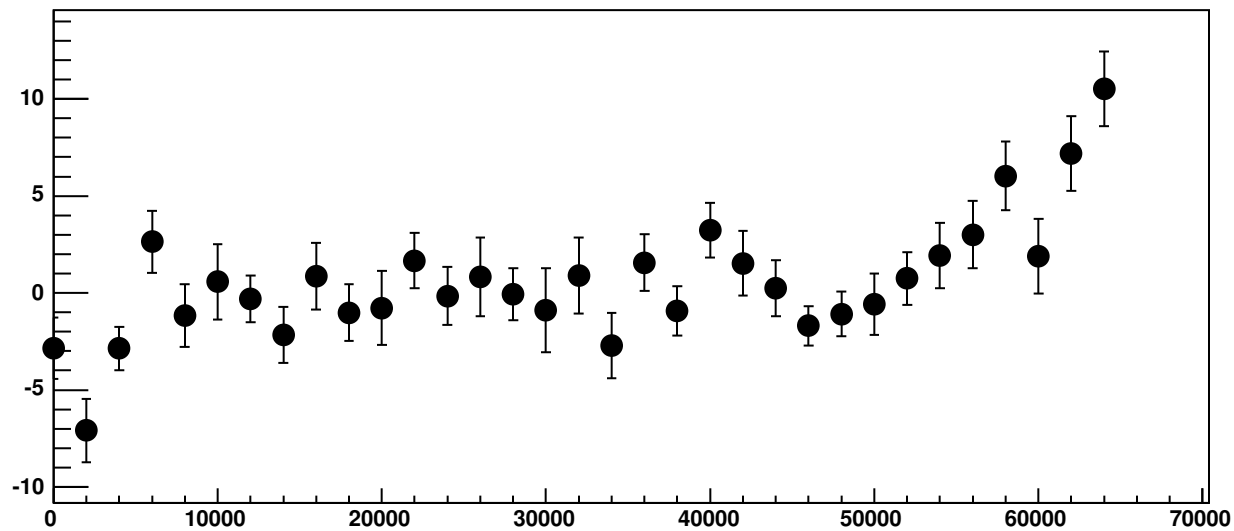
Chip 5, Channel 9, Enable 0, Hold=35, ADC Mean vs DAC

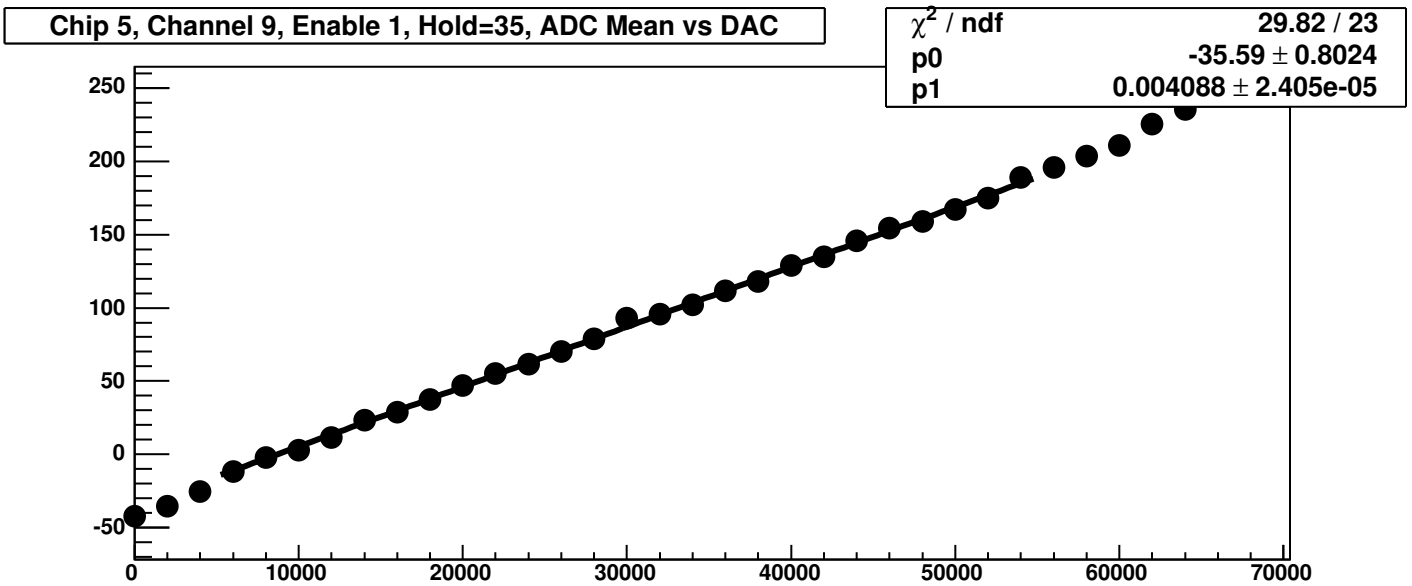


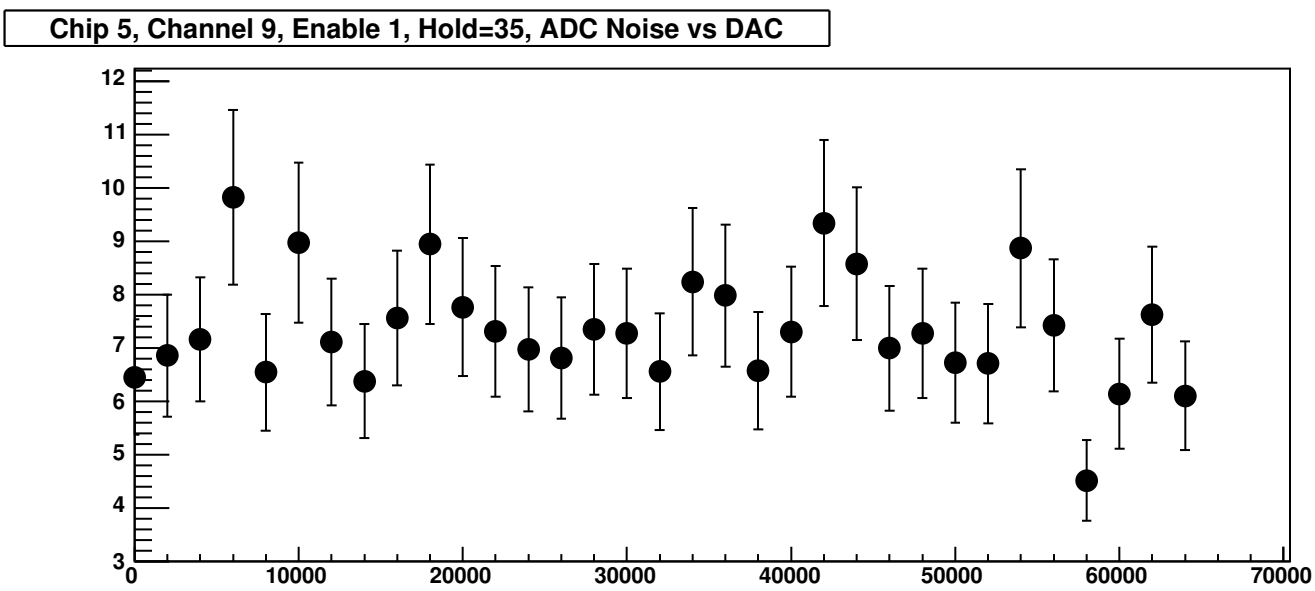
Chip 5, Channel 9, Enable 0, Hold=35, ADC Noise vs DAC

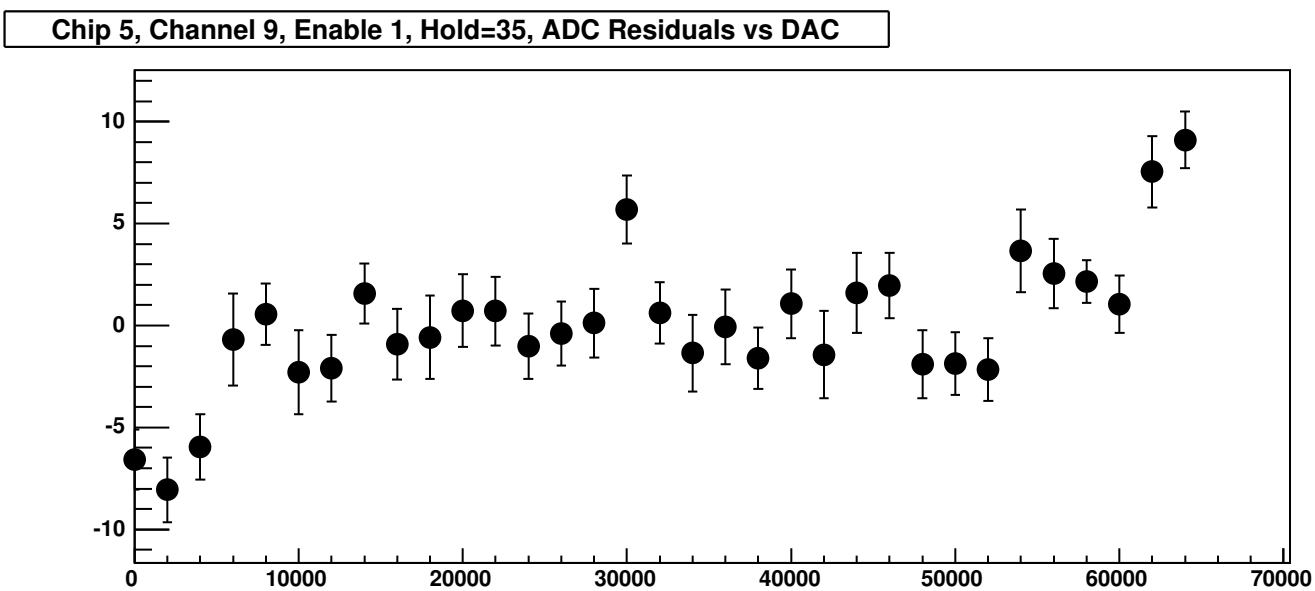


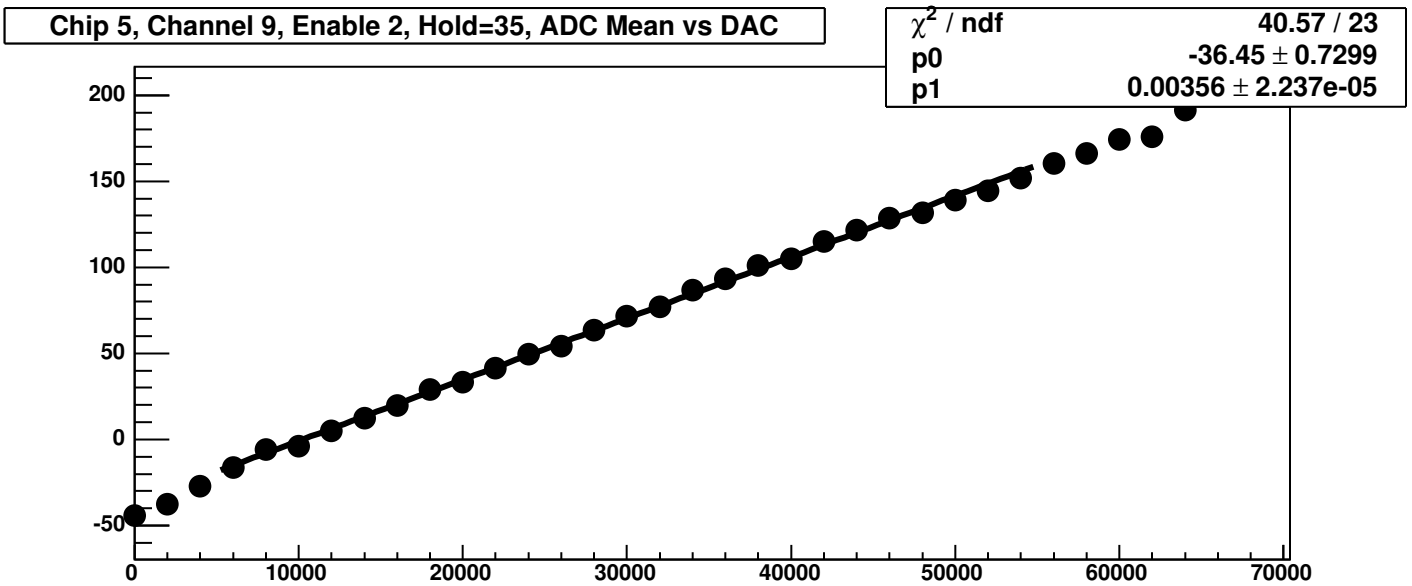
Chip 5, Channel 9, Enable 0, Hold=35, ADC Residuals vs DAC

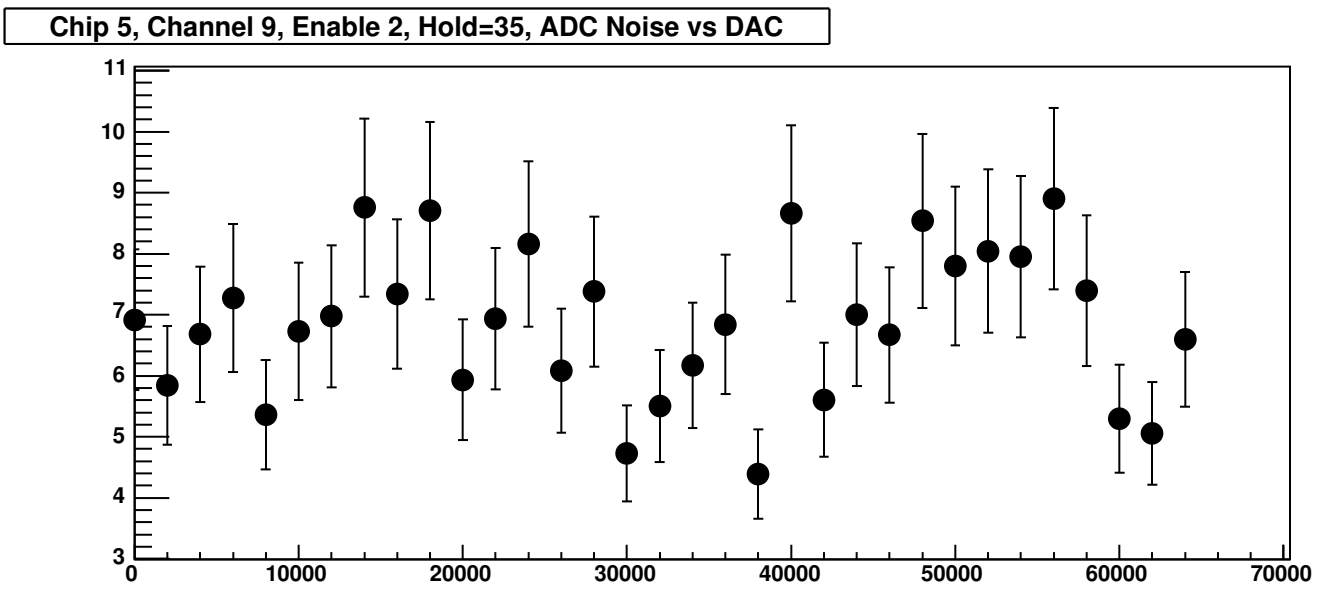


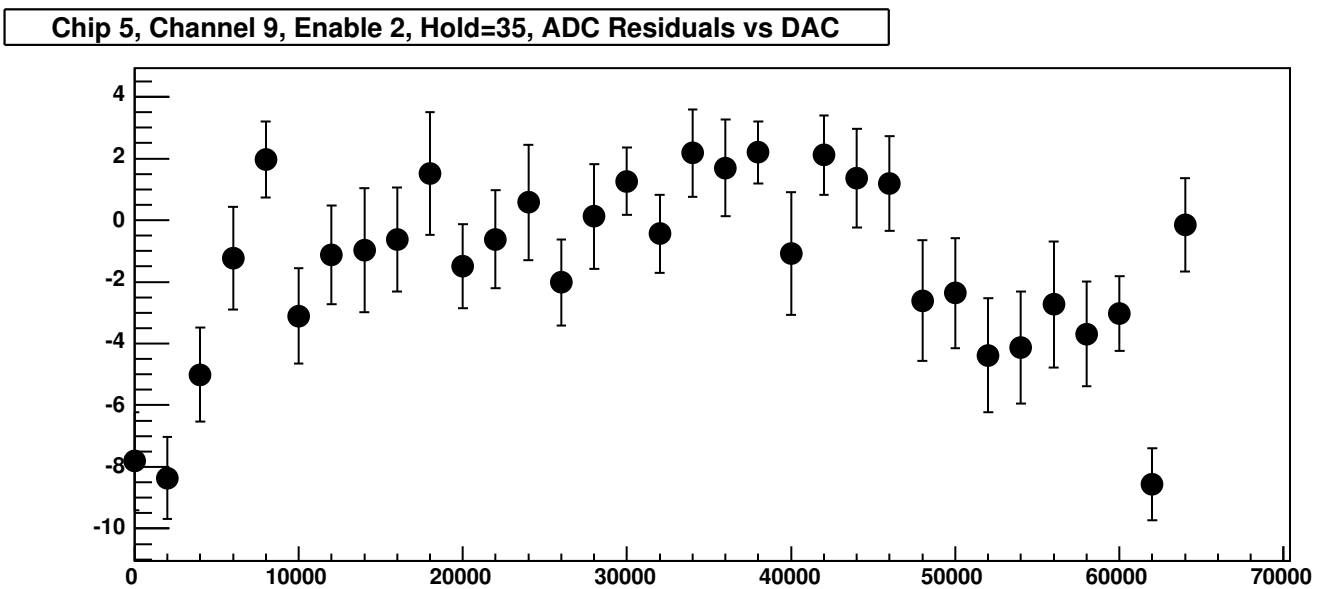

Chip 5, Channel 9, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 29.82 / 23
p0 -35.59 ± 0.8024
p1 0.004088 ± 2.405e-05
250
200
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000

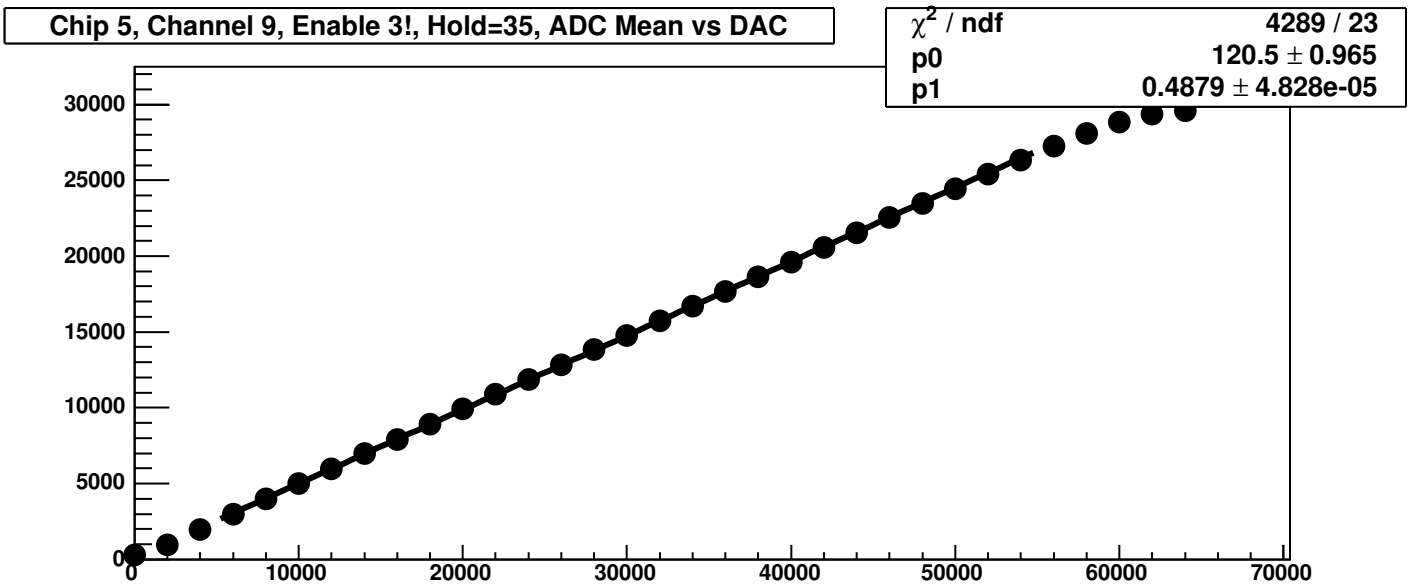

Chip 5, Channel 9, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

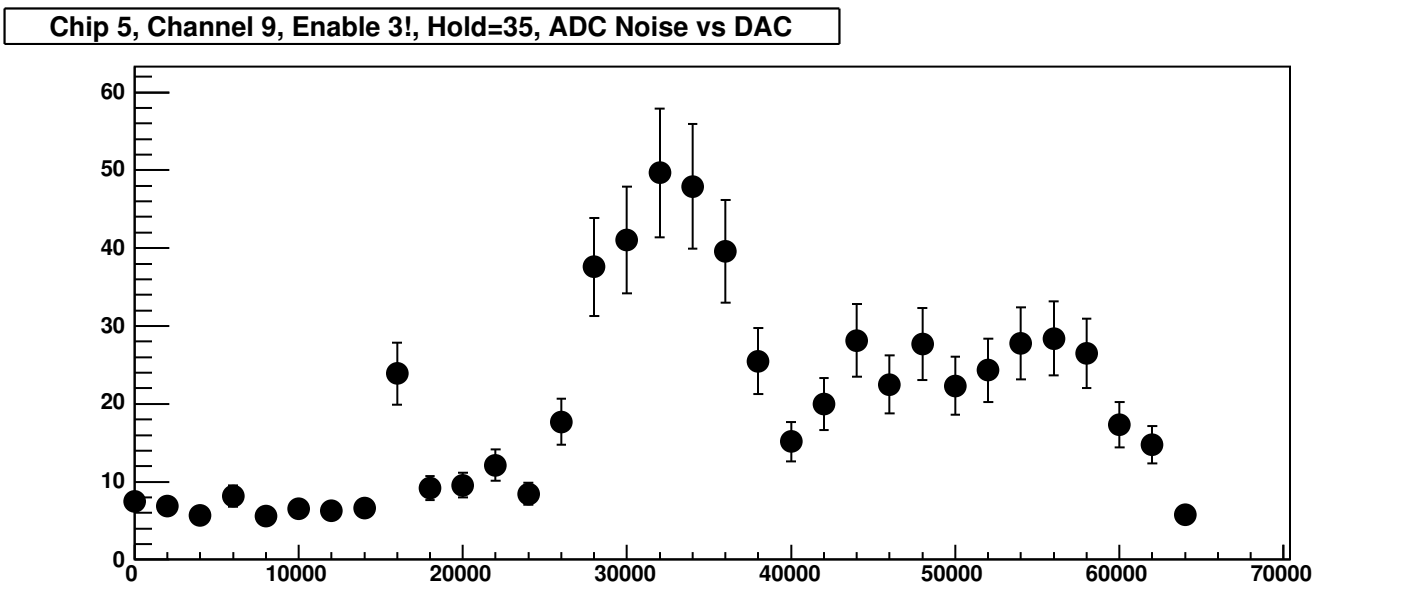

Chip 5, Channel 9, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

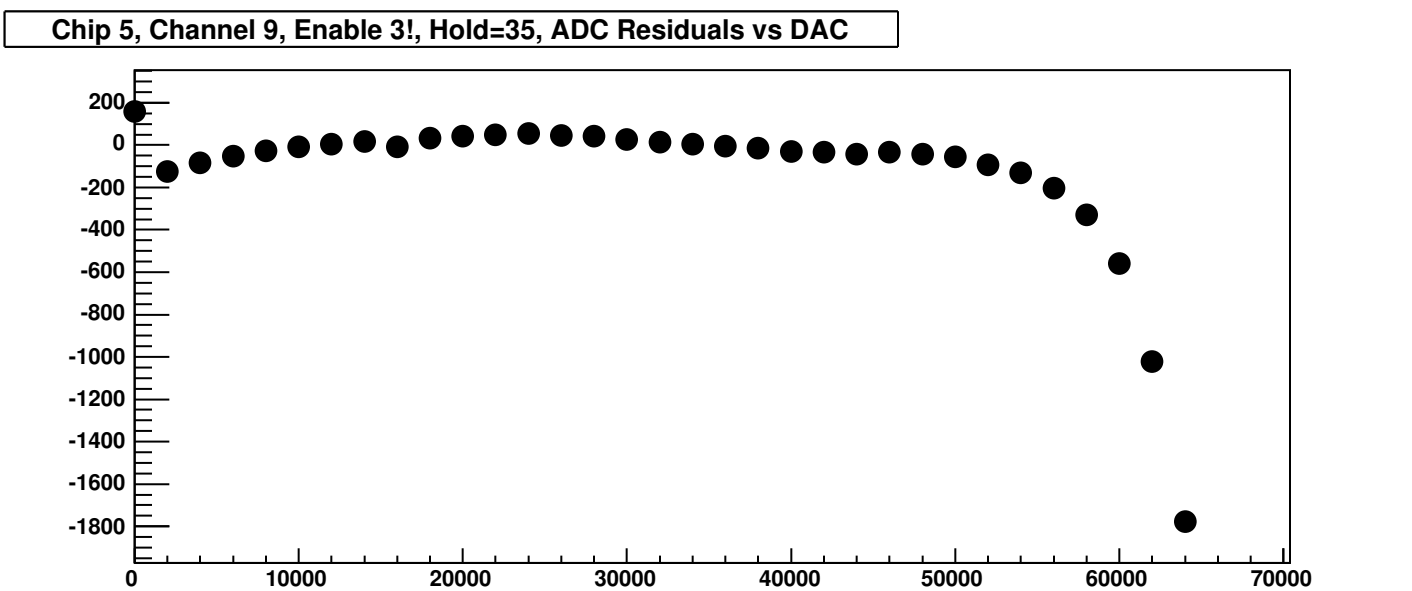

Chip 5, Channel 9, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 40.57 / 23
p0 -36.45 ± 0.7299
p1 0.00356 ± 2.237e-05
200
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 9, Enable 2, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

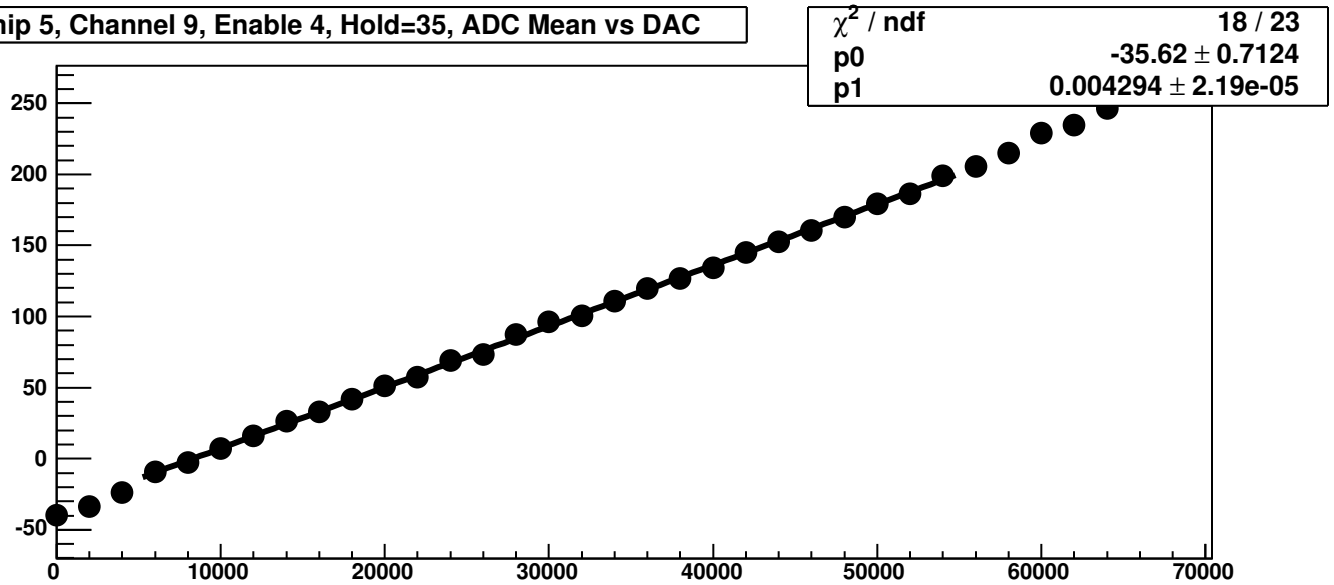

Chip 5, Channel 9, Enable 2, Hold=35, ADC Residuals vs DAC
4
2
0
-2
-4
-6
-8
-10
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 9, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 4289 / 23
p0 120.5 ± 0.965
p1 0.4879 ± 4.828e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

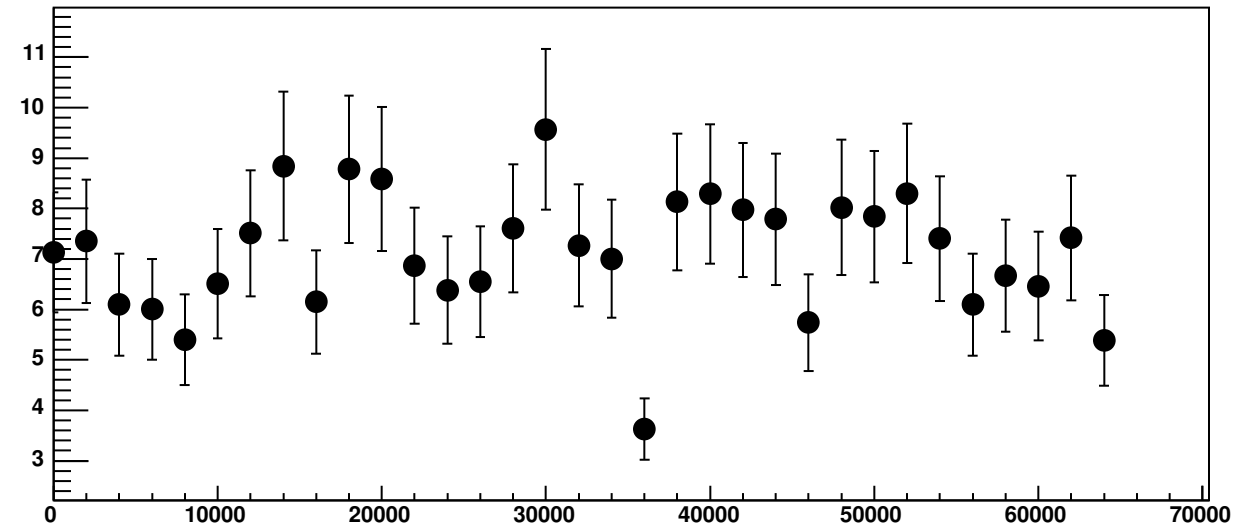

Chip 5, Channel 9, Enable 3!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 9, Enable 3!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
10000
20000
30000
40000
50000
60000
70000

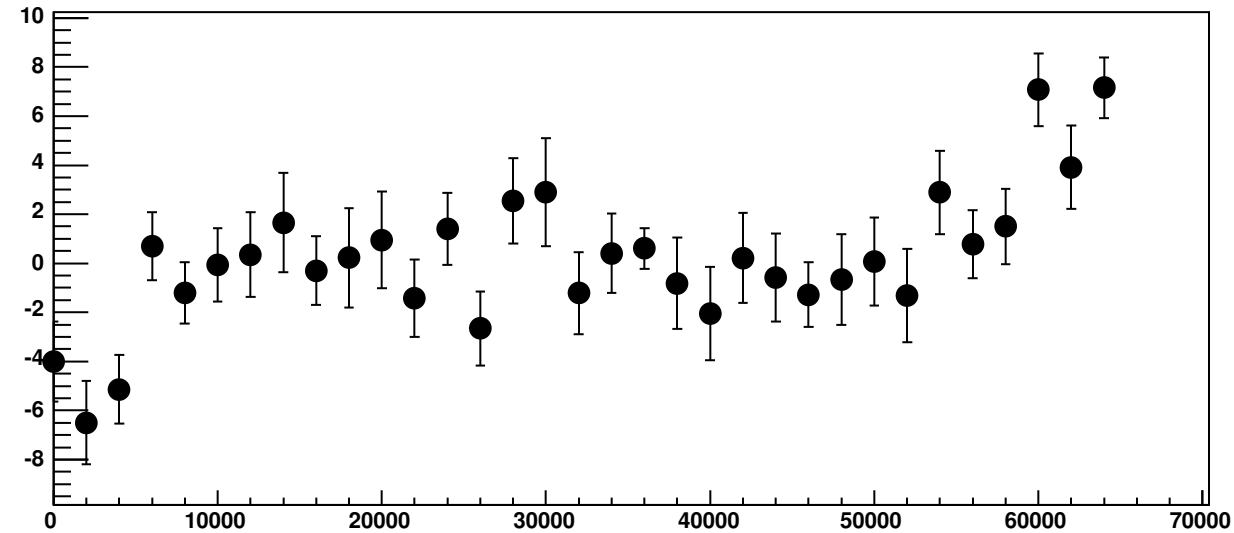
Chip 5, Channel 9, Enable 4, Hold=35, ADC Mean vs DAC



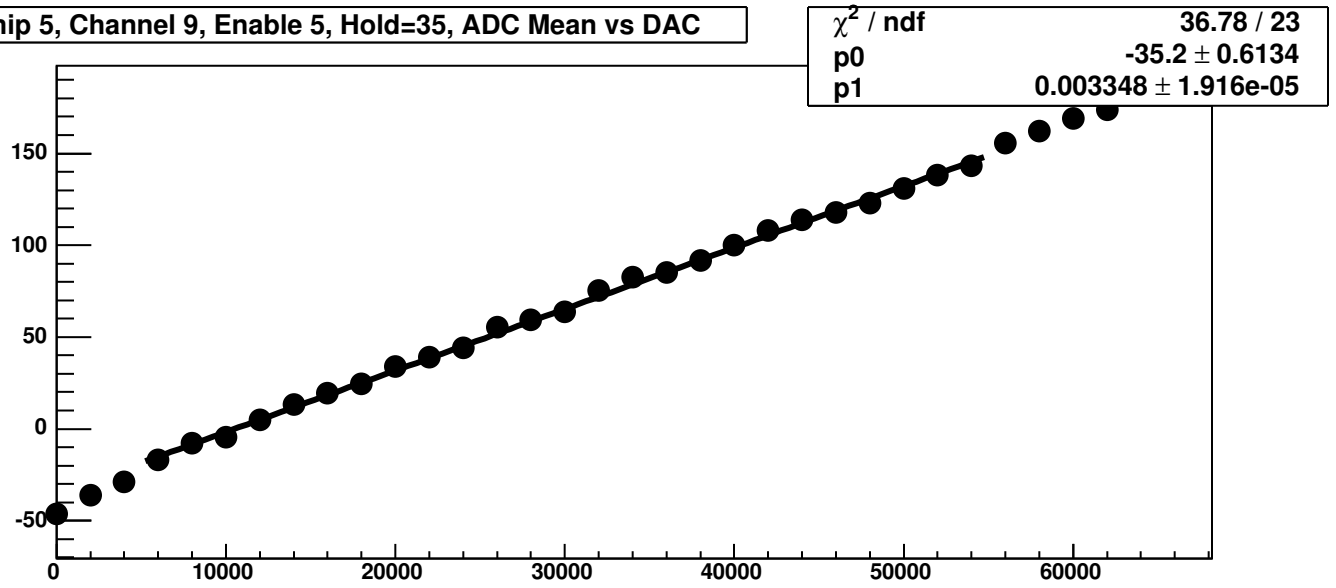
Chip 5, Channel 9, Enable 4, Hold=35, ADC Noise vs DAC



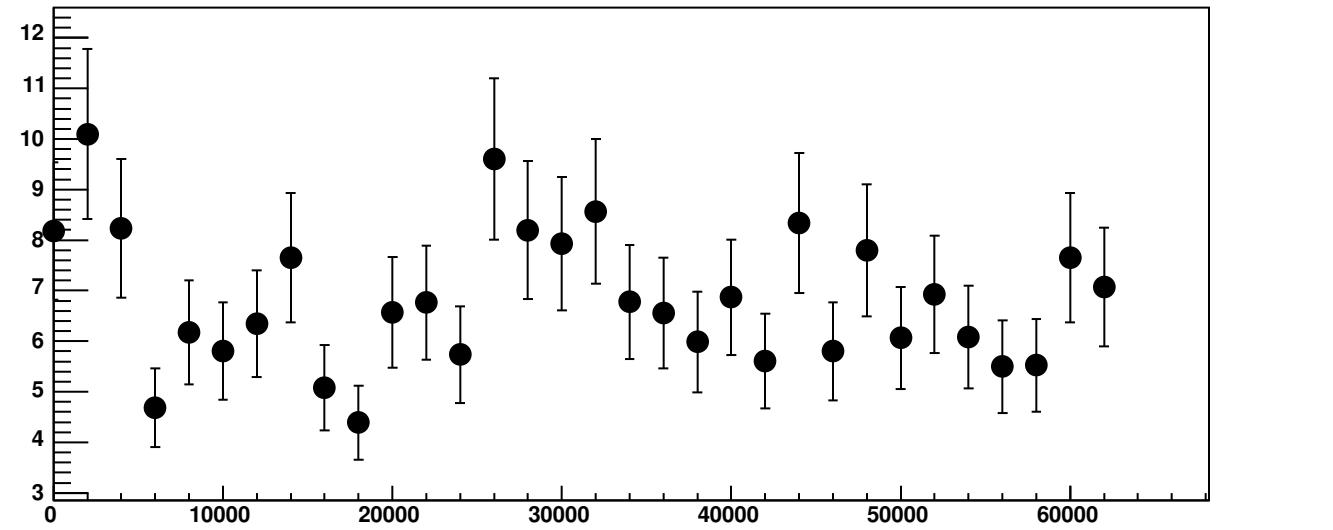
Chip 5, Channel 9, Enable 4, Hold=35, ADC Residuals vs DAC



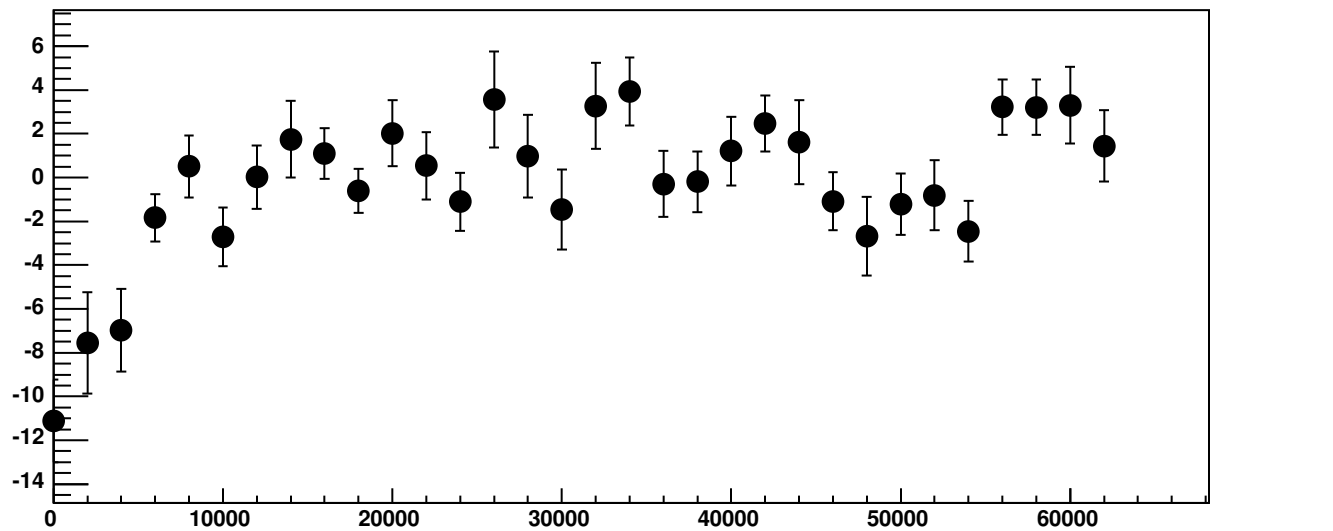
Chip 5, Channel 9, Enable 5, Hold=35, ADC Mean vs DAC

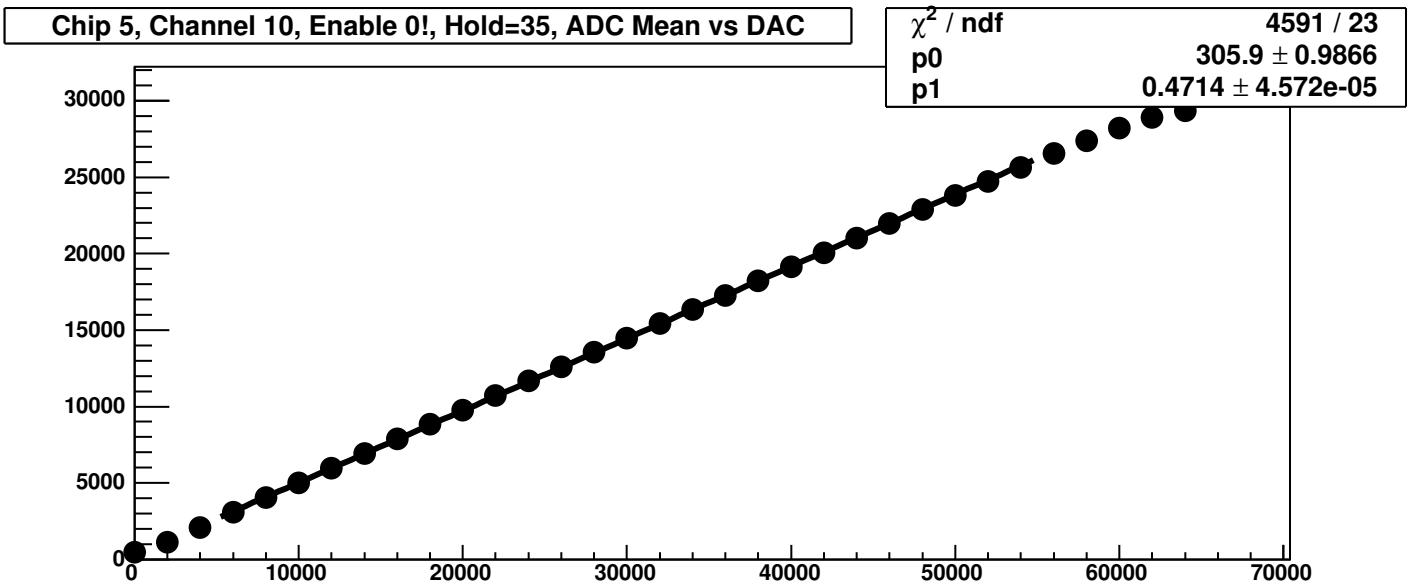


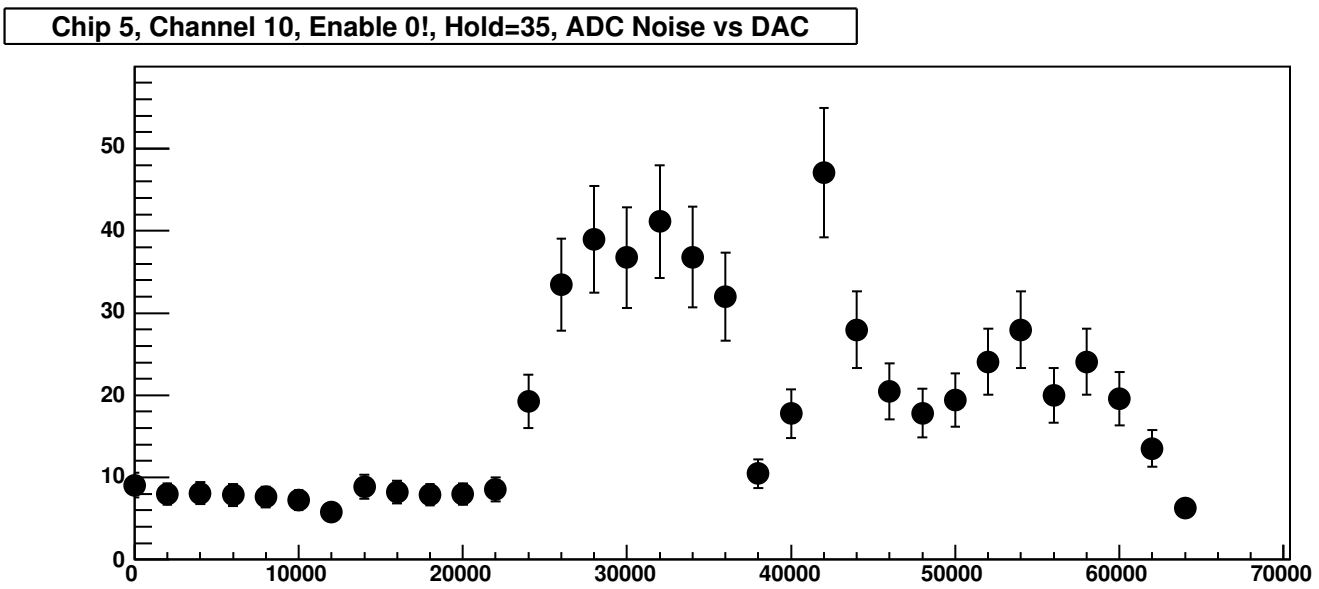
Chip 5, Channel 9, Enable 5, Hold=35, ADC Noise vs DAC

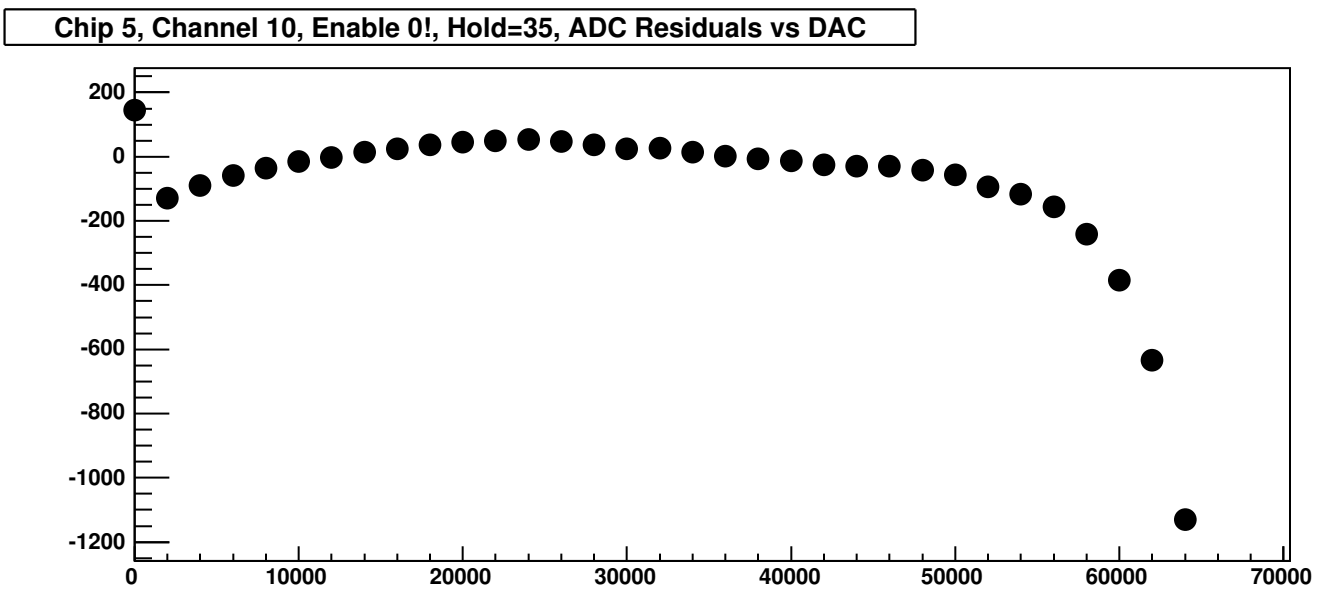


Chip 5, Channel 9, Enable 5, Hold=35, ADC Residuals vs DAC

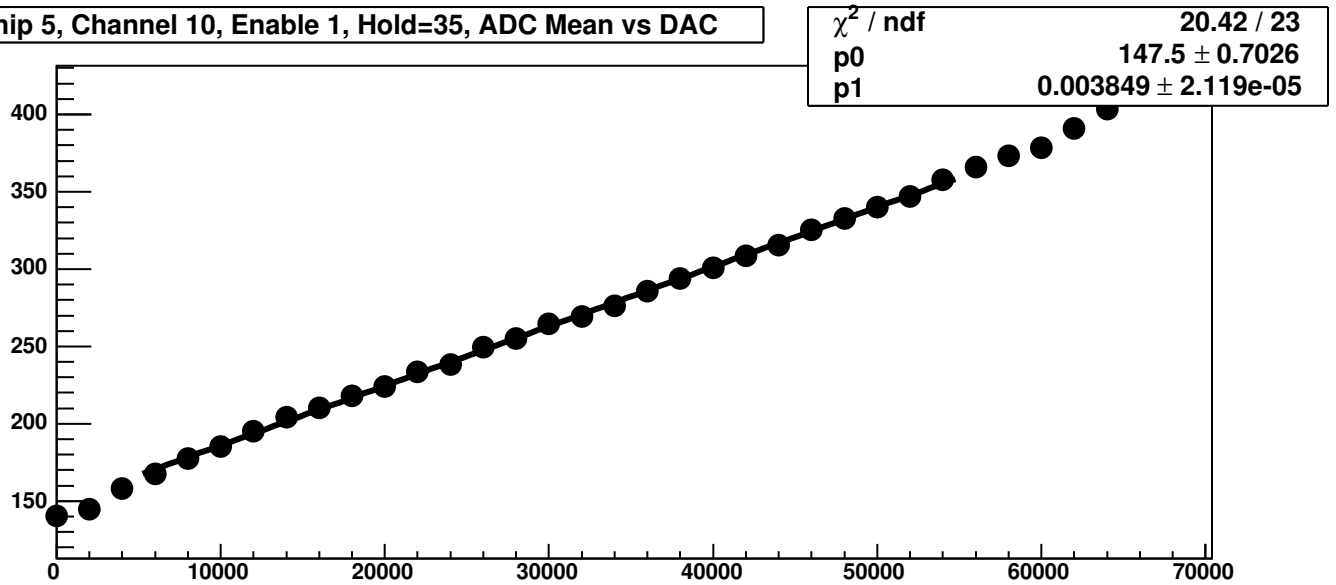



Chip 5, Channel 10, Enable 0!, Hold=35, ADC Mean vs DAC
χ² / ndf 4591 / 23
p0 305.9 ± 0.9866
p1 0.4714 ± 4.572e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

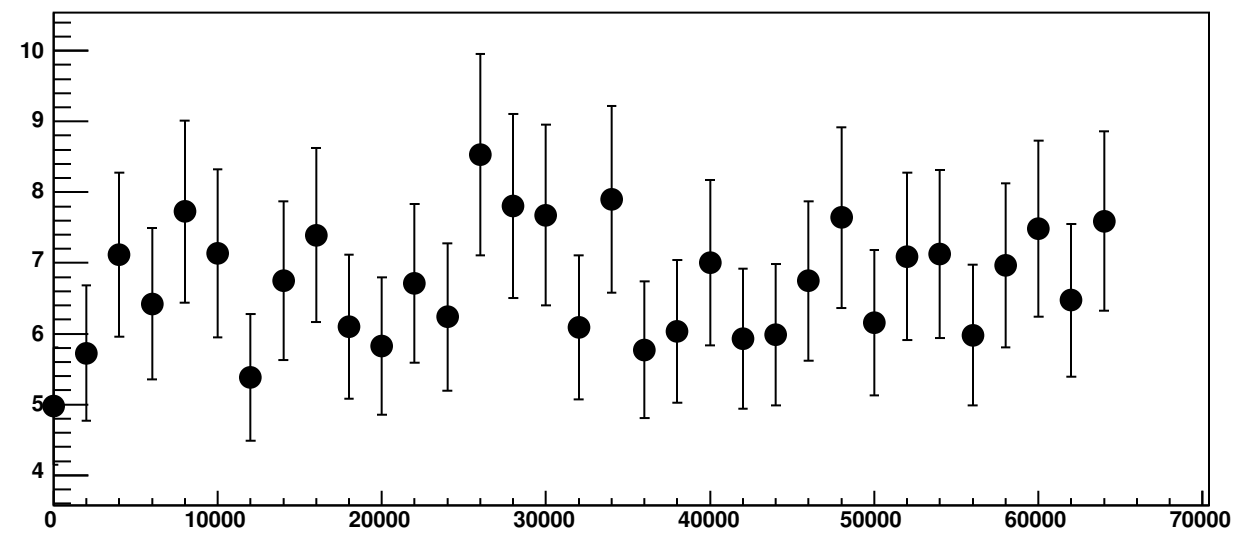

Chip 5, Channel 10, Enable 0!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 10, Enable 0!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
0
10000
20000
30000
40000
50000
60000
70000

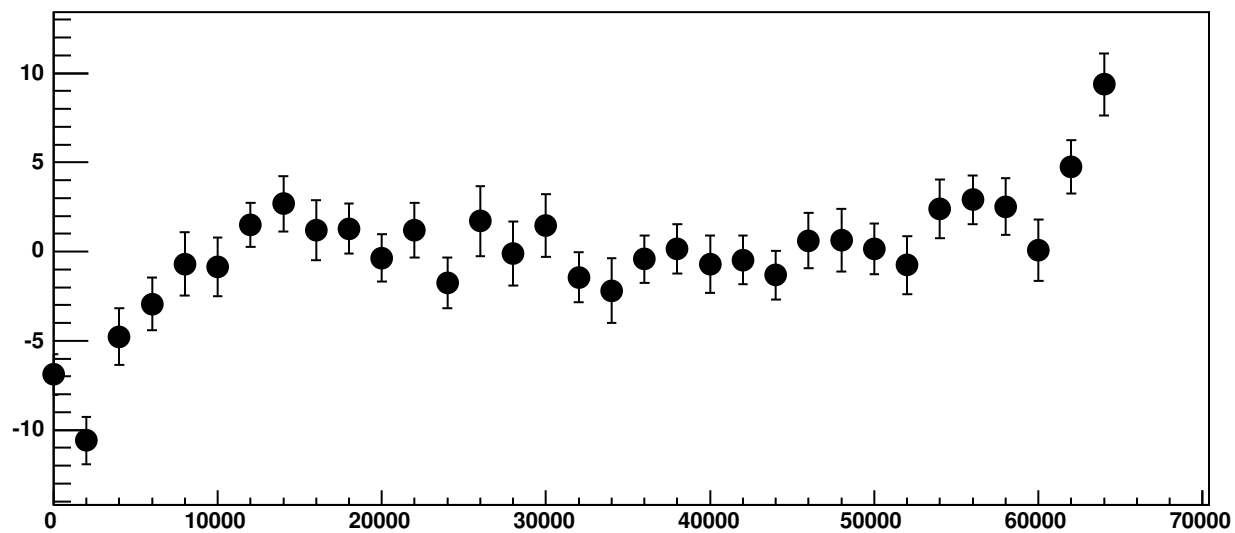
Chip 5, Channel 10, Enable 1, Hold=35, ADC Mean vs DAC

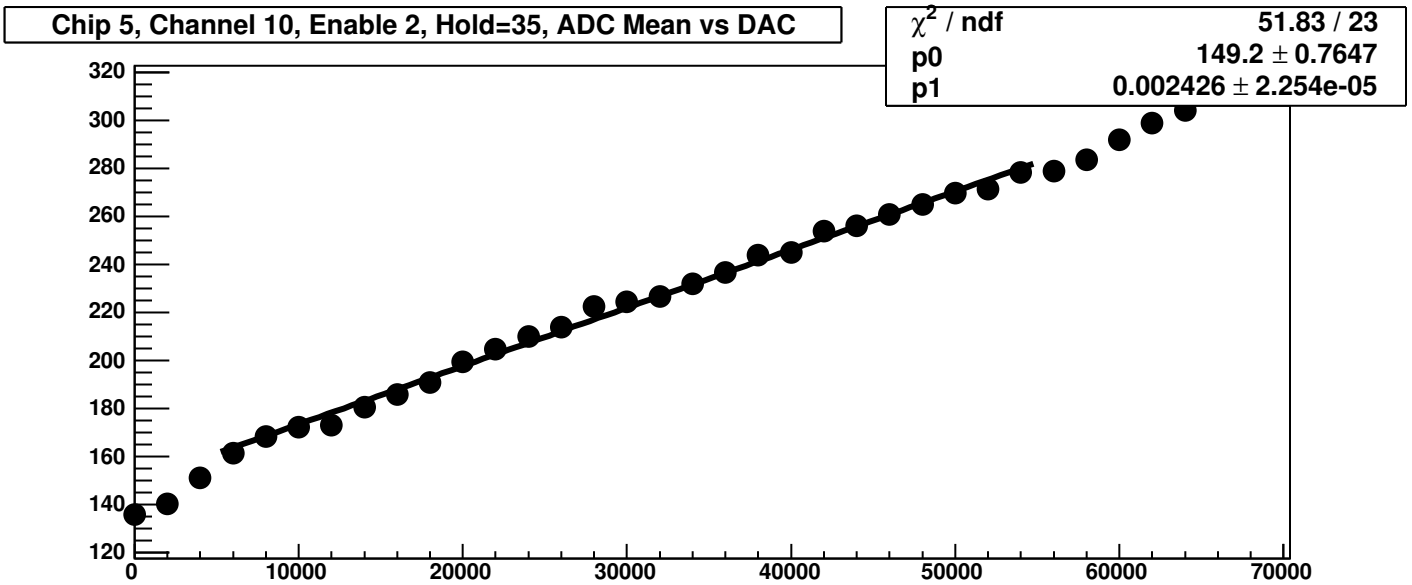


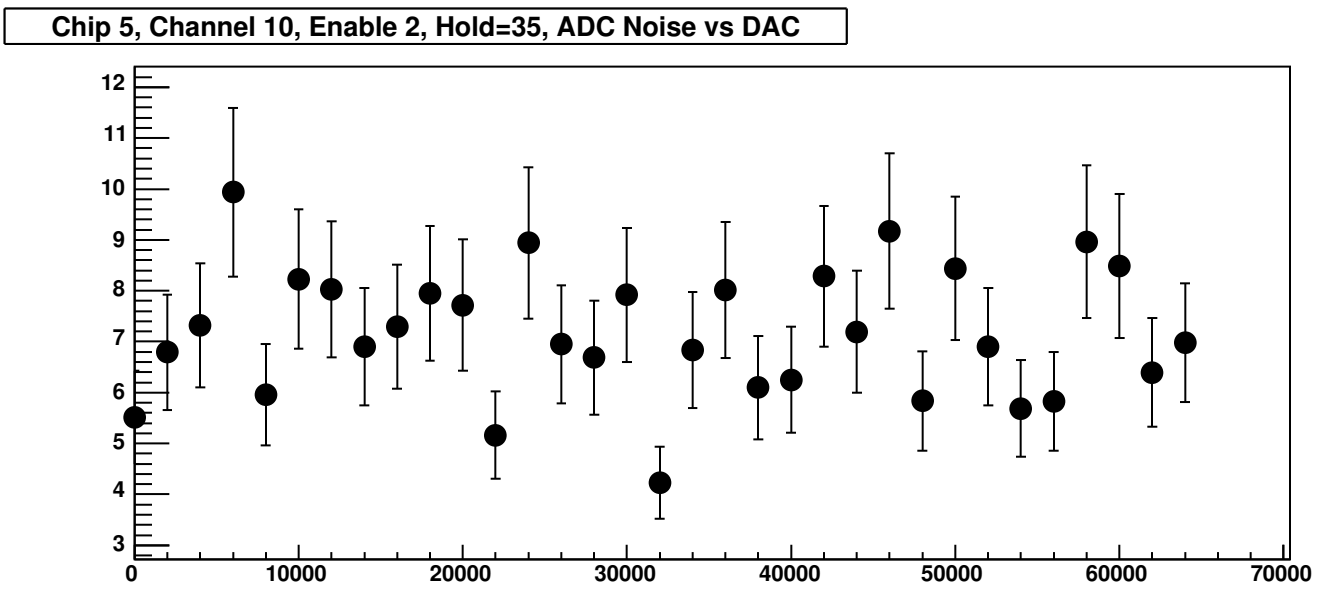
Chip 5, Channel 10, Enable 1, Hold=35, ADC Noise vs DAC

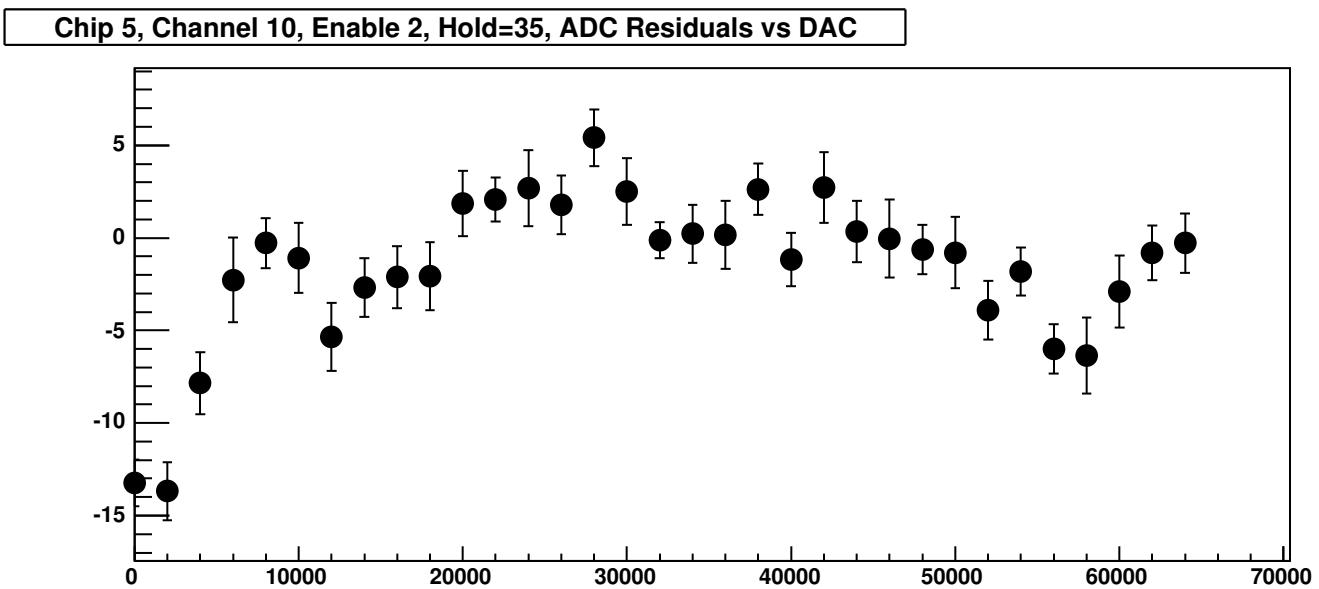


Chip 5, Channel 10, Enable 1, Hold=35, ADC Residuals vs DAC

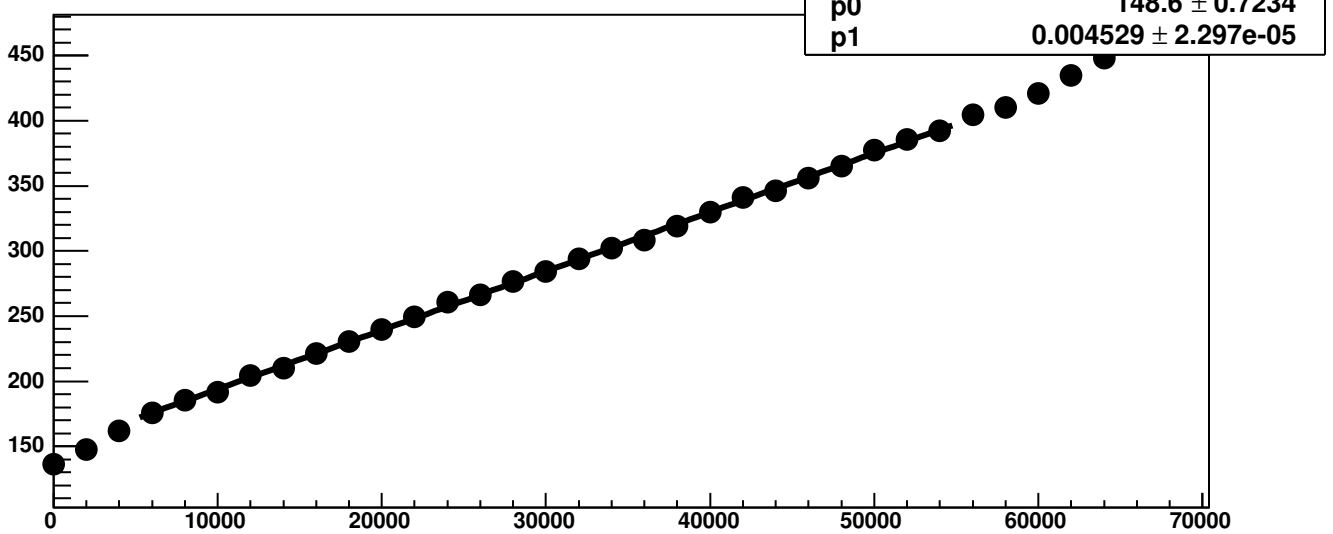



Chip 5, Channel 10, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 51.83 / 23
p0 149.2 ± 0.7647
p1 0.002426 ± 2.254e-05
320
300
280
260
240
220
200
180
160
140
120
0
10000
20000
30000
40000
50000
60000
70000

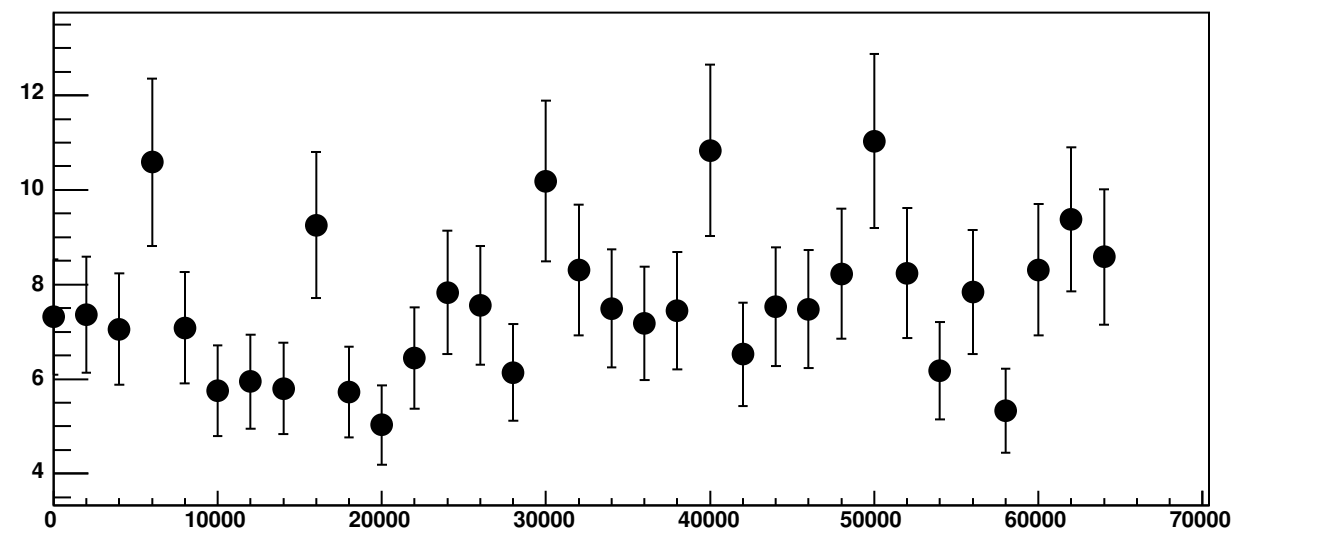

Chip 5, Channel 10, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 10, Enable 2, Hold=35, ADC Residuals vs DAC
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

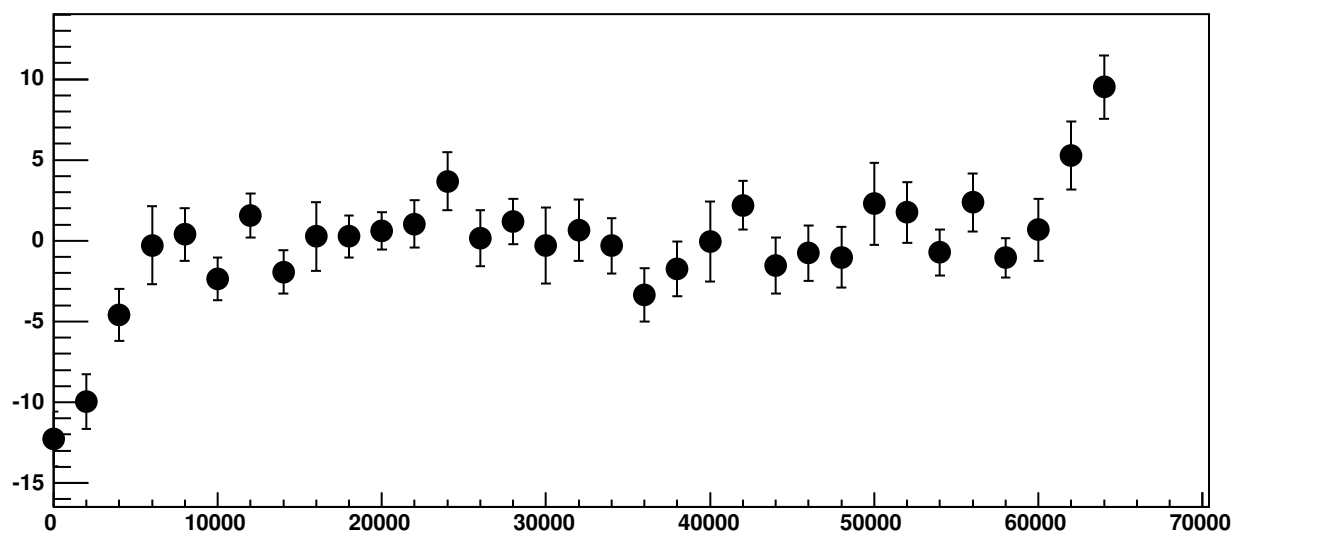
Chip 5, Channel 10, Enable 3, Hold=35, ADC Mean vs DAC

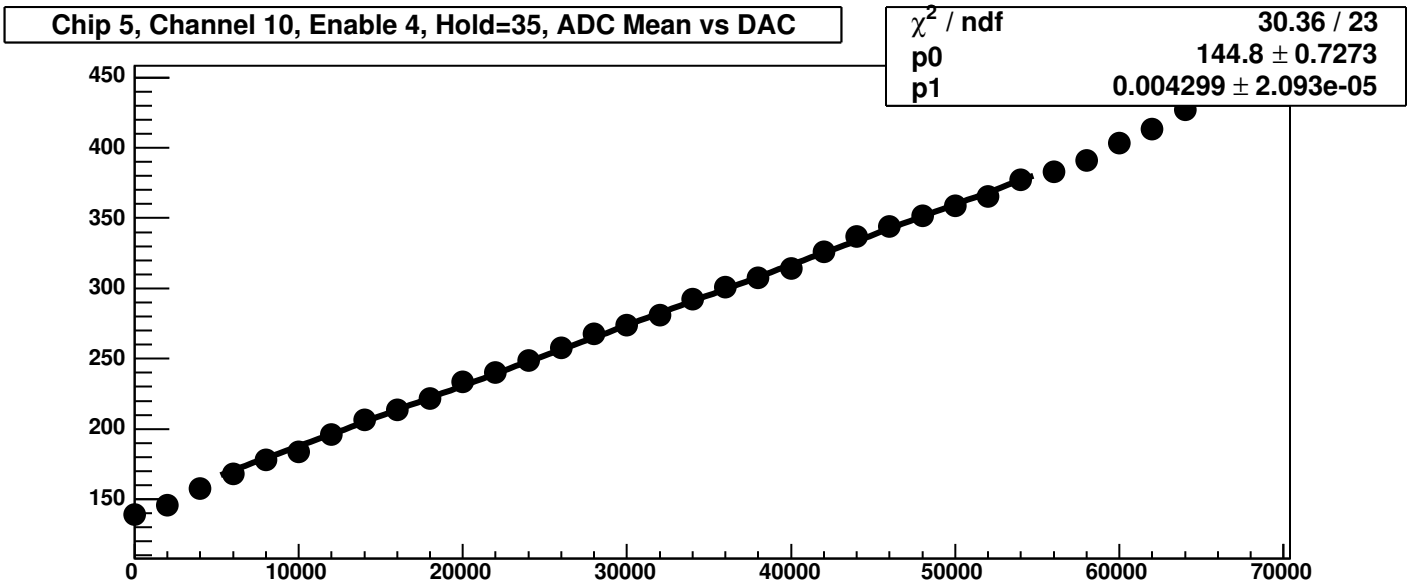


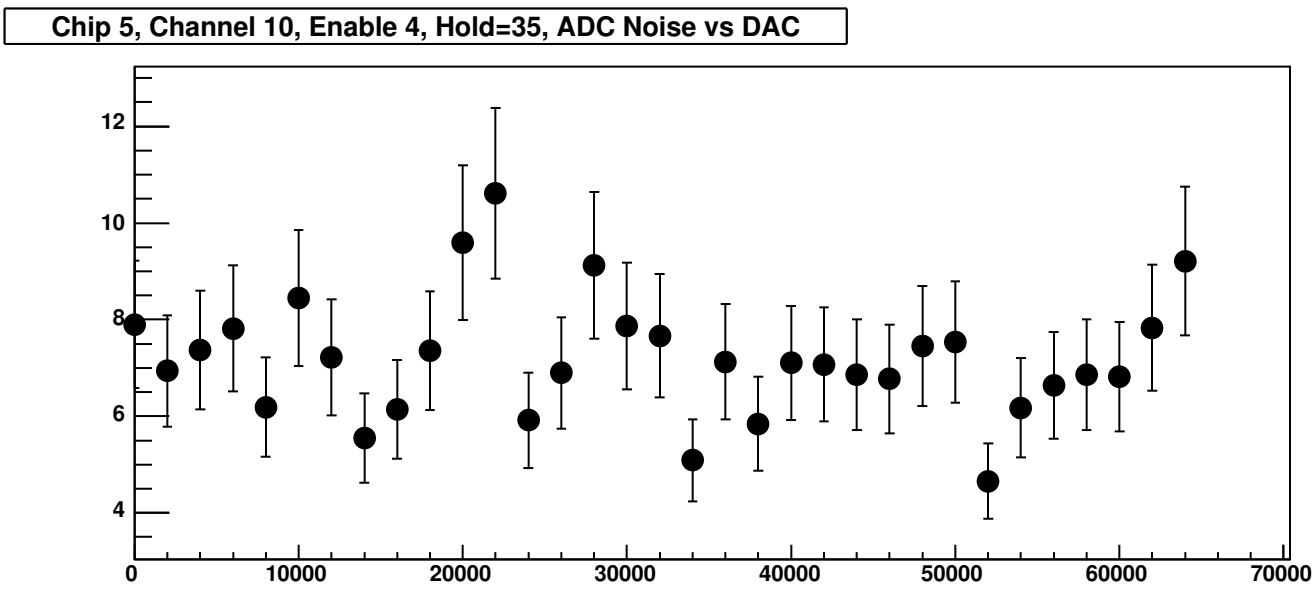
Chip 5, Channel 10, Enable 3, Hold=35, ADC Noise vs DAC

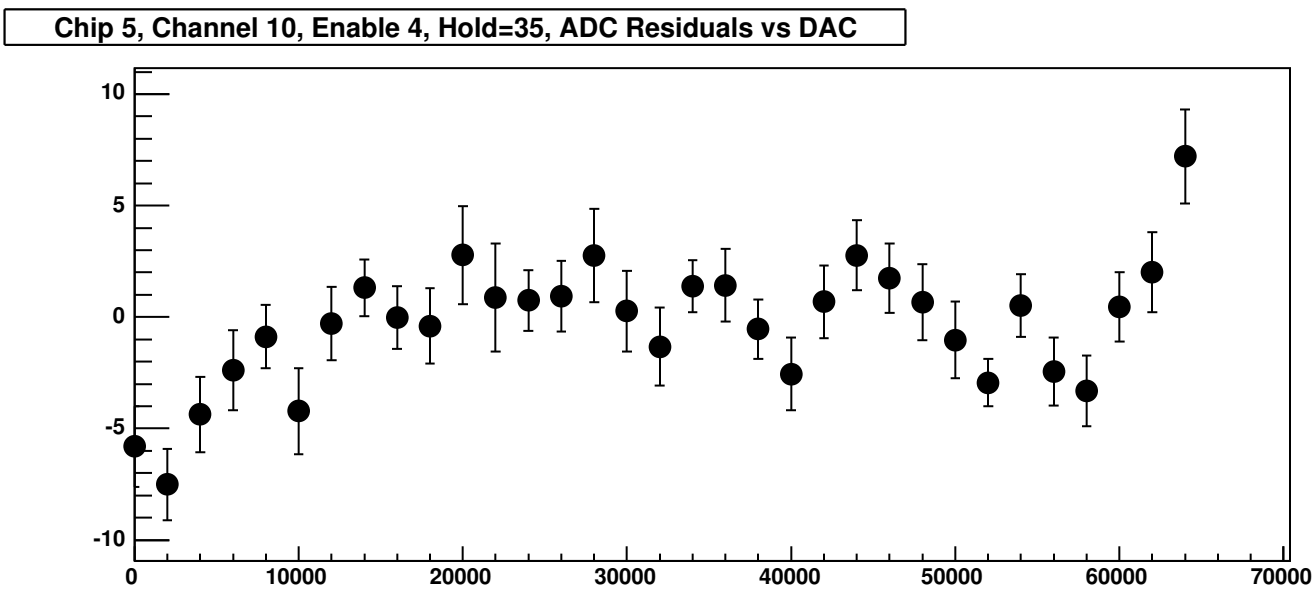


Chip 5, Channel 10, Enable 3, Hold=35, ADC Residuals vs DAC

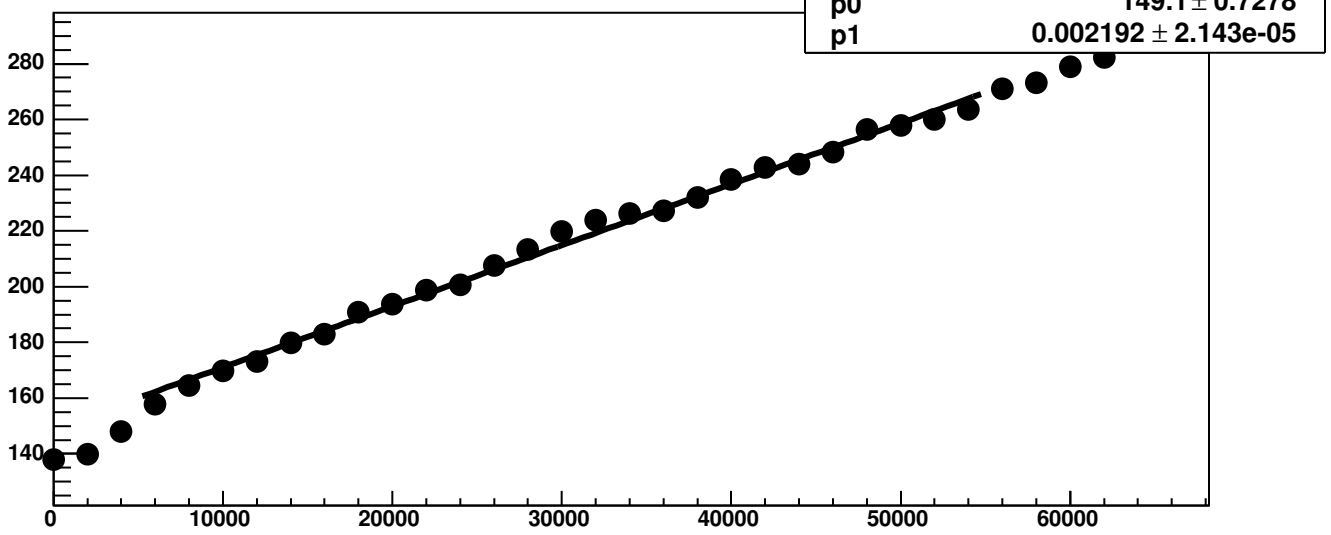



Chip 5, Channel 10, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 30.36 / 23
p0 144.8 ± 0.7273
p1 0.004299 ± 2.093e-05
450
400
350
300
250
200
150
0
10000
20000
30000
40000
50000
60000
70000

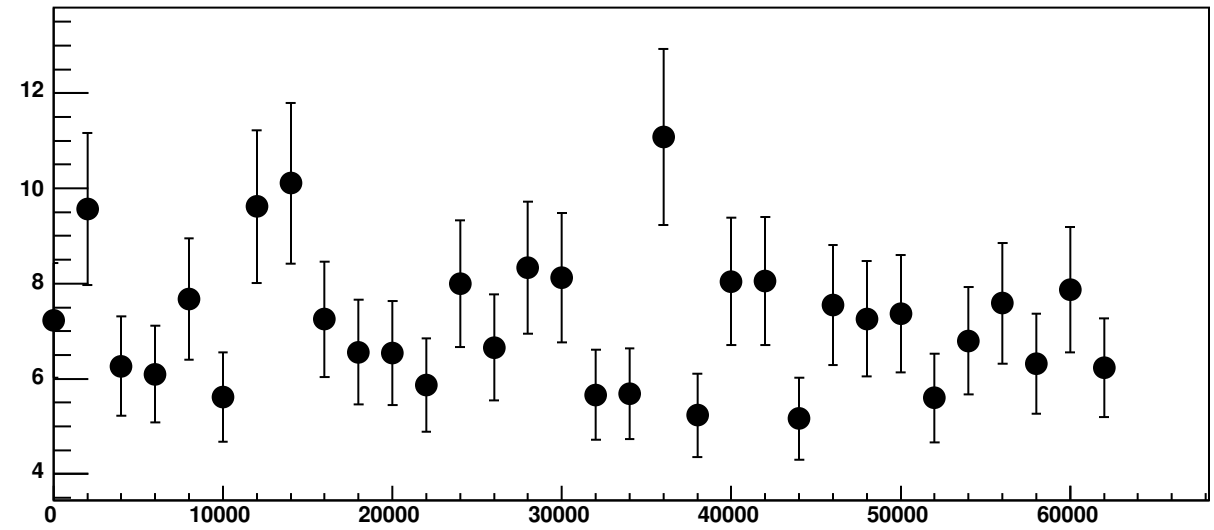

Chip 5, Channel 10, Enable 4, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 10, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

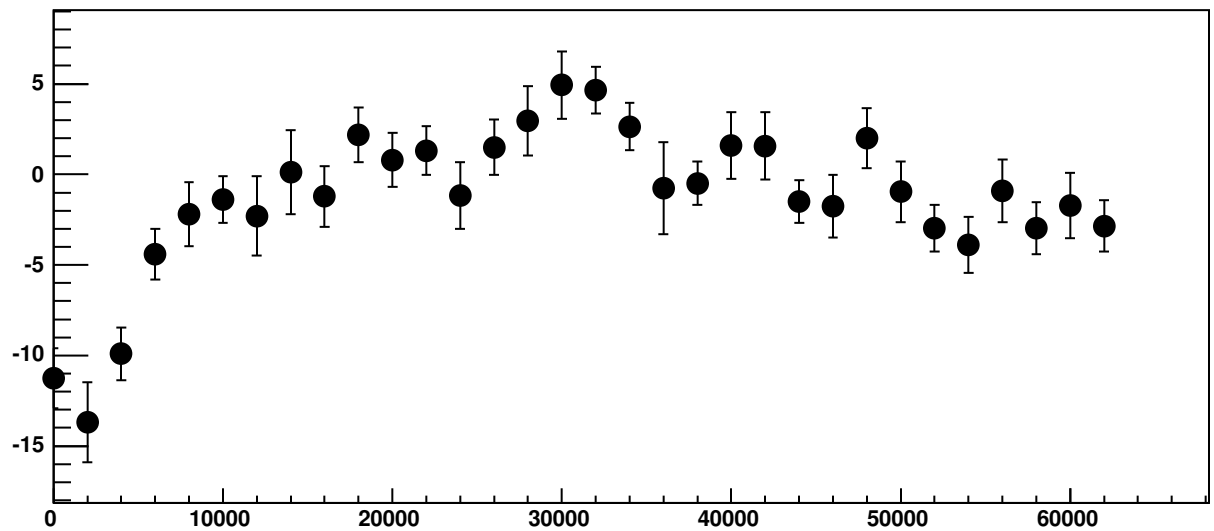
Chip 5, Channel 10, Enable 5, Hold=35, ADC Mean vs DAC



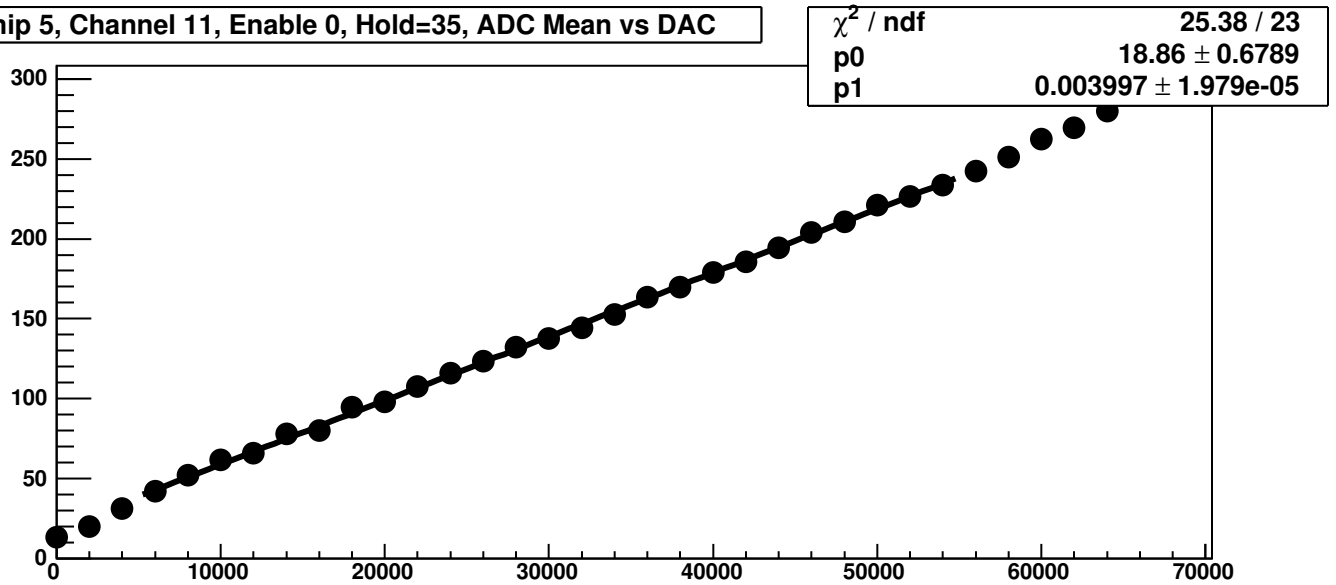
Chip 5, Channel 10, Enable 5, Hold=35, ADC Noise vs DAC



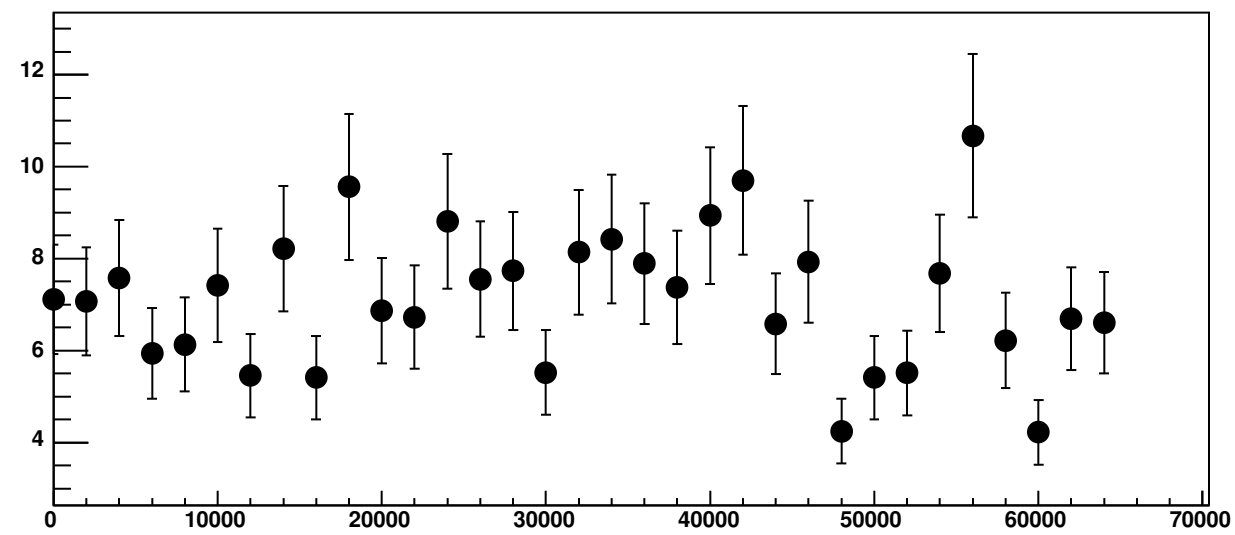
Chip 5, Channel 10, Enable 5, Hold=35, ADC Residuals vs DAC



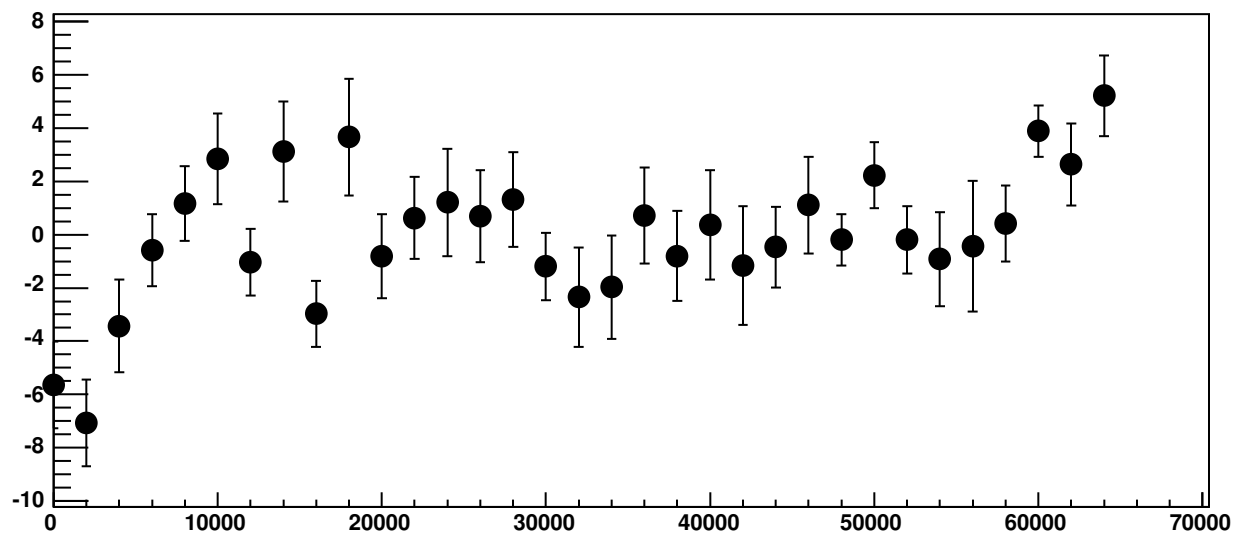
Chip 5, Channel 11, Enable 0, Hold=35, ADC Mean vs DAC

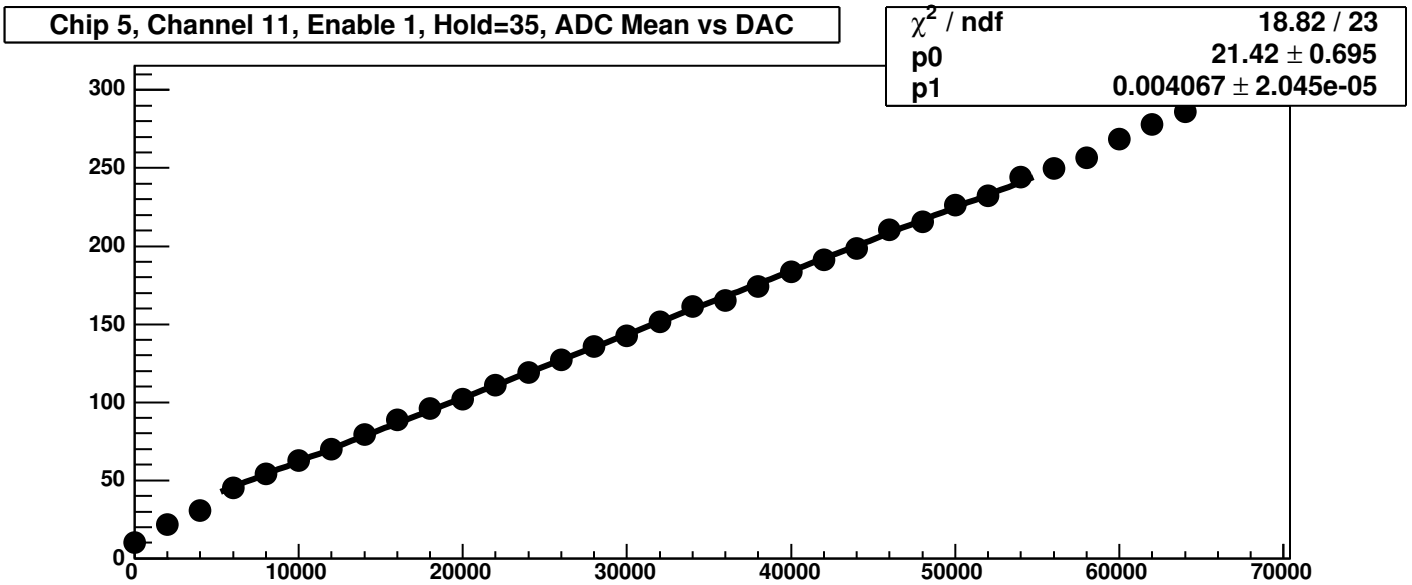


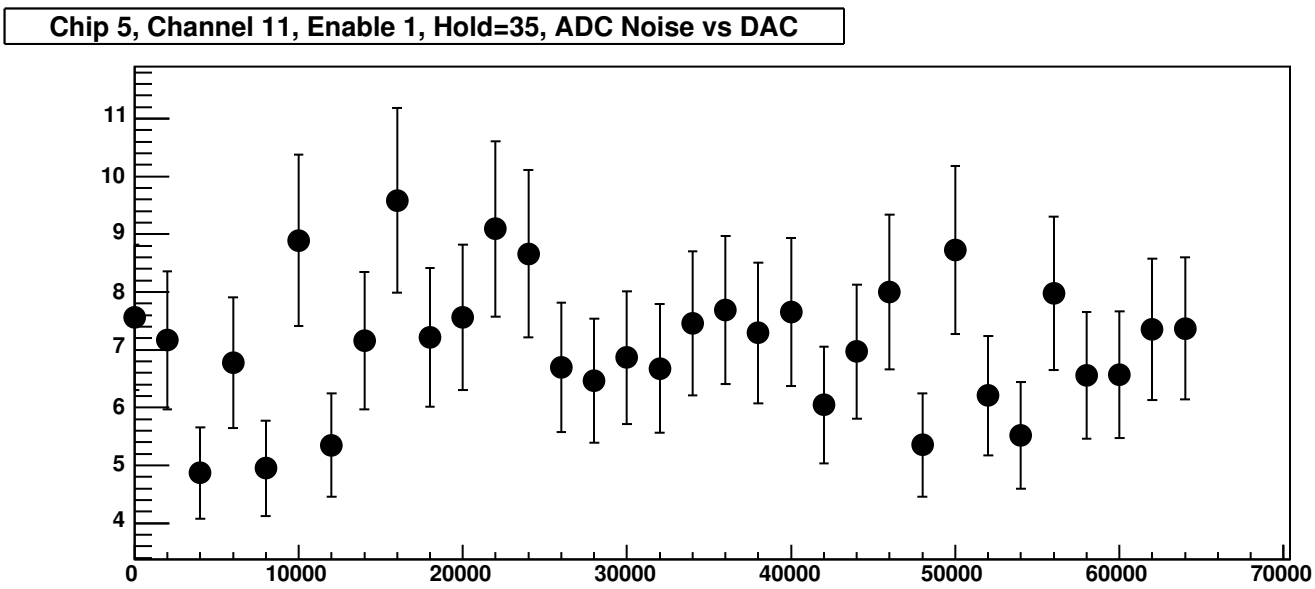
Chip 5, Channel 11, Enable 0, Hold=35, ADC Noise vs DAC

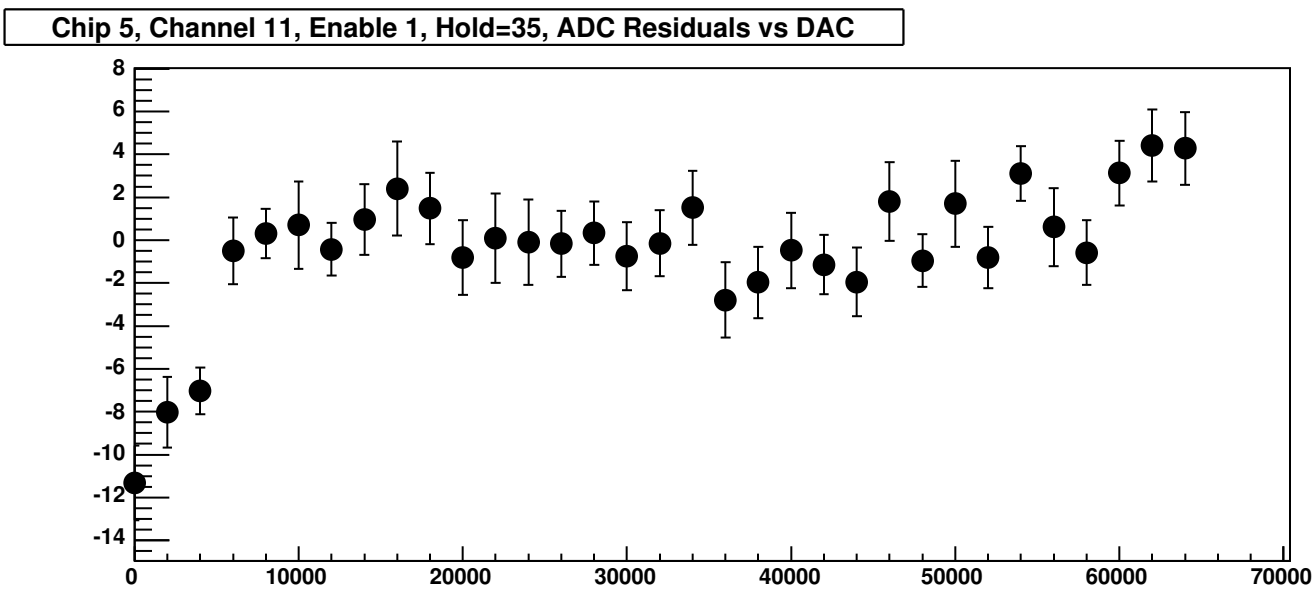


Chip 5, Channel 11, Enable 0, Hold=35, ADC Residuals vs DAC

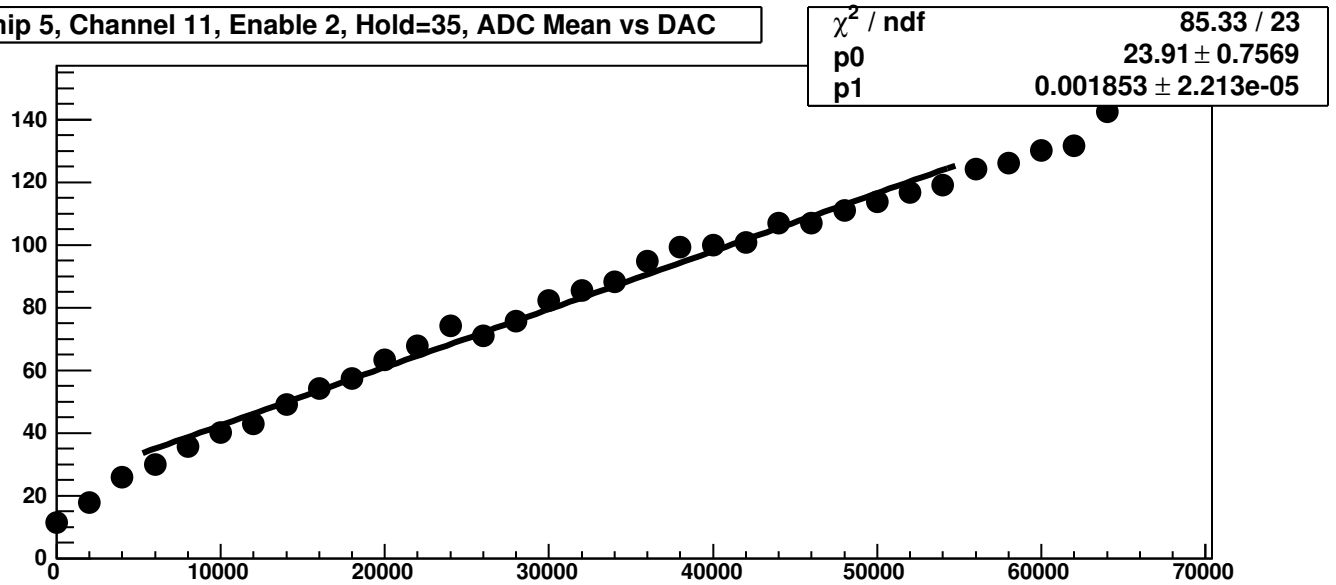



Chip 5, Channel 11, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 18.82 / 23
p0 21.42 ± 0.695
p1 0.004067 ± 2.045e-05
300
250
200
150
100
50
0
0
10000
20000
30000
40000
50000
60000
70000

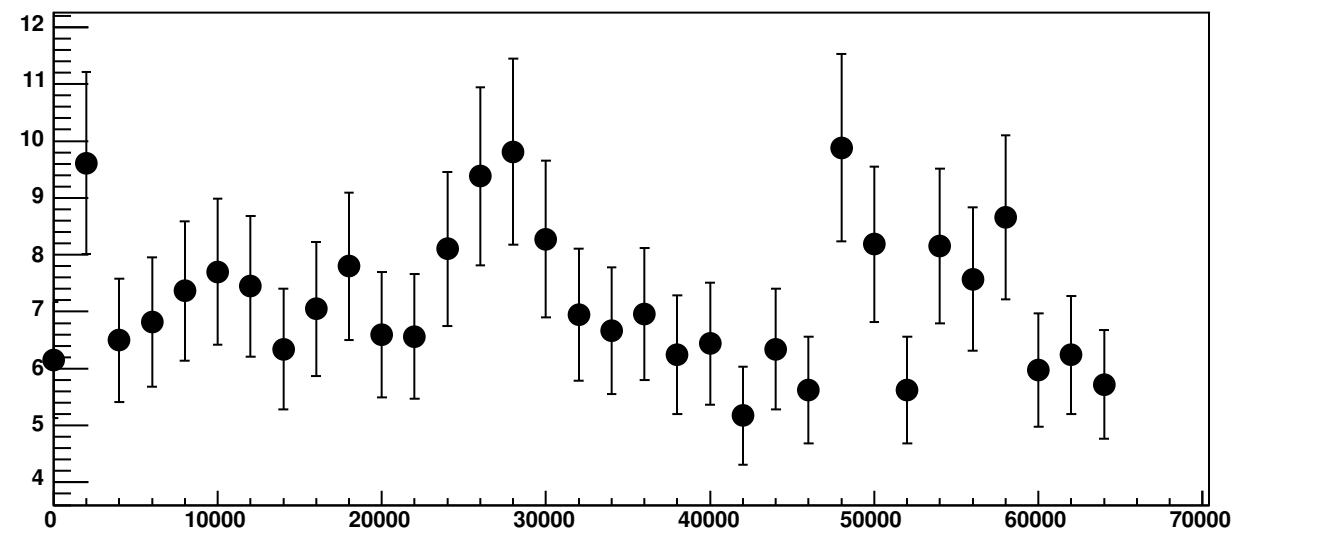

Chip 5, Channel 11, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 11, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
-14
0
10000
20000
30000
40000
50000
60000
70000

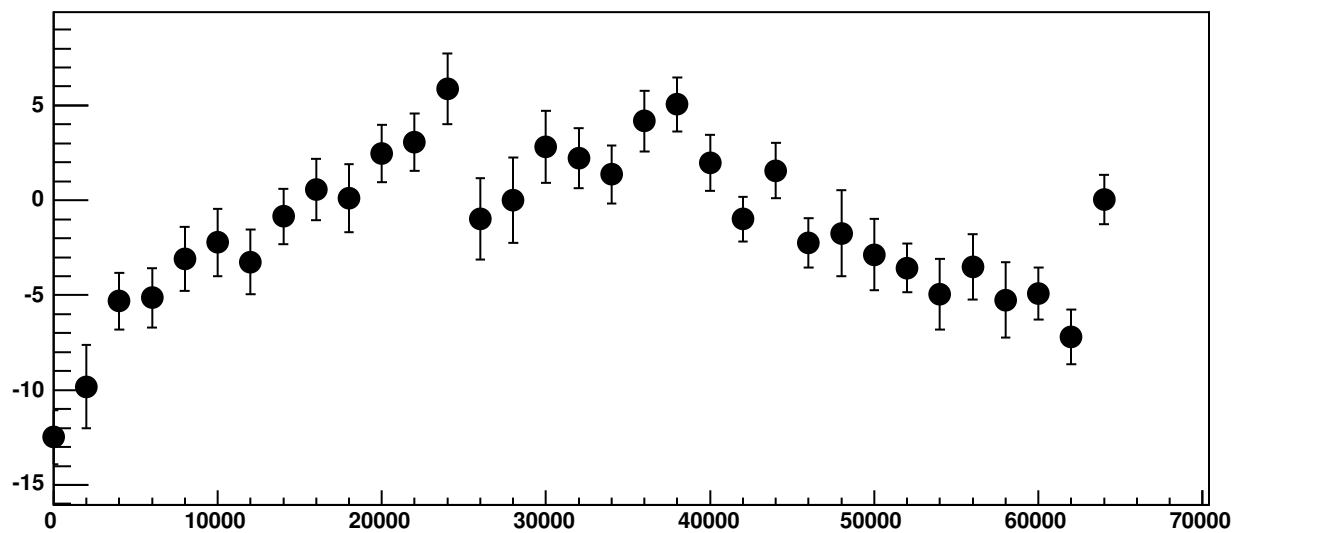
Chip 5, Channel 11, Enable 2, Hold=35, ADC Mean vs DAC

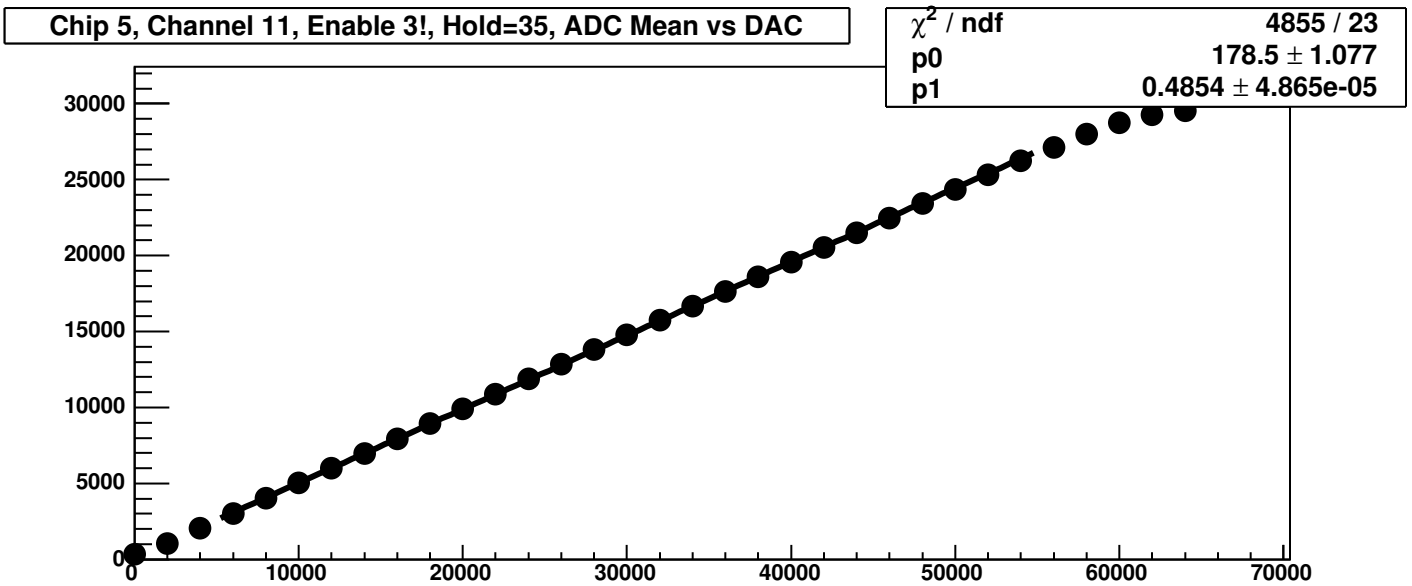


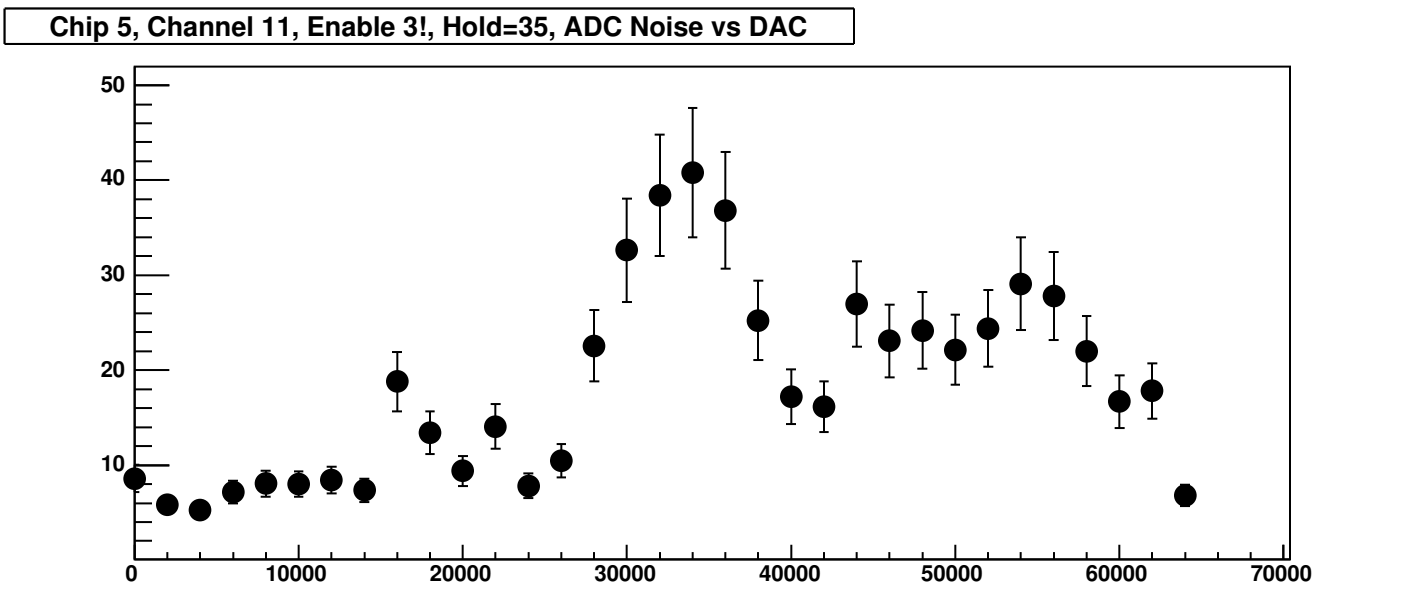
Chip 5, Channel 11, Enable 2, Hold=35, ADC Noise vs DAC

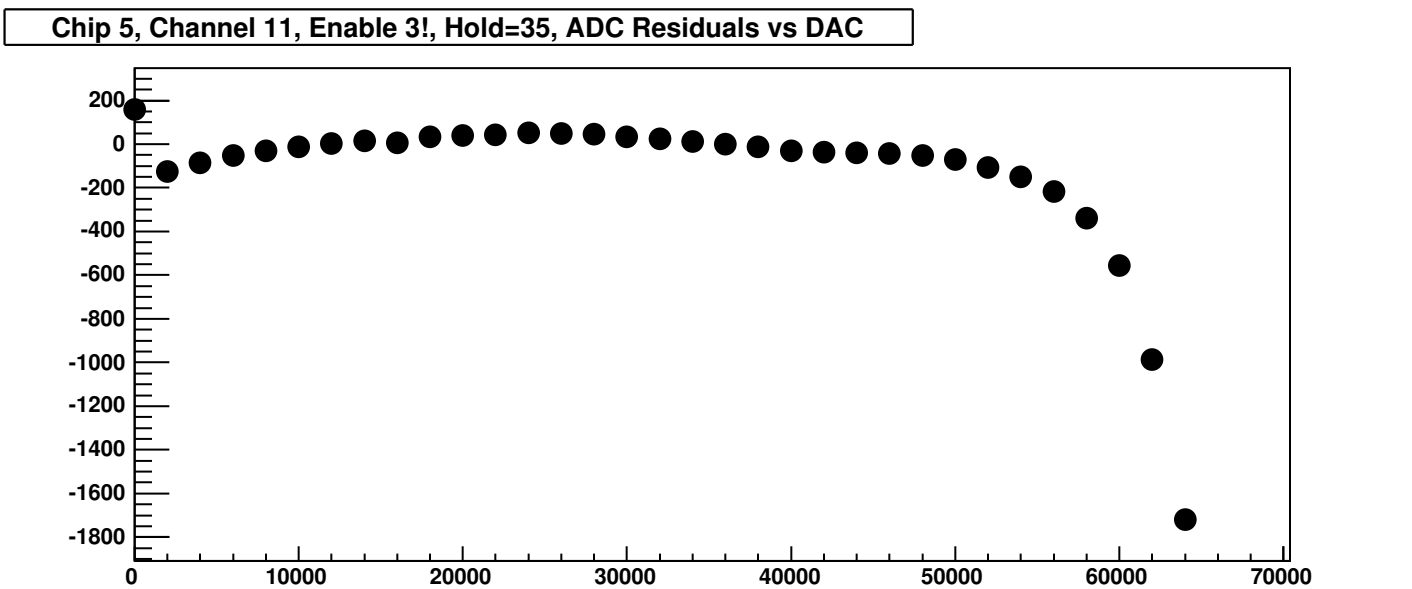


Chip 5, Channel 11, Enable 2, Hold=35, ADC Residuals vs DAC

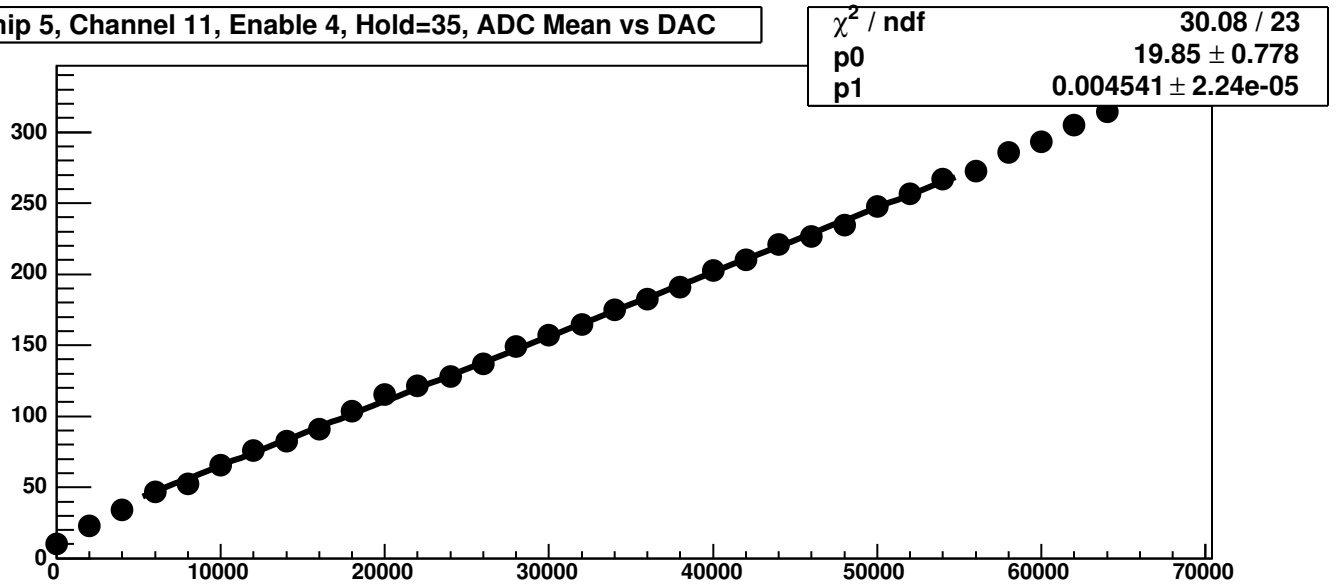



Chip 5, Channel 11, Enable 3!, Hold=35, ADC Mean vs DAC
χ² / ndf 4855 / 23
p0 178.5 ± 1.077
p1 0.4854 ± 4.865e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

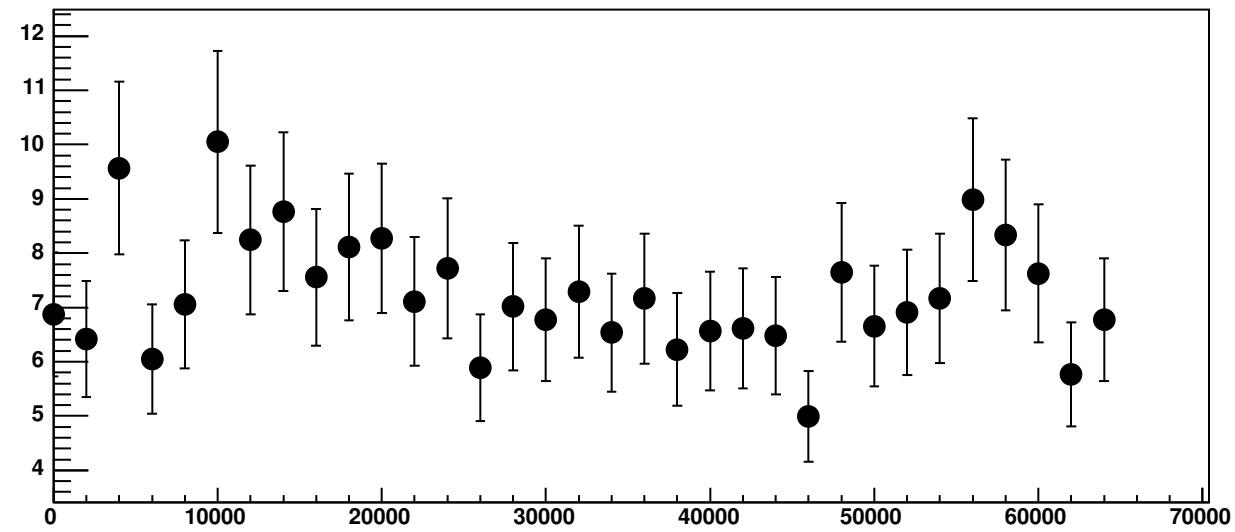

Chip 5, Channel 11, Enable 3!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 11, Enable 3!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
-1400
-1600
-1800
0
10000
20000
30000
40000
50000
60000
70000

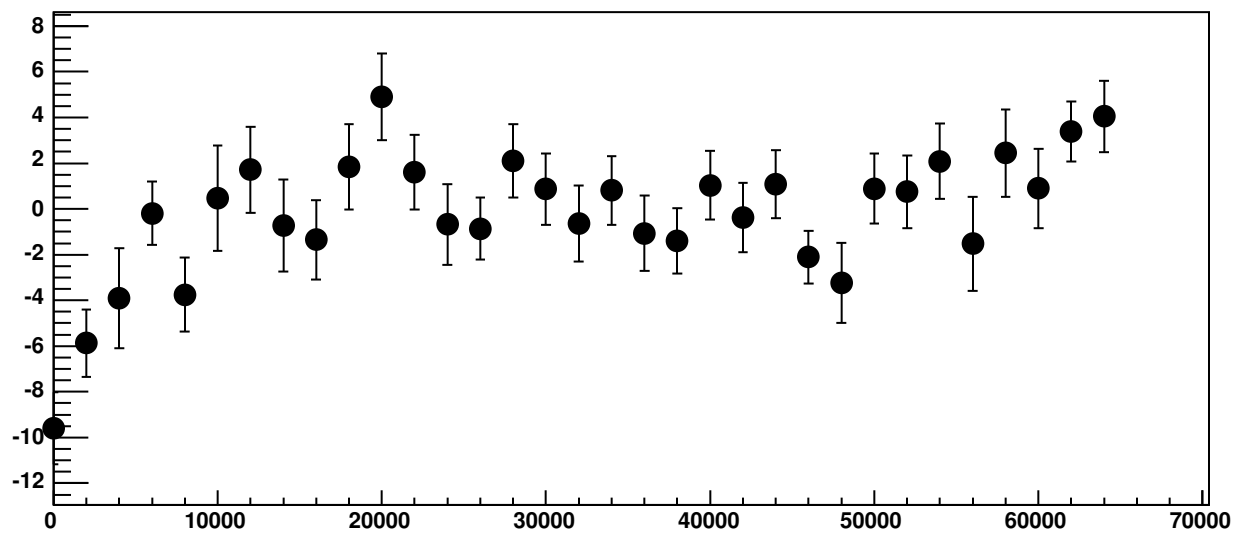
Chip 5, Channel 11, Enable 4, Hold=35, ADC Mean vs DAC



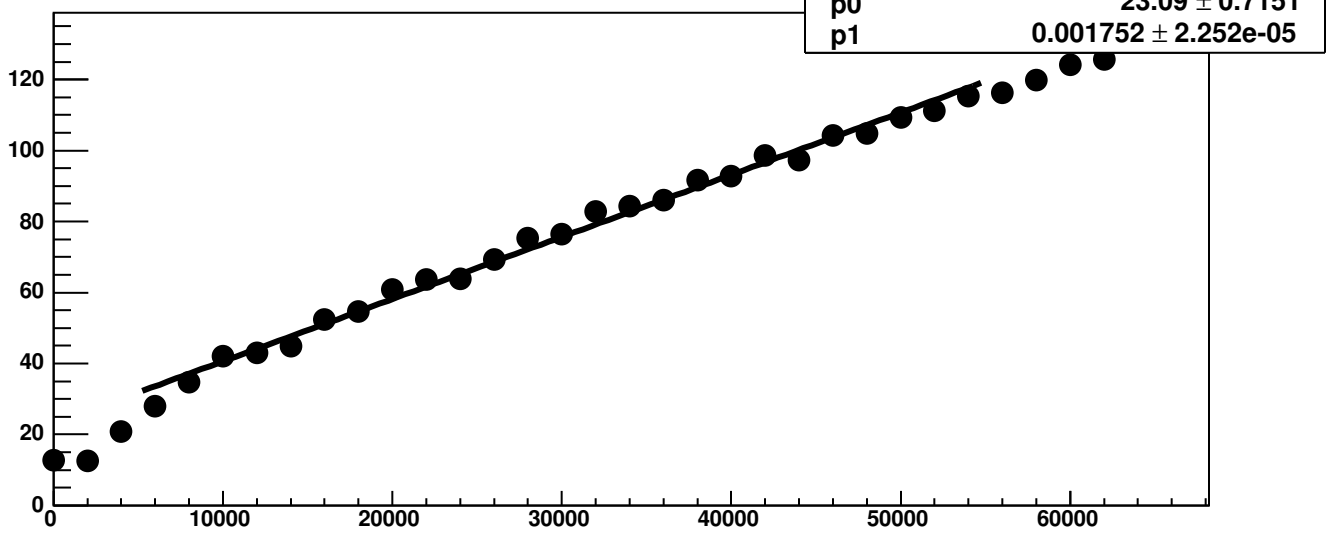
Chip 5, Channel 11, Enable 4, Hold=35, ADC Noise vs DAC



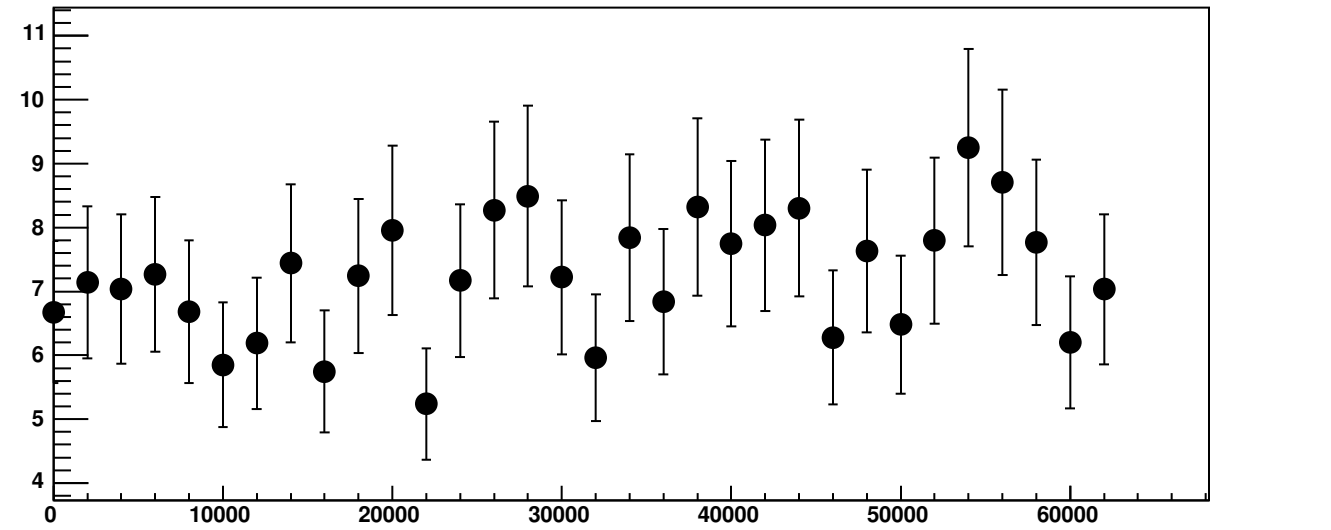
Chip 5, Channel 11, Enable 4, Hold=35, ADC Residuals vs DAC



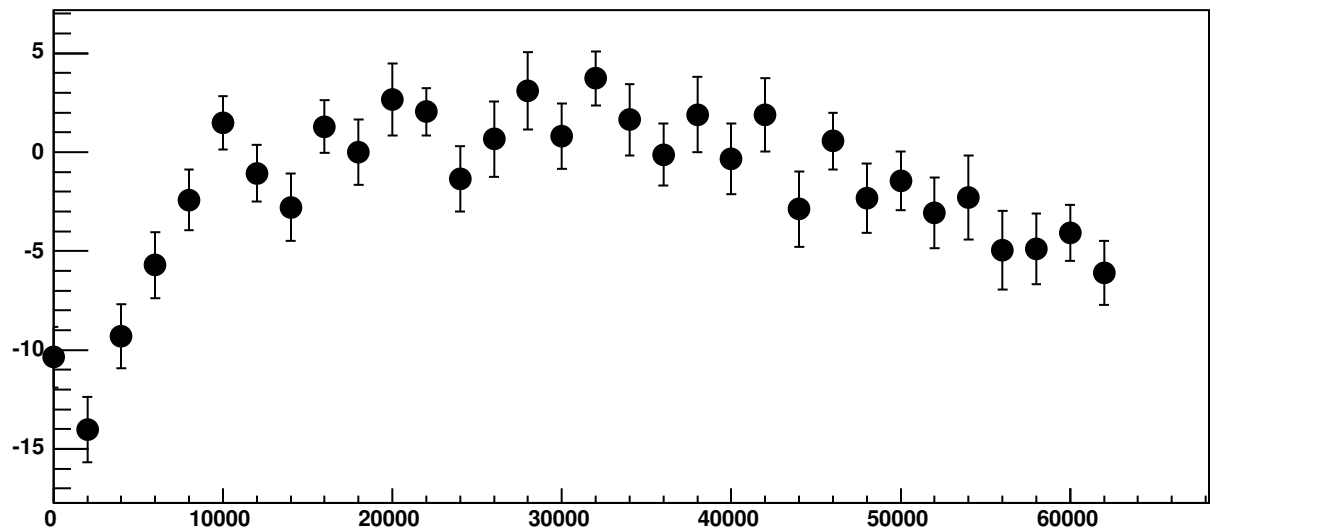
Chip 5, Channel 11, Enable 5, Hold=35, ADC Mean vs DAC

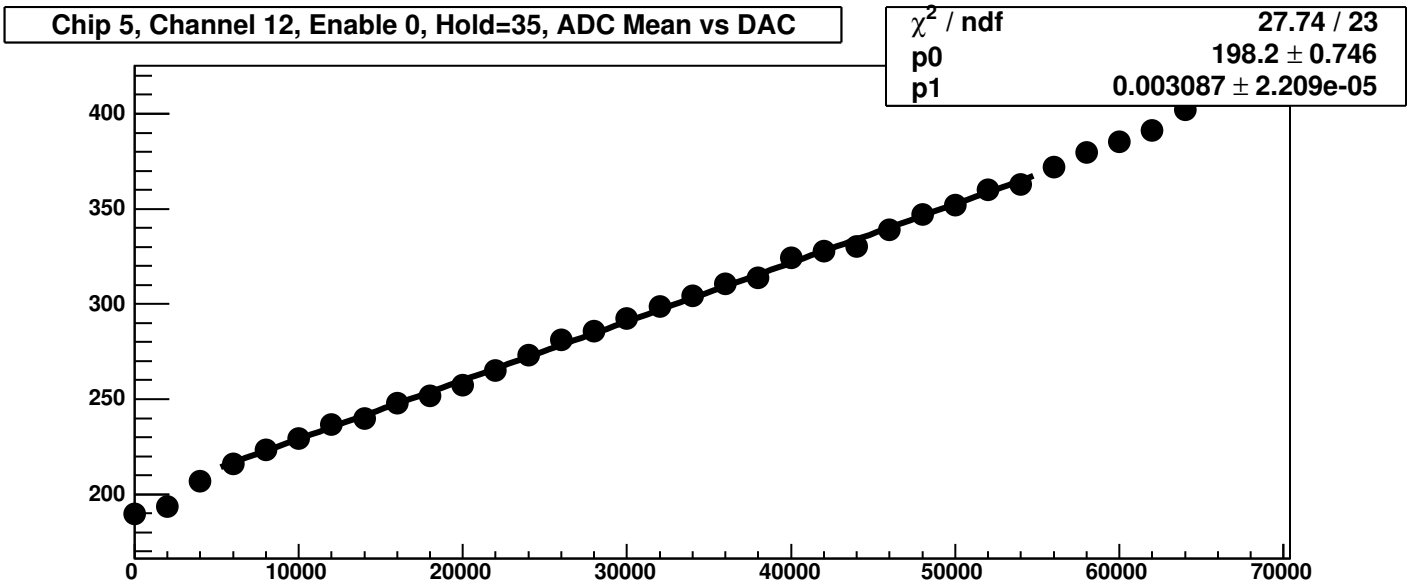


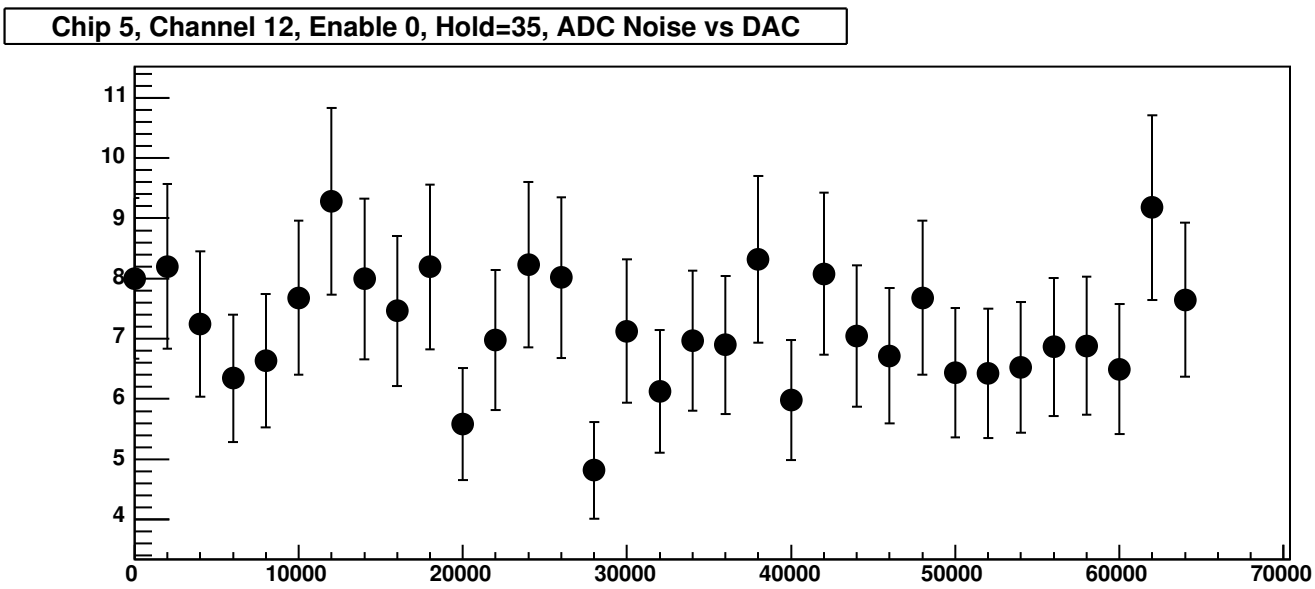
Chip 5, Channel 11, Enable 5, Hold=35, ADC Noise vs DAC

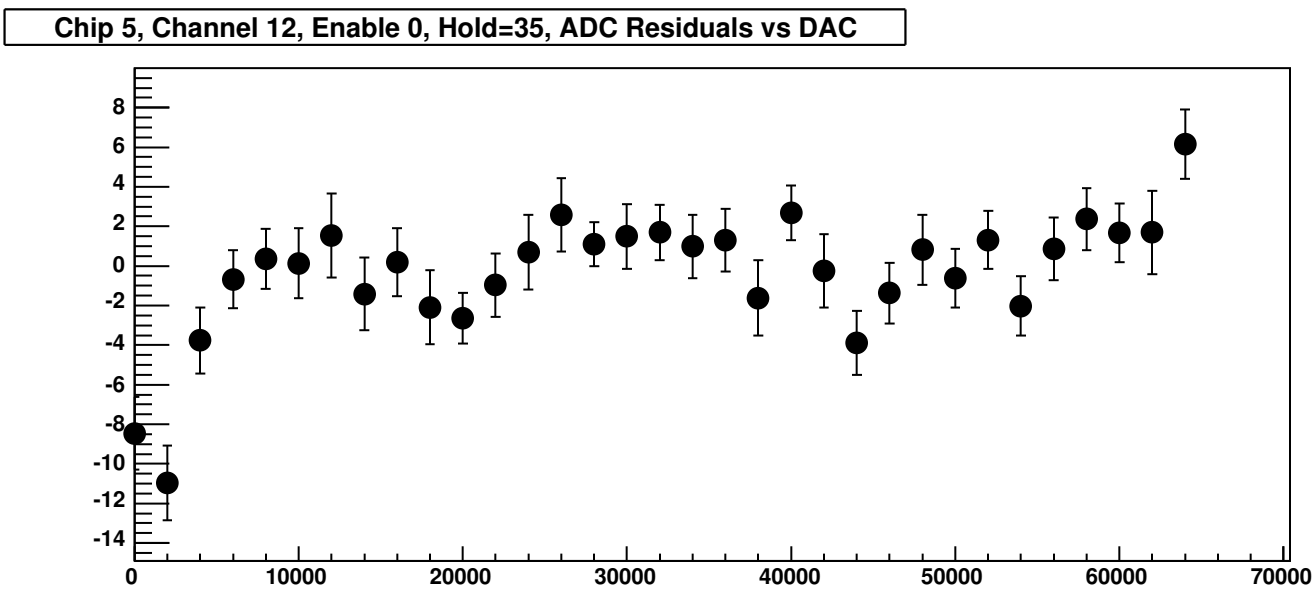


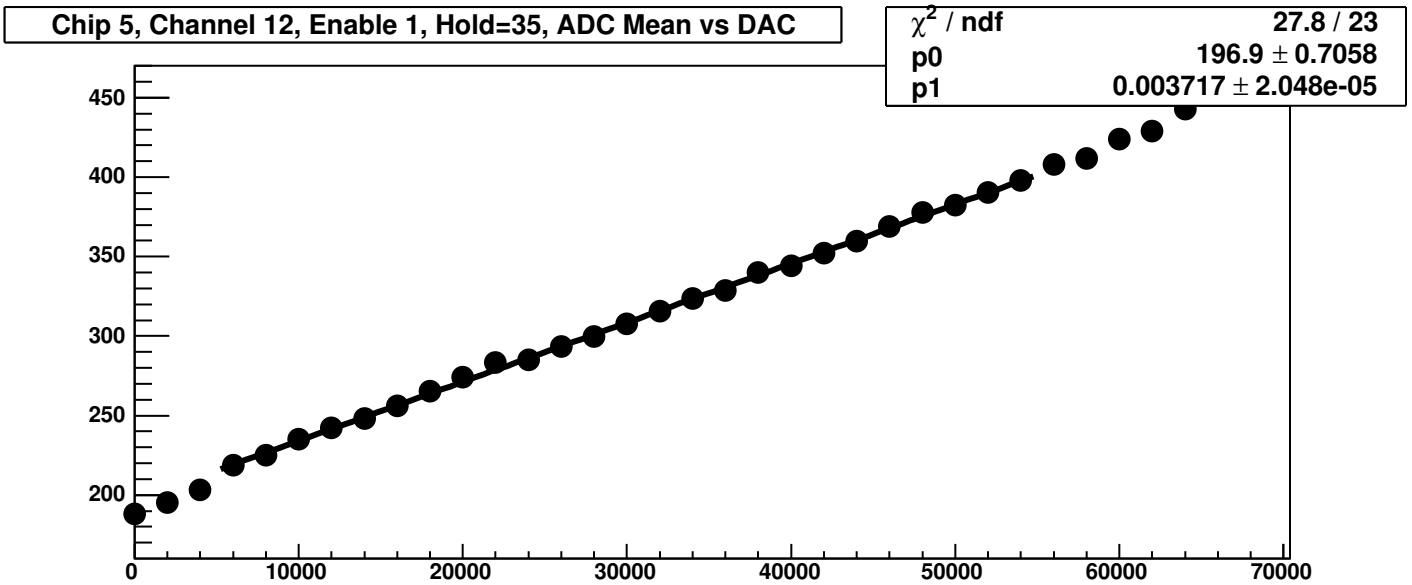
Chip 5, Channel 11, Enable 5, Hold=35, ADC Residuals vs DAC

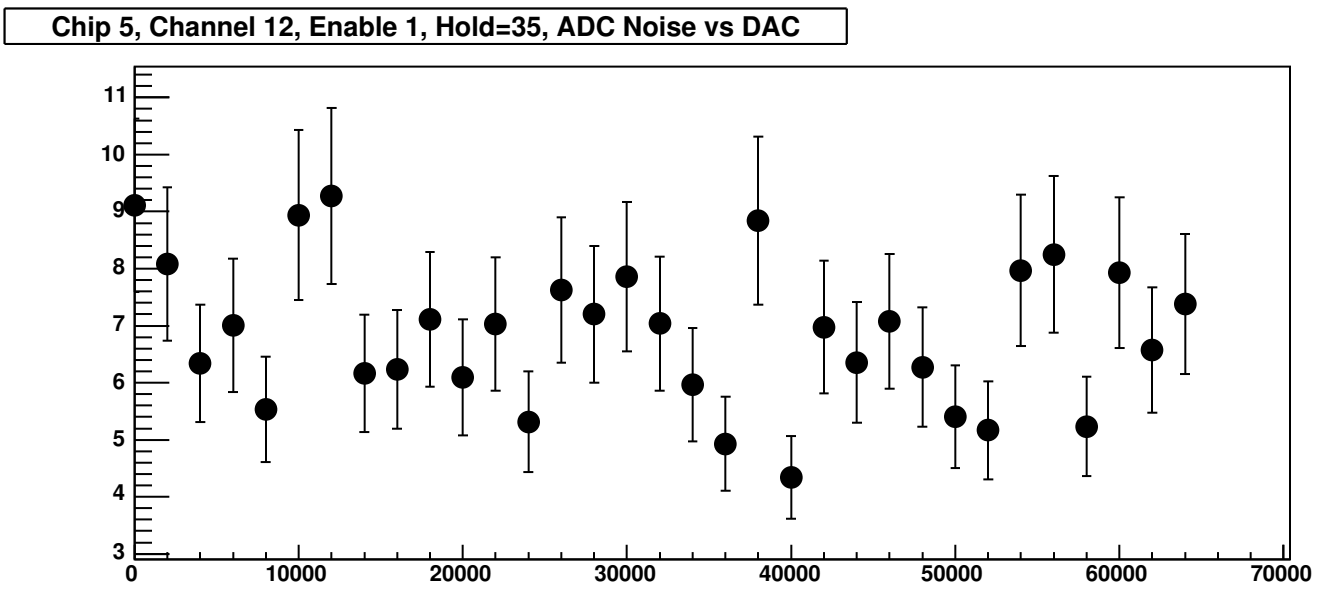


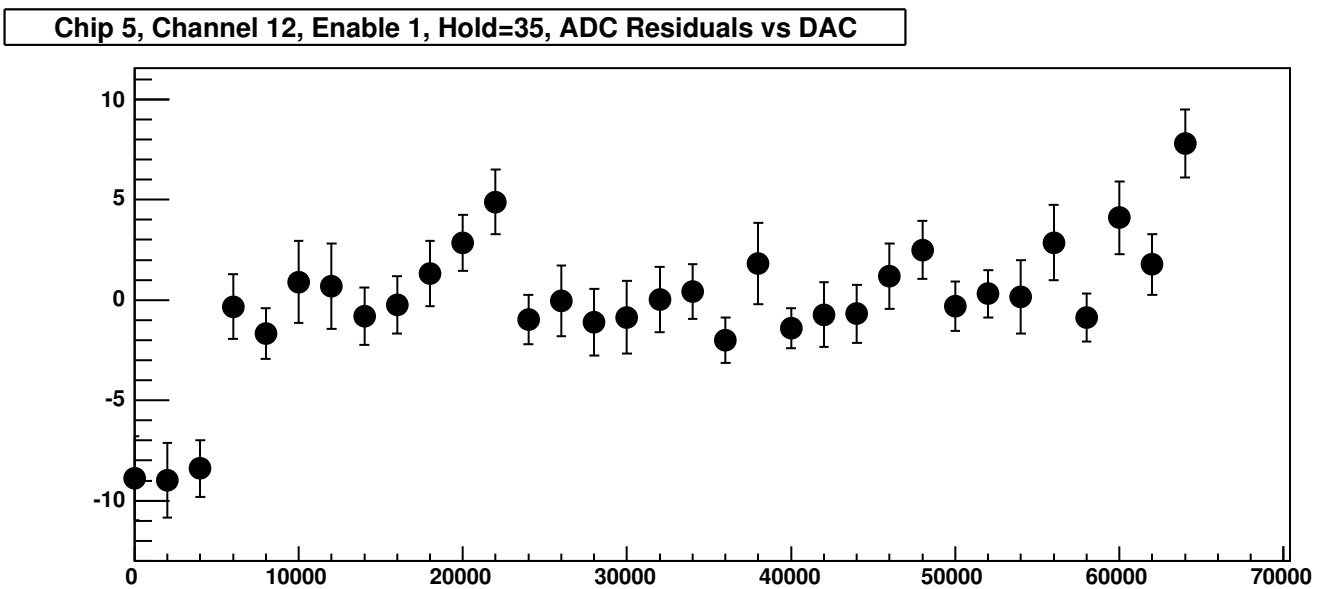

Chip 5, Channel 12, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 27.74 / 23
p0 198.2 ± 0.746
p1 0.003087 ± 2.209e-05
400
350
300
250
200
0
10000
20000
30000
40000
50000
60000
70000

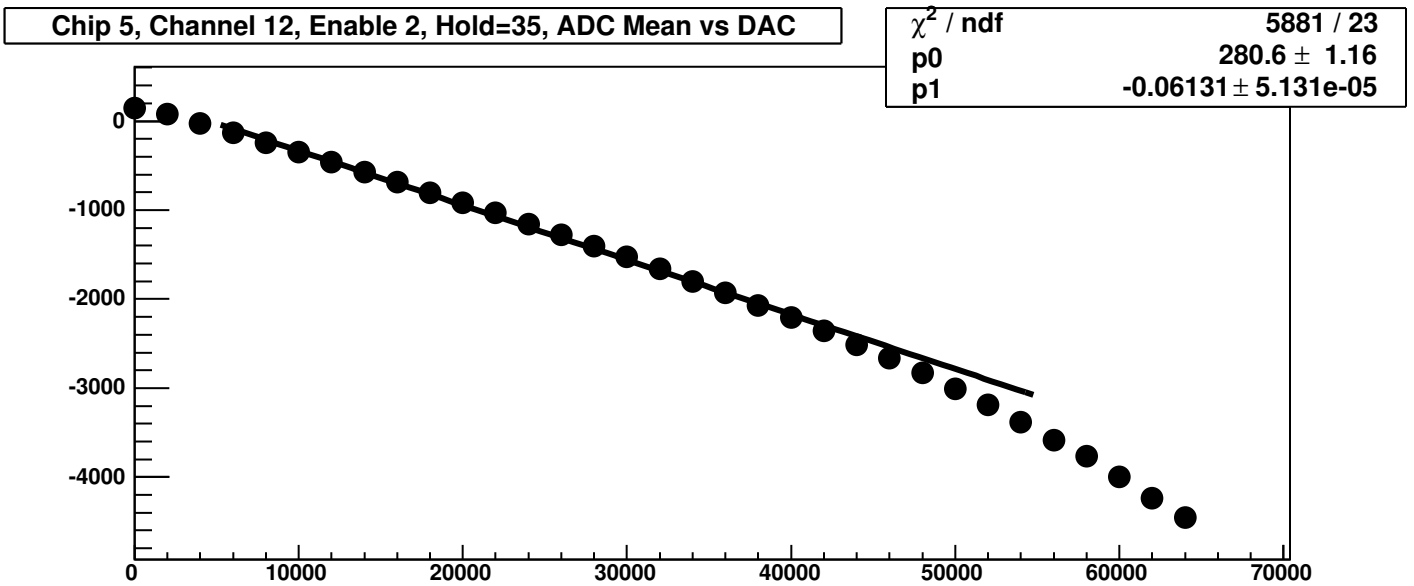

Chip 5, Channel 12, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

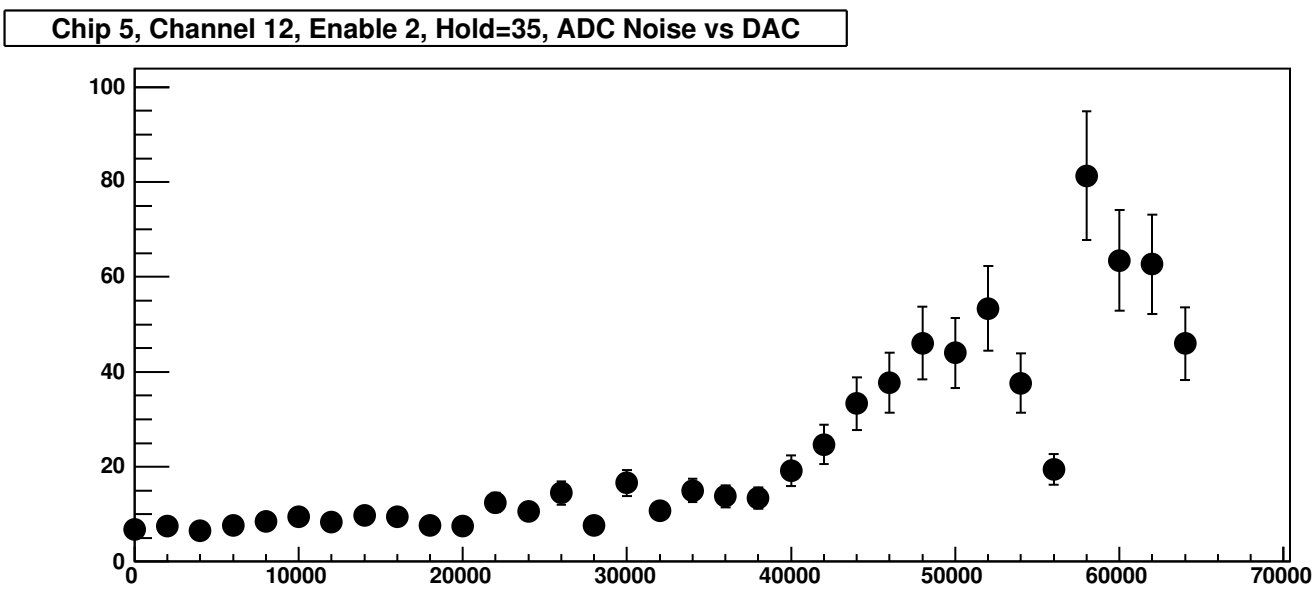

Chip 5, Channel 12, Enable 0, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
-14
0
10000
20000
30000
40000
50000
60000
70000

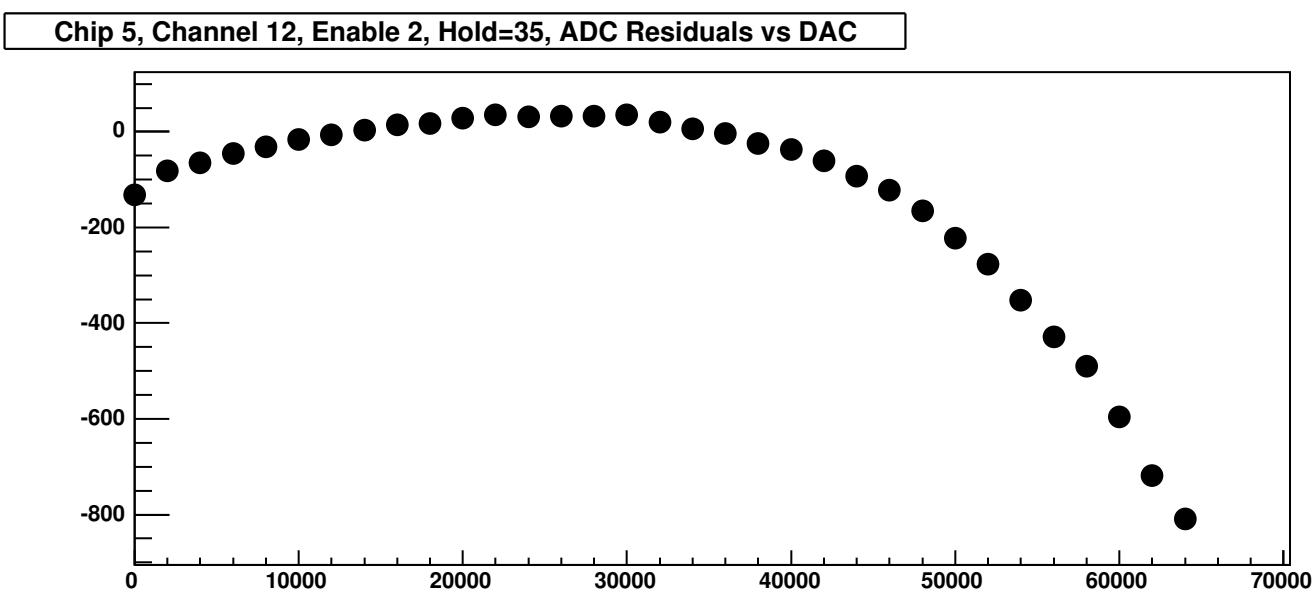

Chip 5, Channel 12, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 27.8 / 23
p0 196.9 ± 0.7058
p1 0.003717 ± 2.048e-05
450
400
350
300
250
200
0
10000
20000
30000
40000
50000
60000
70000

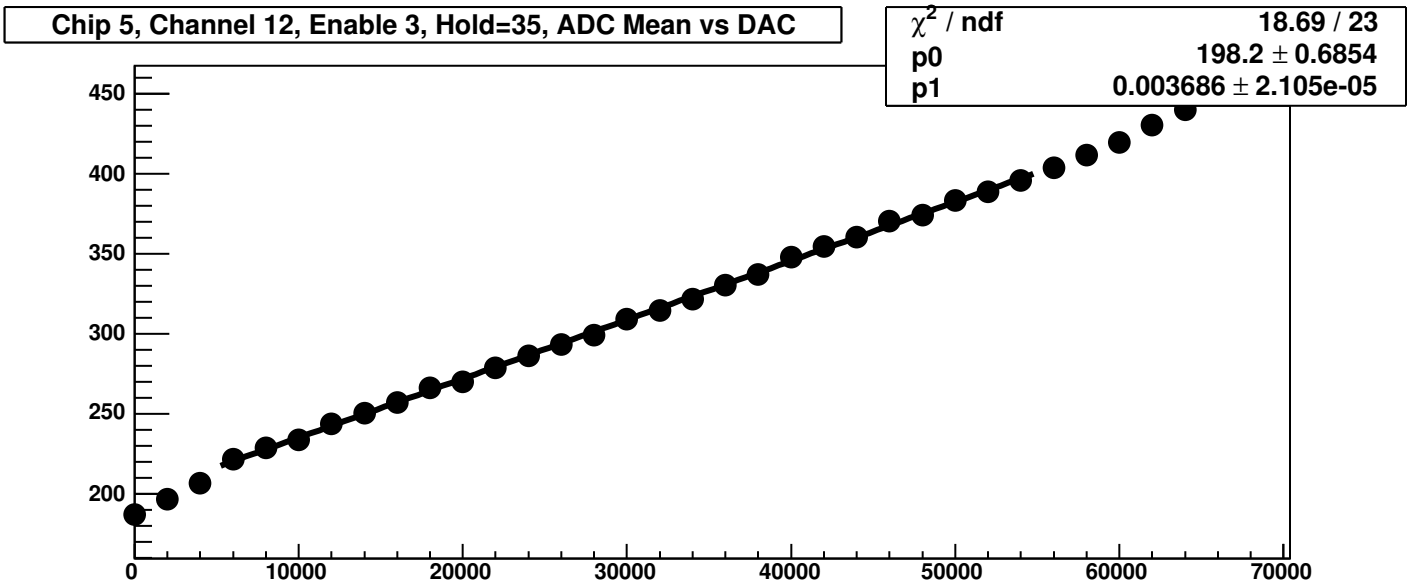

Chip 5, Channel 12, Enable 1, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

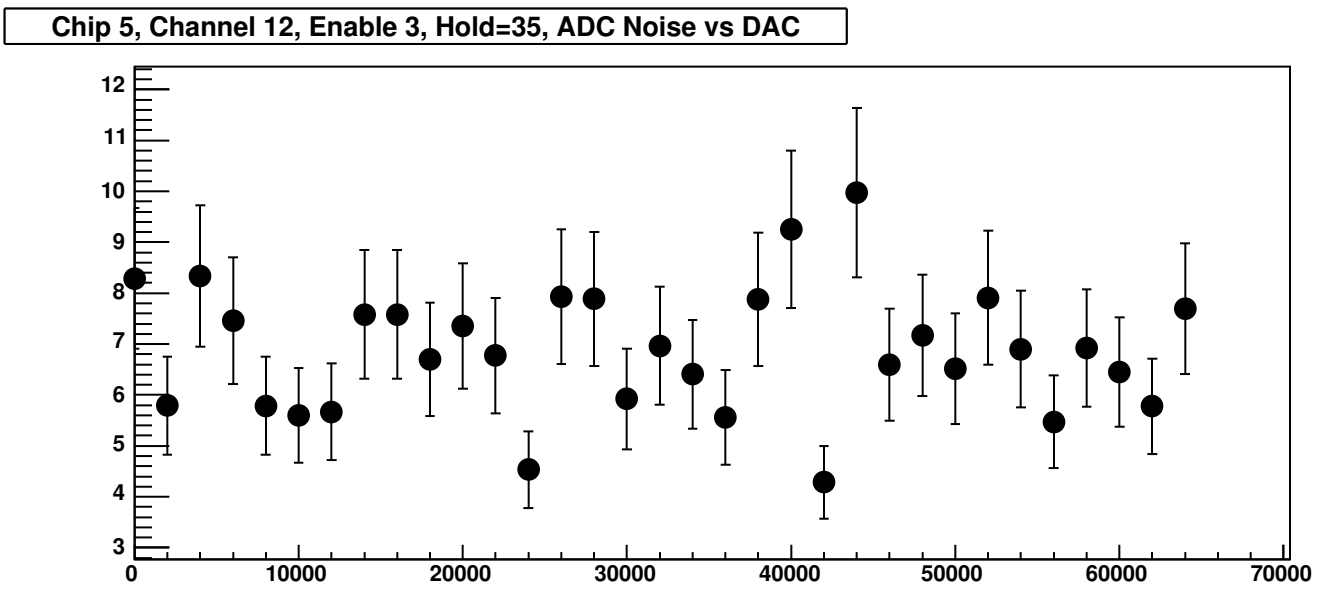

Chip 5, Channel 12, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

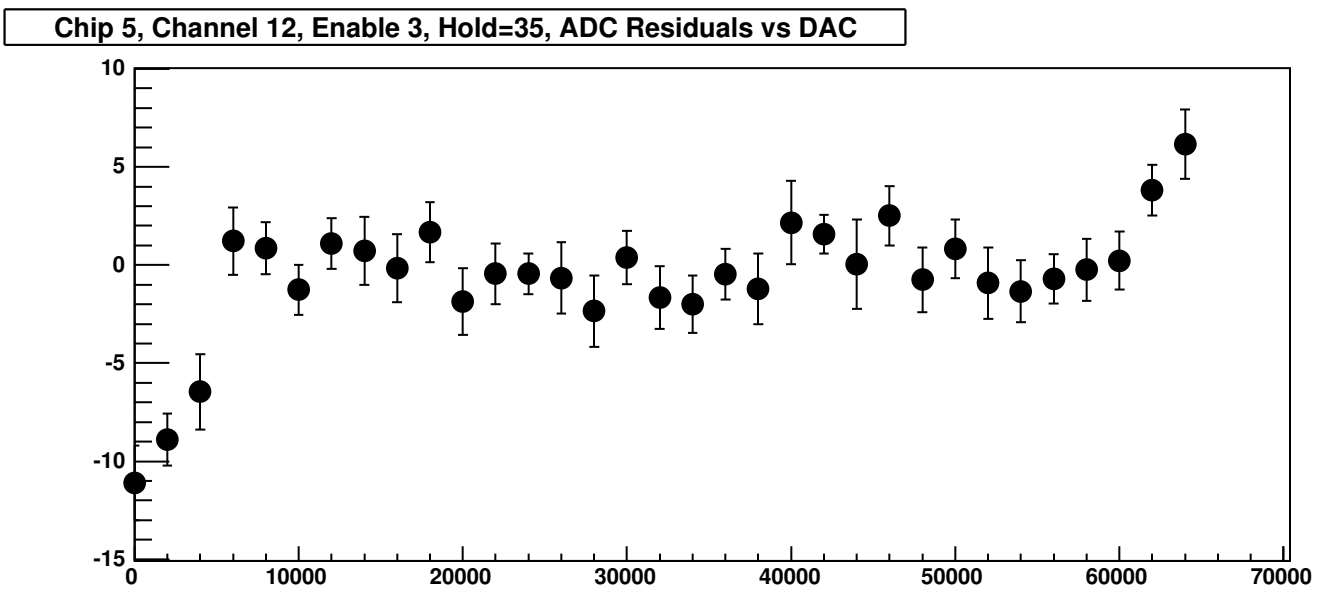

Chip 5, Channel 12, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 5881 / 23
p0 280.6 ± 1.16
p1 -0.06131 ± 5.131e-05
0
-1000
-2000
-3000
-4000
0
10000
20000
30000
40000
50000
60000
70000

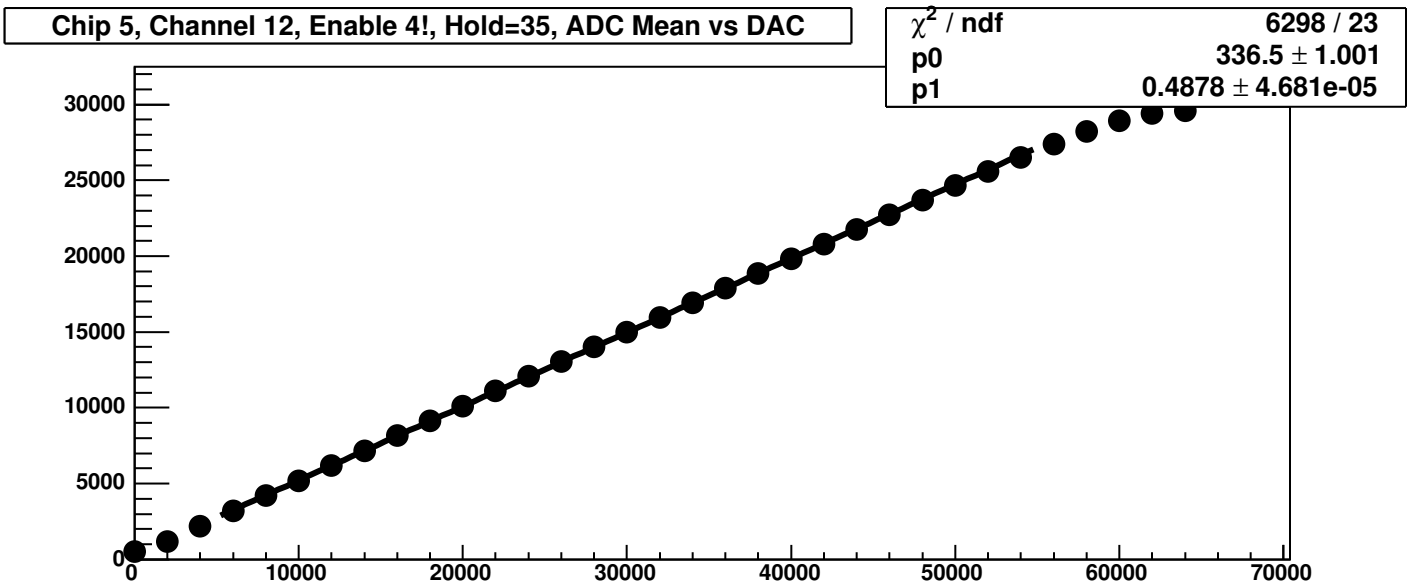

Chip 5, Channel 12, Enable 2, Hold=35, ADC Noise vs DAC
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000
70000

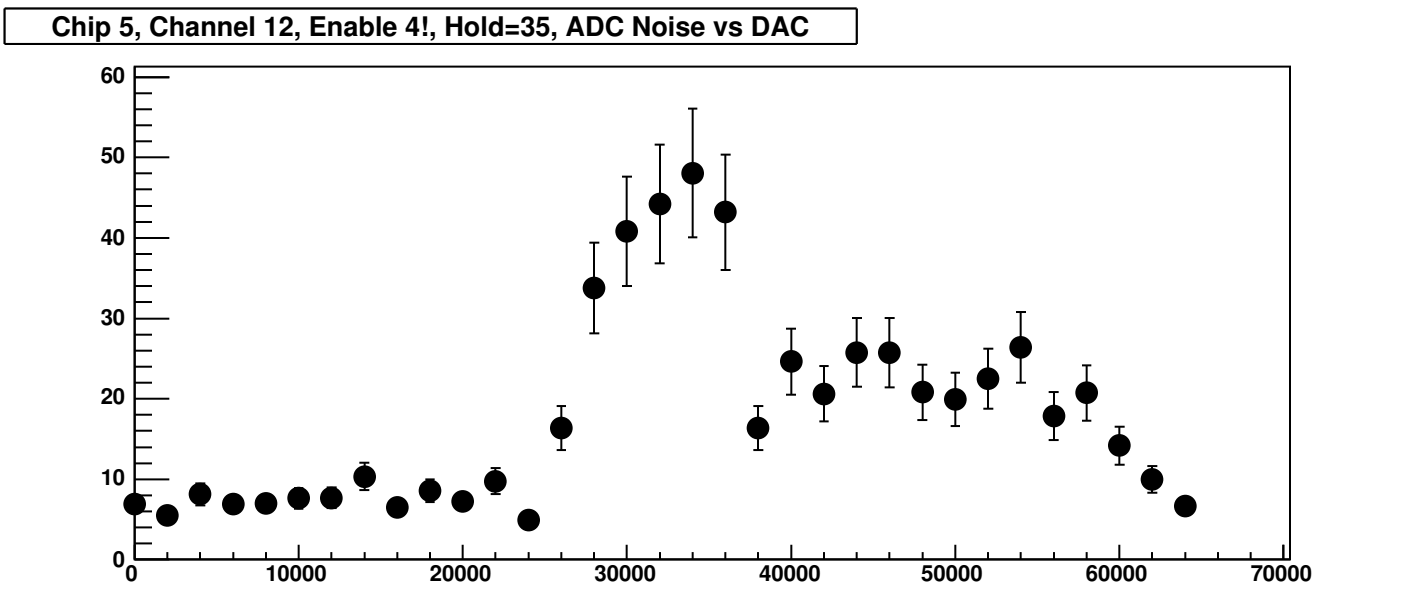

Chip 5, Channel 12, Enable 2, Hold=35, ADC Residuals vs DAC
0
-200
-400
-600
-800
0
10000
20000
30000
40000
50000
60000
70000

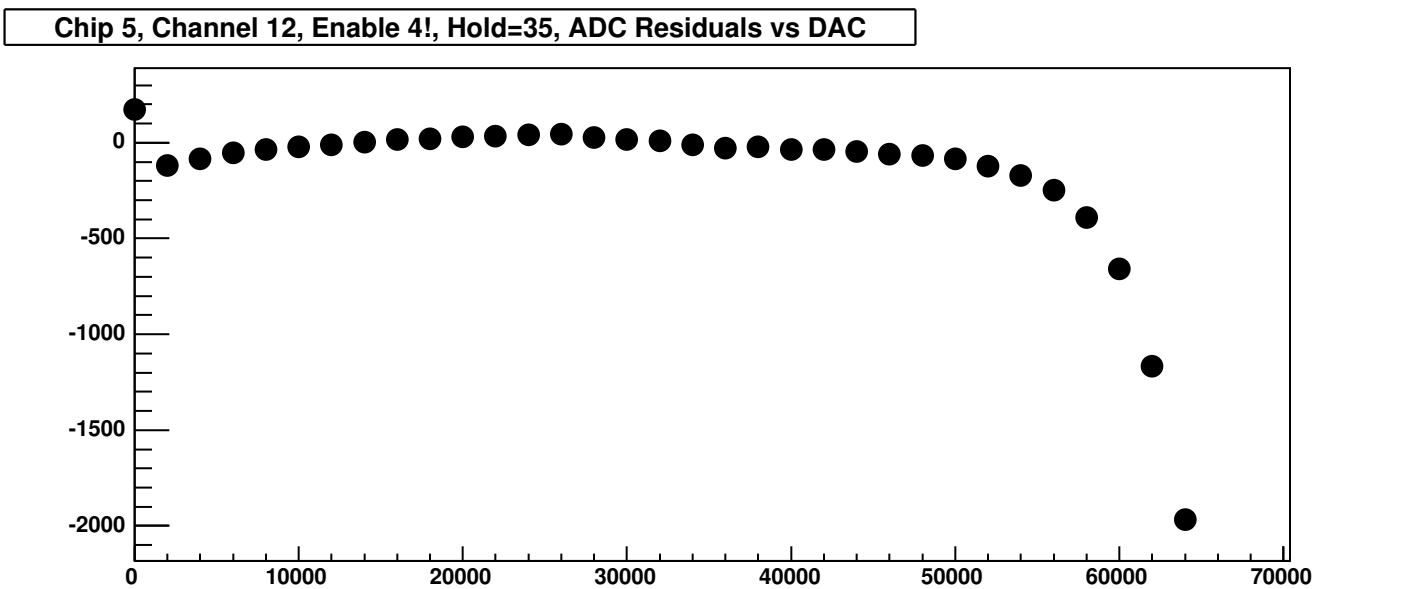

Chip 5, Channel 12, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 18.69 / 23
p0 198.2 ± 0.6854
p1 0.003686 ± 2.105e-05
450
400
350
300
250
200
0
10000
20000
30000
40000
50000
60000
70000

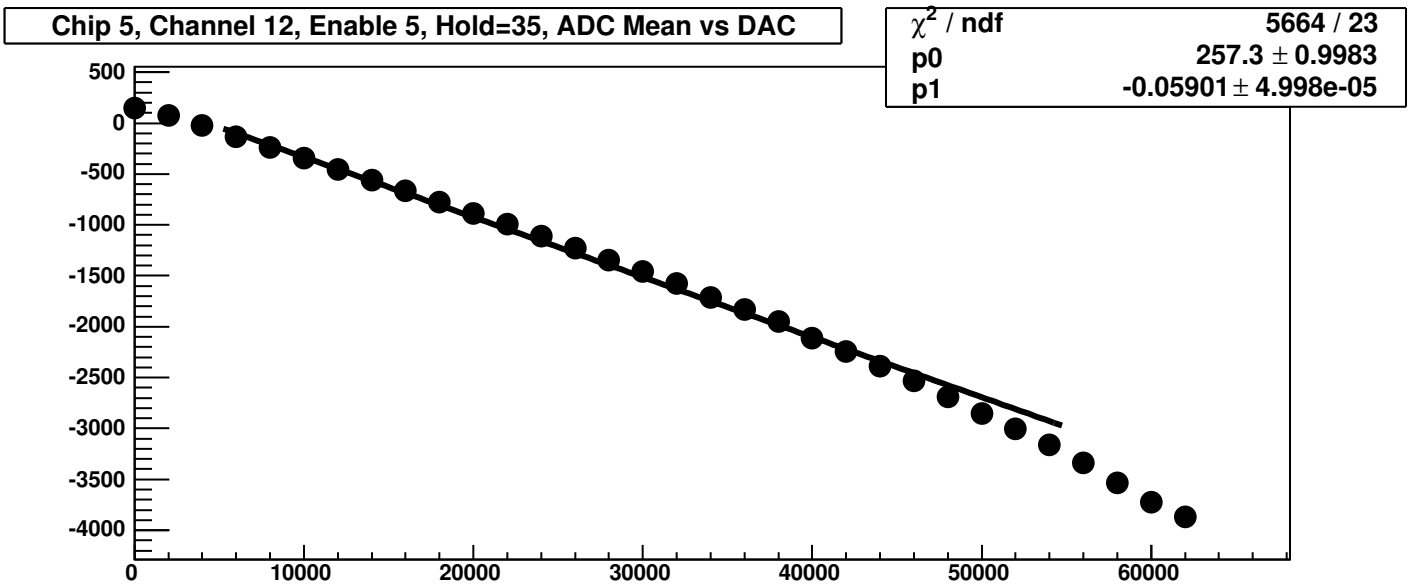

Chip 5, Channel 12, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

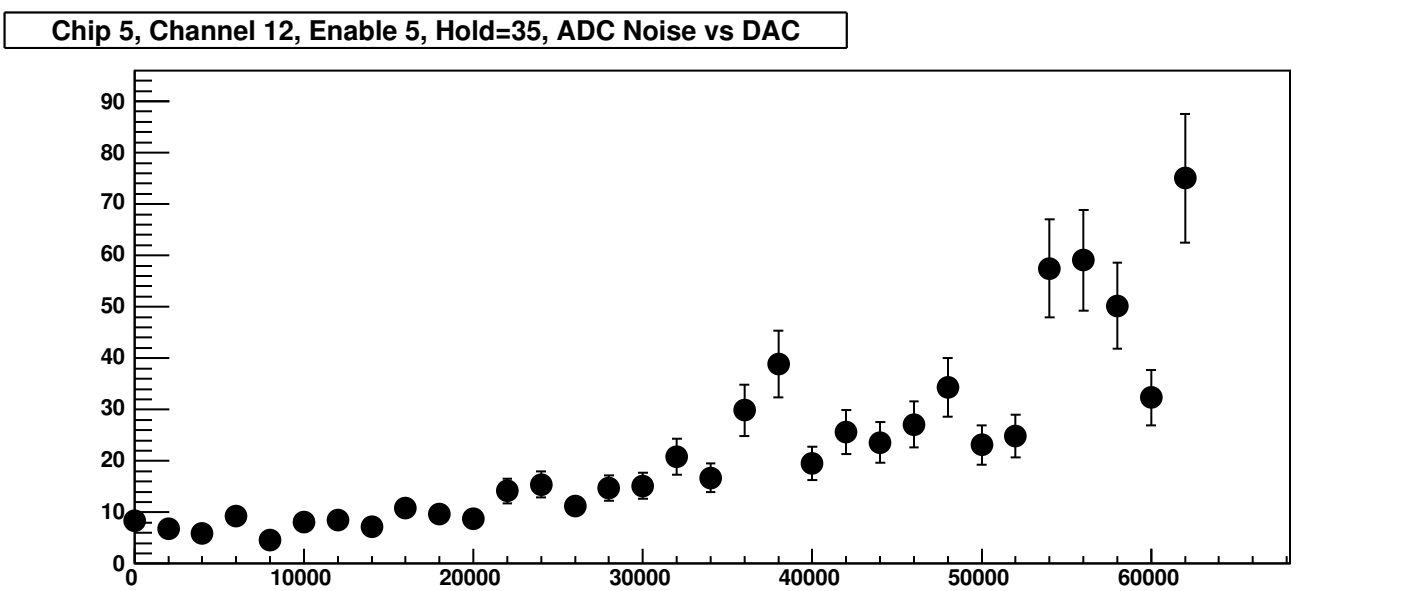

Chip 5, Channel 12, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

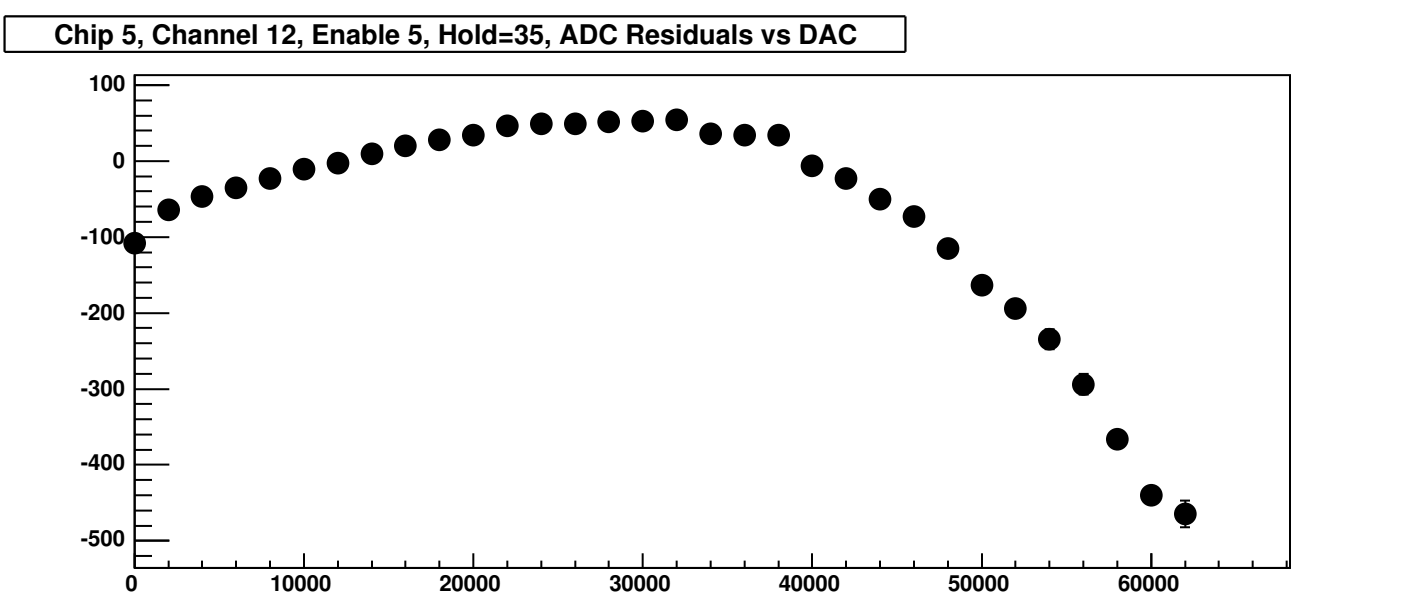

Chip 5, Channel 12, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 6298 / 23
p0 336.5 ± 1.001
p1 0.4878 ± 4.681e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 12, Enable 4!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

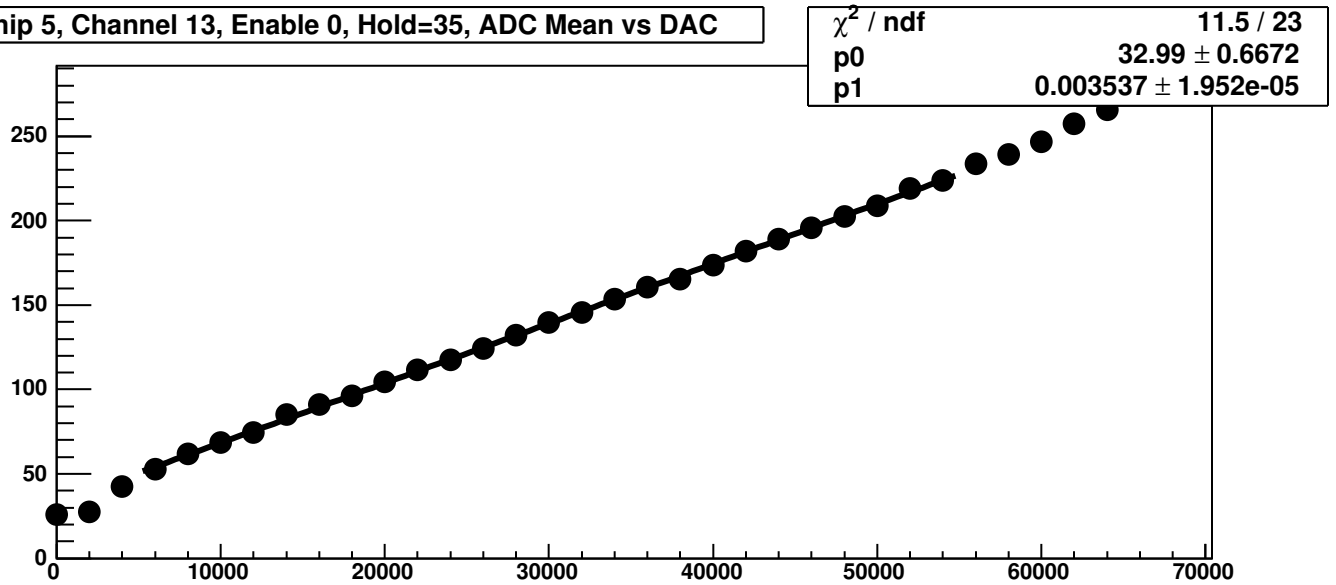

Chip 5, Channel 12, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 12, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 5664 / 23
p0 257.3 ± 0.9983
p1 -0.05901 ± 4.998e-05
500
0
-500
-1000
-1500
-2000
-2500
-3000
-3500
-4000
0
10000
20000
30000
40000
50000
60000

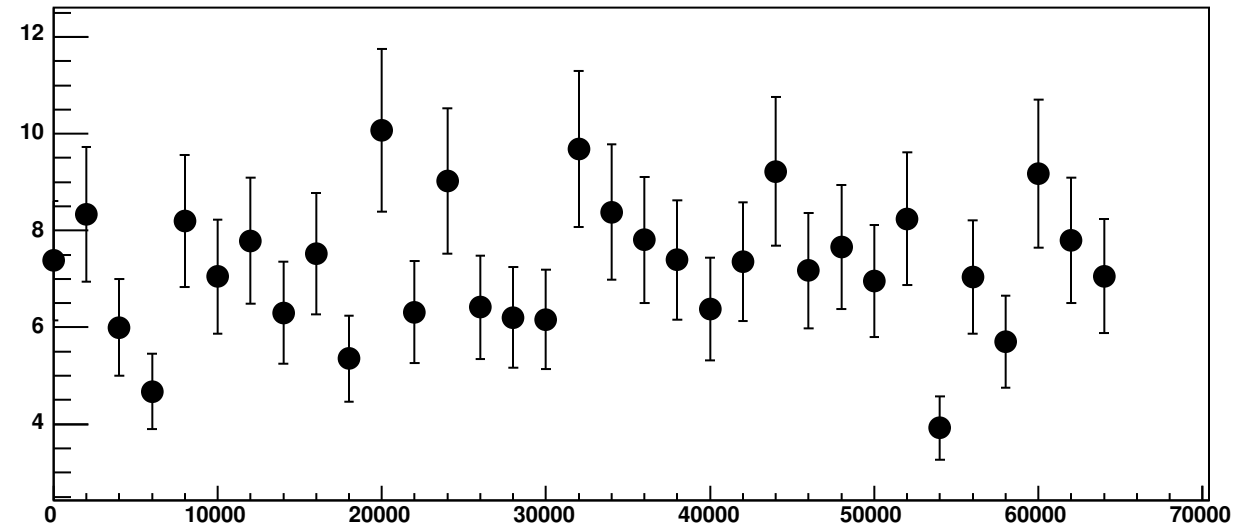

Chip 5, Channel 12, Enable 5, Hold=35, ADC Noise vs DAC
90
80
70
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 12, Enable 5, Hold=35, ADC Residuals vs DAC
100
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000

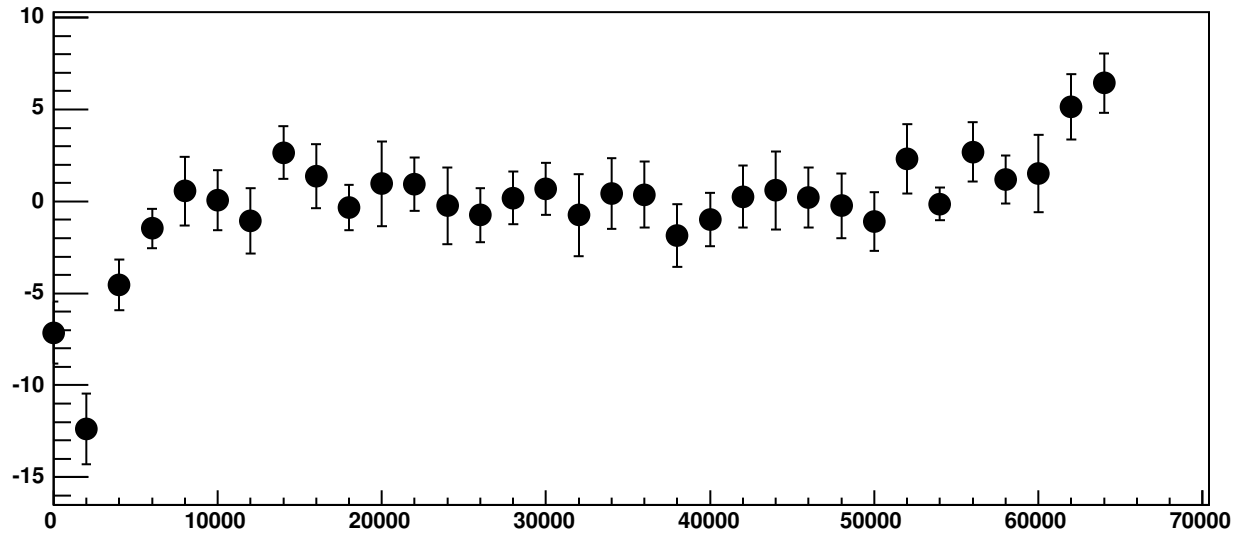
Chip 5, Channel 13, Enable 0, Hold=35, ADC Mean vs DAC



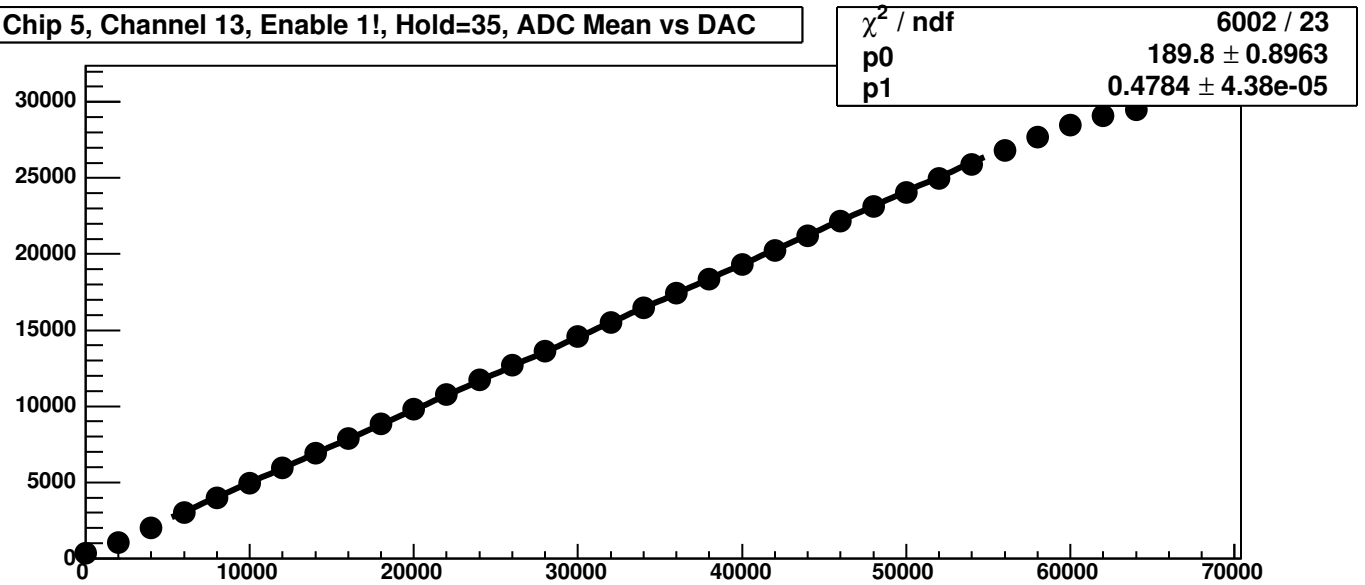
Chip 5, Channel 13, Enable 0, Hold=35, ADC Noise vs DAC



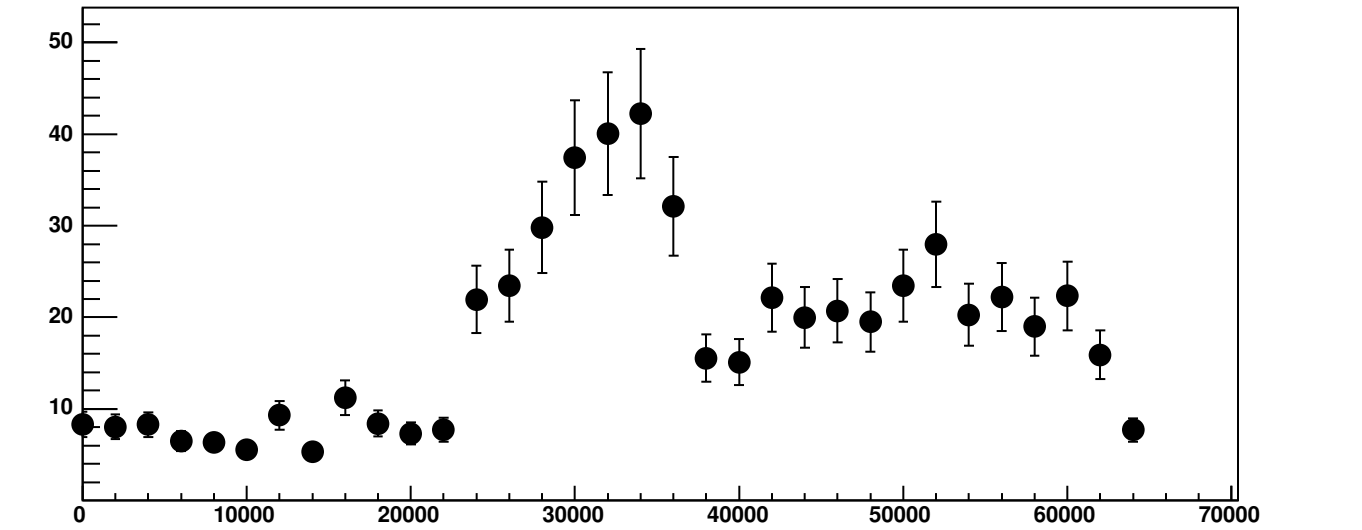
Chip 5, Channel 13, Enable 0, Hold=35, ADC Residuals vs DAC



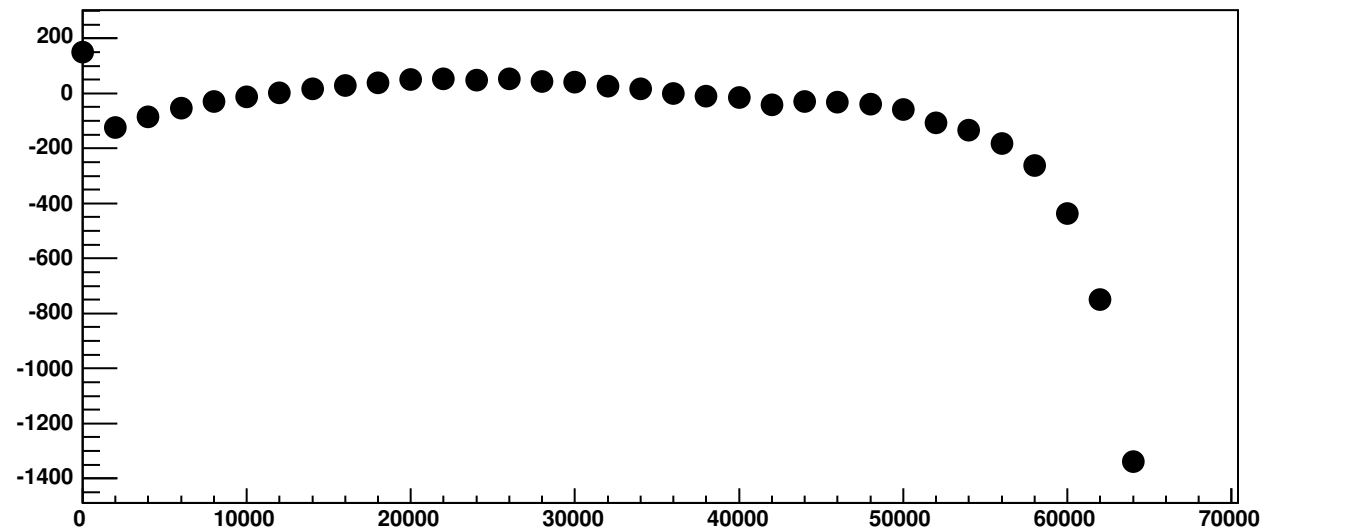
Chip 5, Channel 13, Enable 1!, Hold=35, ADC Mean vs DAC



Chip 5, Channel 13, Enable 1!, Hold=35, ADC Noise vs DAC

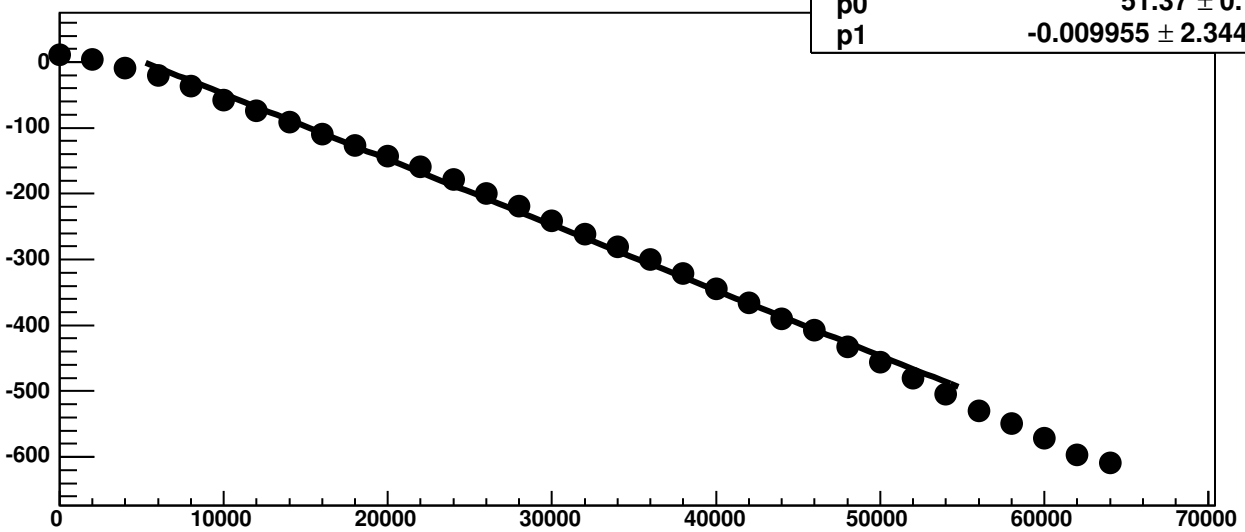


Chip 5, Channel 13, Enable 1!, Hold=35, ADC Residuals vs DAC

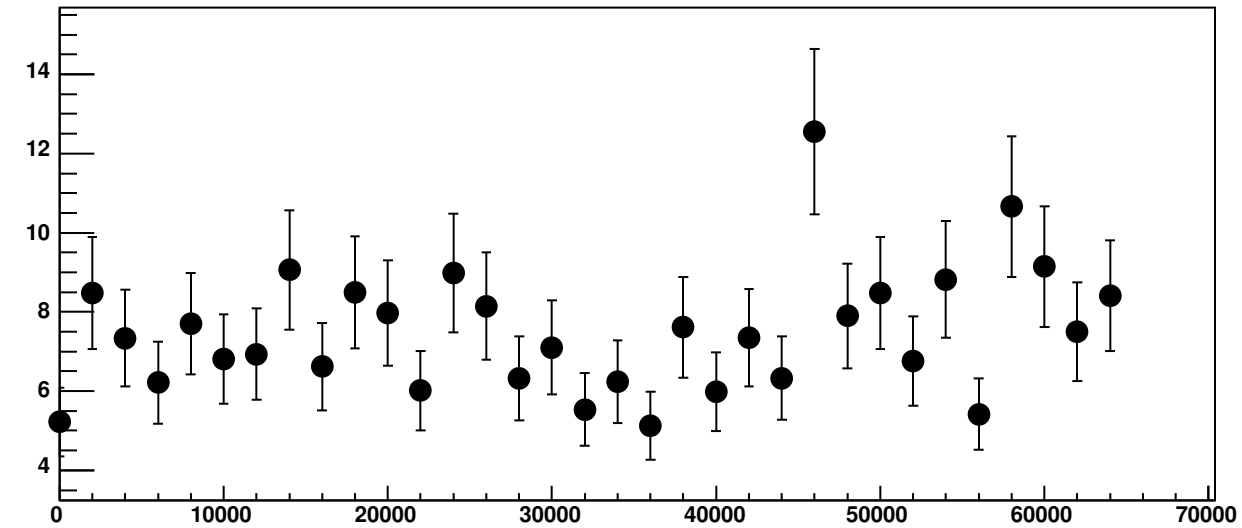


Chip 5, Channel 13, Enable 2, Hold=35, ADC Mean vs DAC

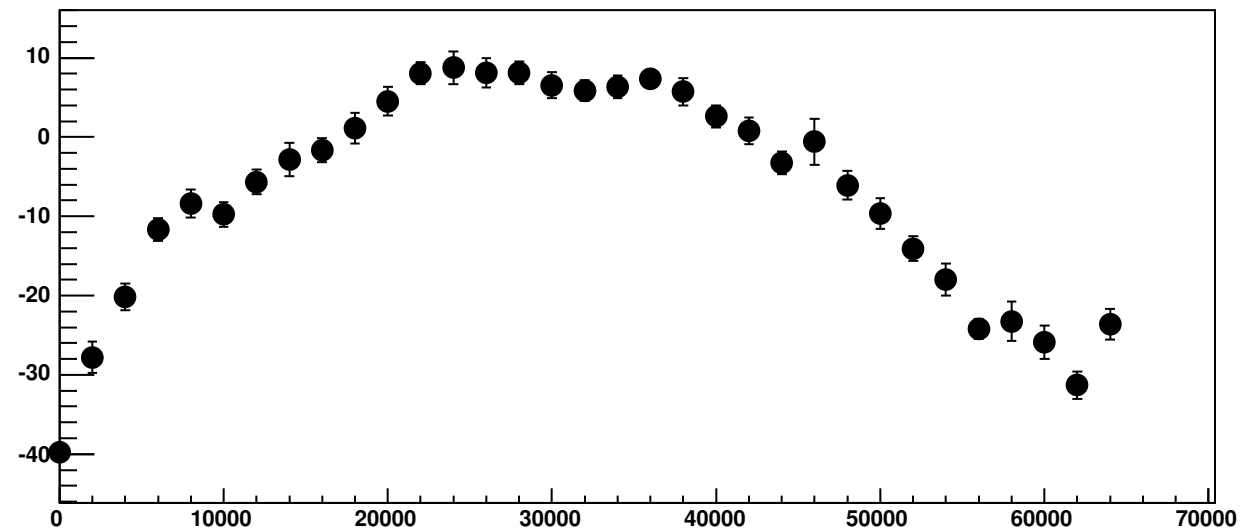
χ^2 / ndf	566.1 / 23
p0	51.37 ± 0.7654
p1	-0.009955 ± 2.344e-05

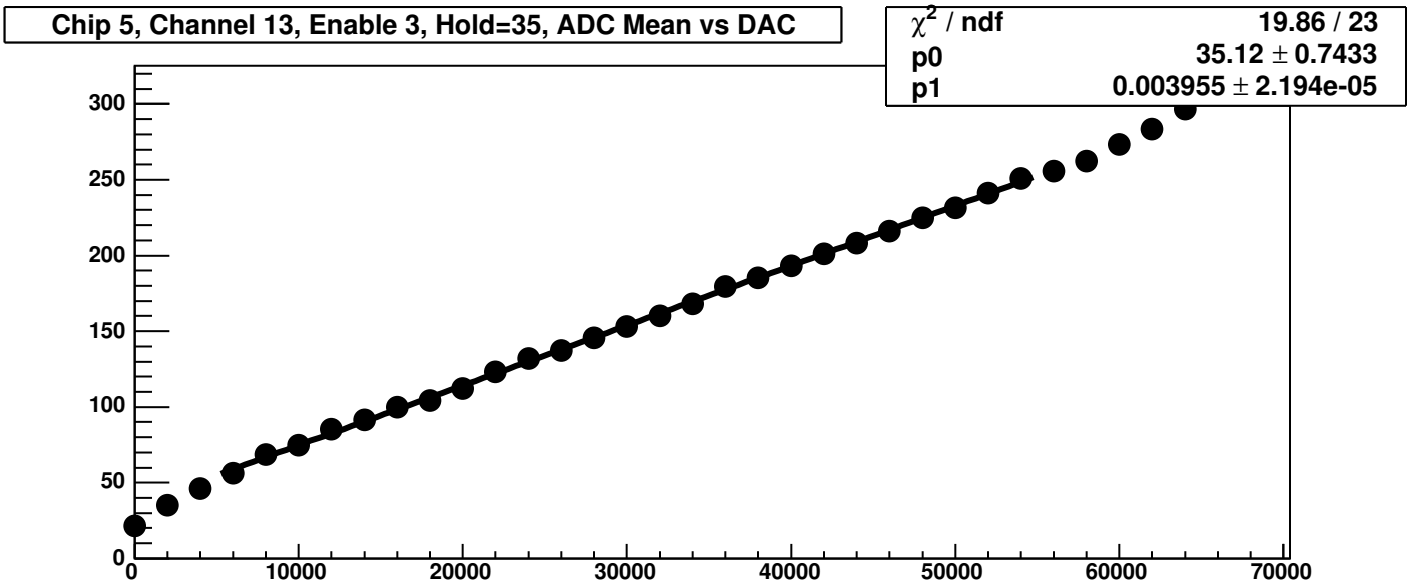


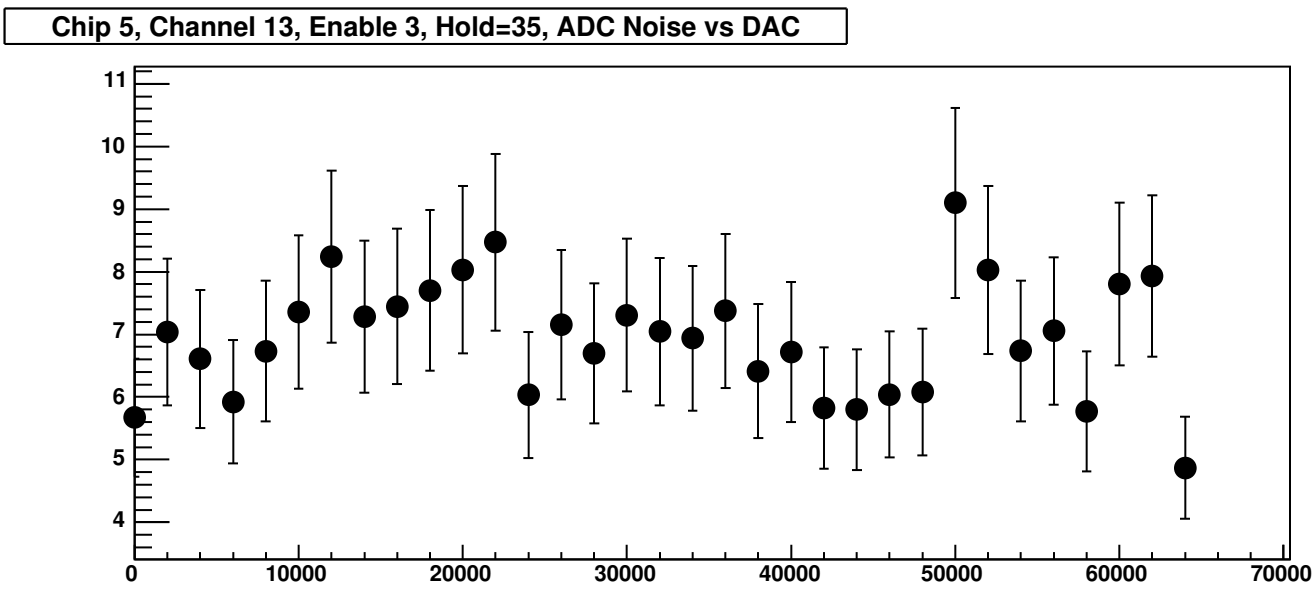
Chip 5, Channel 13, Enable 2, Hold=35, ADC Noise vs DAC

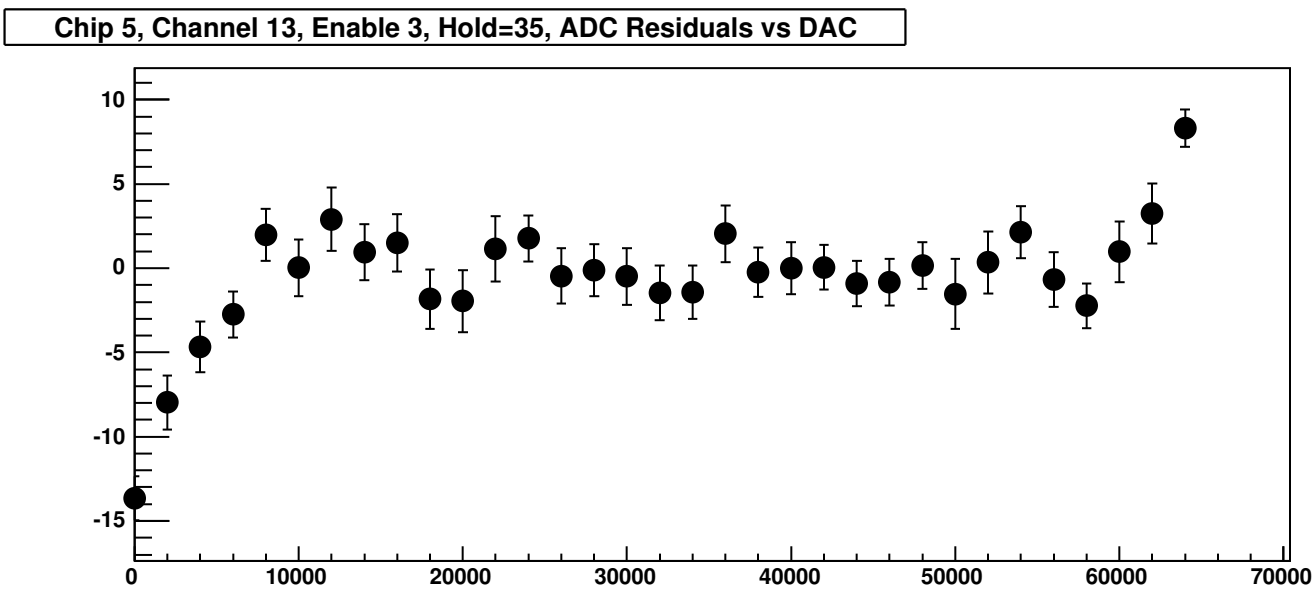


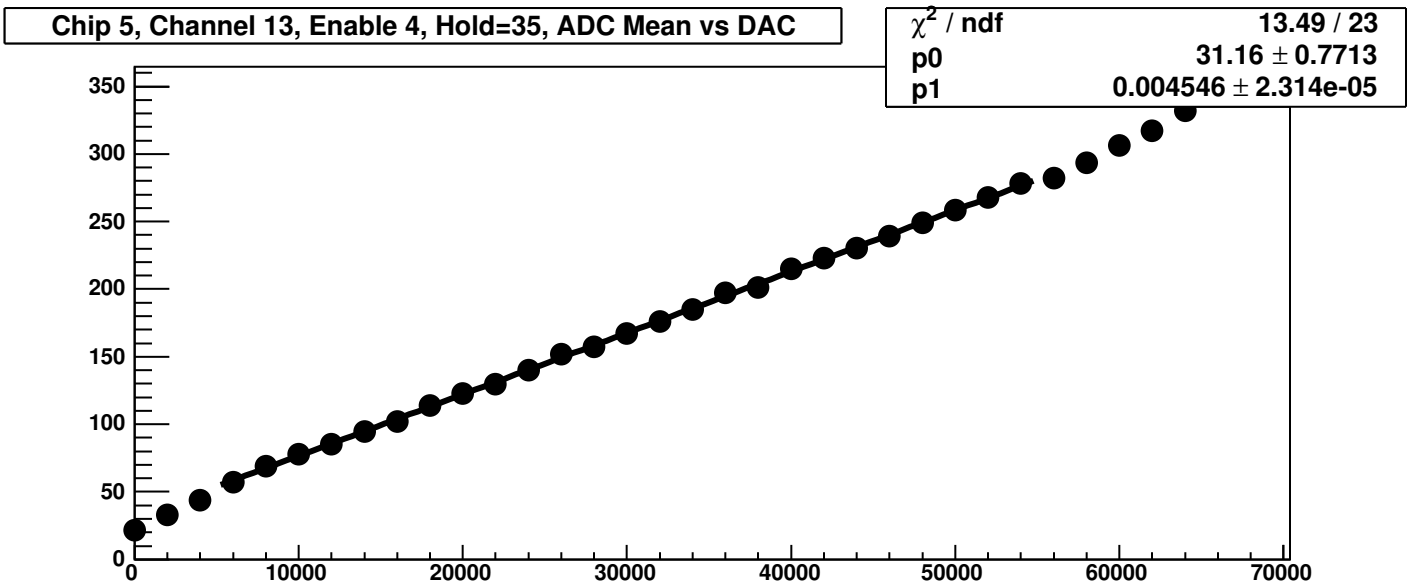
Chip 5, Channel 13, Enable 2, Hold=35, ADC Residuals vs DAC

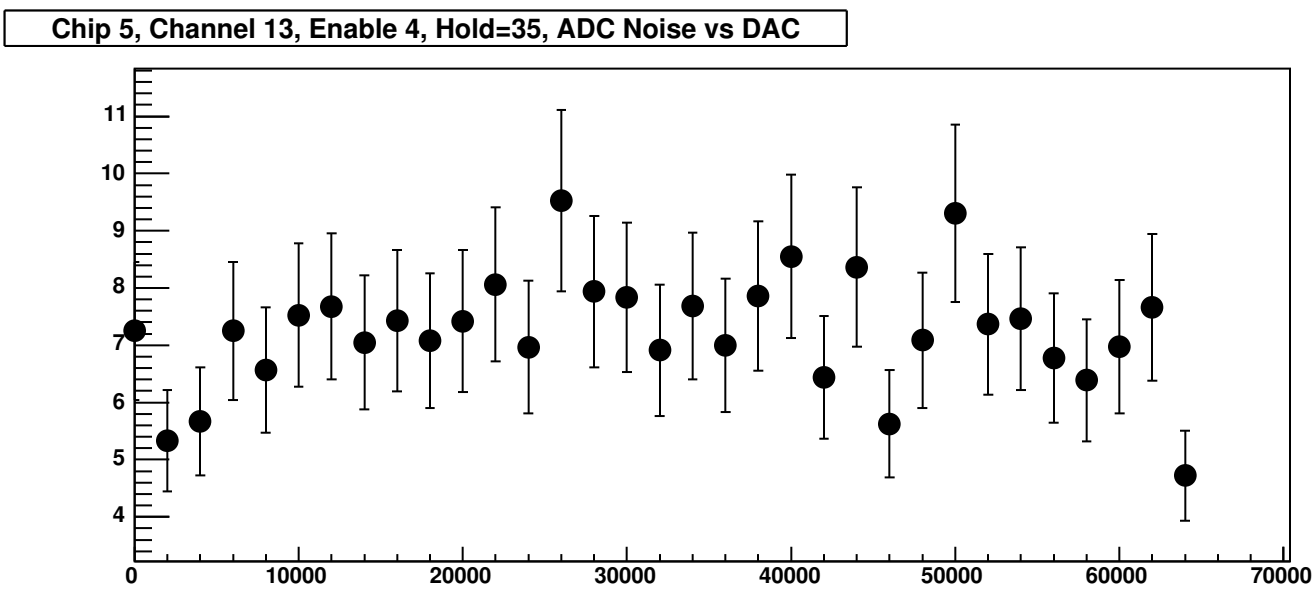


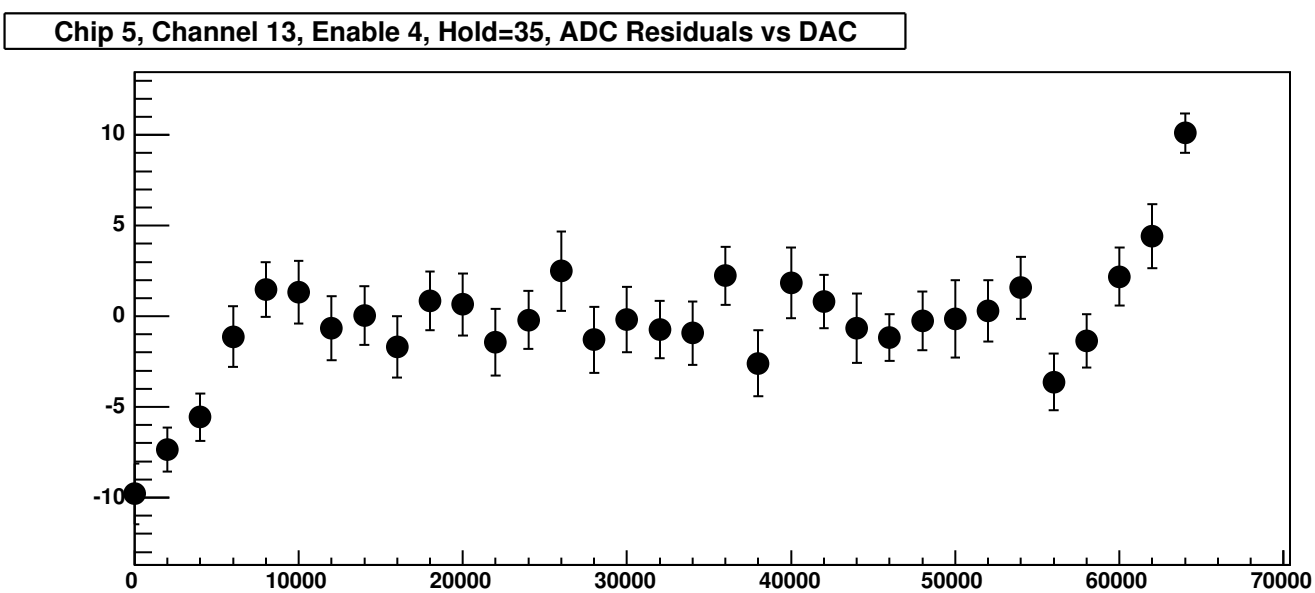

Chip 5, Channel 13, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 19.86 / 23
p0 35.12 ± 0.7433
p1 0.003955 ± 2.194e-05
300
250
200
150
100
50
0
0
10000
20000
30000
40000
50000
60000
70000

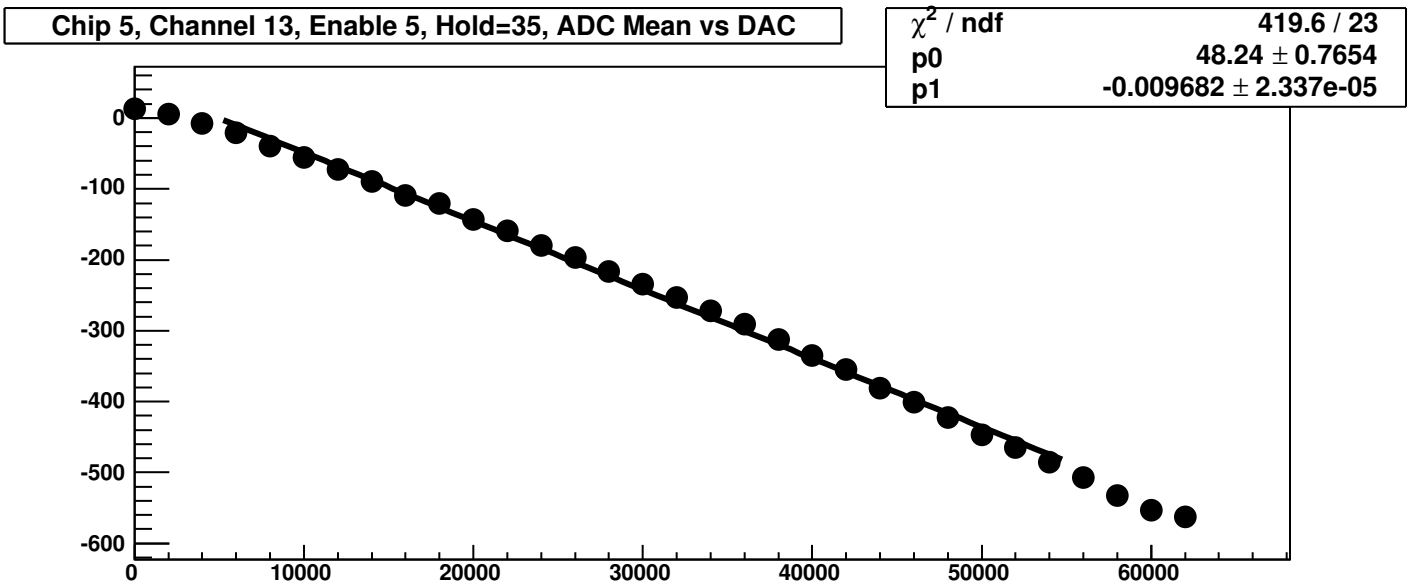

Chip 5, Channel 13, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

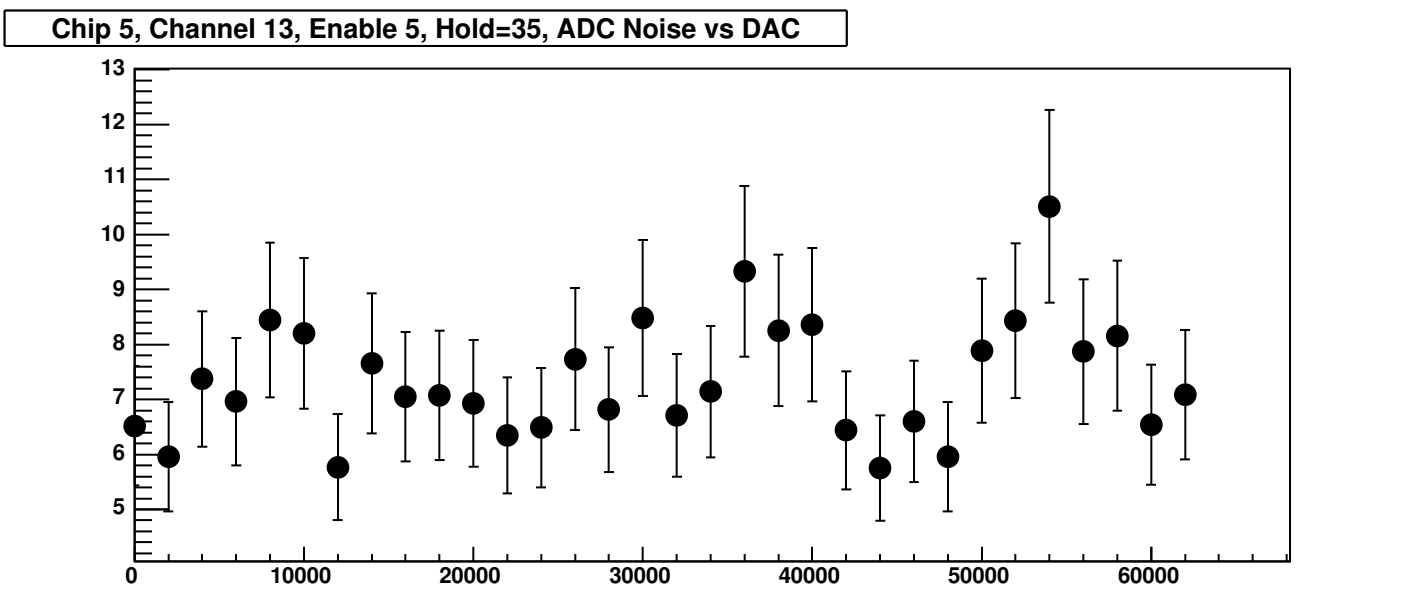

Chip 5, Channel 13, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

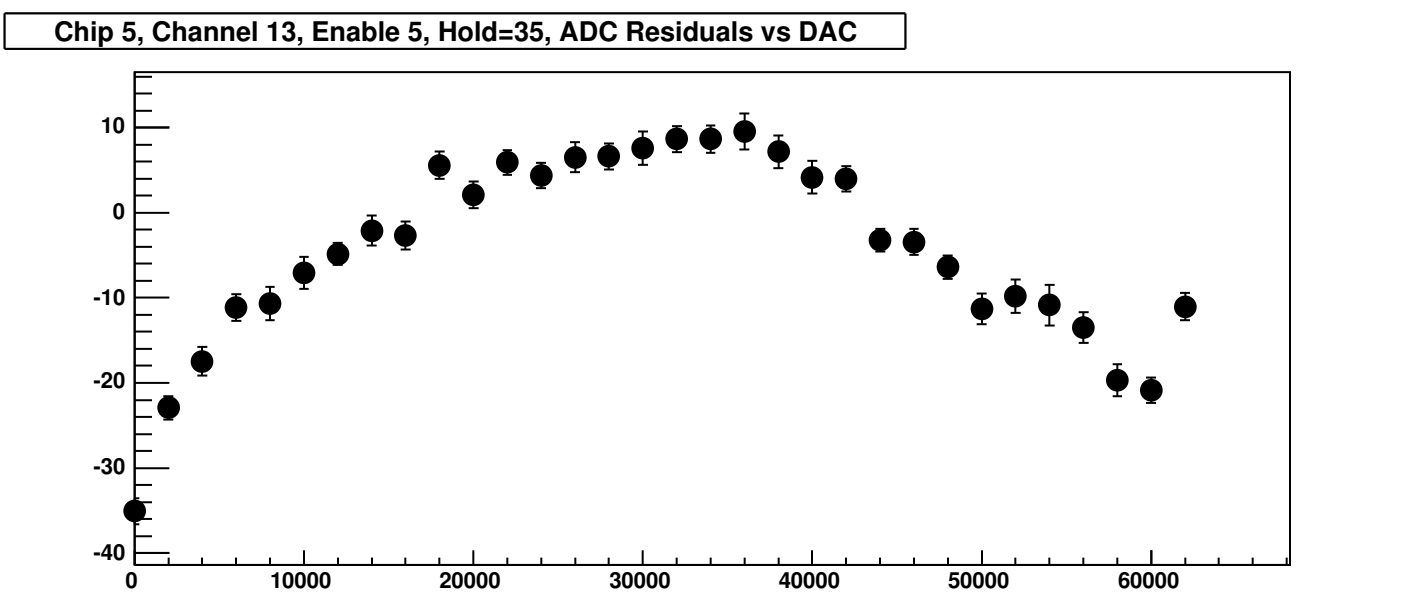

Chip 5, Channel 13, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 13.49 / 23
p0 31.16 ± 0.7713
p1 0.004546 ± 2.314e-05
350
300
250
200
150
100
50
0
0
10000
20000
30000
40000
50000
60000
70000

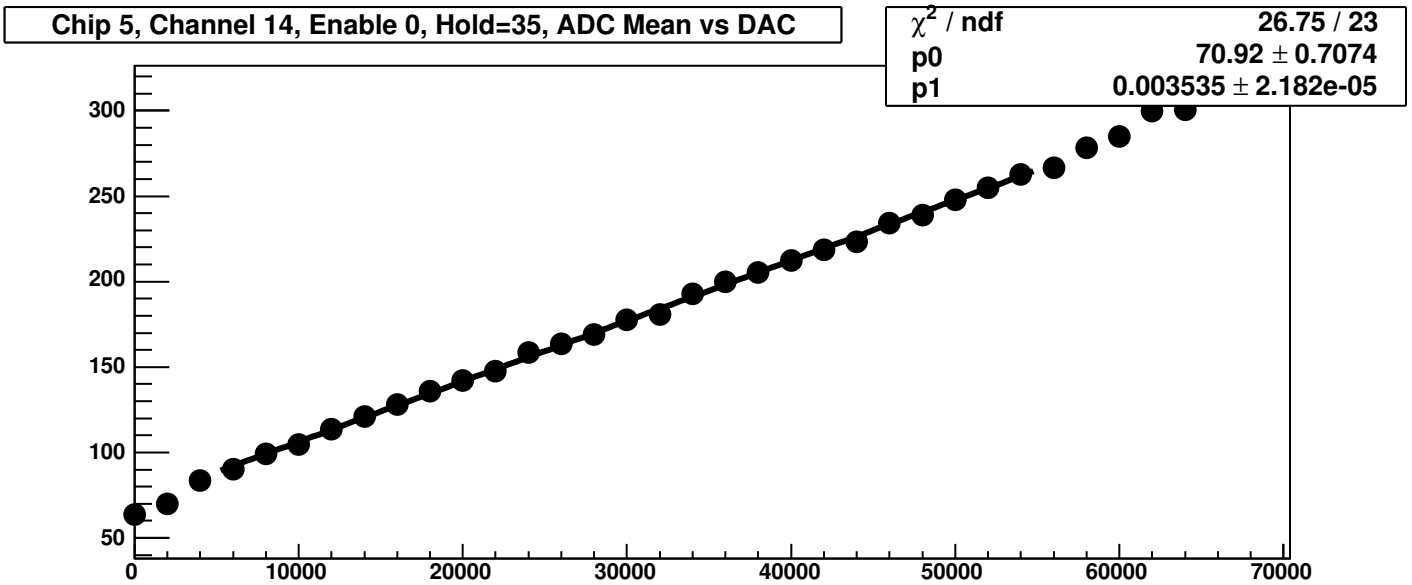

Chip 5, Channel 13, Enable 4, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

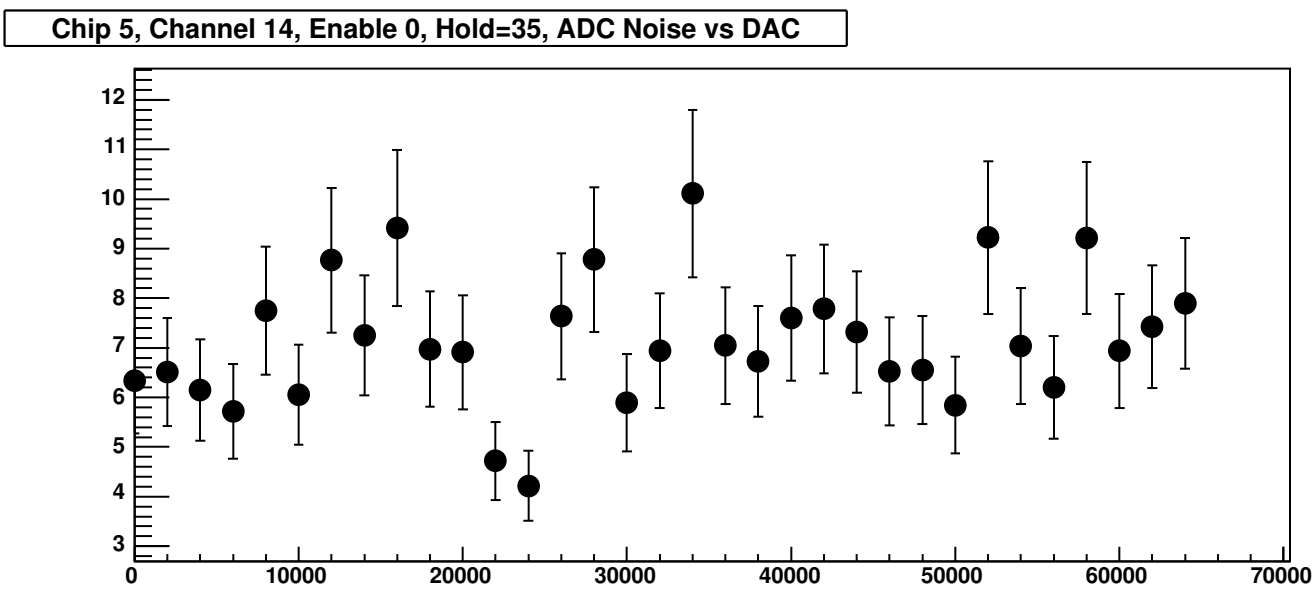

Chip 5, Channel 13, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

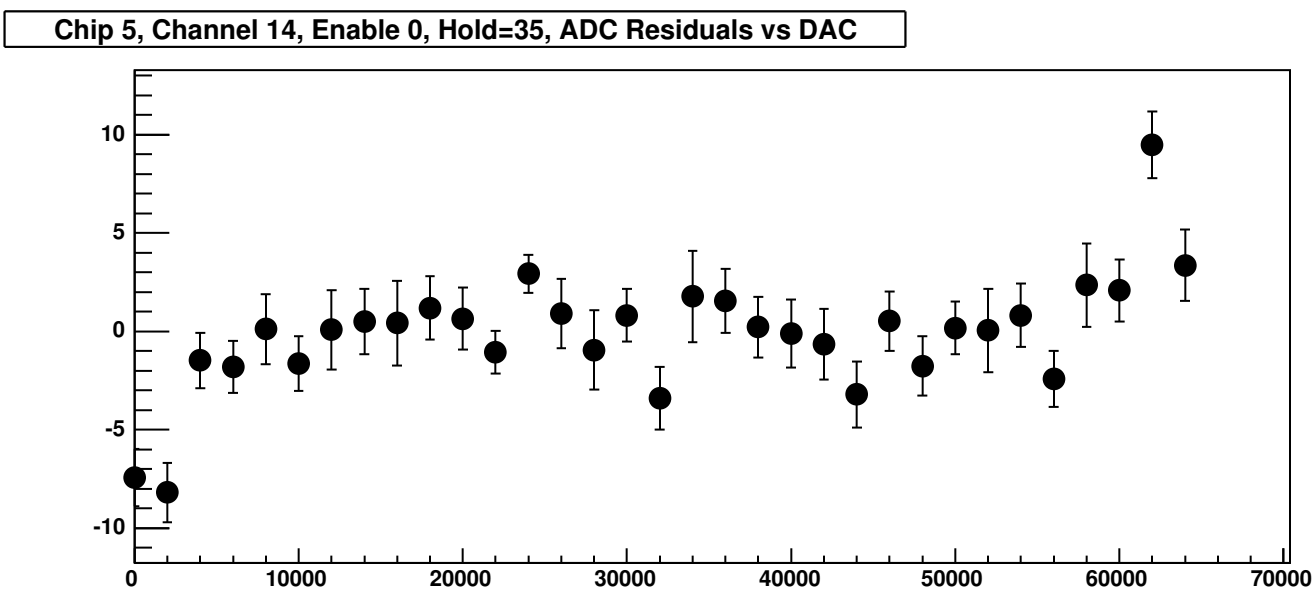

Chip 5, Channel 13, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 419.6 / 23
p0 48.24 ± 0.7654
p1 -0.009682 ± 2.337e-05
0
-100
-200
-300
-400
-500
-600
0
10000
20000
30000
40000
50000
60000

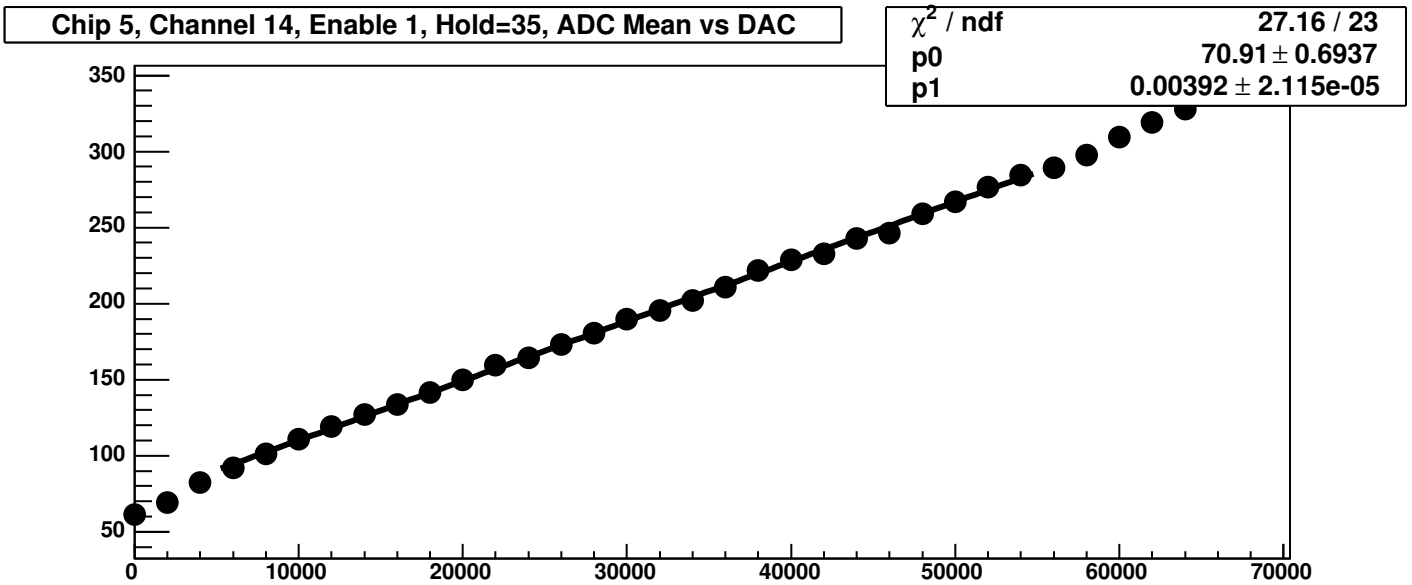

Chip 5, Channel 13, Enable 5, Hold=35, ADC Noise vs DAC
13
12
11
10
9
8
7
6
5
0
10000
20000
30000
40000
50000
60000

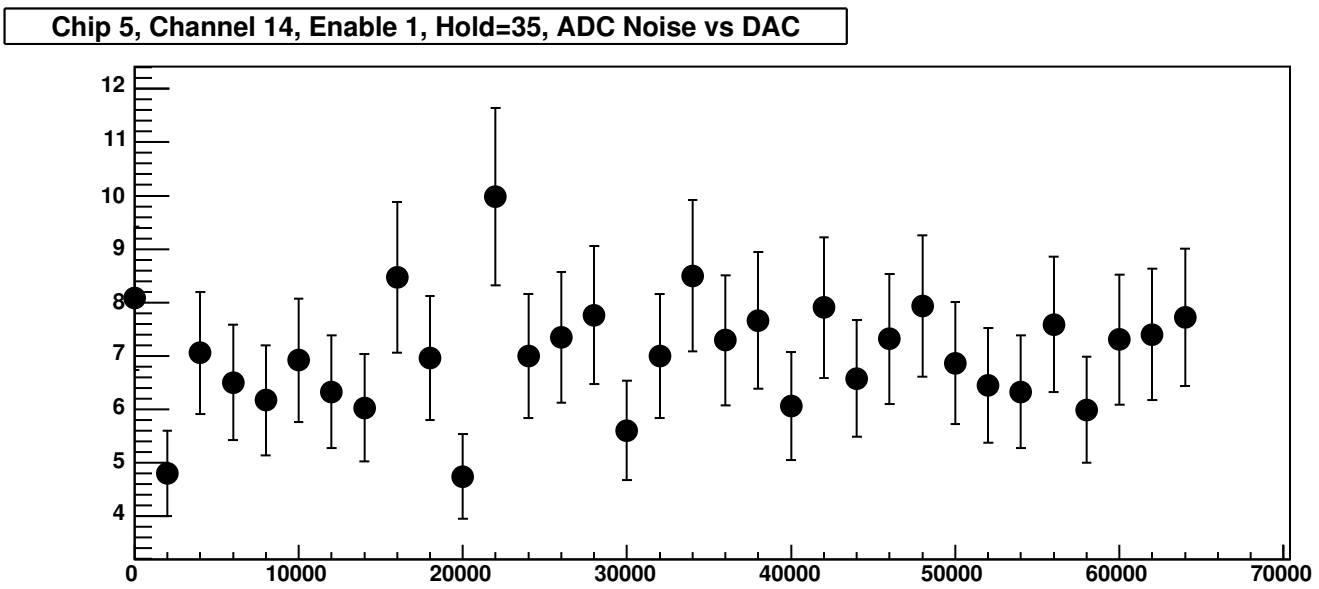

Chip 5, Channel 13, Enable 5, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
-40
0
10000
20000
30000
40000
50000
60000

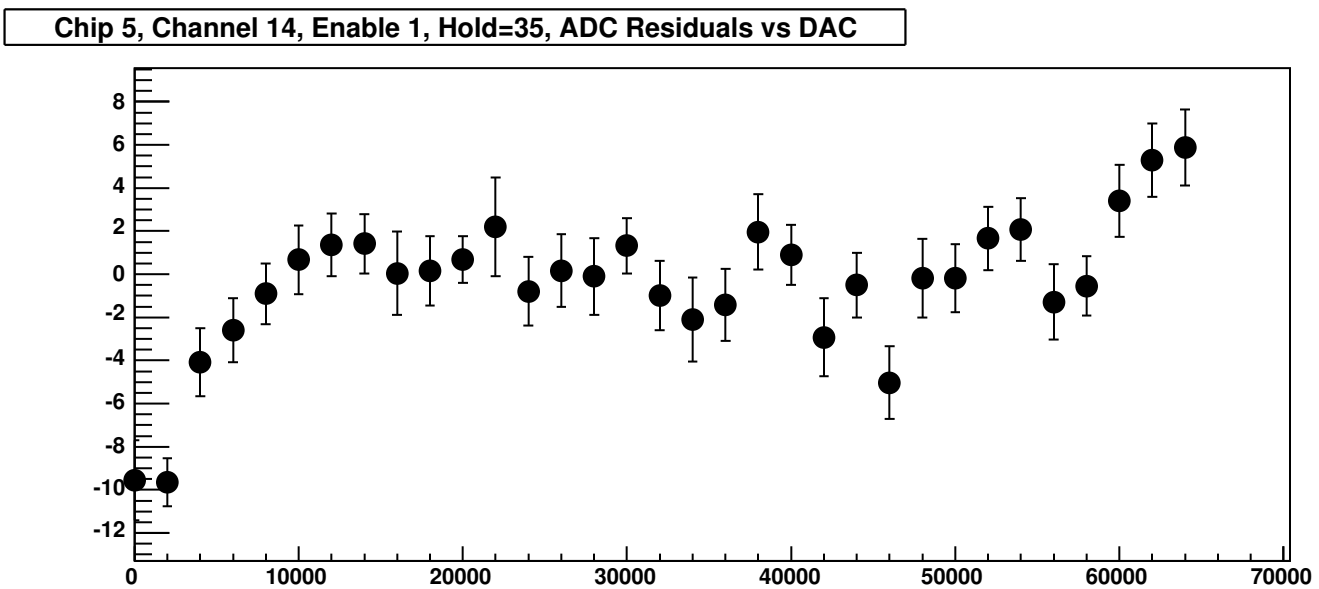

Chip 5, Channel 14, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 26.75 / 23
p0 70.92 ± 0.7074
p1 0.003535 ± 2.182e-05
300
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

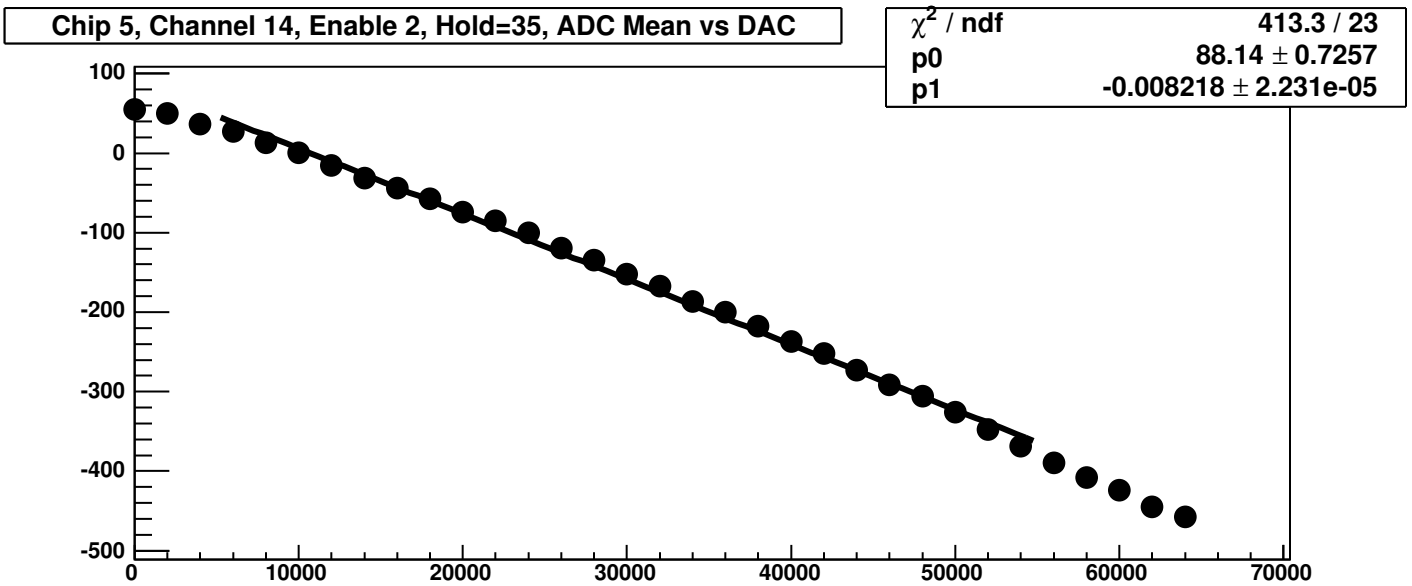

Chip 5, Channel 14, Enable 0, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
3
0
10000
20000
30000
40000
50000
60000
70000

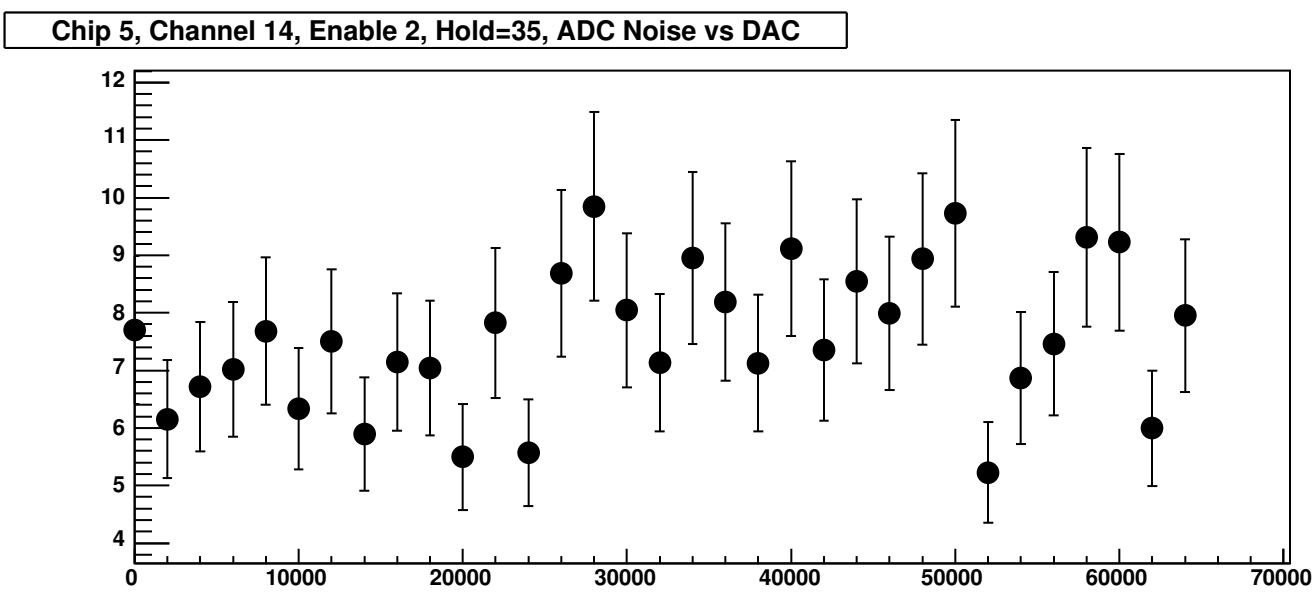

Chip 5, Channel 14, Enable 0, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

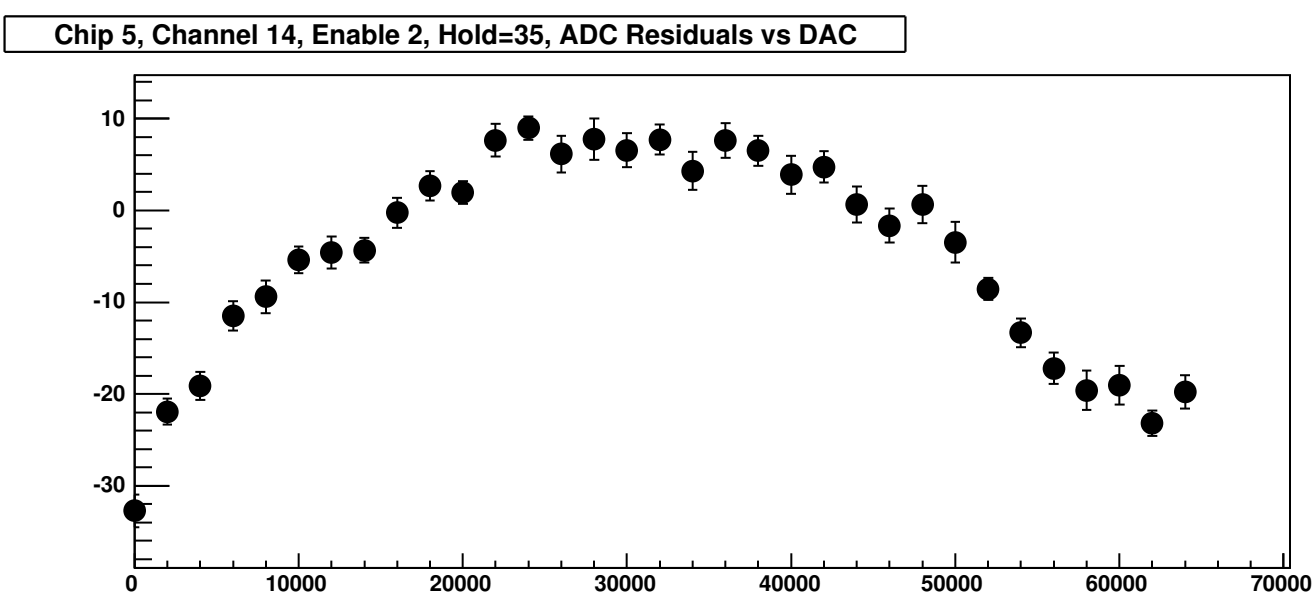

Chip 5, Channel 14, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 27.16 / 23
p0 70.91 ± 0.6937
p1 0.00392 ± 2.115e-05
350
300
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

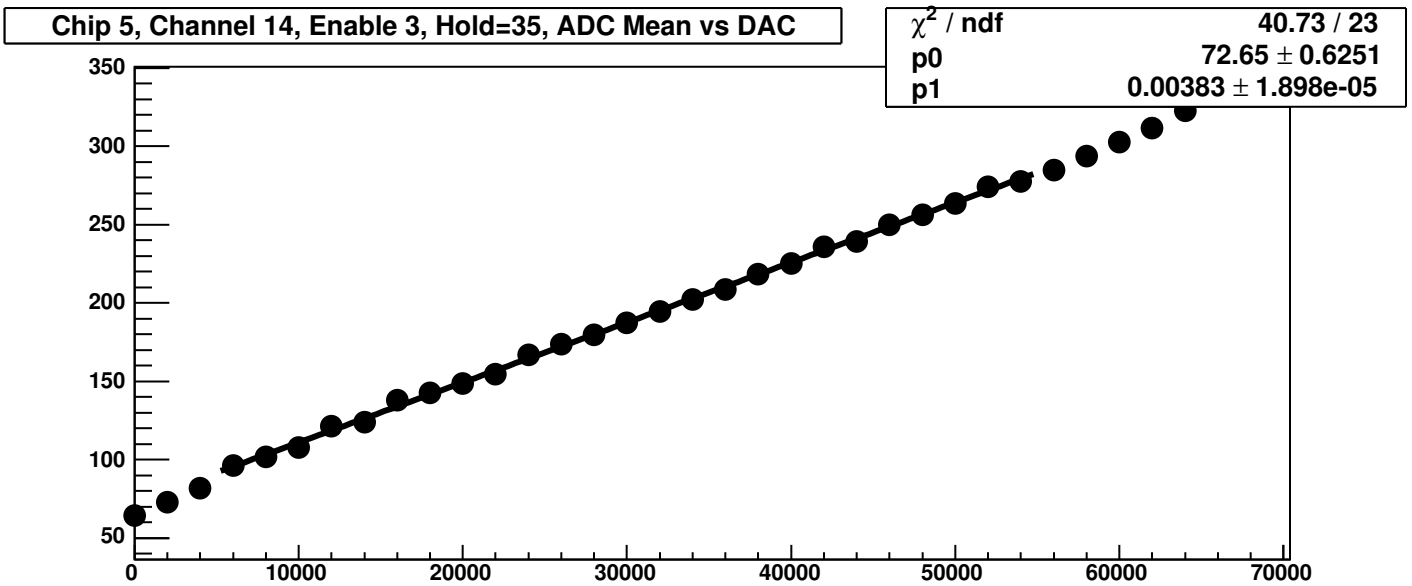

Chip 5, Channel 14, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

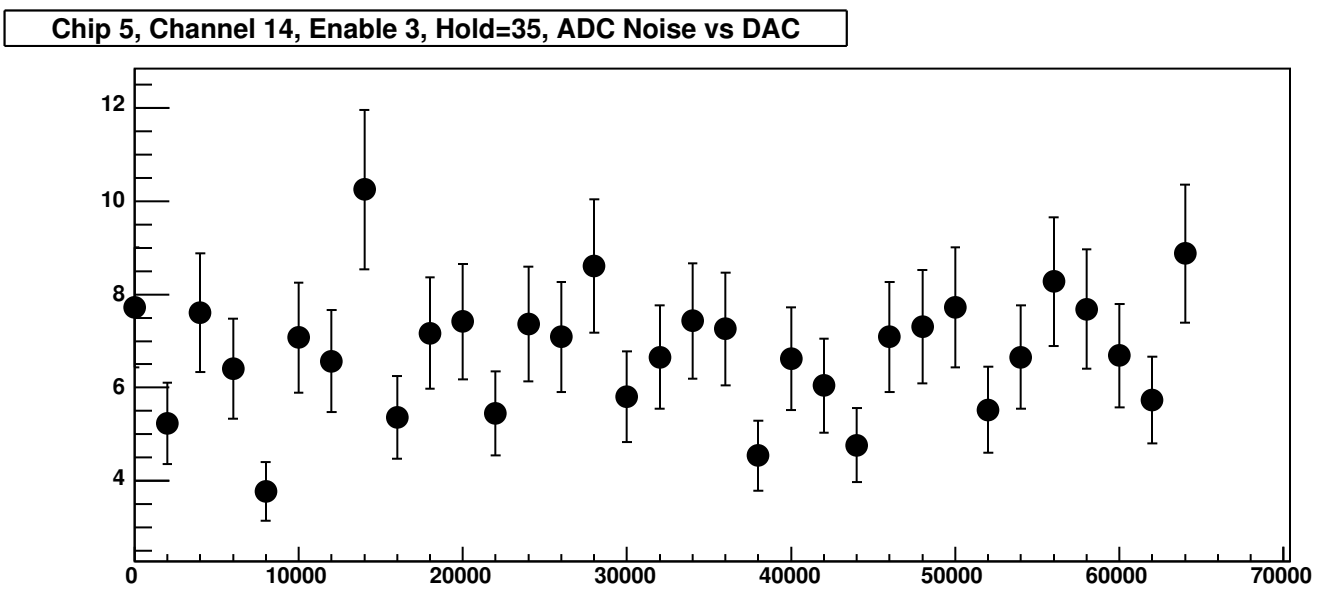

Chip 5, Channel 14, Enable 1, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
0
10000
20000
30000
40000
50000
60000
70000

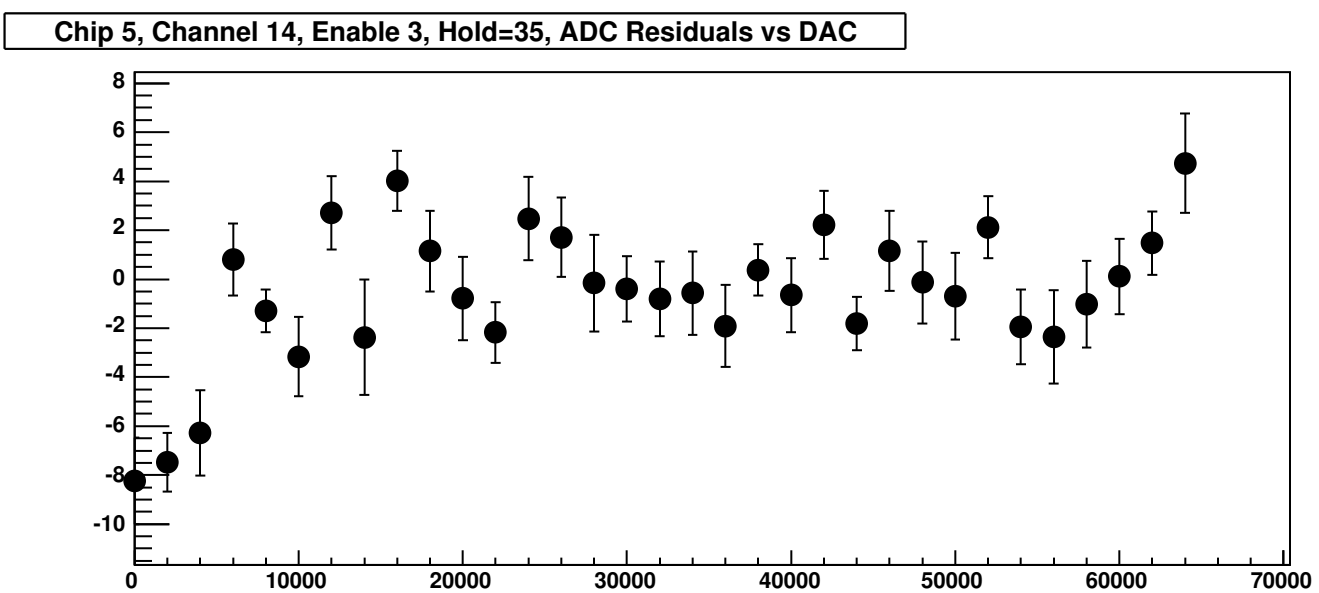

Chip 5, Channel 14, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 413.3 / 23
p0 88.14 ± 0.7257
p1 -0.008218 ± 2.231e-05
100
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000
70000

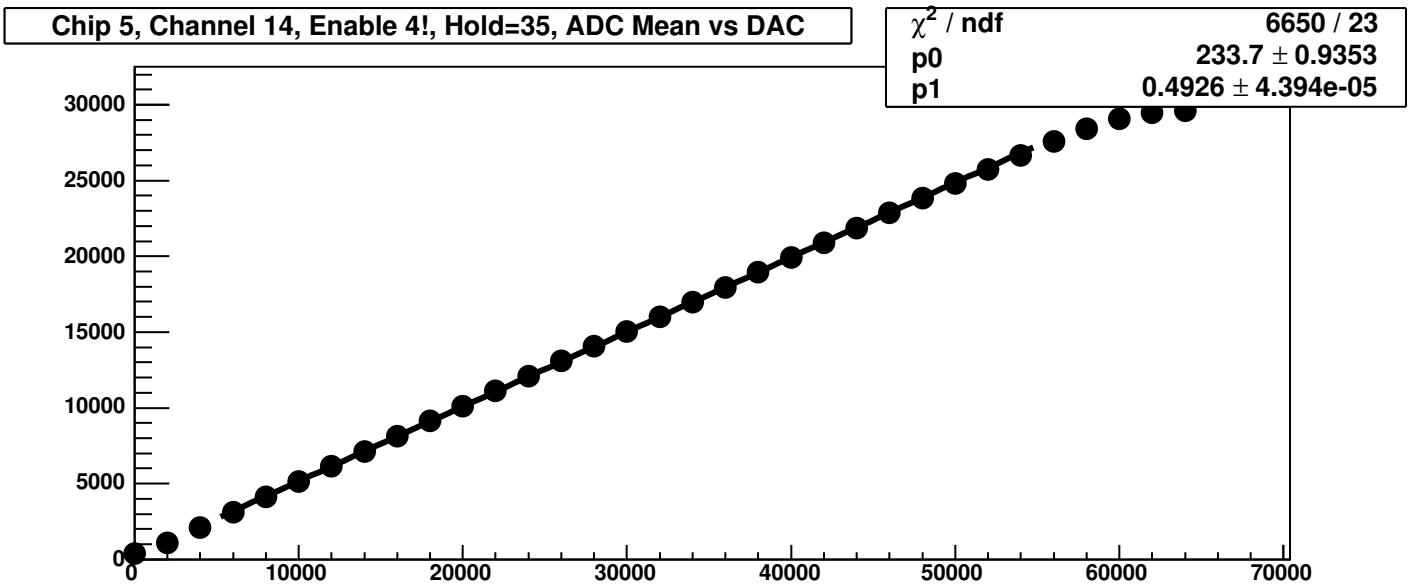

Chip 5, Channel 14, Enable 2, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

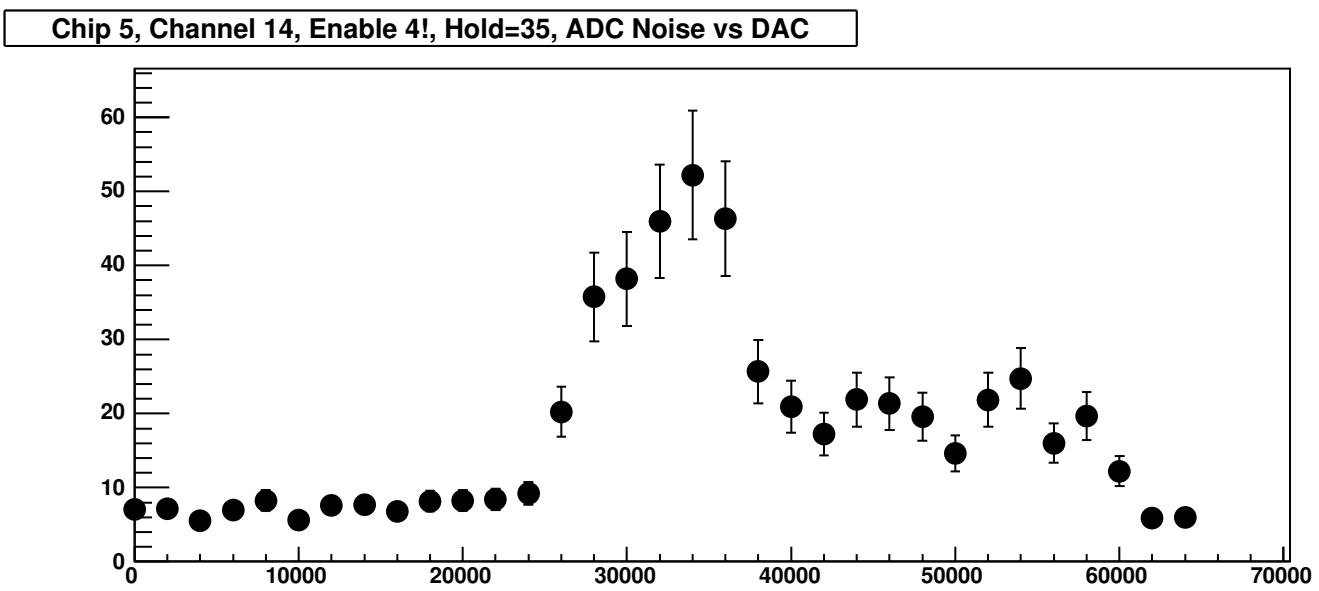

Chip 5, Channel 14, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
10000
20000
30000
40000
50000
60000
70000

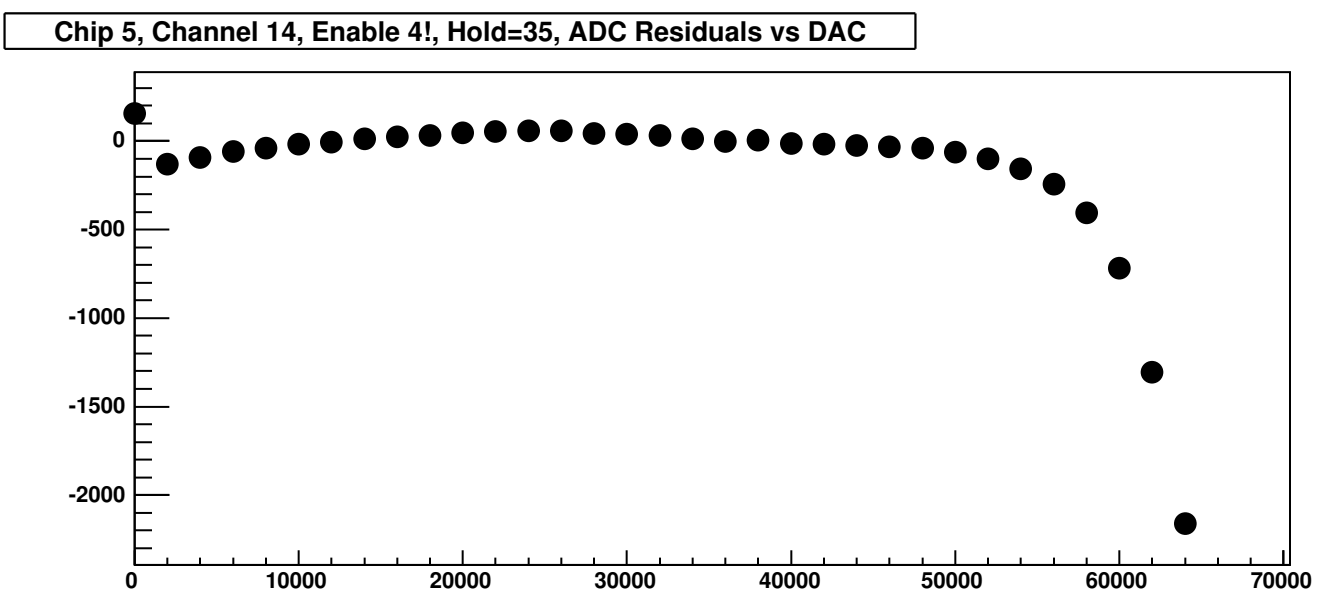

Chip 5, Channel 14, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 40.73 / 23
p0 72.65 ± 0.6251
p1 0.00383 ± 1.898e-05
350
300
250
200
150
100
50
0
10000
20000
30000
40000
50000
60000
70000

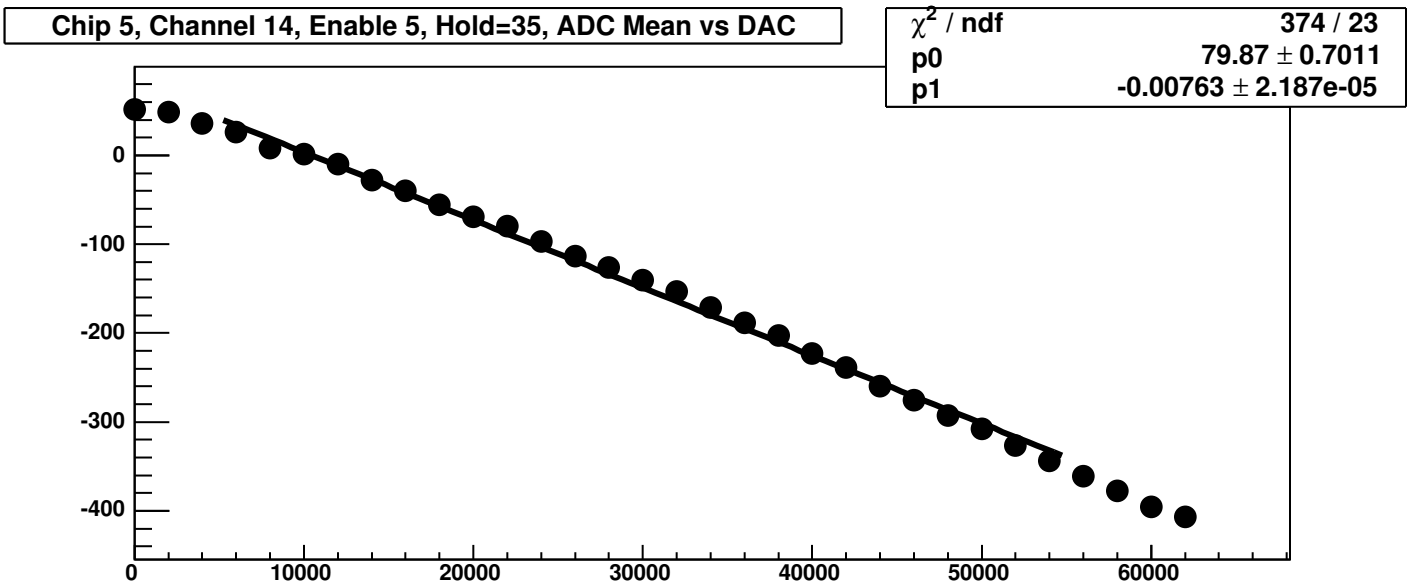

Chip 5, Channel 14, Enable 3, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

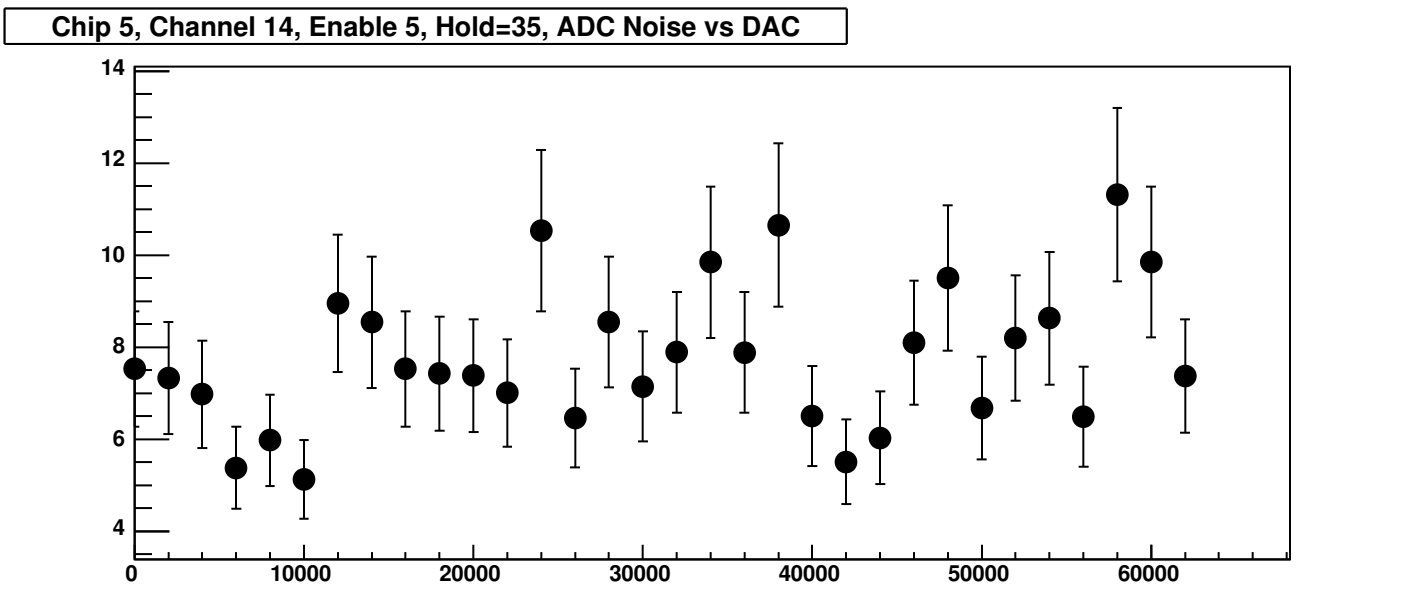

Chip 5, Channel 14, Enable 3, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
0
10000
20000
30000
40000
50000
60000
70000

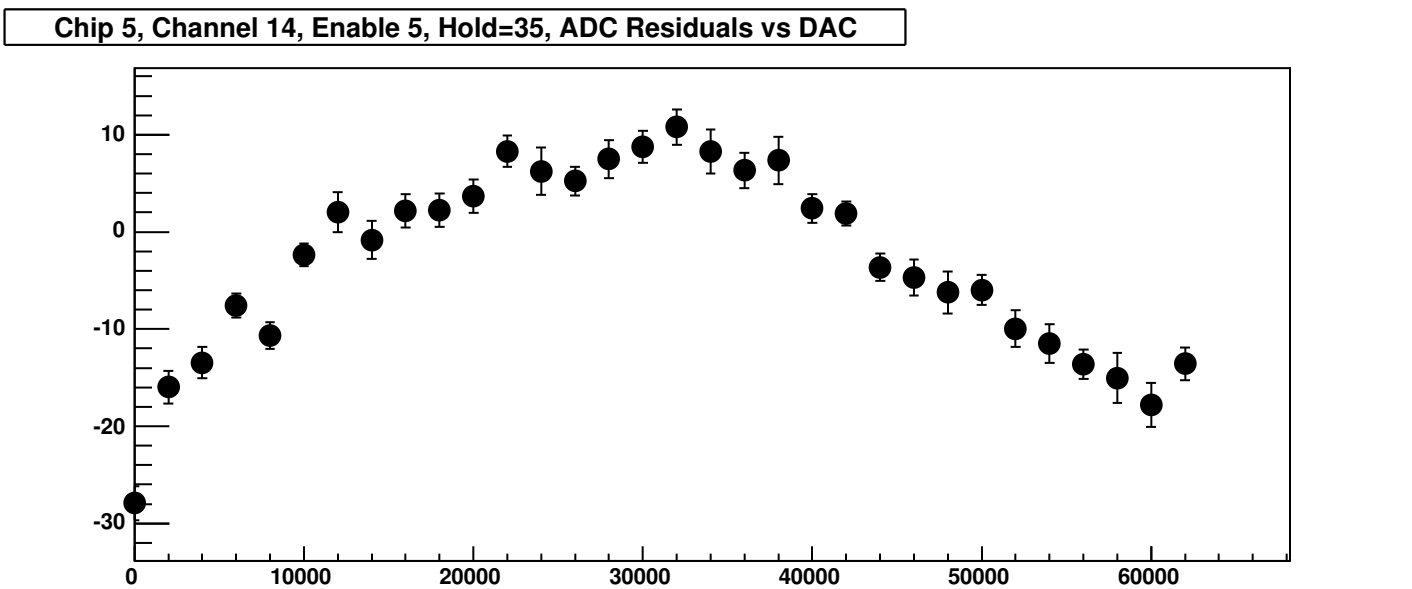

Chip 5, Channel 14, Enable 4!, Hold=35, ADC Mean vs DAC
χ² / ndf 6650 / 23
p0 233.7 ± 0.9353
p1 0.4926 ± 4.394e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 14, Enable 4!, Hold=35, ADC Noise vs DAC
60
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 14, Enable 4!, Hold=35, ADC Residuals vs DAC
0
-500
-1000
-1500
-2000
0
10000
20000
30000
40000
50000
60000
70000

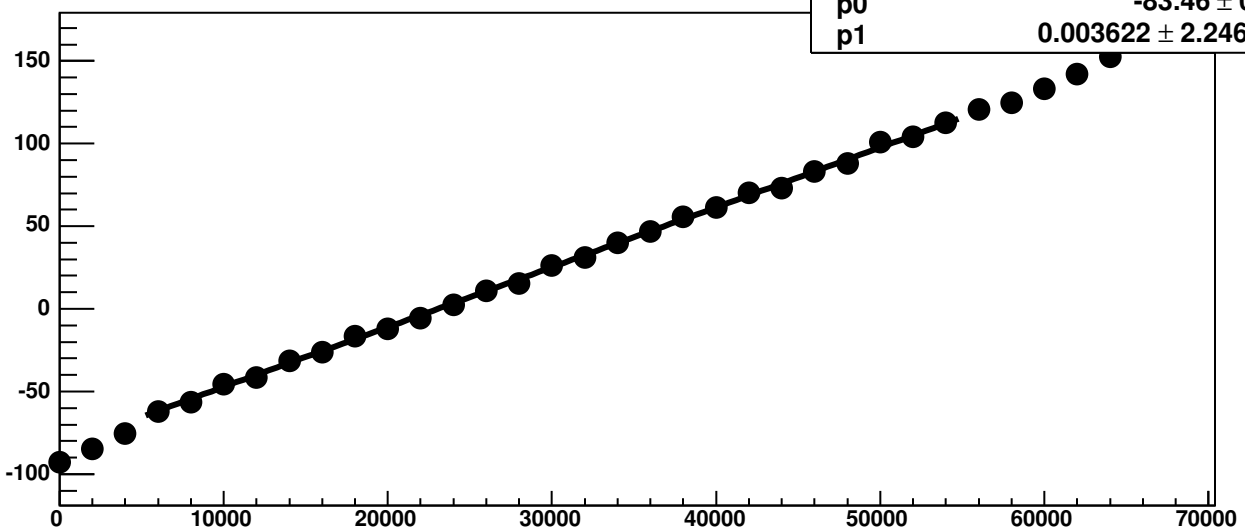

Chip 5, Channel 14, Enable 5, Hold=35, ADC Mean vs DAC
χ² / ndf 374 / 23
p0 79.87 ± 0.7011
p1 -0.00763 ± 2.187e-05
0
-100
-200
-300
-400
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 14, Enable 5, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000

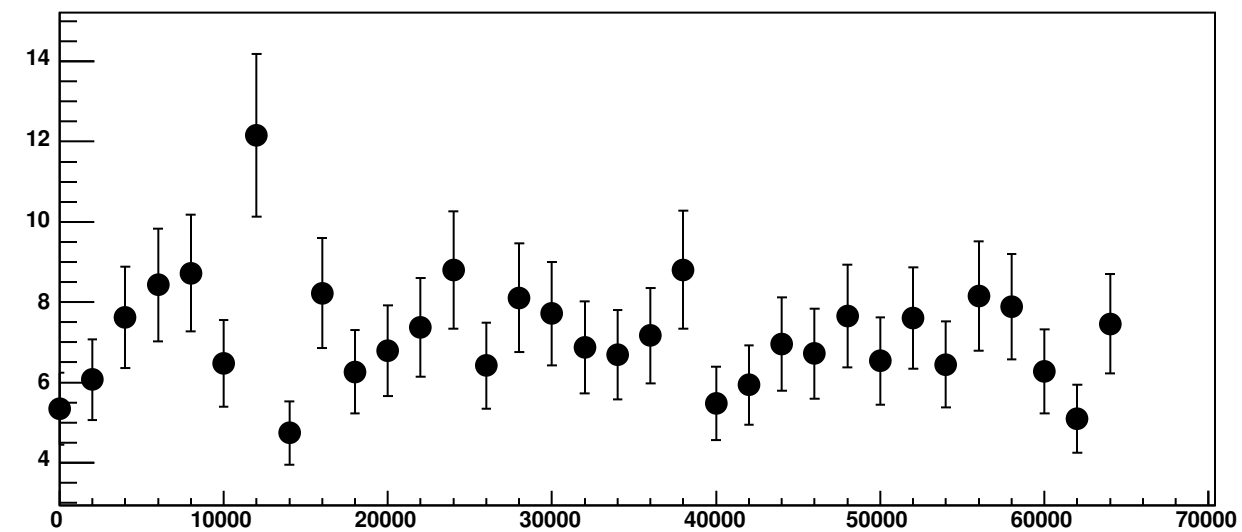

Chip 5, Channel 14, Enable 5, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
10000
20000
30000
40000
50000
60000

Chip 5, Channel 15, Enable 0, Hold=35, ADC Mean vs DAC

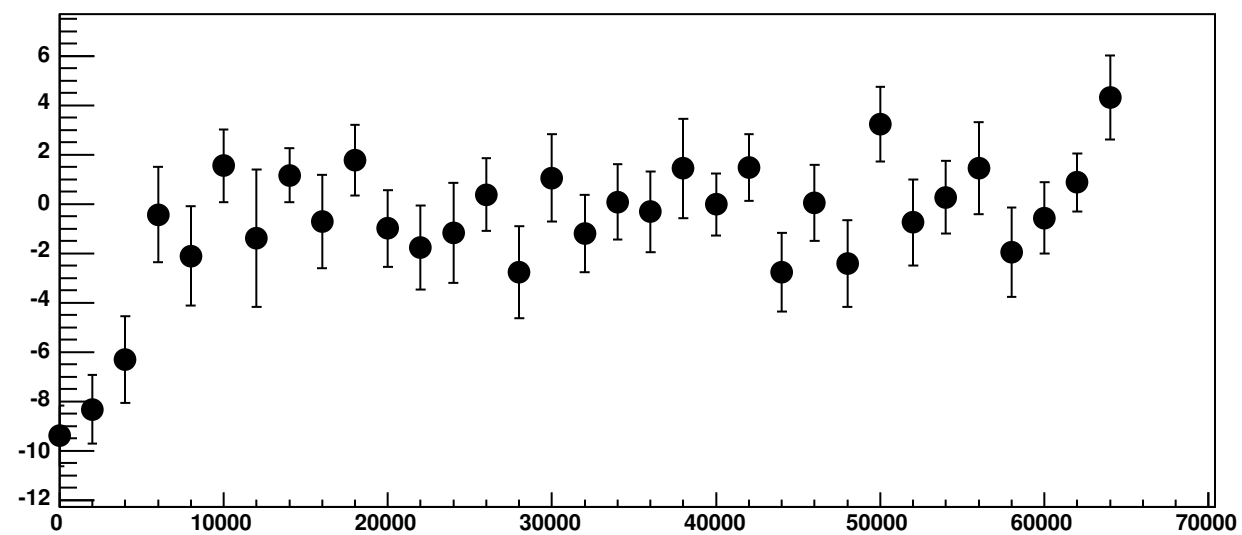
χ^2 / ndf	21.81 / 23
p0	-83.46 ± 0.761
p1	0.003622 ± 2.246e-05

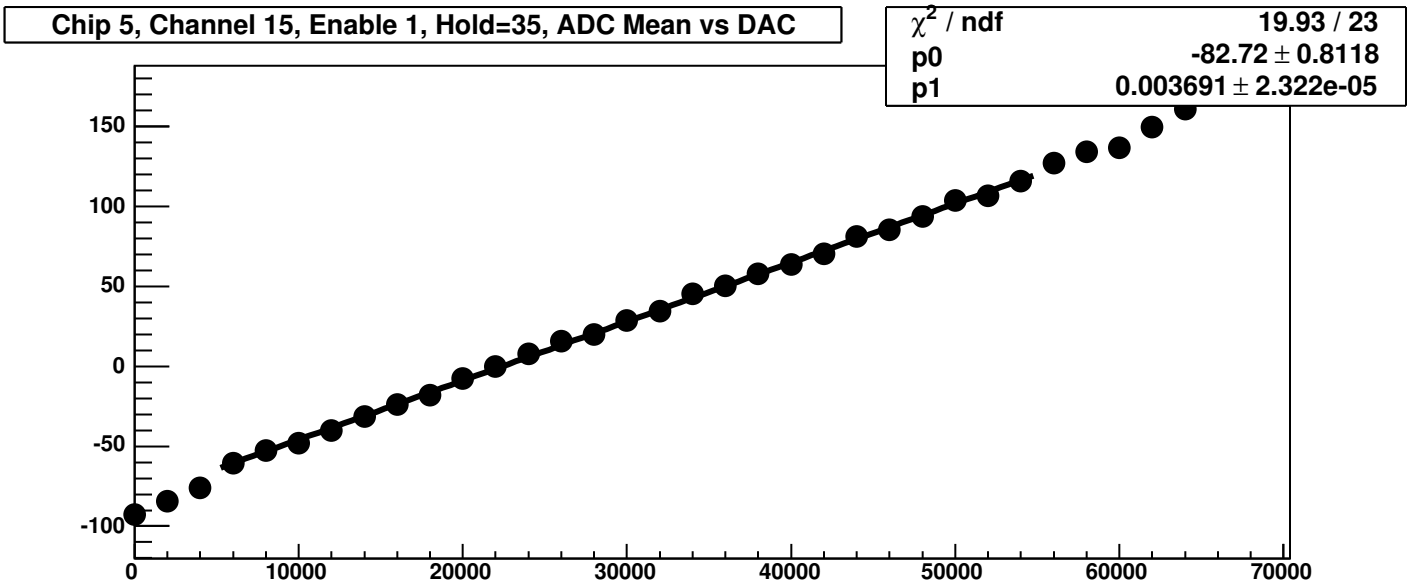


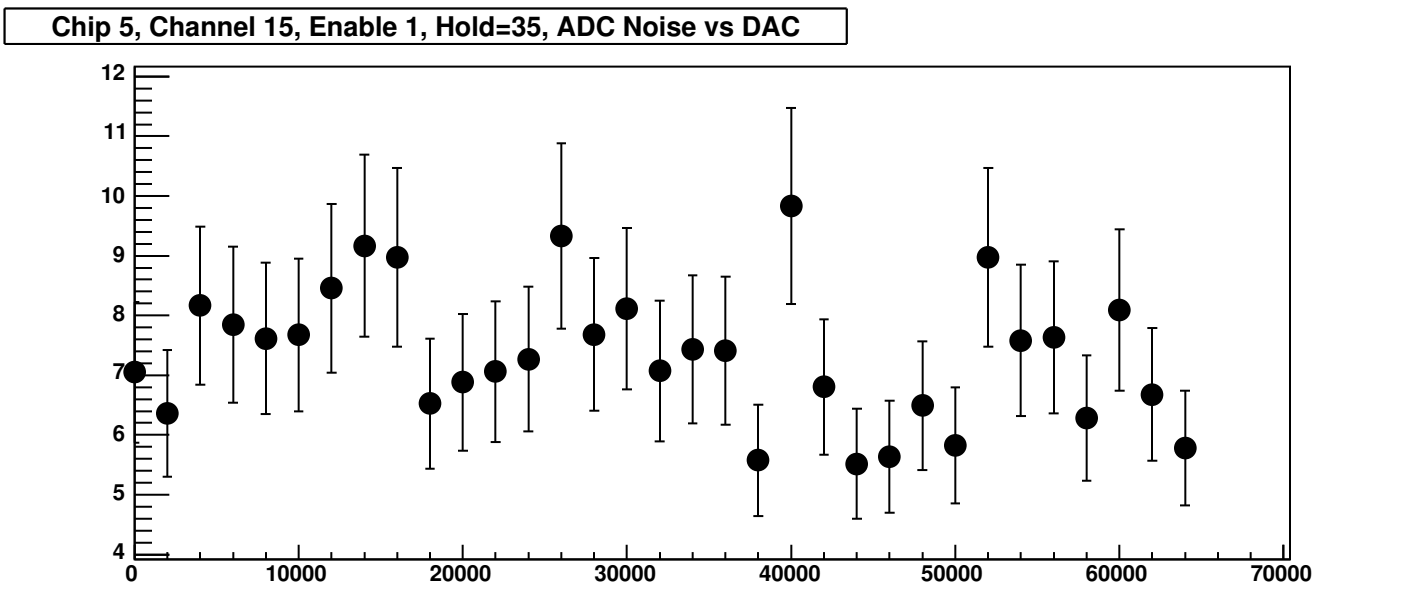
Chip 5, Channel 15, Enable 0, Hold=35, ADC Noise vs DAC

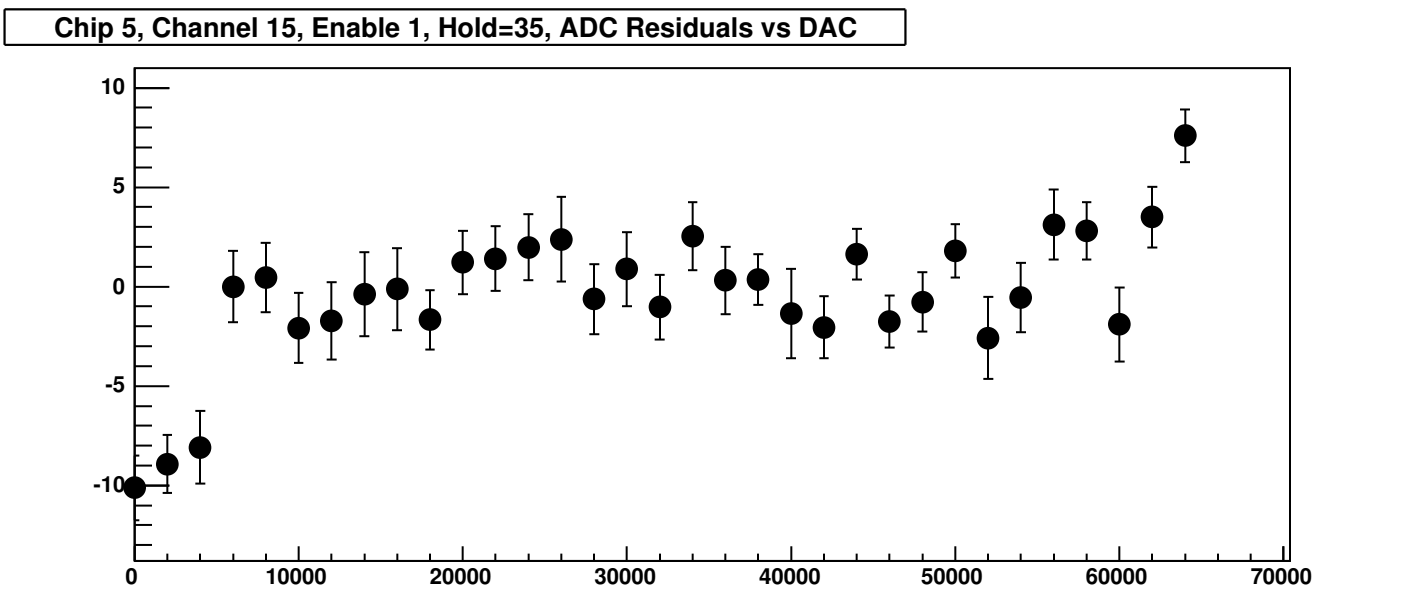


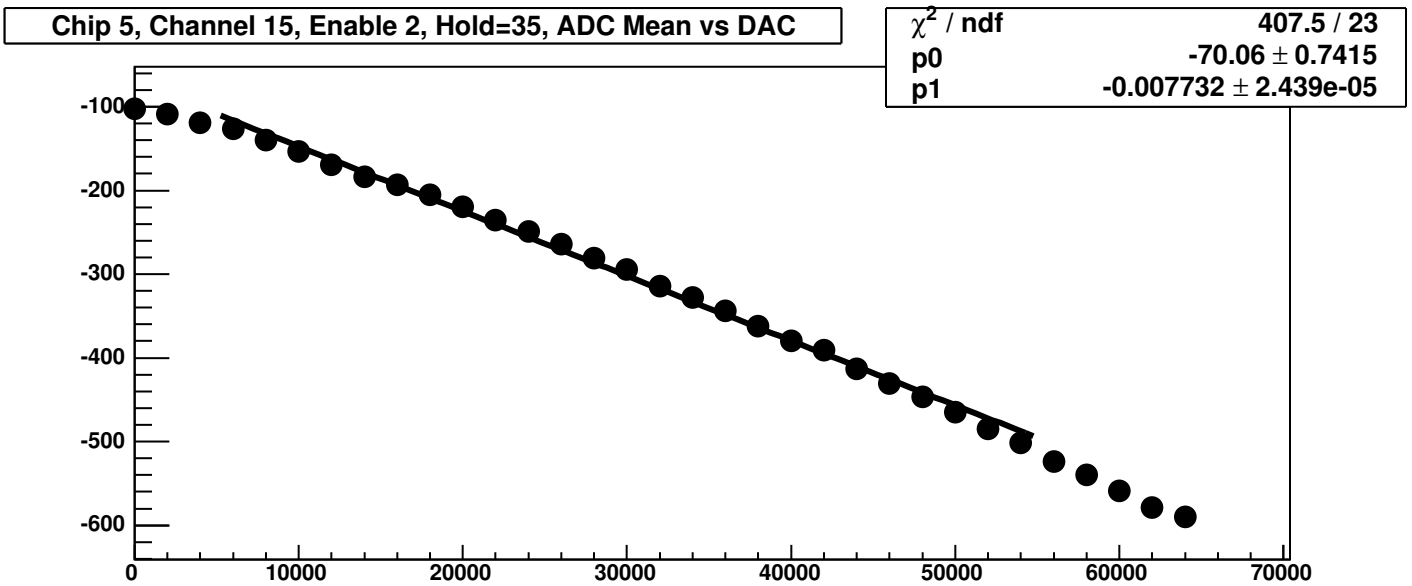
Chip 5, Channel 15, Enable 0, Hold=35, ADC Residuals vs DAC

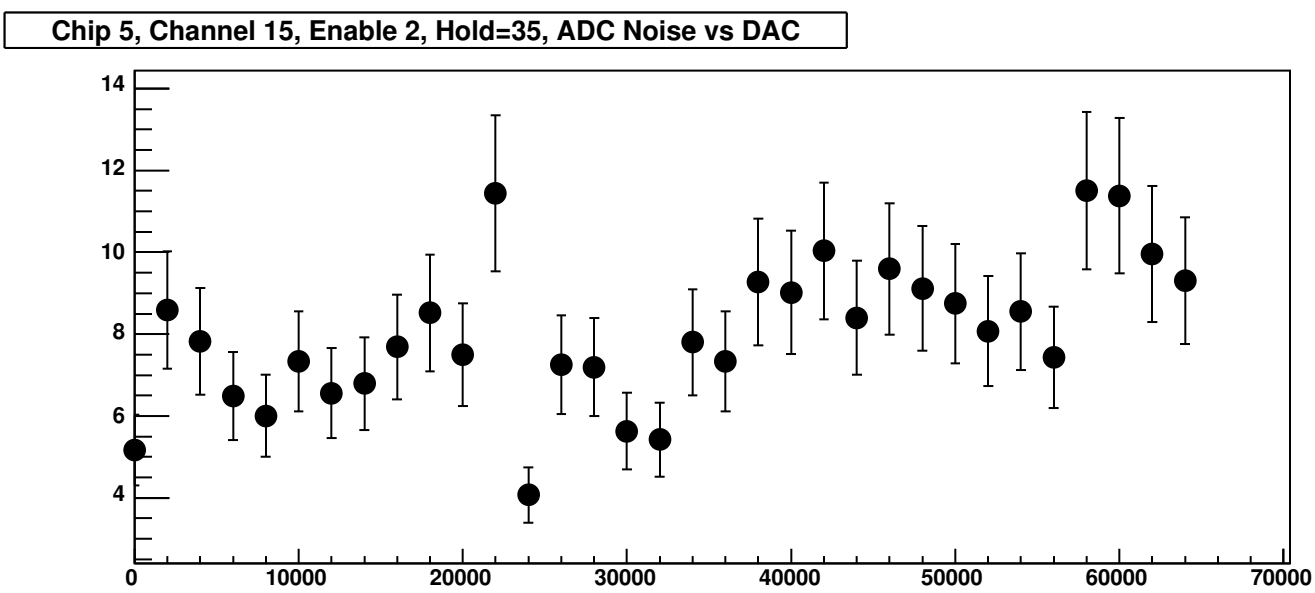


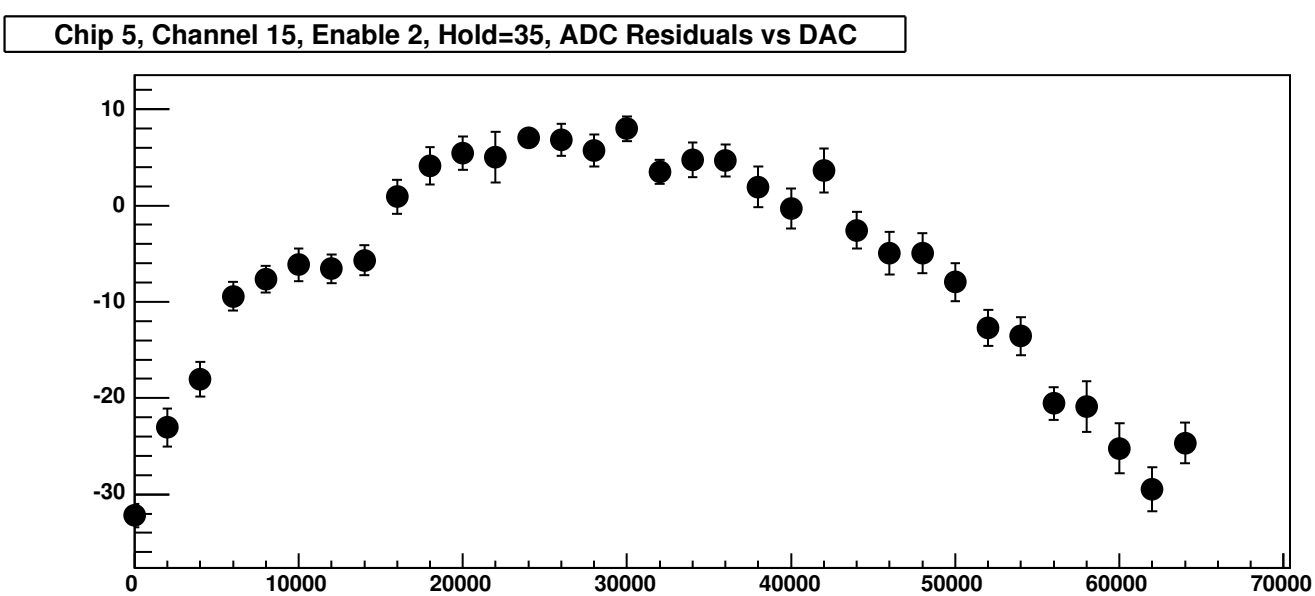

Chip 5, Channel 15, Enable 1, Hold=35, ADC Mean vs DAC
χ² / ndf 19.93 / 23
p0 -82.72 ± 0.8118
p1 0.003691 ± 2.322e-05
150
100
50
0
-50
-100
0
10000
20000
30000
40000
50000
60000
70000

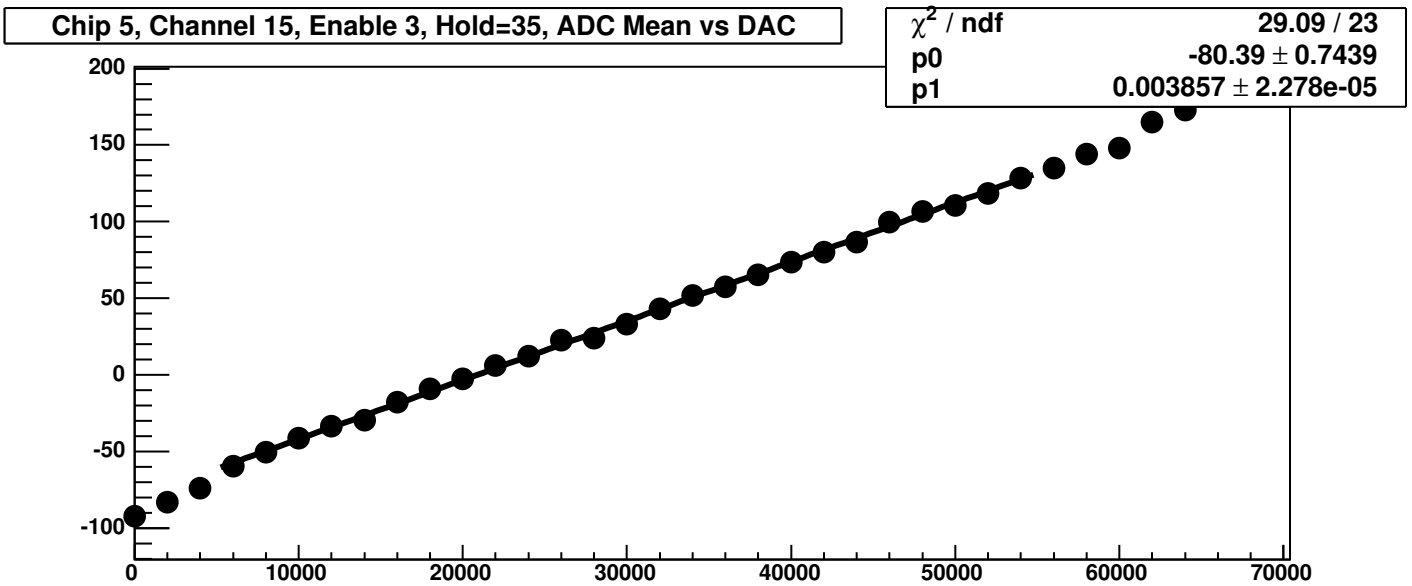

Chip 5, Channel 15, Enable 1, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

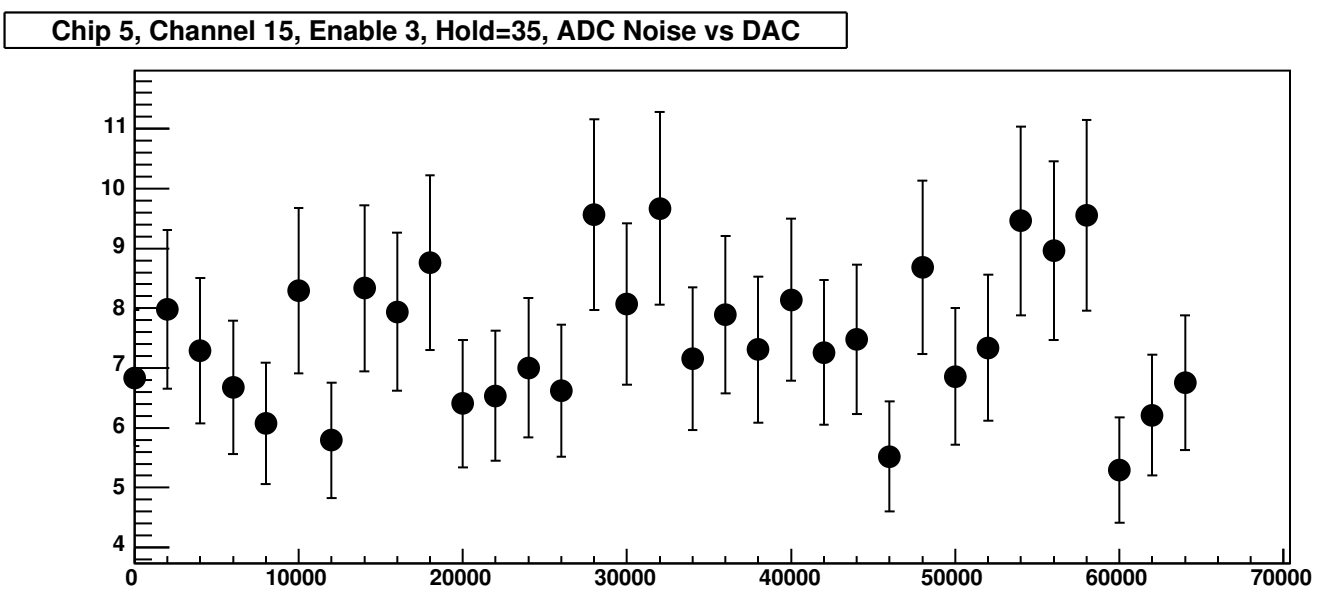

Chip 5, Channel 15, Enable 1, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
0
10000
20000
30000
40000
50000
60000
70000

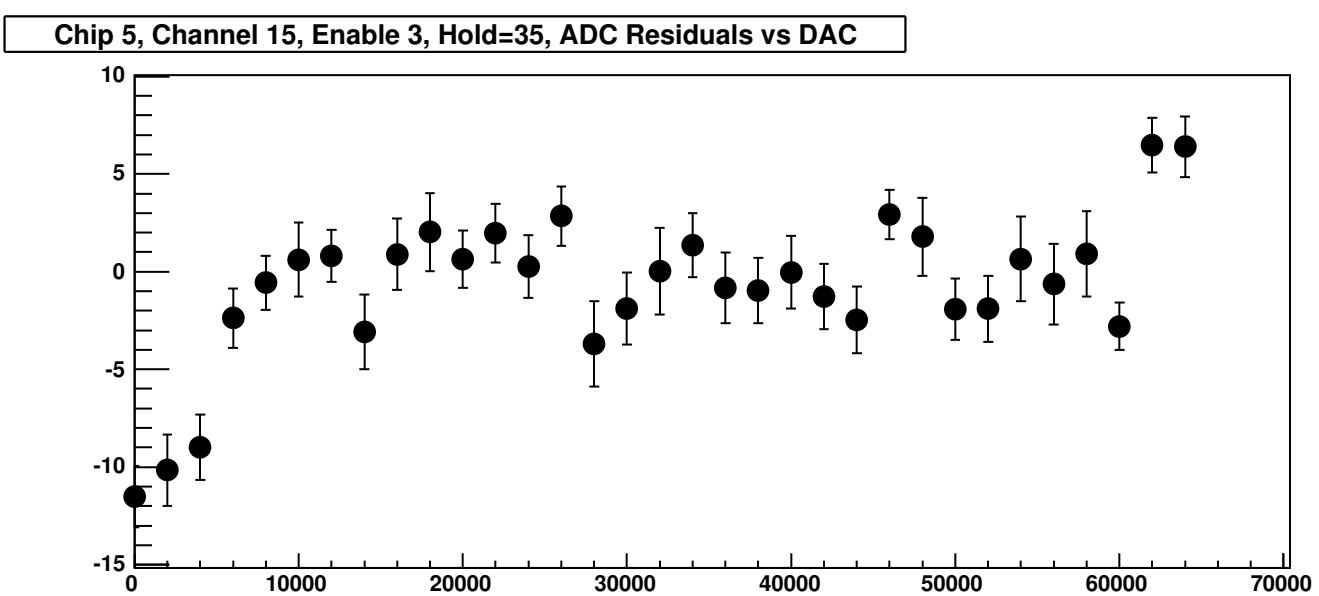

Chip 5, Channel 15, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 407.5 / 23
p0 -70.06 ± 0.7415
p1 -0.007732 ± 2.439e-05
-100
-200
-300
-400
-500
-600
0
10000
20000
30000
40000
50000
60000
70000

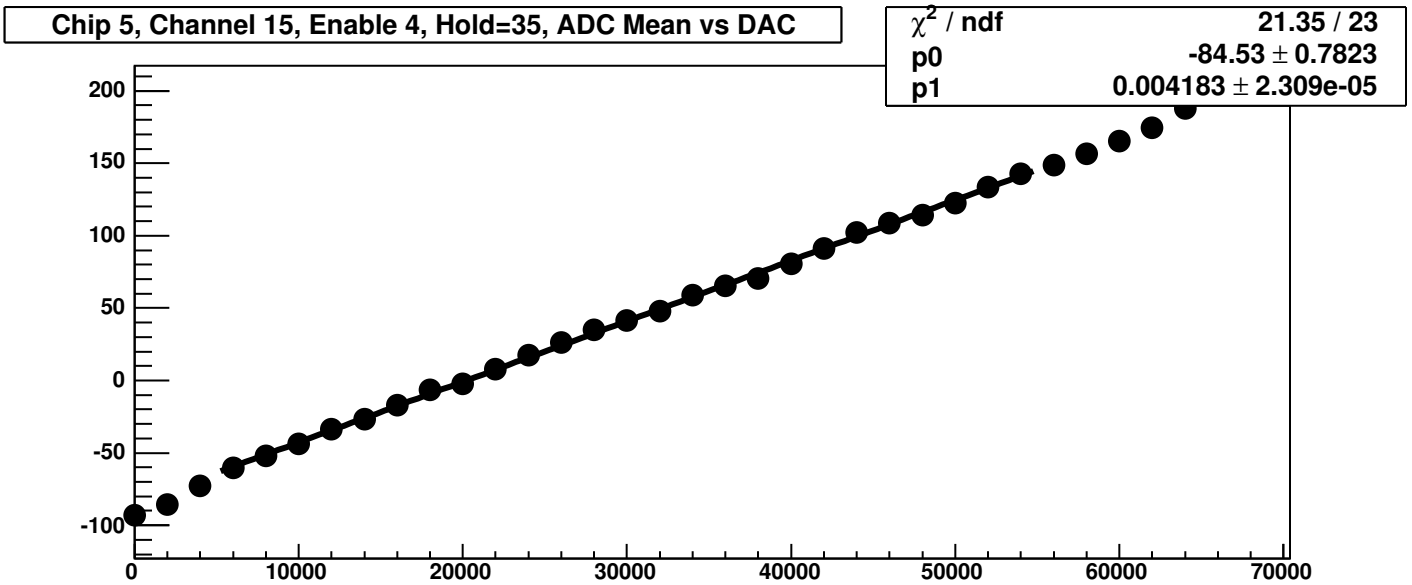

Chip 5, Channel 15, Enable 2, Hold=35, ADC Noise vs DAC
14
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000

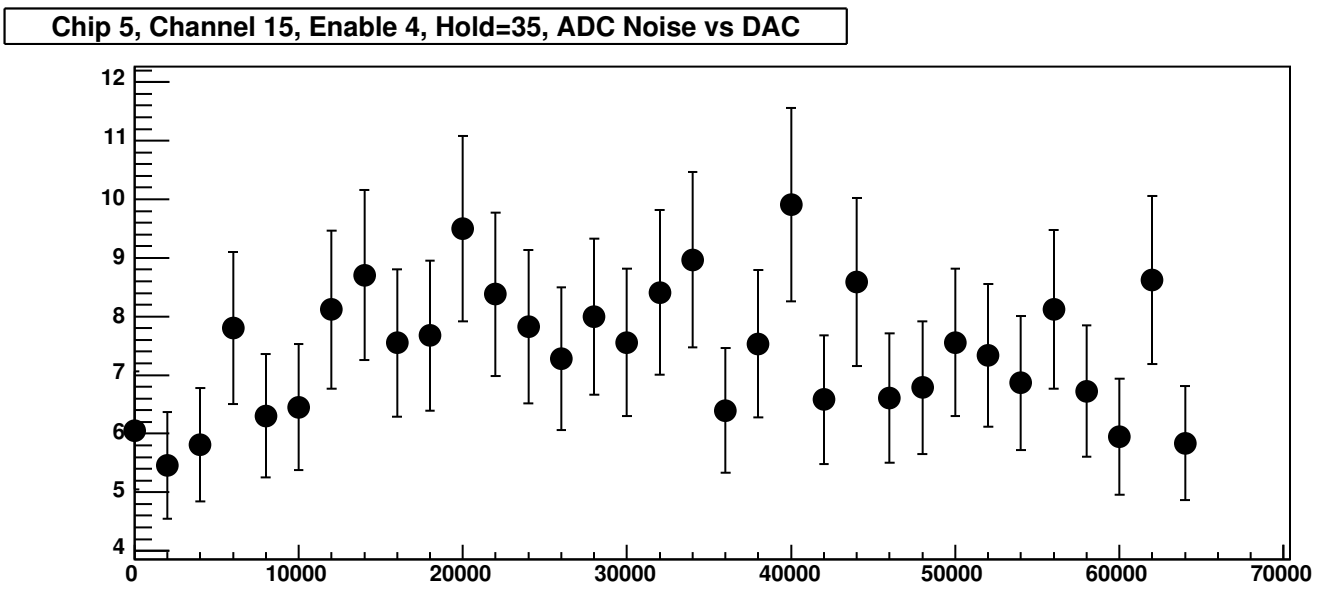

Chip 5, Channel 15, Enable 2, Hold=35, ADC Residuals vs DAC
10
0
-10
-20
-30
0
10000
20000
30000
40000
50000
60000
70000

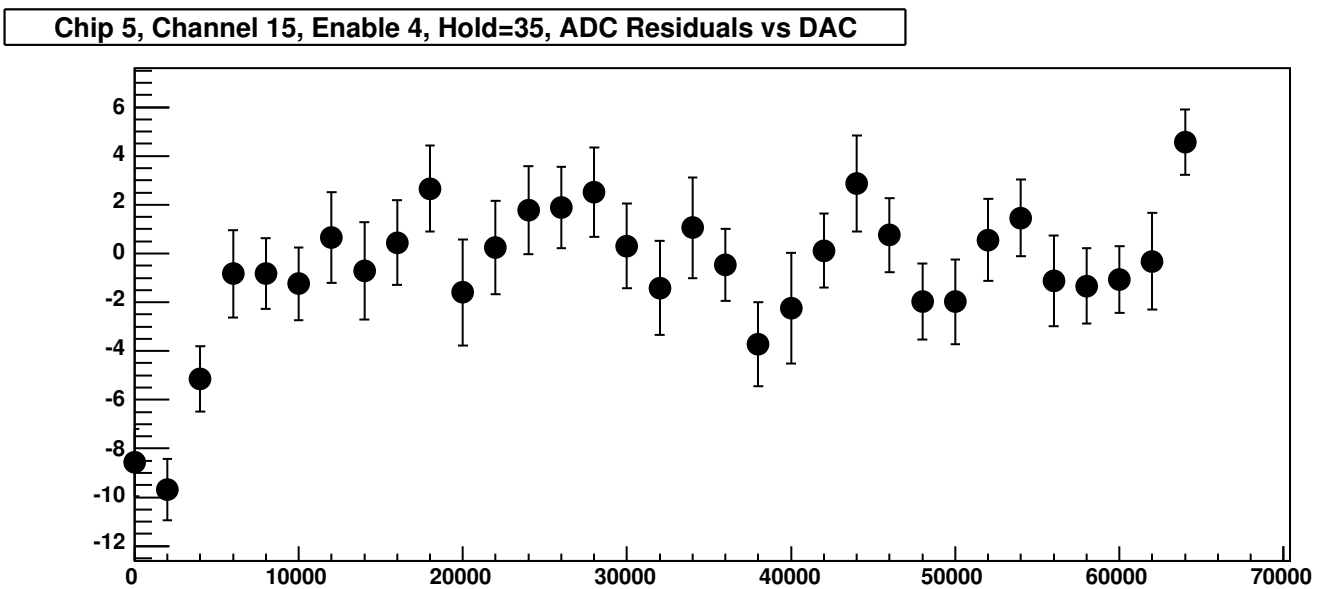

Chip 5, Channel 15, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 29.09 / 23
p0 -80.39 ± 0.7439
p1 0.003857 ± 2.278e-05
200
150
100
50
0
-50
-100
0
10000
20000
30000
40000
50000
60000
70000

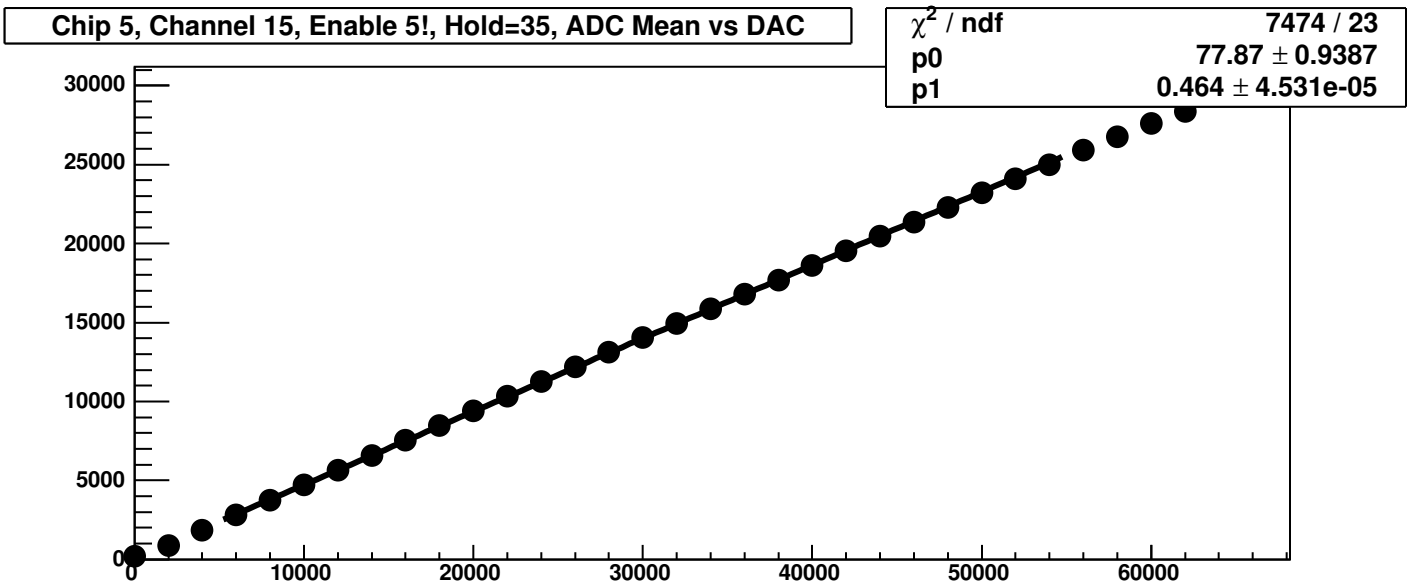

Chip 5, Channel 15, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

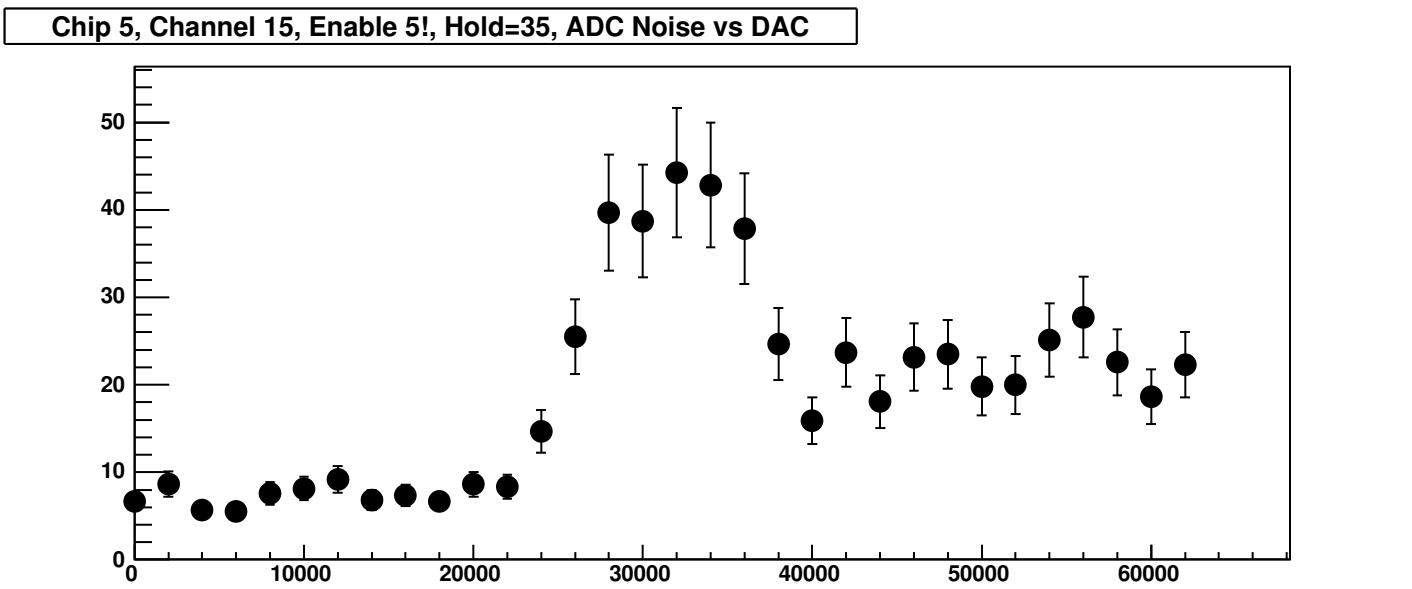

Chip 5, Channel 15, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

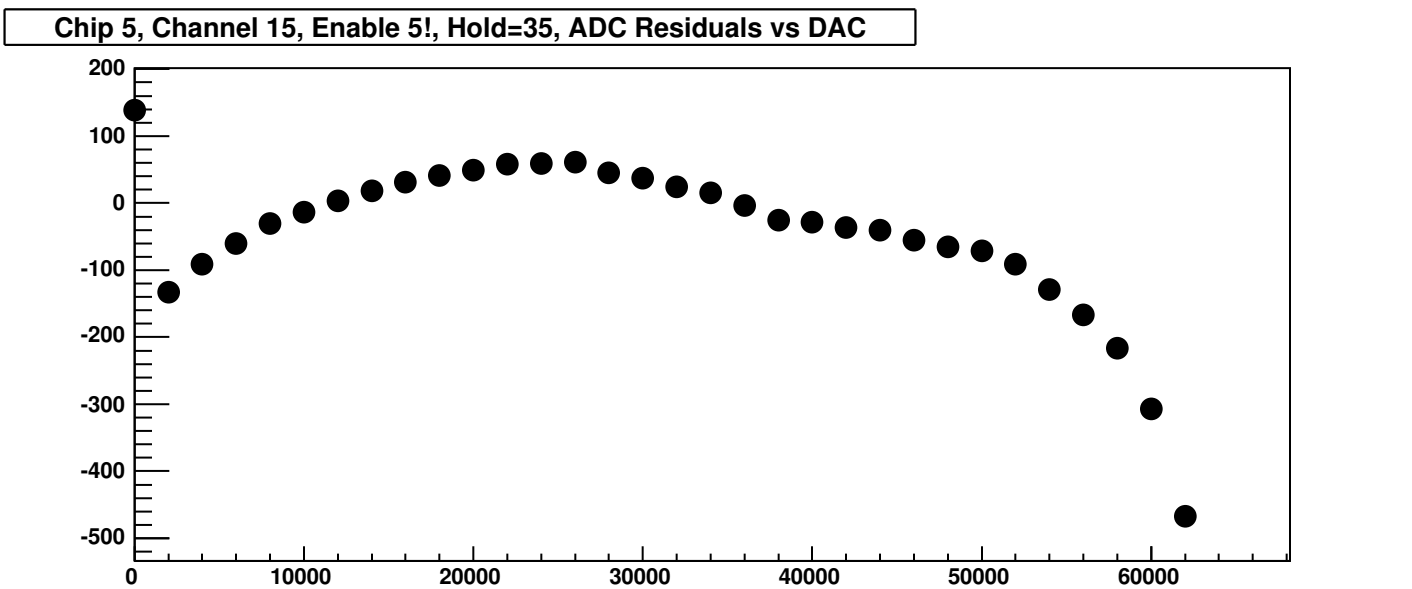

Chip 5, Channel 15, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 21.35 / 23
p0 -84.53 ± 0.7823
p1 0.004183 ± 2.309e-05
200
150
100
50
0
-50
-100
0
10000
20000
30000
40000
50000
60000
70000

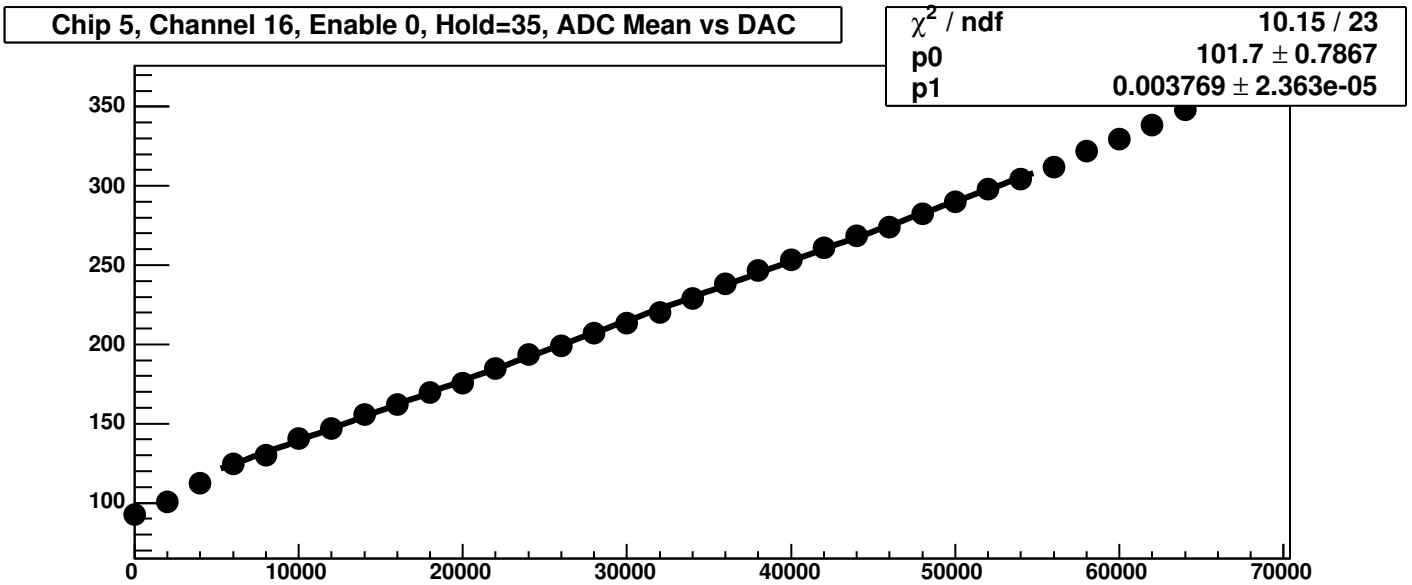

Chip 5, Channel 15, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

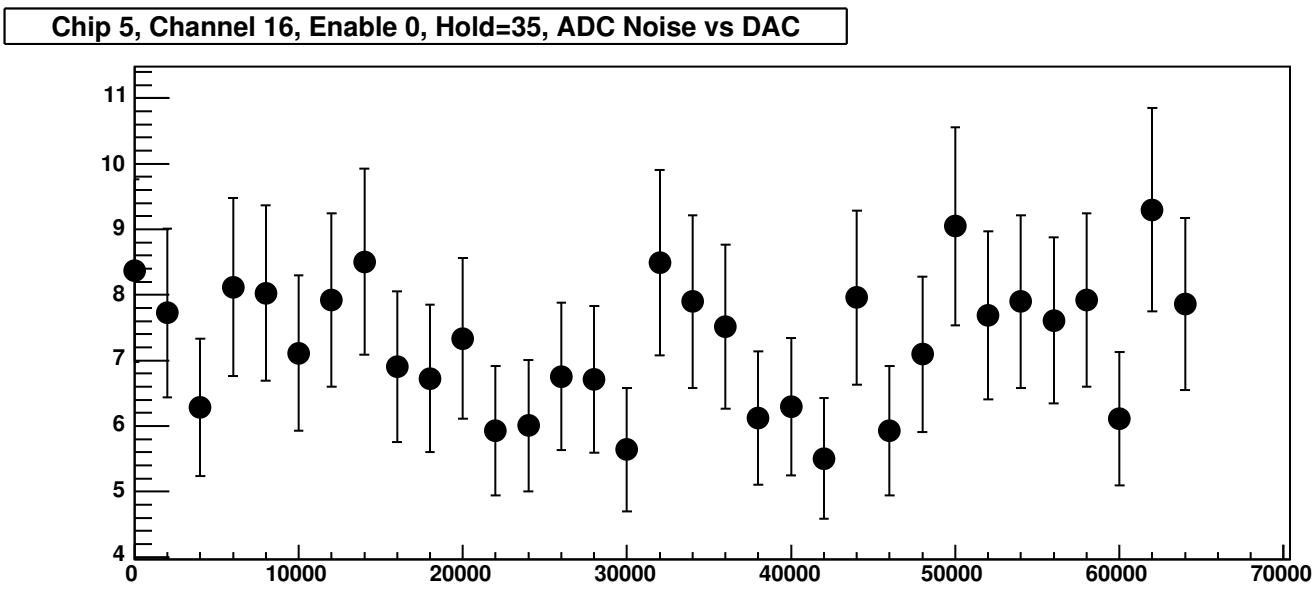

Chip 5, Channel 15, Enable 4, Hold=35, ADC Residuals vs DAC
6
4
2
0
-2
-4
-6
-8
-10
-12
0
10000
20000
30000
40000
50000
60000
70000

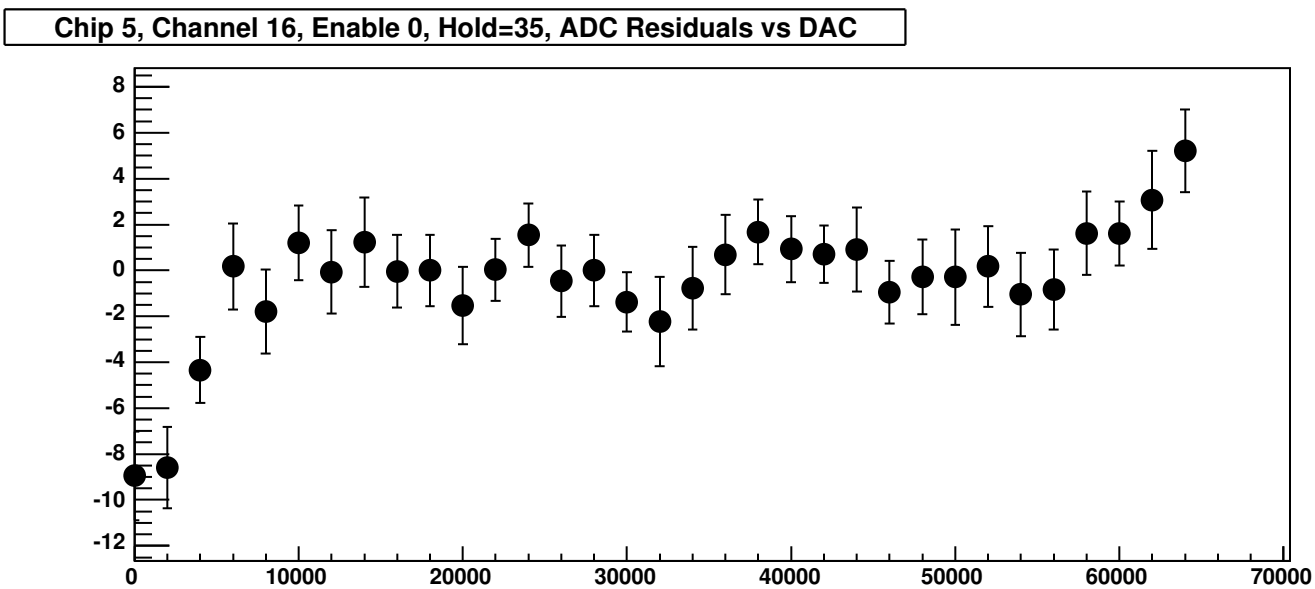

Chip 5, Channel 15, Enable 5!, Hold=35, ADC Mean vs DAC
χ² / ndf 7474 / 23
p0 77.87 ± 0.9387
p1 0.464 ± 4.531e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 15, Enable 5!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000

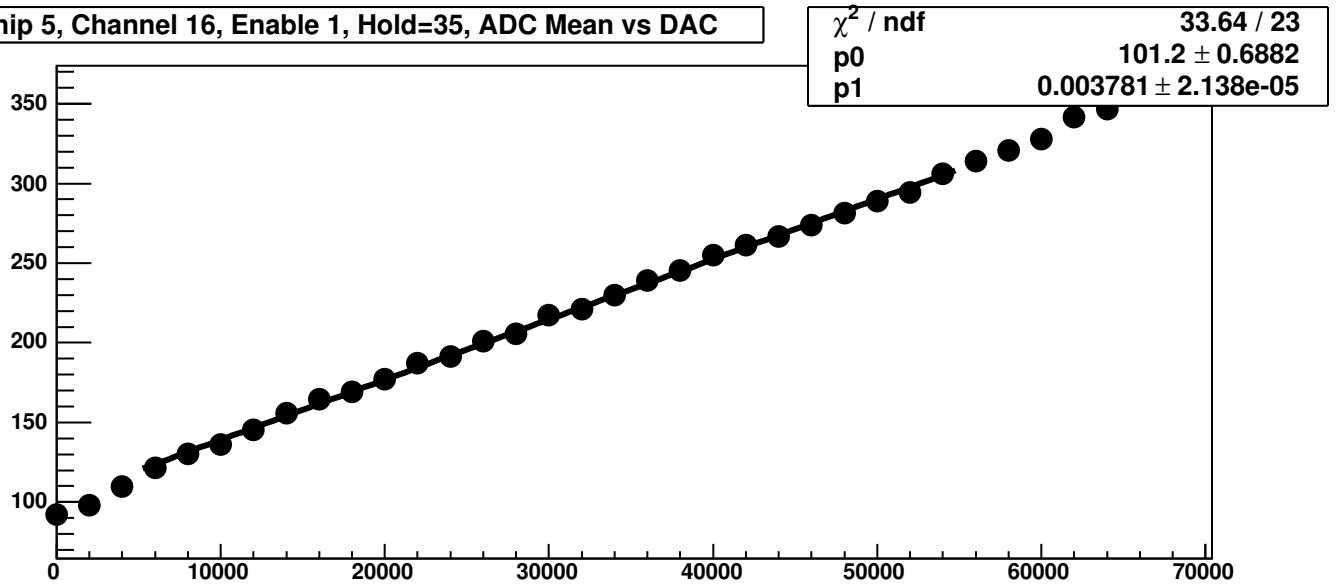

Chip 5, Channel 15, Enable 5!, Hold=35, ADC Residuals vs DAC
200
100
0
-100
-200
-300
-400
-500
0
10000
20000
30000
40000
50000
60000


Chip 5, Channel 16, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 10.15 / 23
p0 101.7 ± 0.7867
p1 0.003769 ± 2.363e-05
350
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

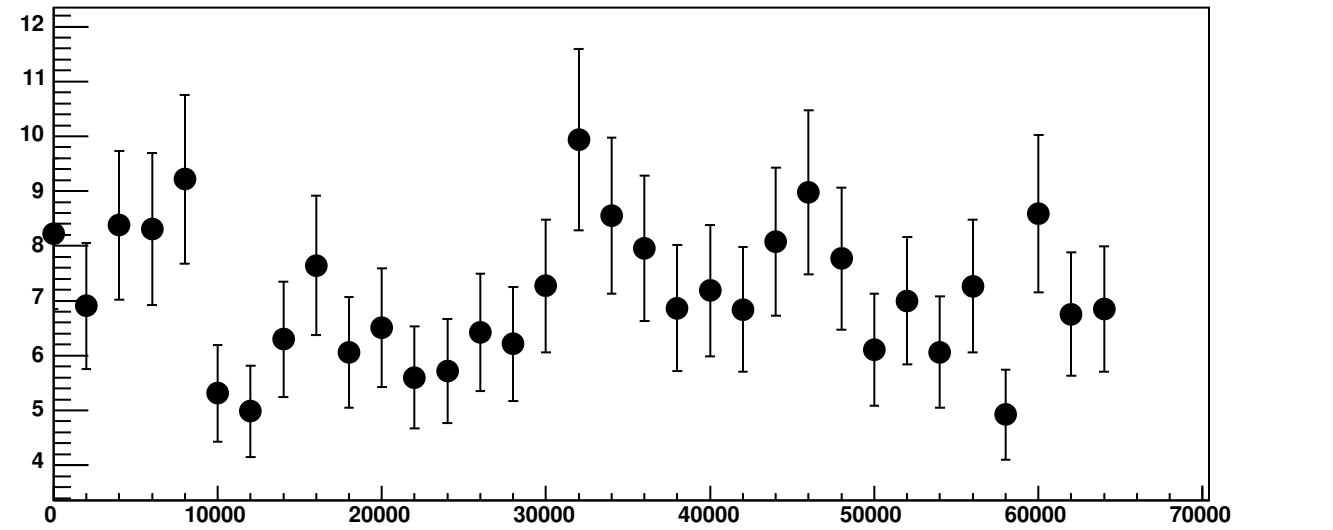

Chip 5, Channel 16, Enable 0, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 16, Enable 0, Hold=35, ADC Residuals vs DAC
8
6
4
2
0
-2
-4
-6
-8
-10
-12
0
10000
20000
30000
40000
50000
60000
70000

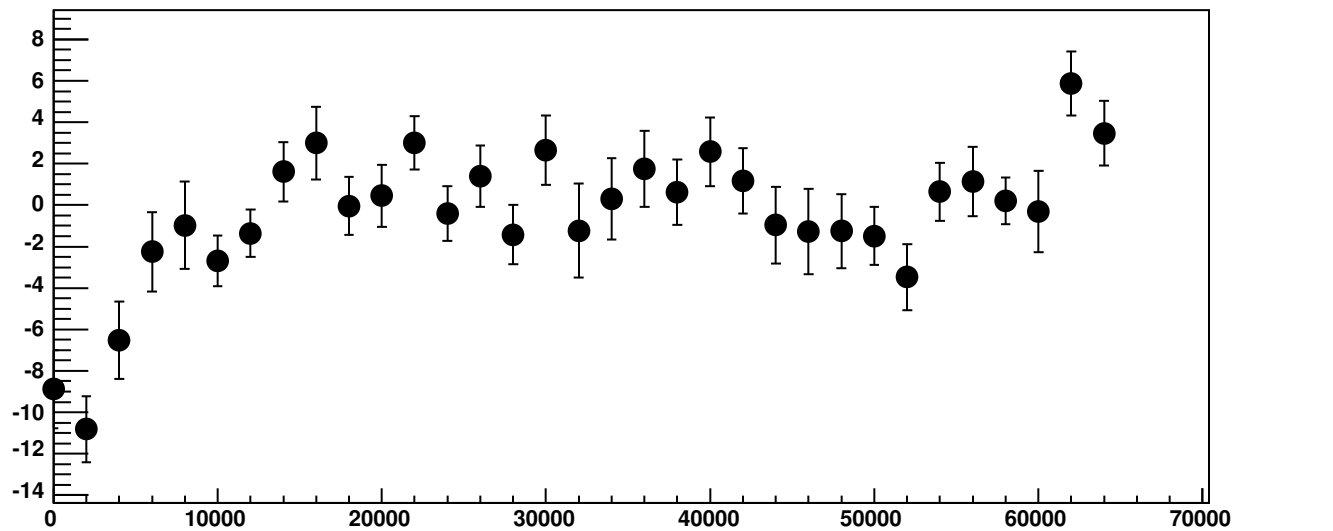
Chip 5, Channel 16, Enable 1, Hold=35, ADC Mean vs DAC

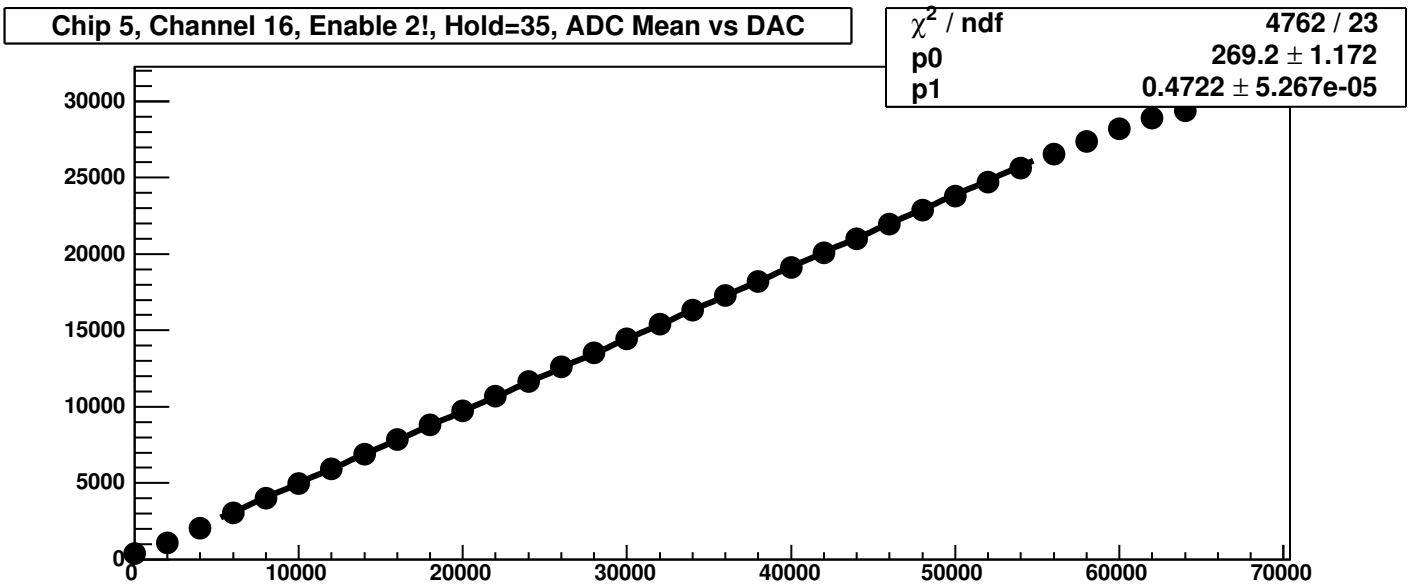


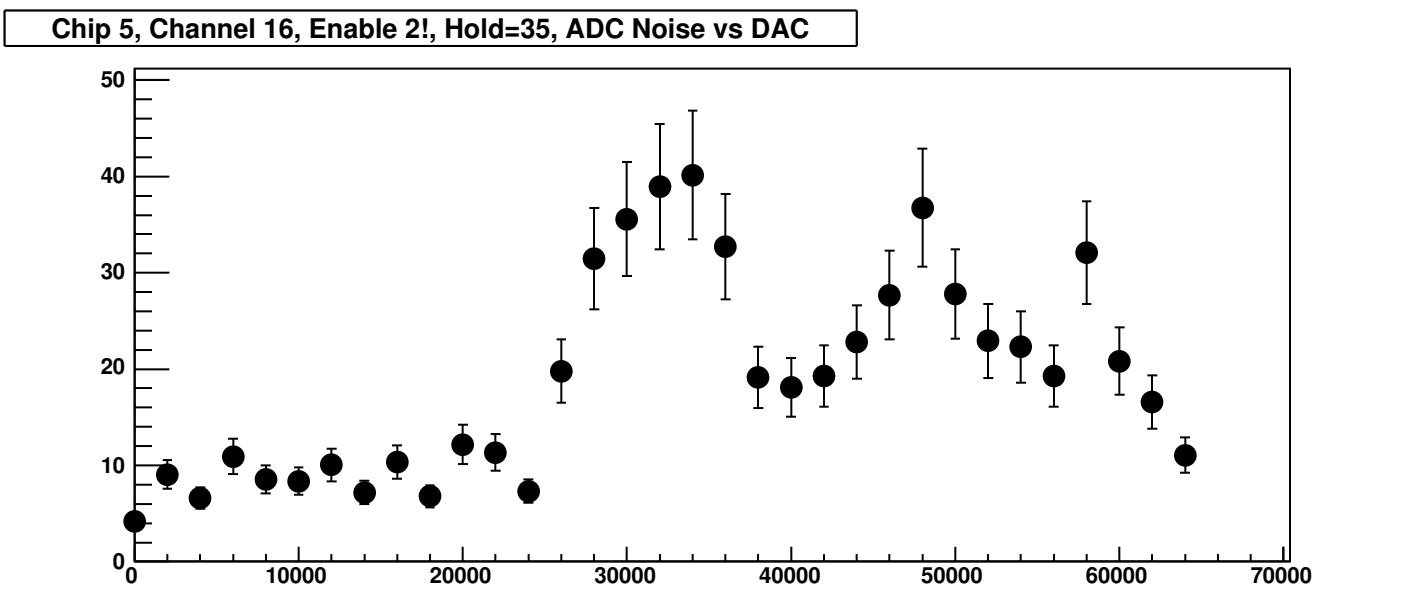
Chip 5, Channel 16, Enable 1, Hold=35, ADC Noise vs DAC

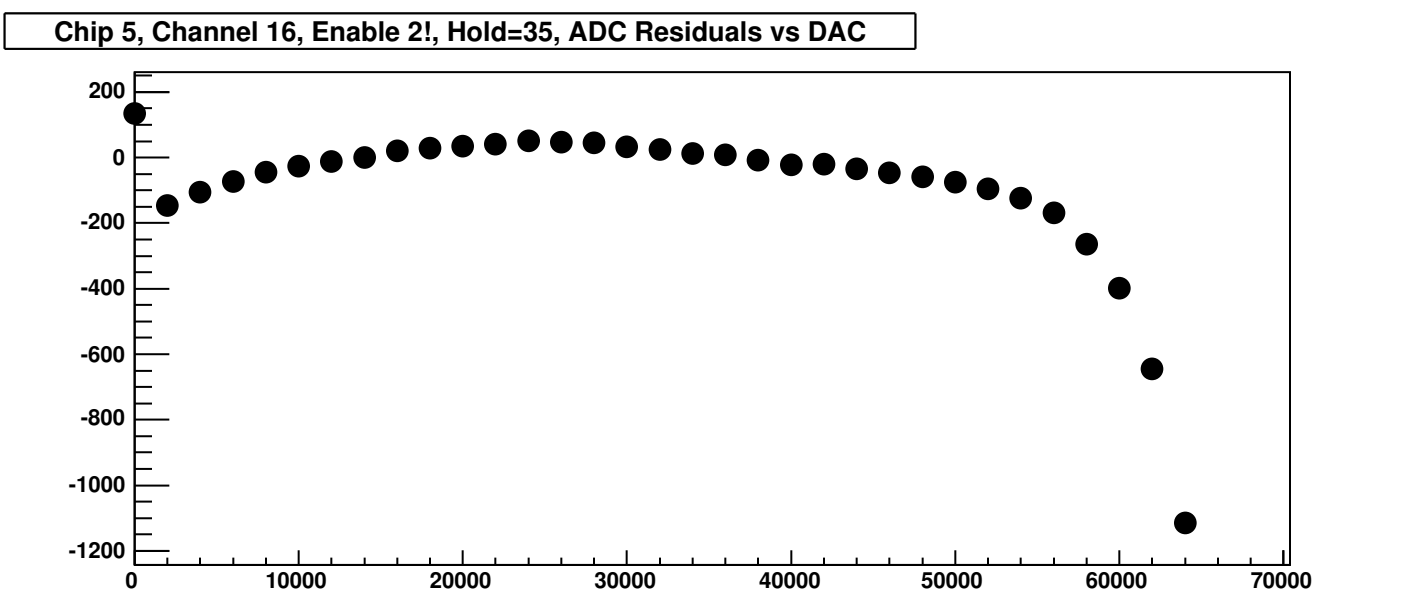


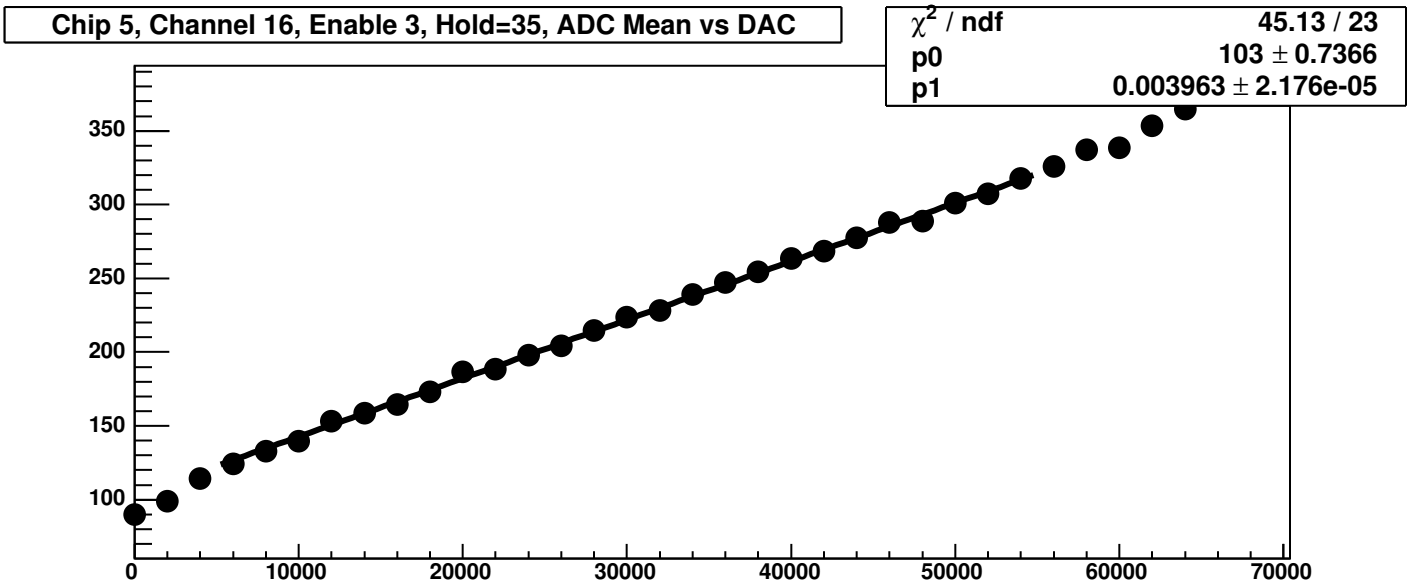
Chip 5, Channel 16, Enable 1, Hold=35, ADC Residuals vs DAC

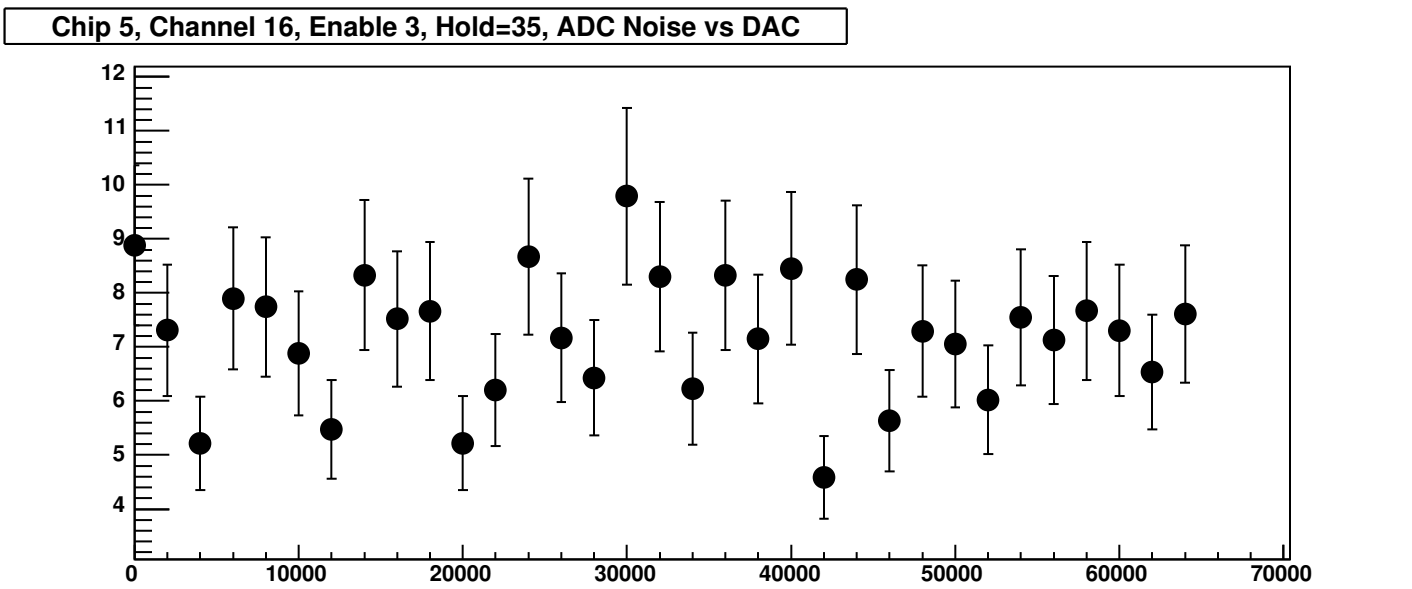


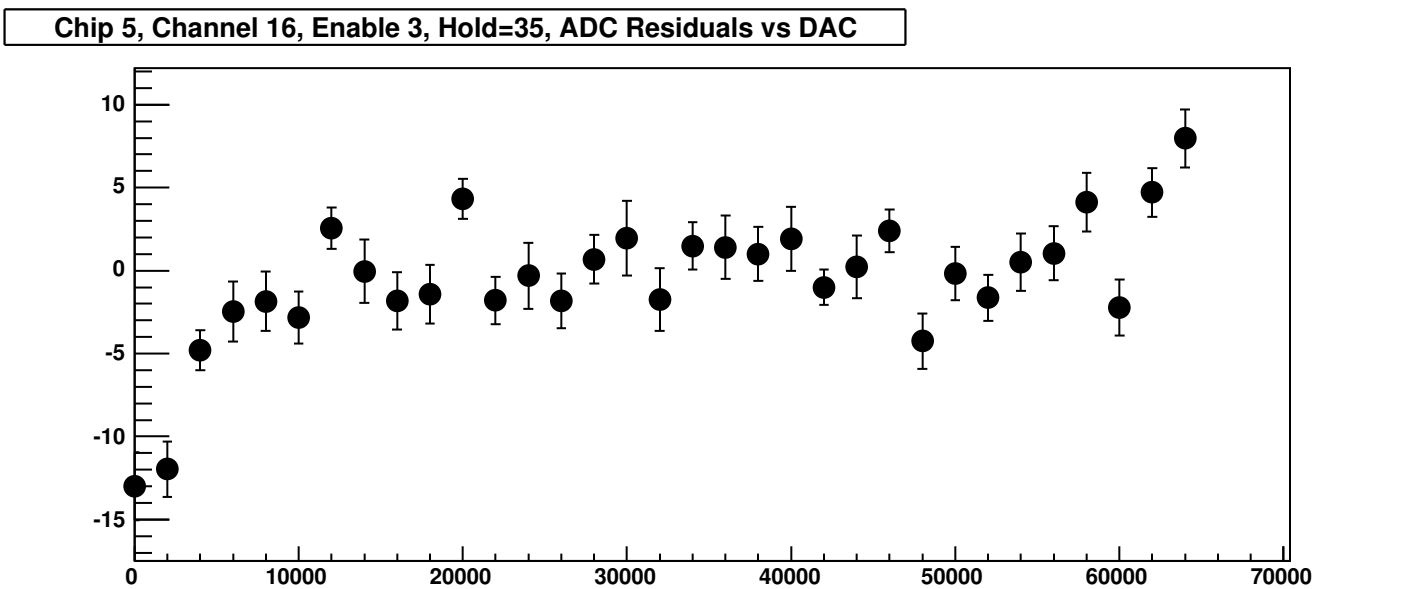

Chip 5, Channel 16, Enable 2!, Hold=35, ADC Mean vs DAC
χ² / ndf 4762 / 23
p0 269.2 ± 1.172
p1 0.4722 ± 5.267e-05
30000
25000
20000
15000
10000
5000
0
0
10000
20000
30000
40000
50000
60000
70000

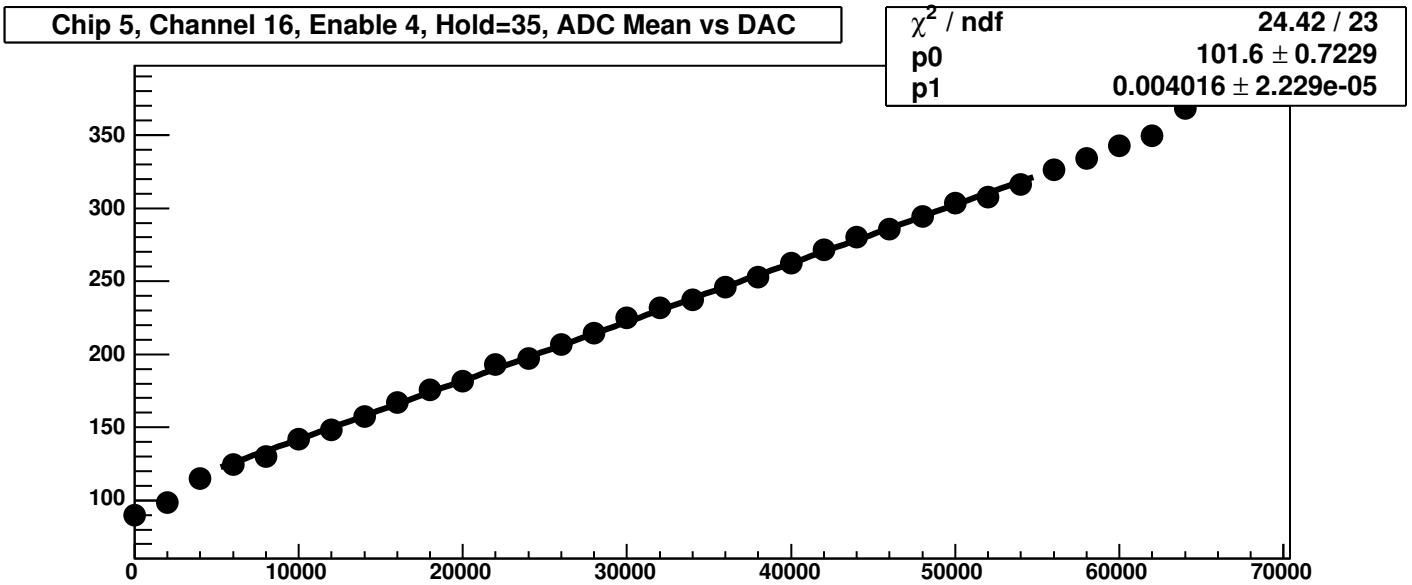

Chip 5, Channel 16, Enable 2!, Hold=35, ADC Noise vs DAC
50
40
30
20
10
0
0
10000
20000
30000
40000
50000
60000
70000

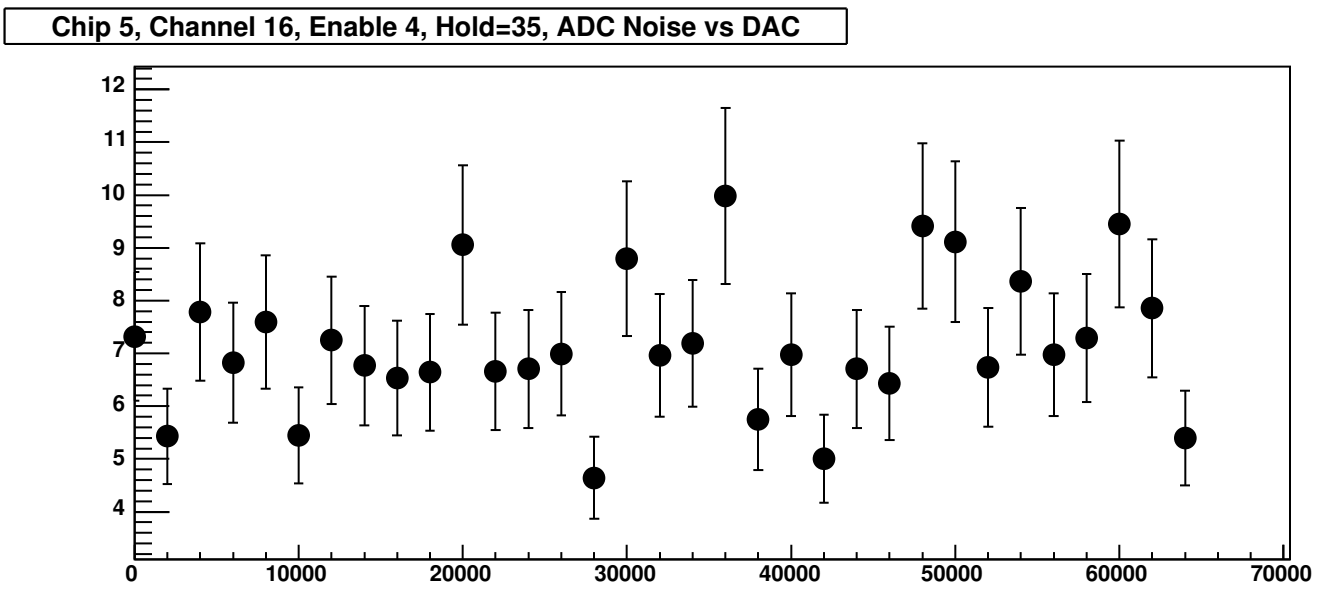

Chip 5, Channel 16, Enable 2!, Hold=35, ADC Residuals vs DAC
200
0
-200
-400
-600
-800
-1000
-1200
0
10000
20000
30000
40000
50000
60000
70000

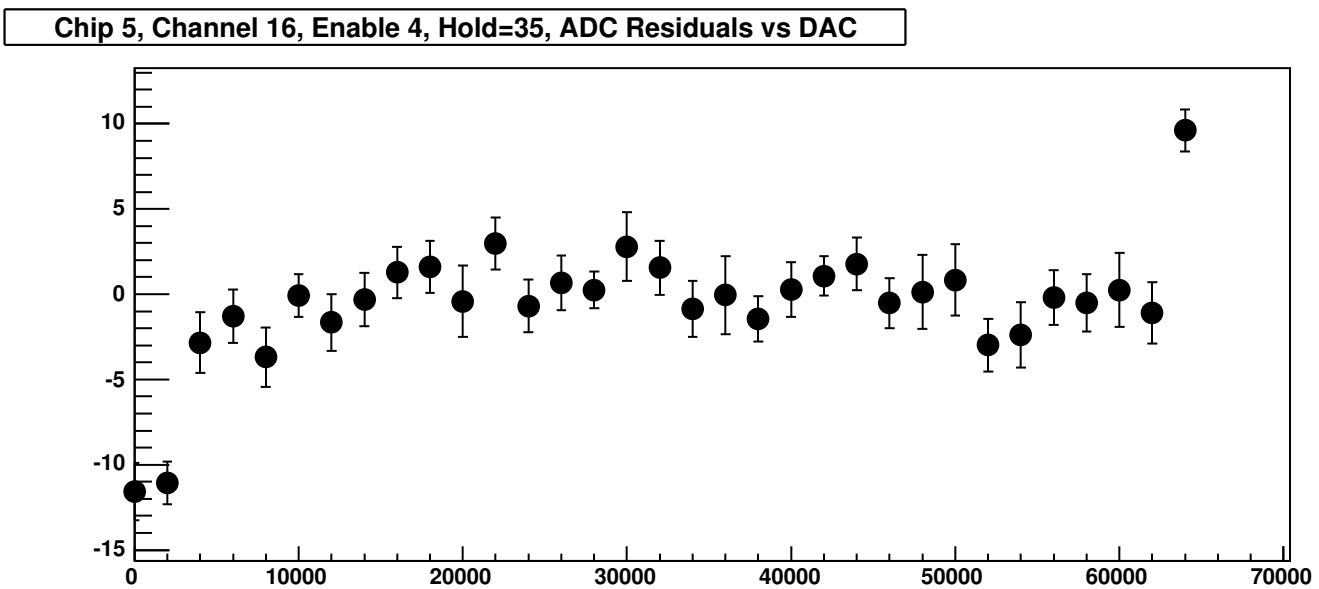

Chip 5, Channel 16, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 45.13 / 23
p0 103 ± 0.7366
p1 0.003963 ± 2.176e-05
350
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 16, Enable 3, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000

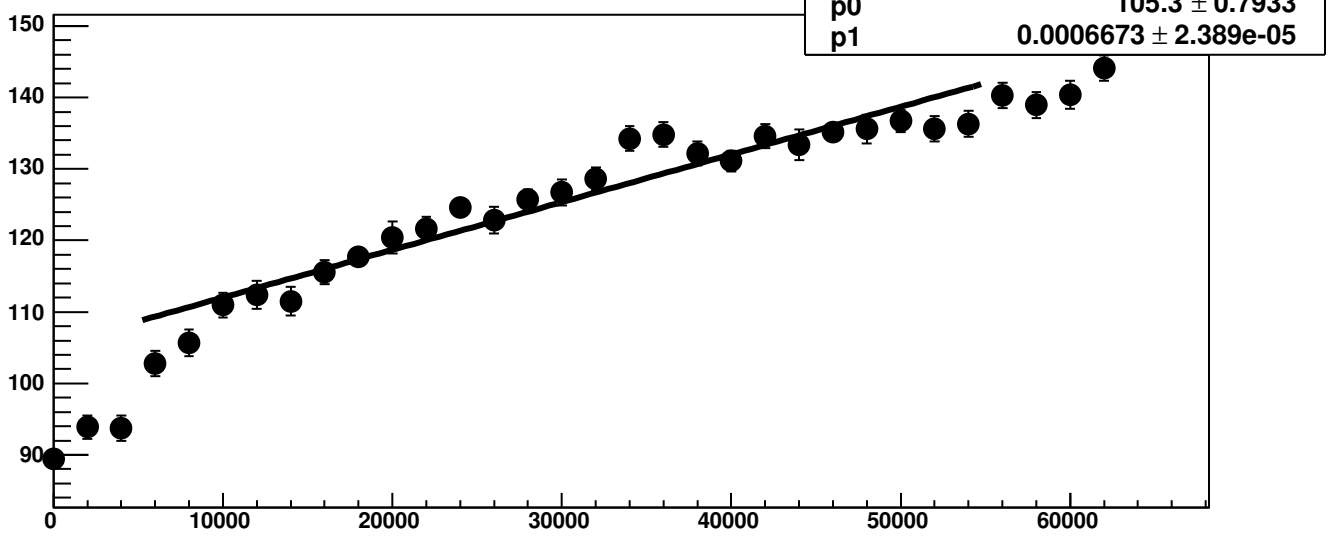

Chip 5, Channel 16, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 16, Enable 4, Hold=35, ADC Mean vs DAC
χ² / ndf 24.42 / 23
p0 101.6 ± 0.7229
p1 0.004016 ± 2.229e-05
350
300
250
200
150
100
0
10000
20000
30000
40000
50000
60000
70000

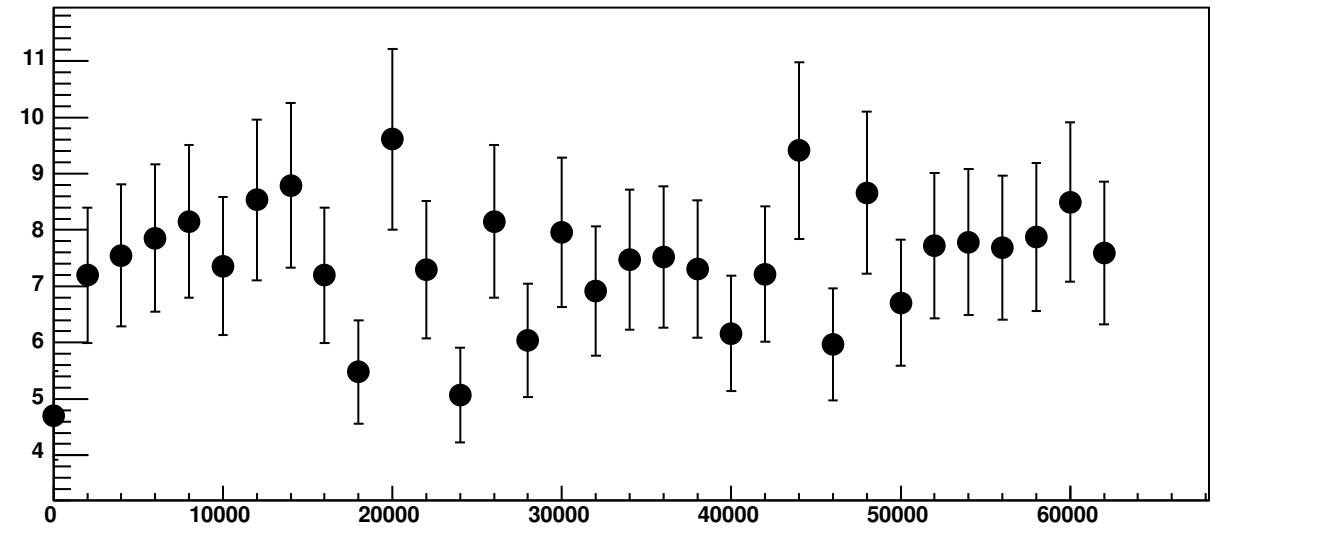

Chip 5, Channel 16, Enable 4, Hold=35, ADC Noise vs DAC
12
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 16, Enable 4, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

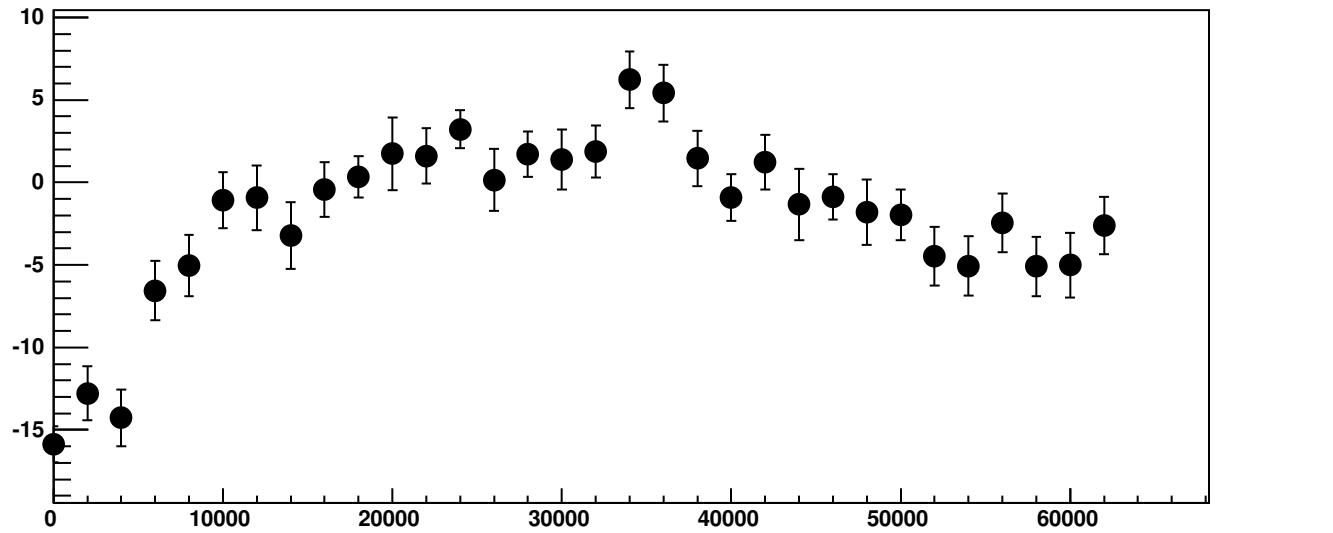
Chip 5, Channel 16, Enable 5, Hold=35, ADC Mean vs DAC

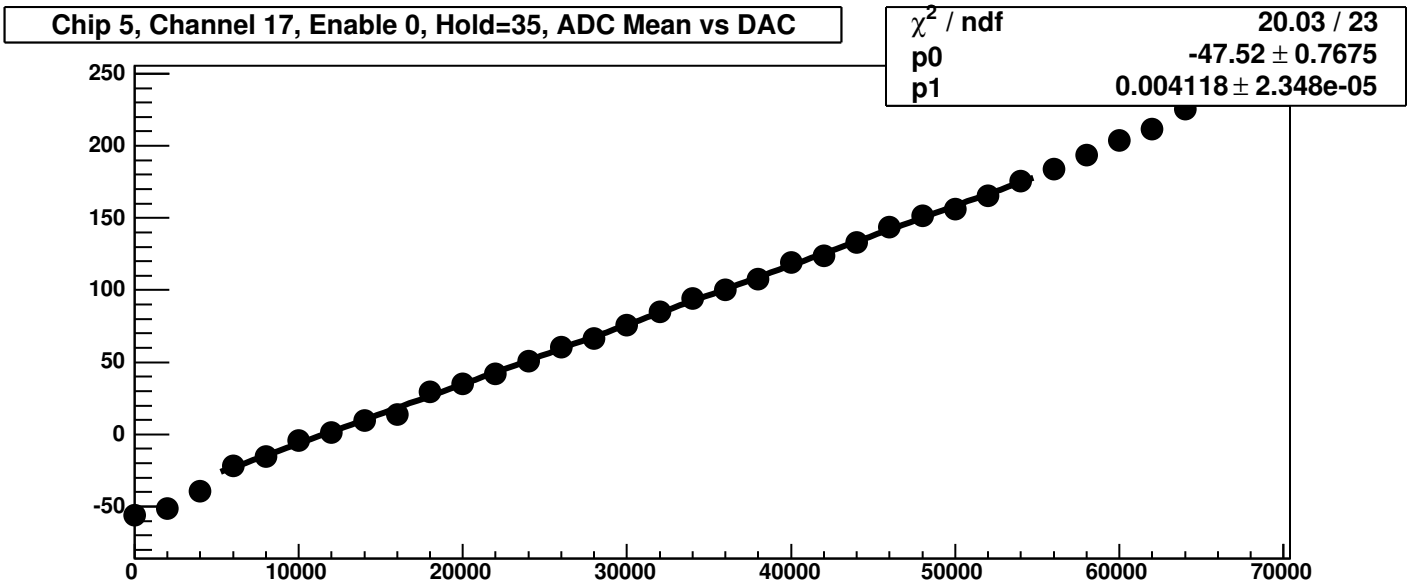


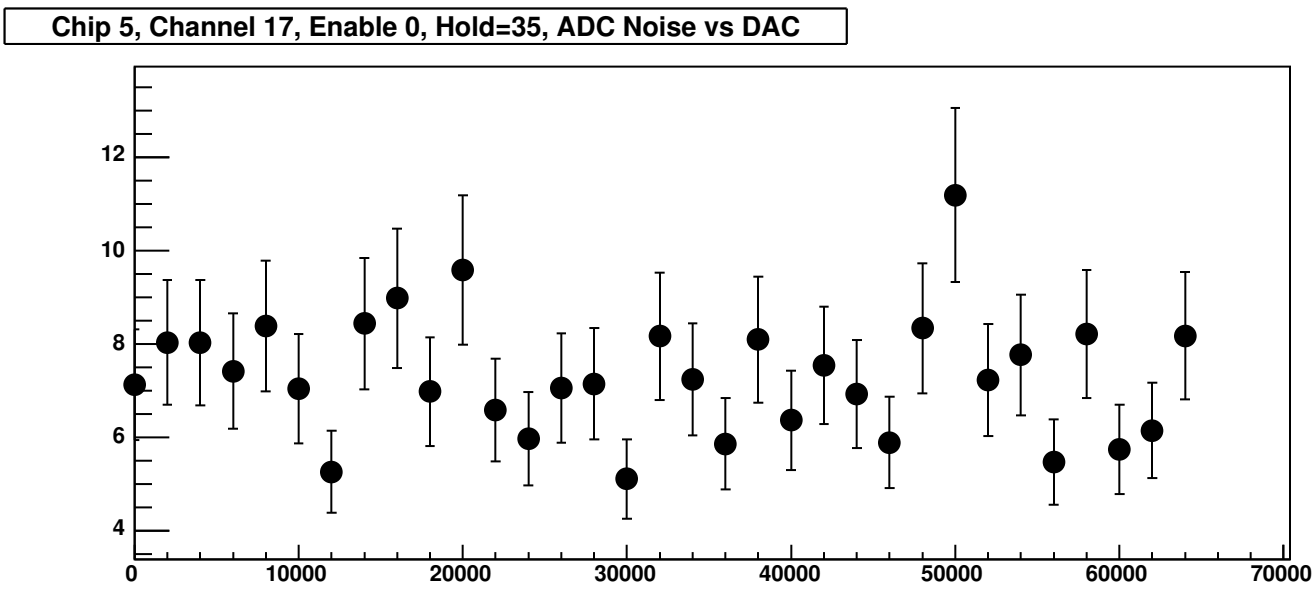
Chip 5, Channel 16, Enable 5, Hold=35, ADC Noise vs DAC

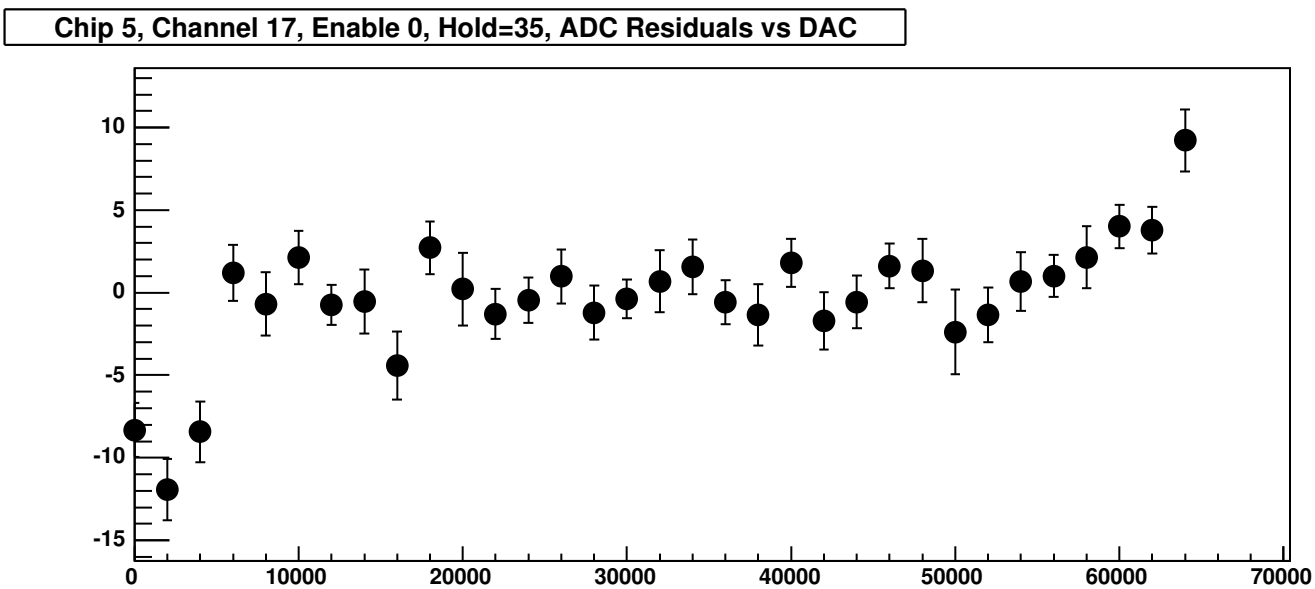


Chip 5, Channel 16, Enable 5, Hold=35, ADC Residuals vs DAC

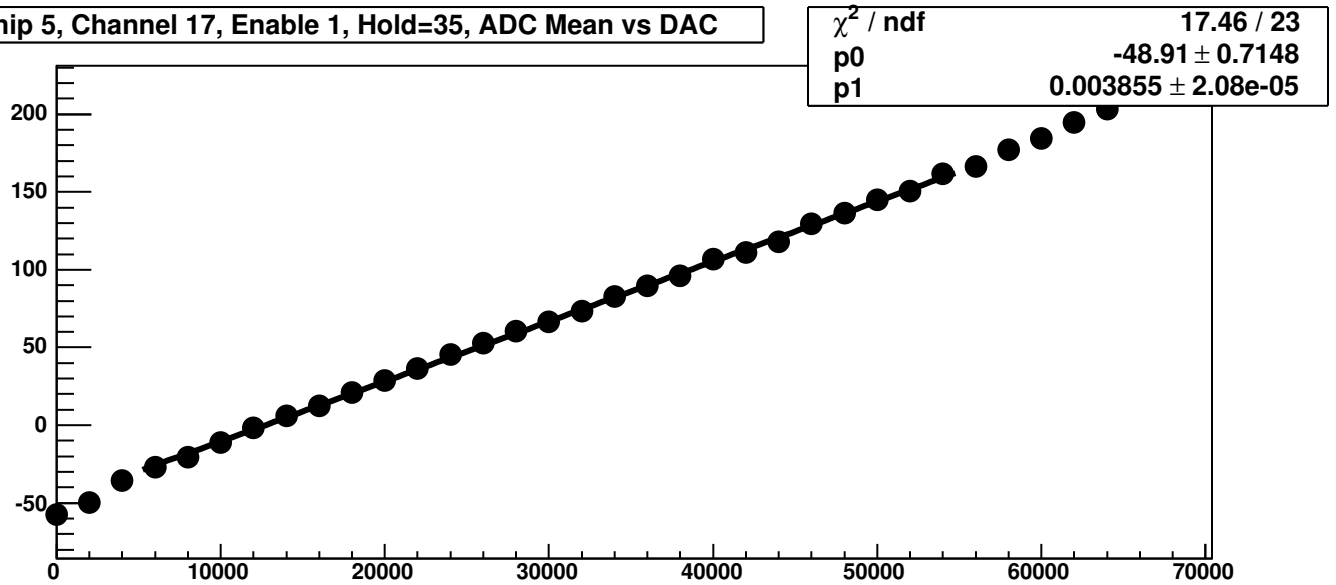



Chip 5, Channel 17, Enable 0, Hold=35, ADC Mean vs DAC
χ² / ndf 20.03 / 23
p0 -47.52 ± 0.7675
p1 0.004118 ± 2.348e-05
250
200
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000

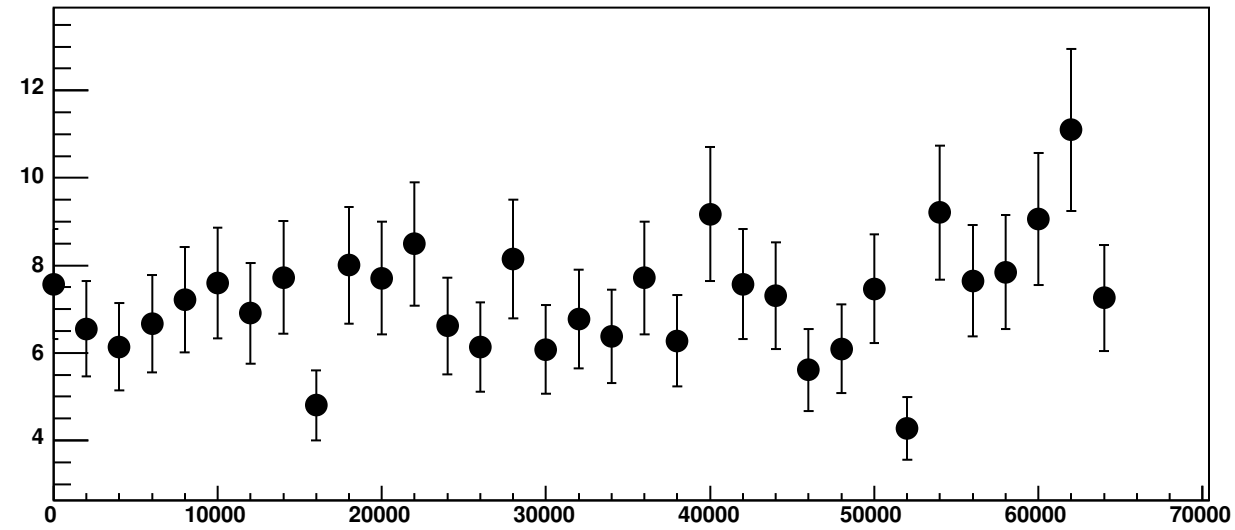

Chip 5, Channel 17, Enable 0, Hold=35, ADC Noise vs DAC
12
10
8
6
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 17, Enable 0, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

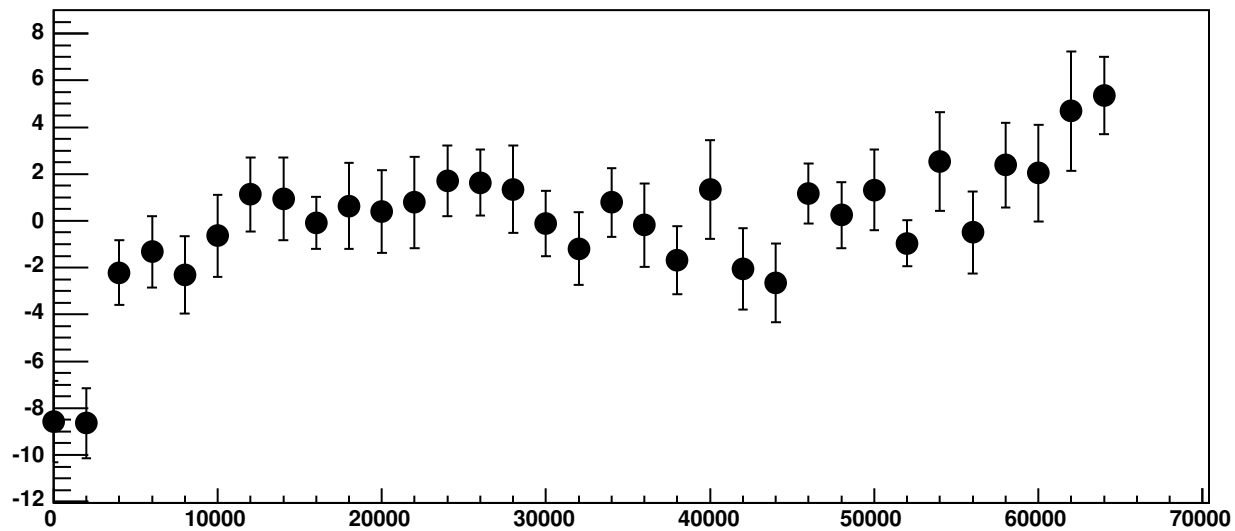
Chip 5, Channel 17, Enable 1, Hold=35, ADC Mean vs DAC

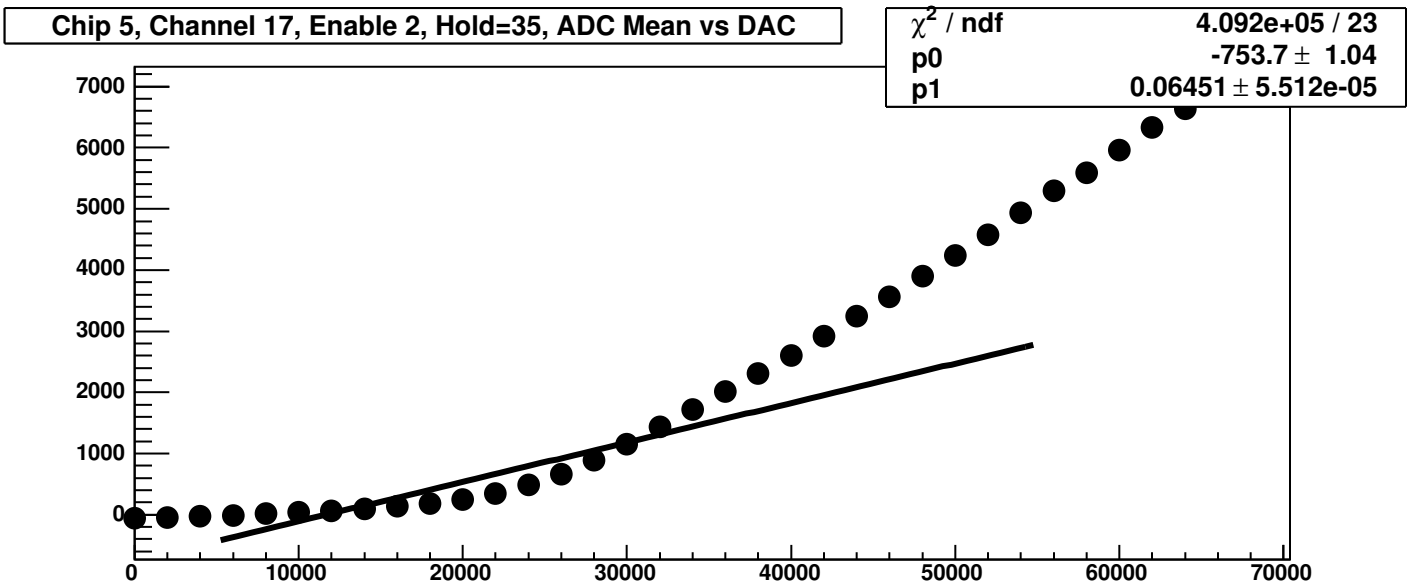


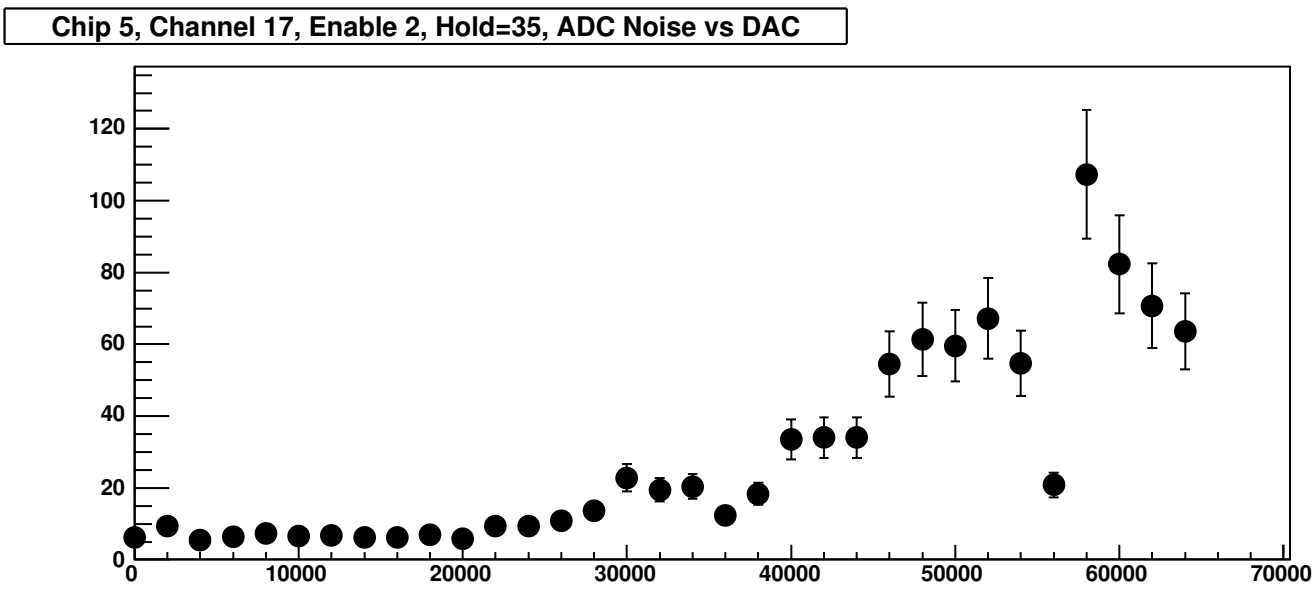
Chip 5, Channel 17, Enable 1, Hold=35, ADC Noise vs DAC

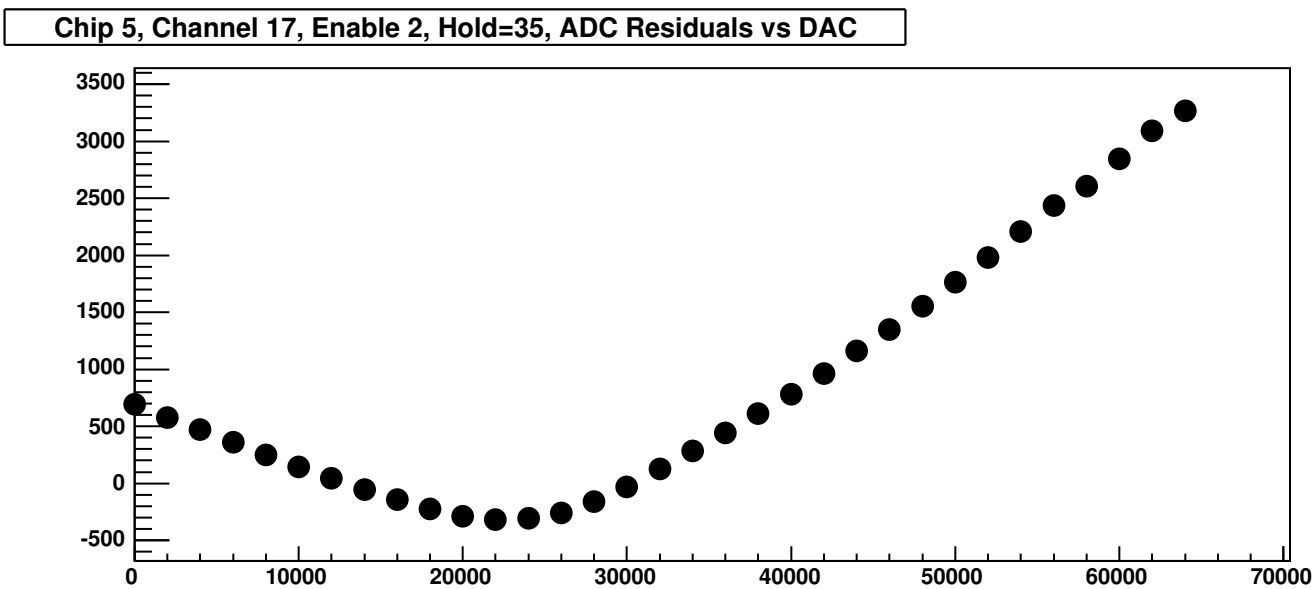


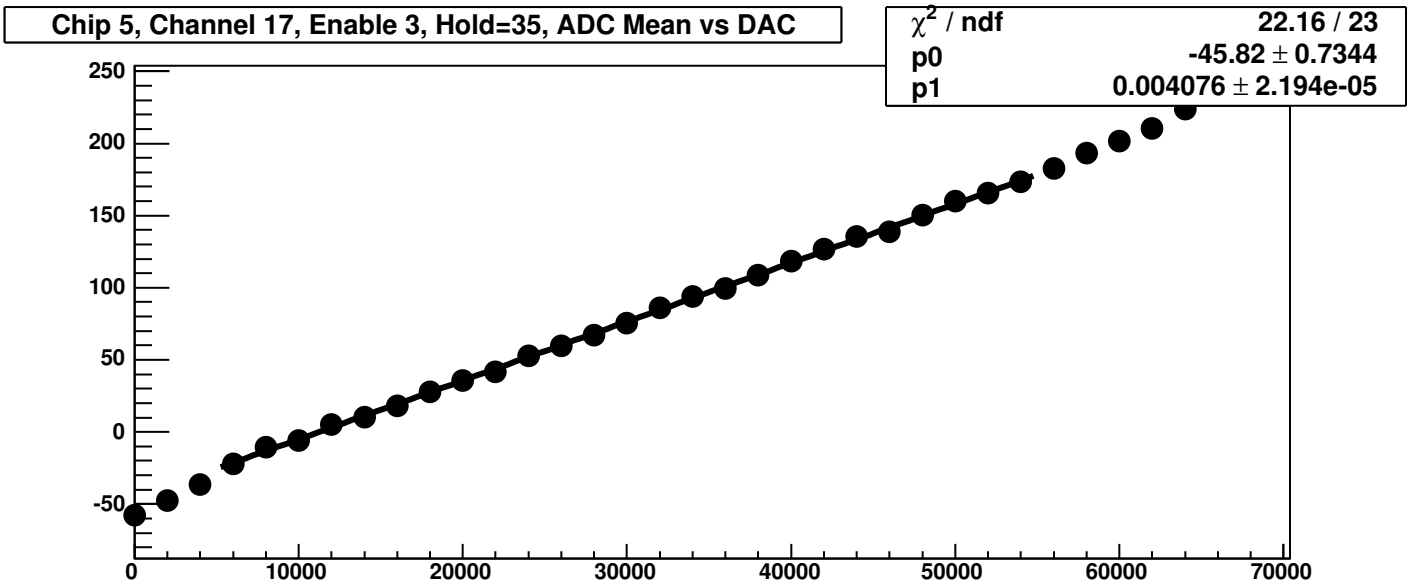
Chip 5, Channel 17, Enable 1, Hold=35, ADC Residuals vs DAC

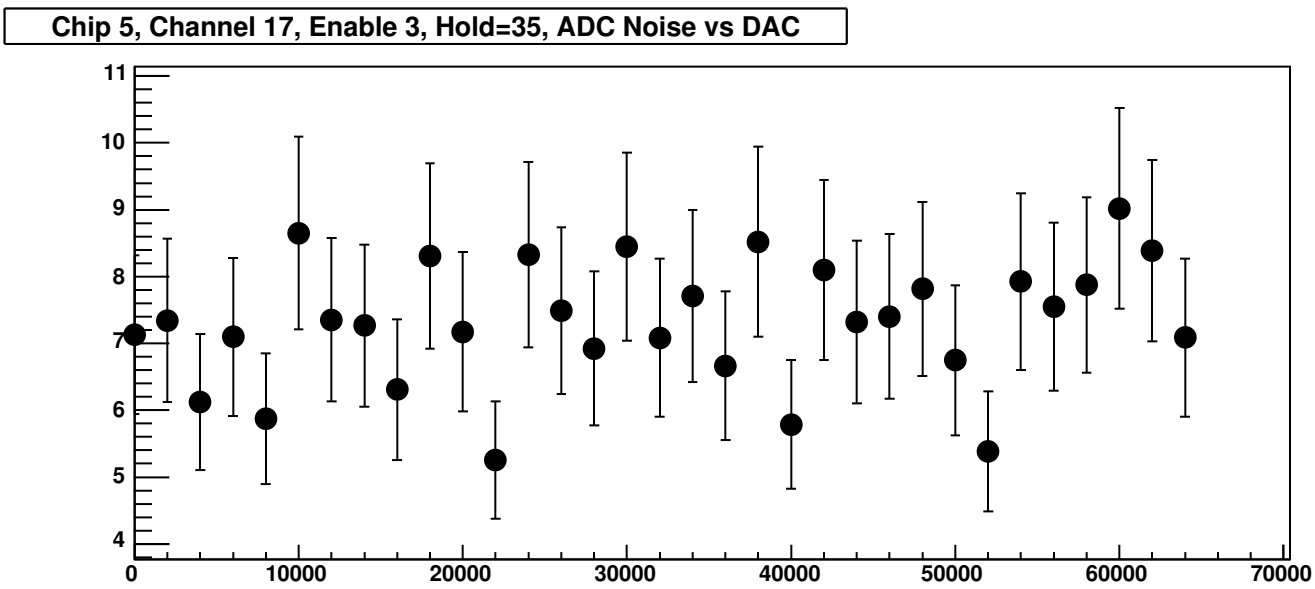


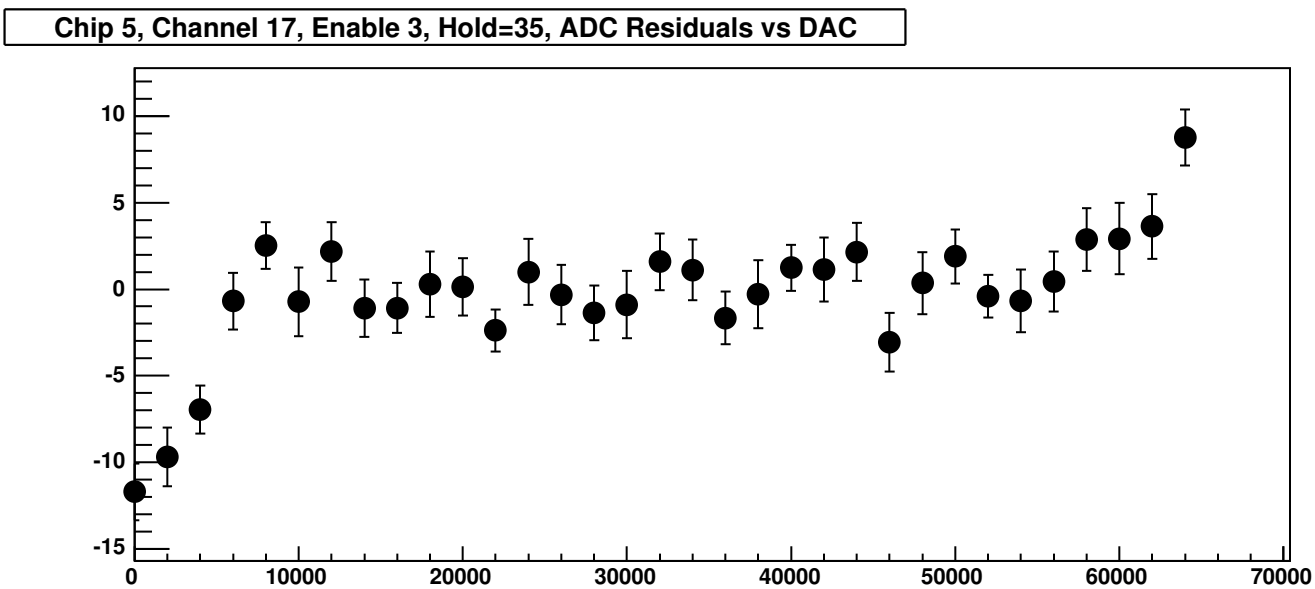

Chip 5, Channel 17, Enable 2, Hold=35, ADC Mean vs DAC
χ² / ndf 4.092e+05 / 23
p0 -753.7 ± 1.04
p1 0.06451 ± 5.512e-05
7000
6000
5000
4000
3000
2000
1000
0
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 17, Enable 2, Hold=35, ADC Noise vs DAC
120
100
80
60
40
20
0
0
10000
20000
30000
40000
50000
60000
70000

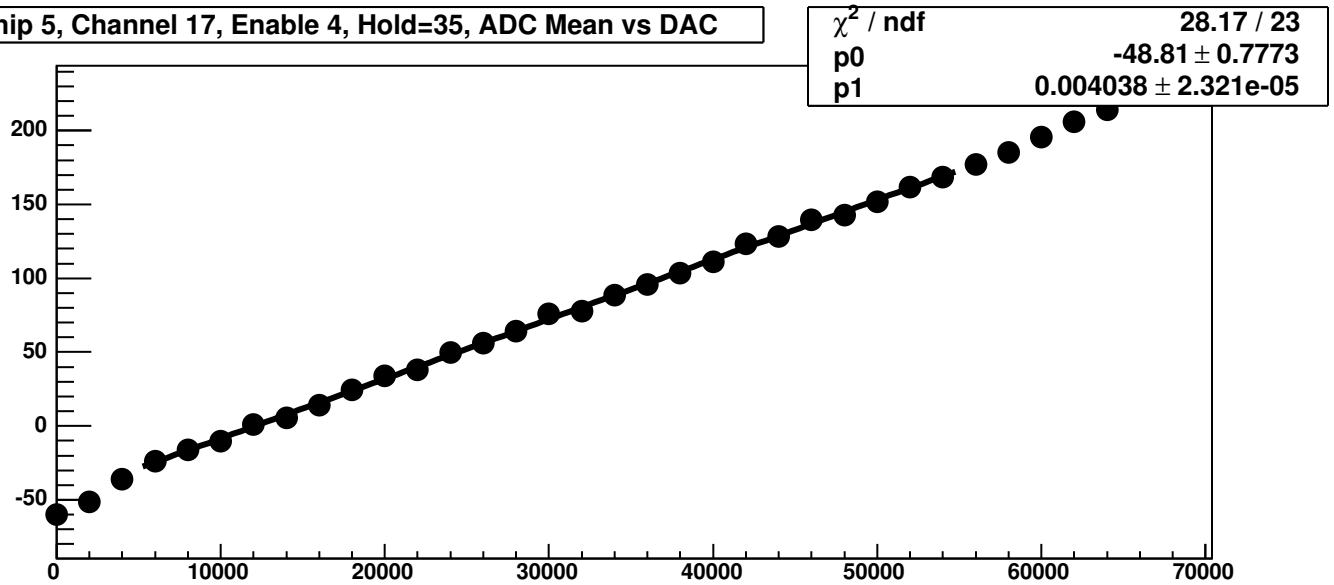

Chip 5, Channel 17, Enable 2, Hold=35, ADC Residuals vs DAC
3500
3000
2500
2000
1500
1000
500
0
-500
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 17, Enable 3, Hold=35, ADC Mean vs DAC
χ² / ndf 22.16 / 23
p0 -45.82 ± 0.7344
p1 0.004076 ± 2.194e-05
250
200
150
100
50
0
-50
0
10000
20000
30000
40000
50000
60000
70000

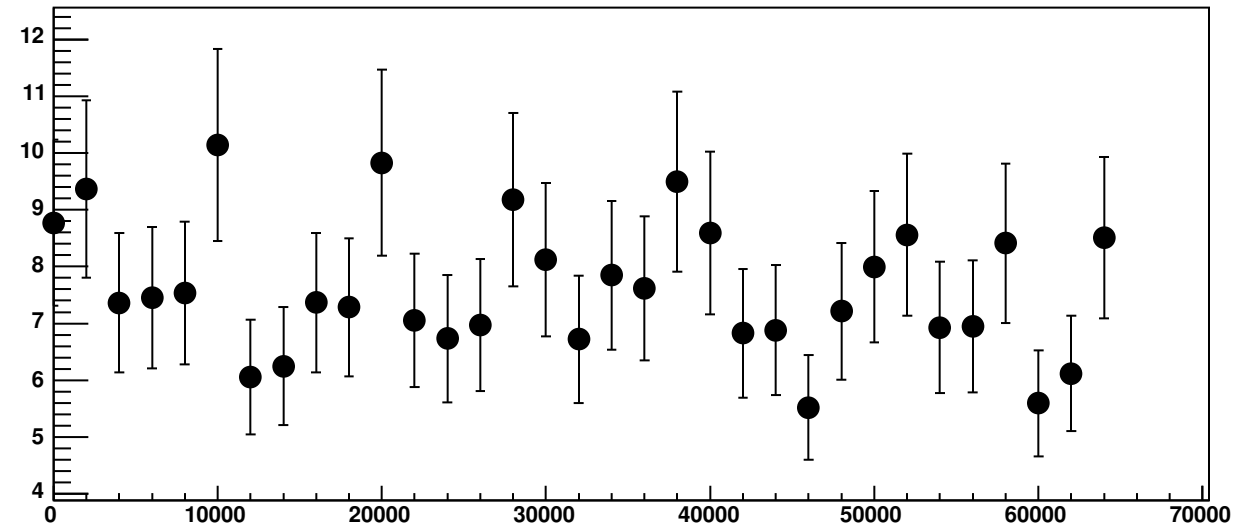
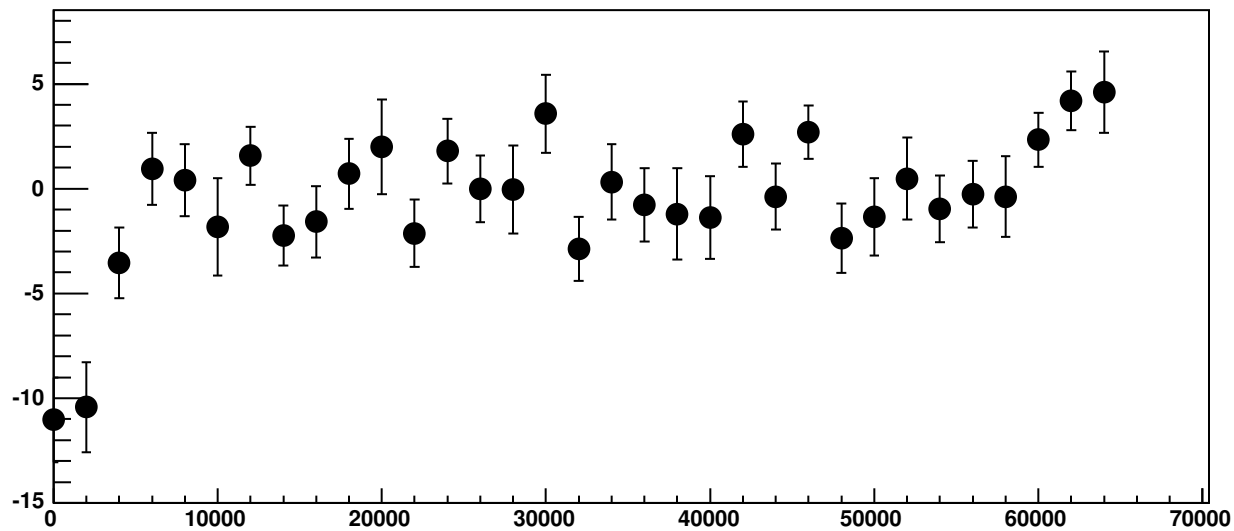

Chip 5, Channel 17, Enable 3, Hold=35, ADC Noise vs DAC
11
10
9
8
7
6
5
4
0
10000
20000
30000
40000
50000
60000
70000


Chip 5, Channel 17, Enable 3, Hold=35, ADC Residuals vs DAC
10
5
0
-5
-10
-15
0
10000
20000
30000
40000
50000
60000
70000

Chip 5, Channel 17, Enable 4, Hold=35, ADC Mean vs DAC



Chip 5, Channel 17, Enable 4, Hold=35, ADC Noise vs DAC



Chip 5, Channel 17, Enable 4, Hold=35, ADC Residuals vs DAC