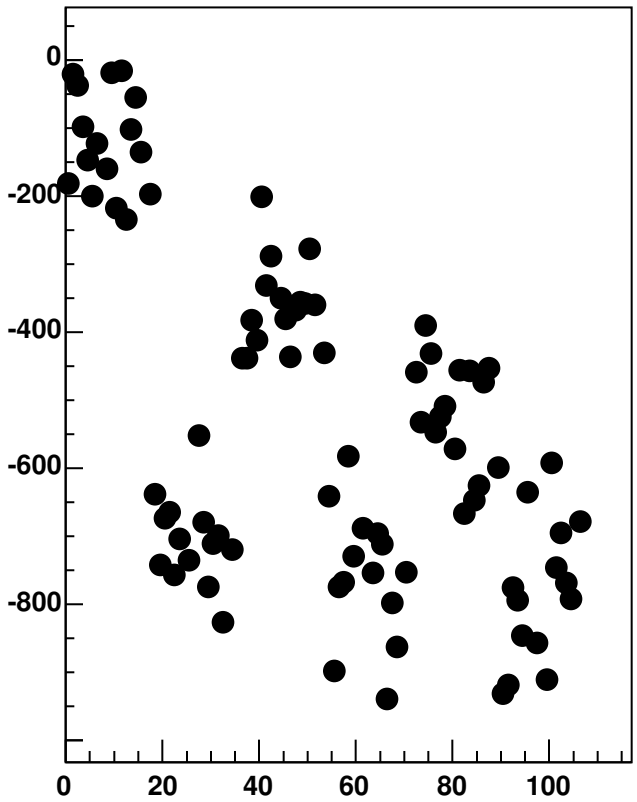
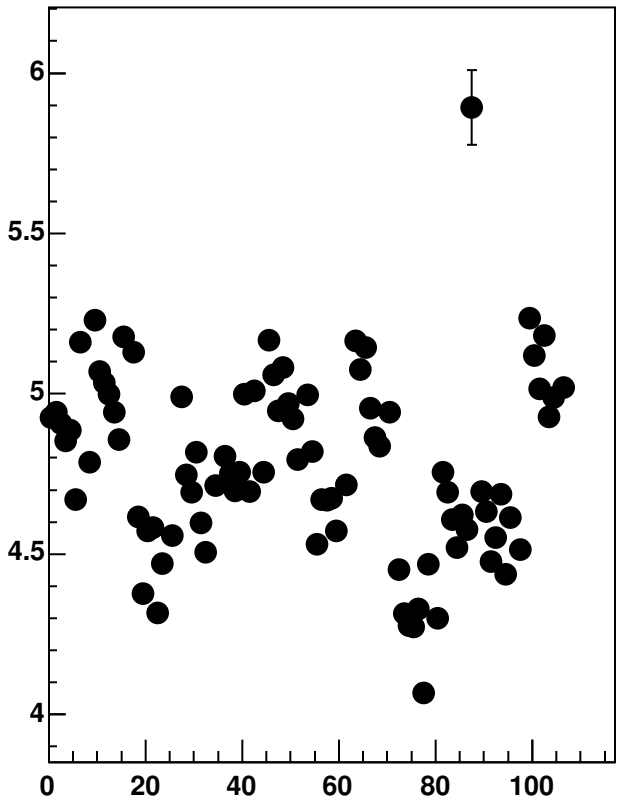
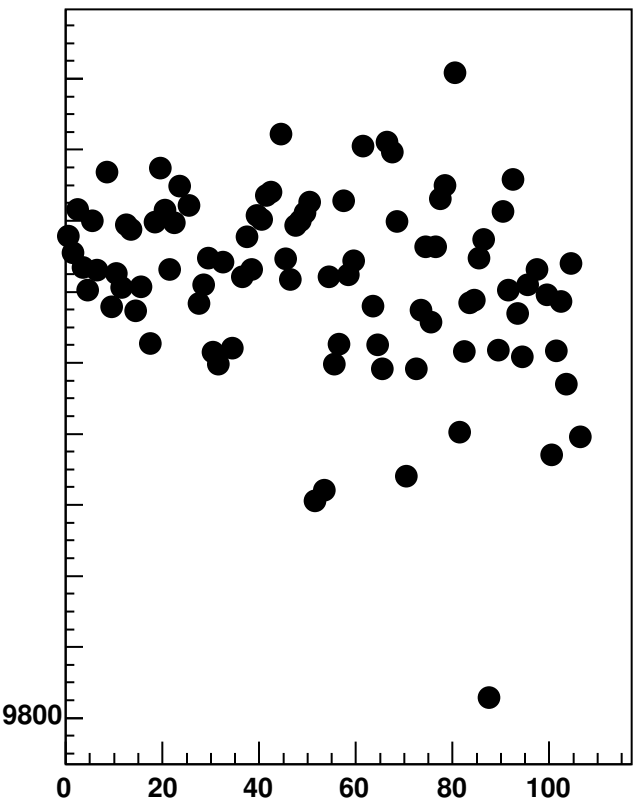
Channel Enabled, DAC=4000, Fit Baseline vs 18*Chip+Chan

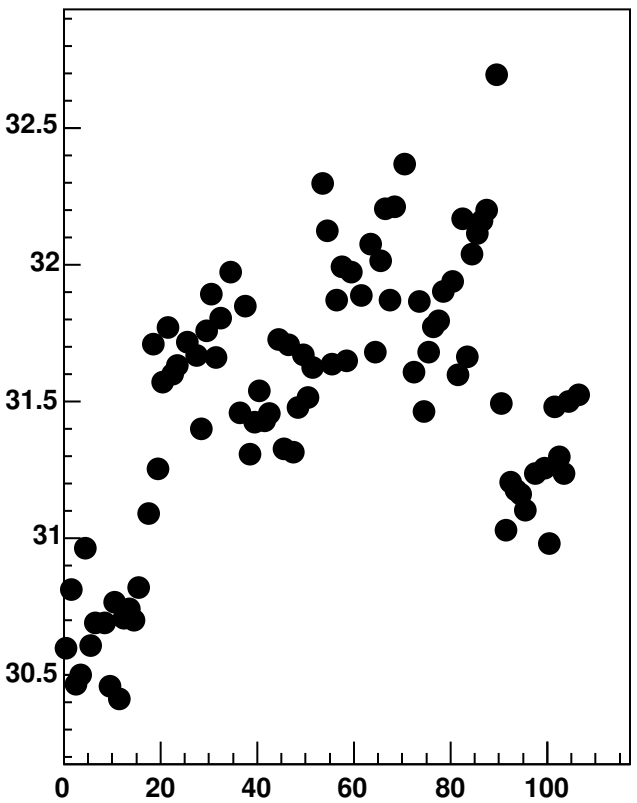


Channel Enabled, DAC=4000, Fit Start Time vs 18*Chip+Chan

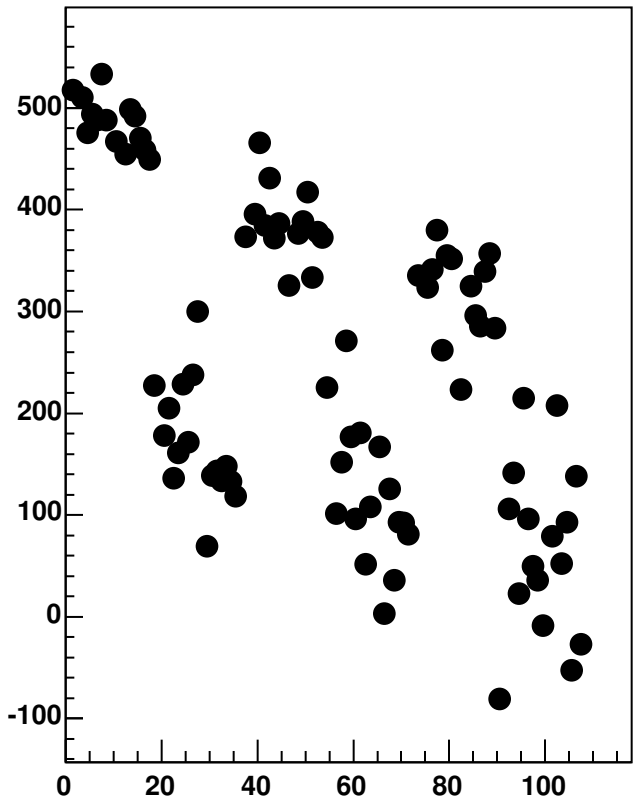


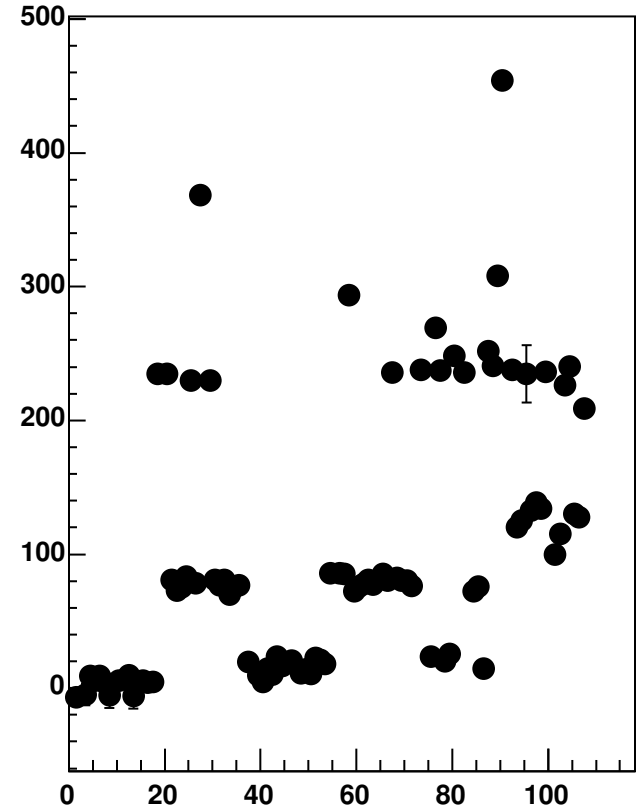
Channel Enabled, DAC=4000, Fit Normalisation vs 18*Chip+Chan



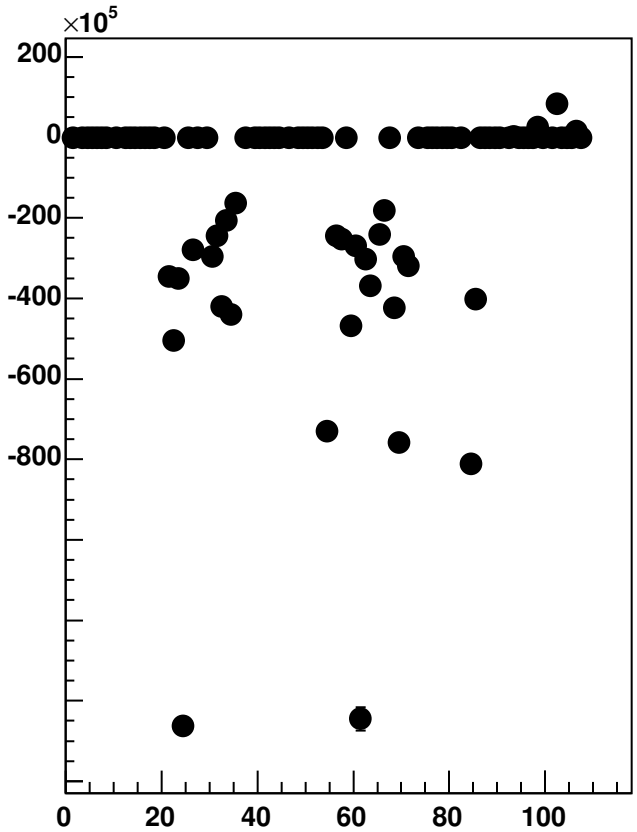


Disabled 0, DAC=4000, Fit Baseline vs 18*Chip+Chan

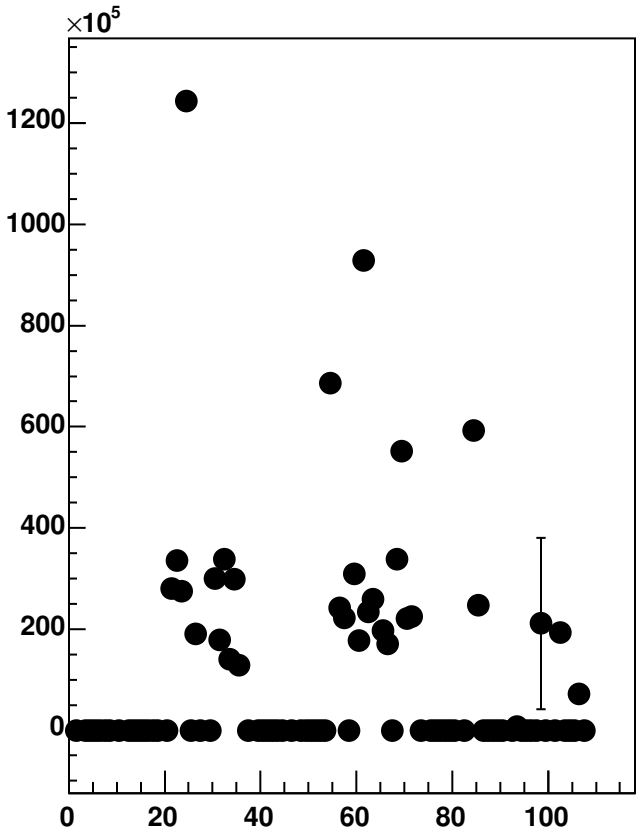




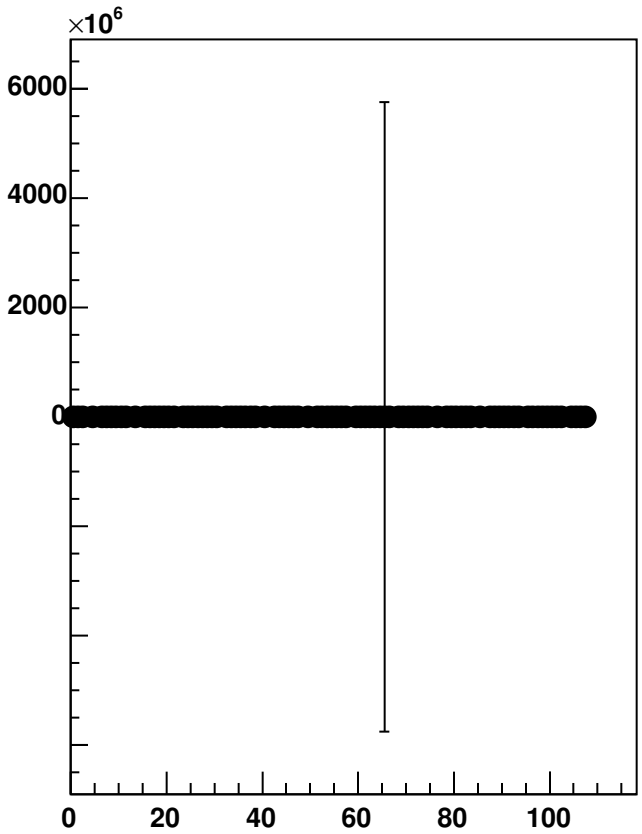
Disabled 0, DAC=4000, Fit Normalisation vs 18*Chip+Chan

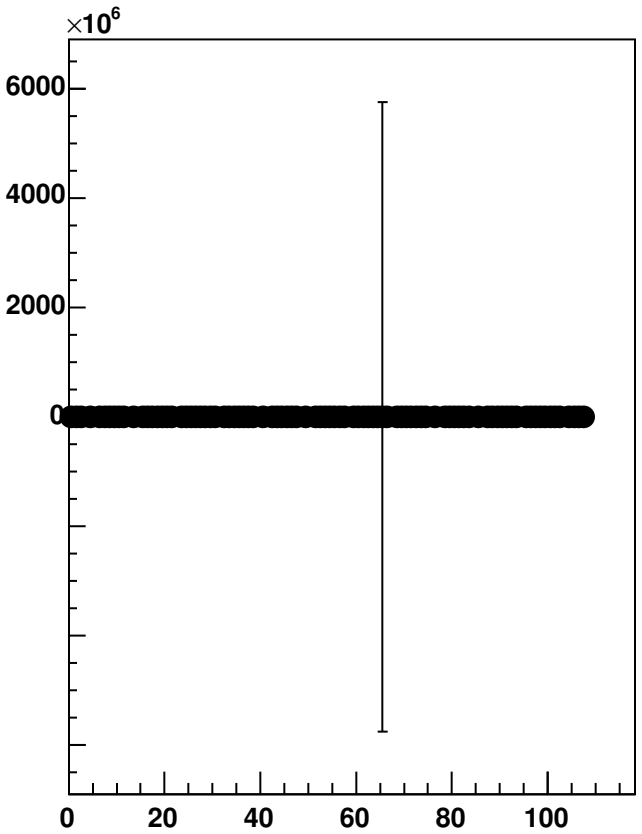


Disabled 0, DAC=4000, Fit Shaping Time vs 18*Chip+Chan



Disabled 1, DAC=4000, Fit Baseline vs 18*Chip+Chan





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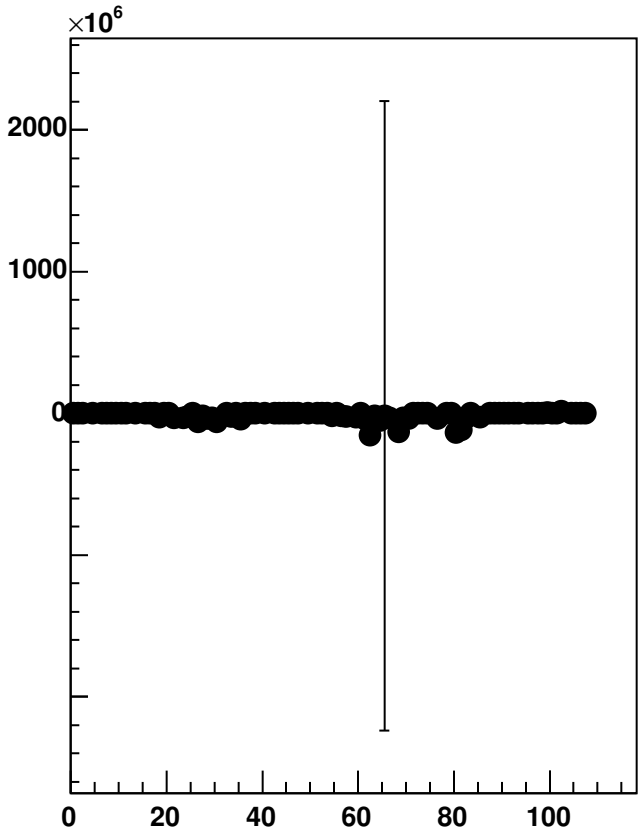
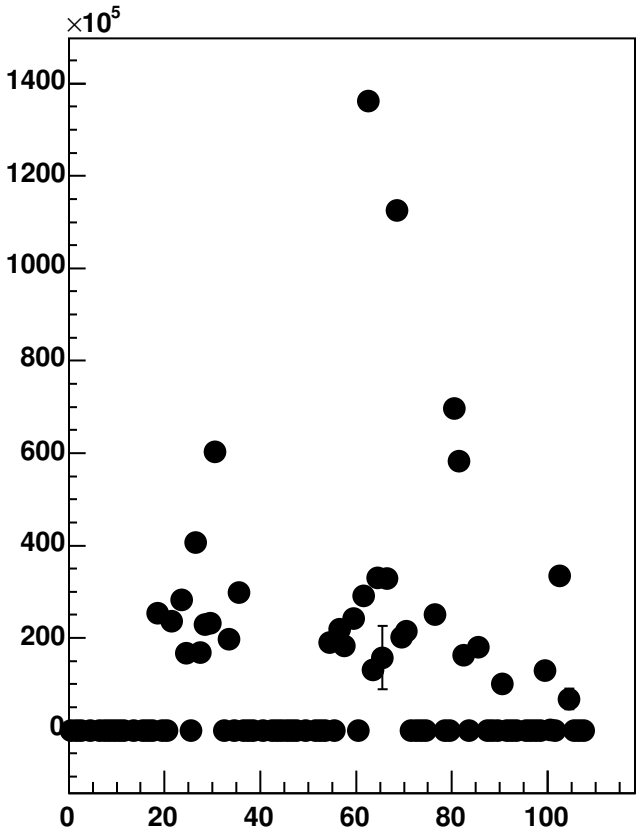


Disabled 1, DAC=4000, Fit Shaping Time vs 18*Chip+Chan



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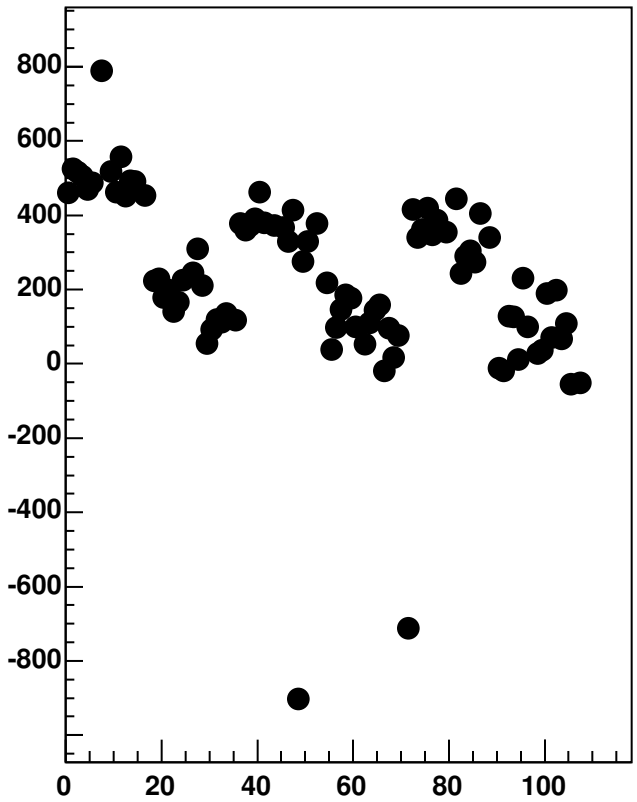

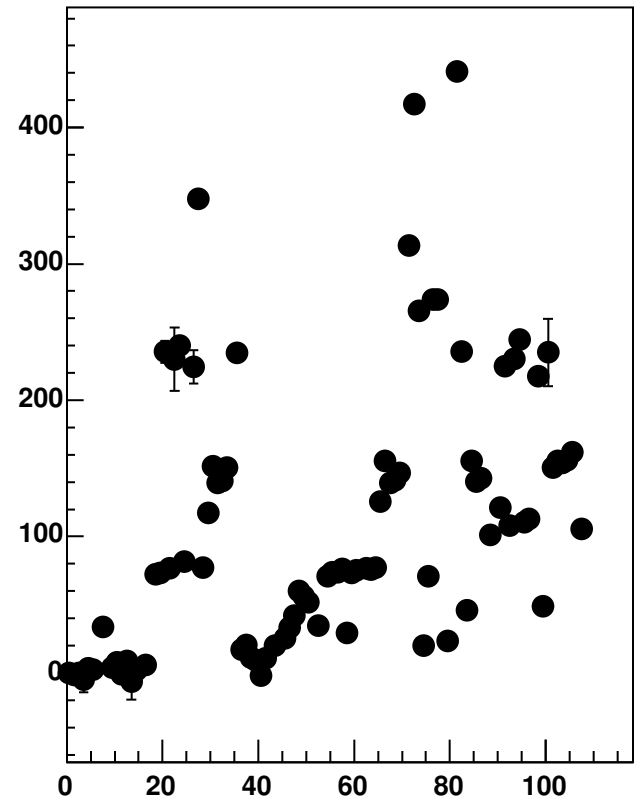

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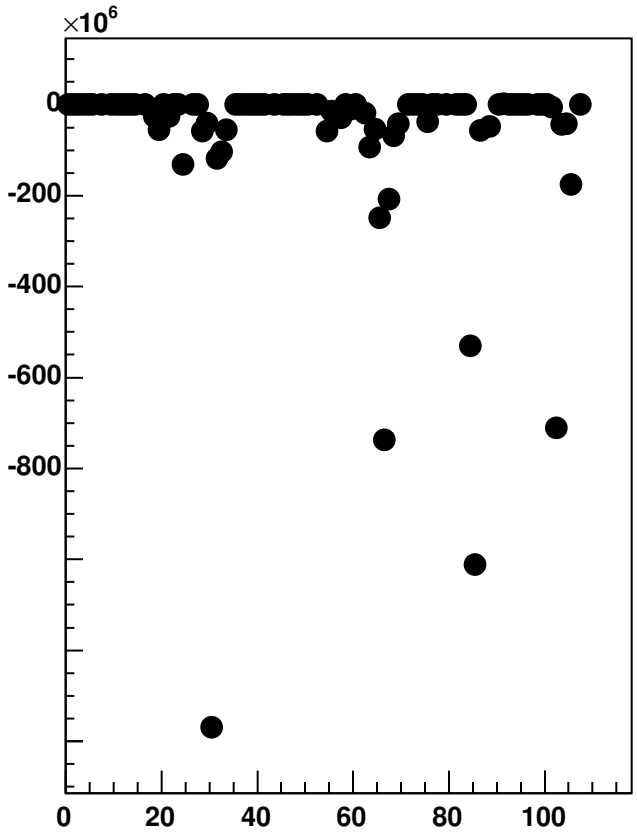

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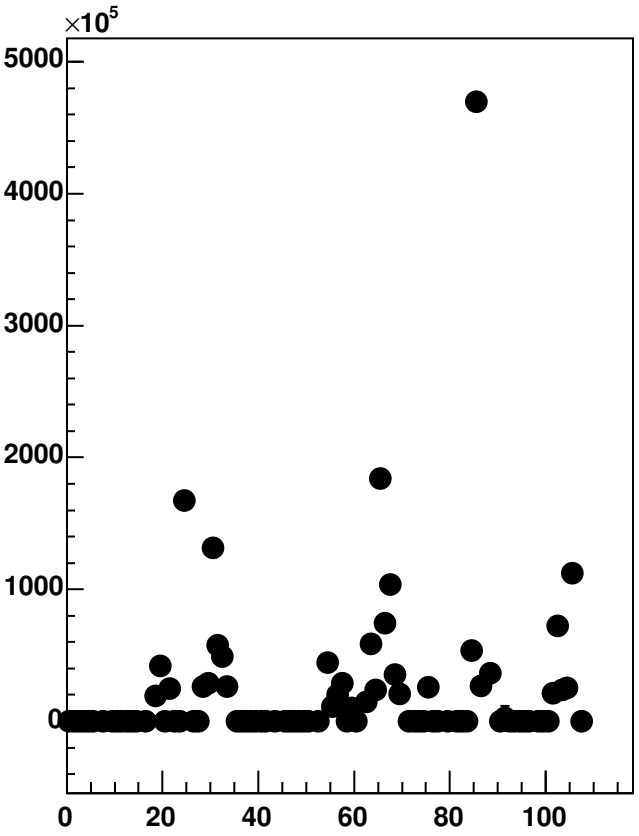
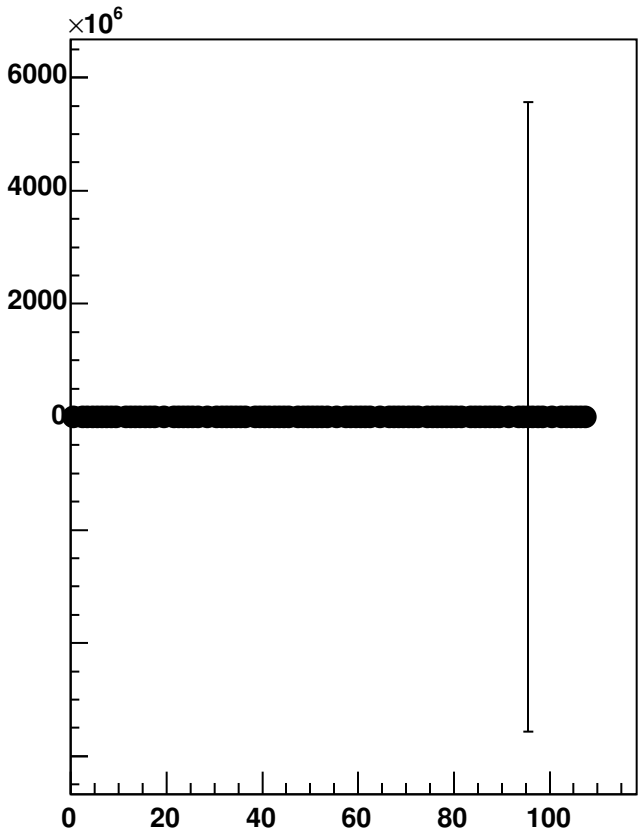
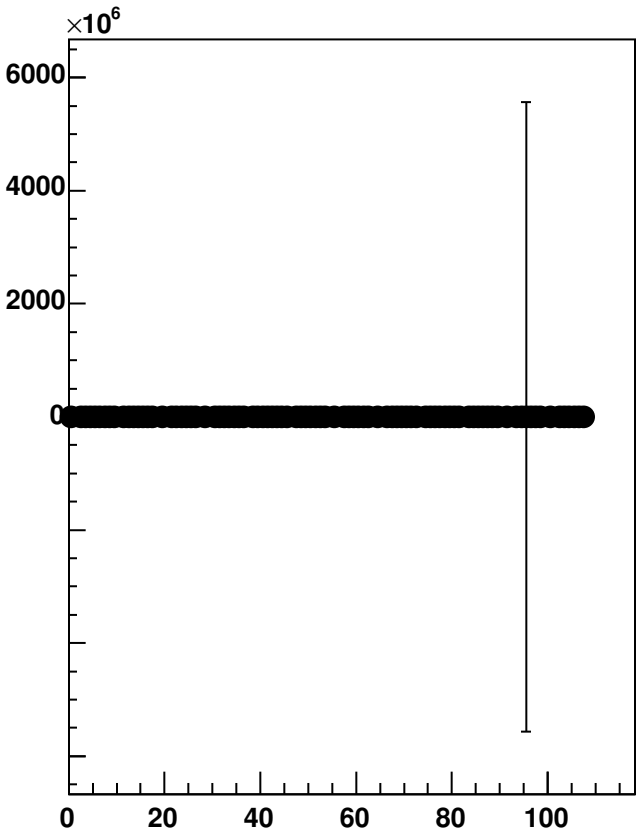


Disabled 3, DAC=4000, Fit Baseline vs 18*Chip+Chan





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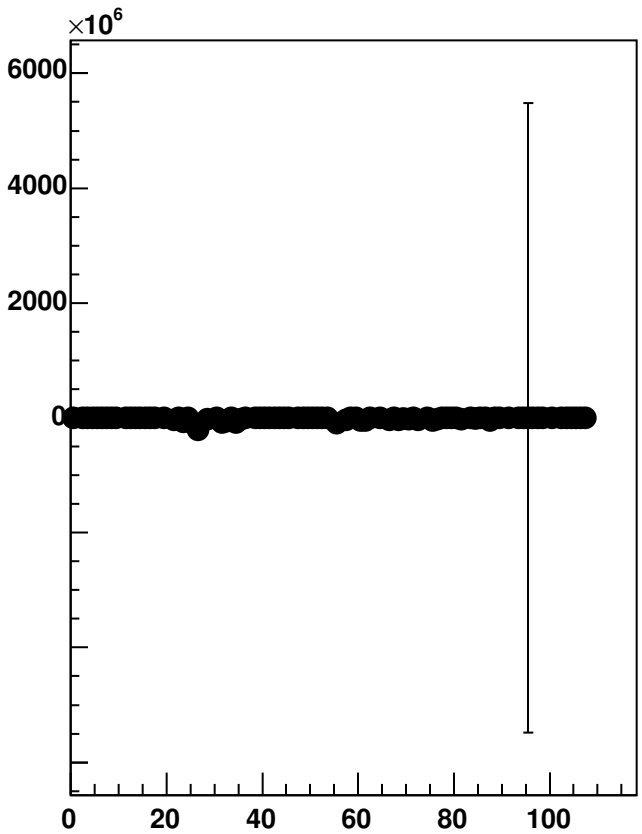

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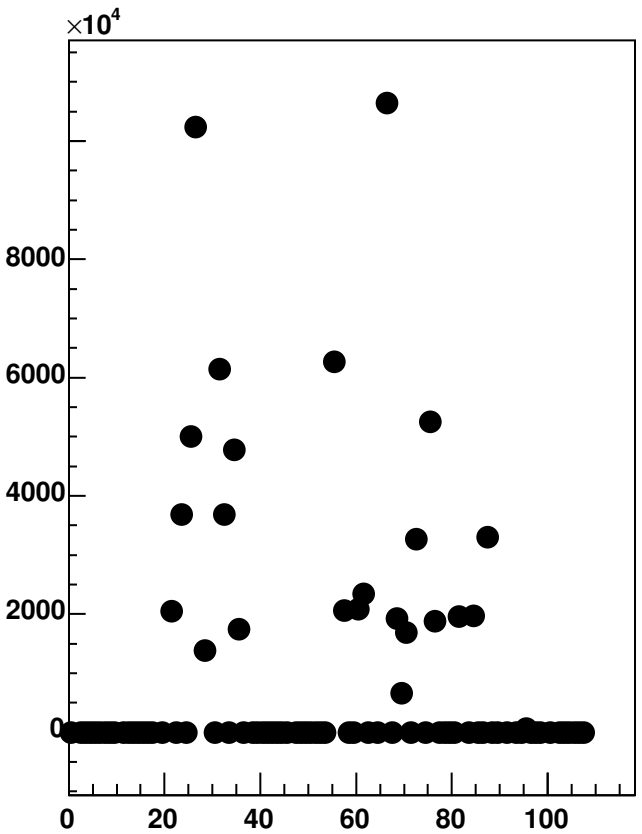
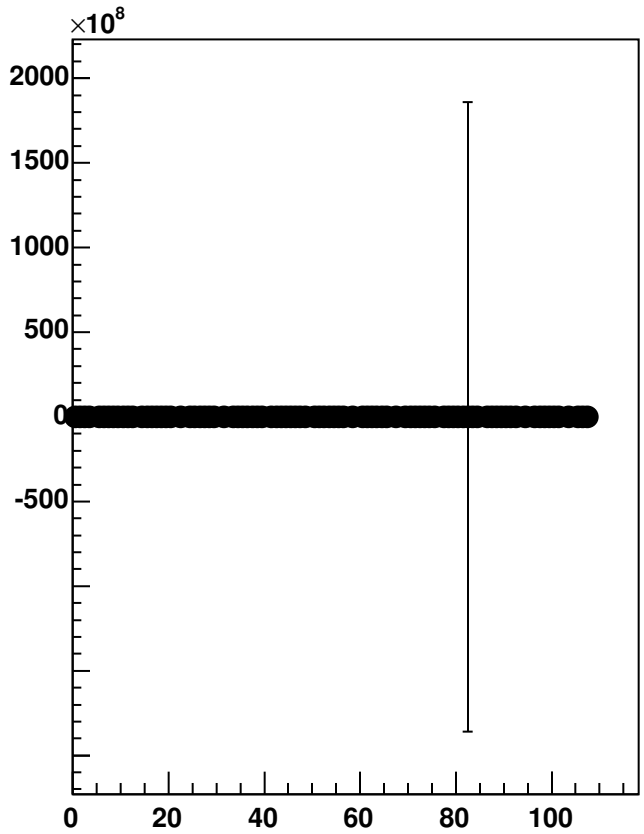
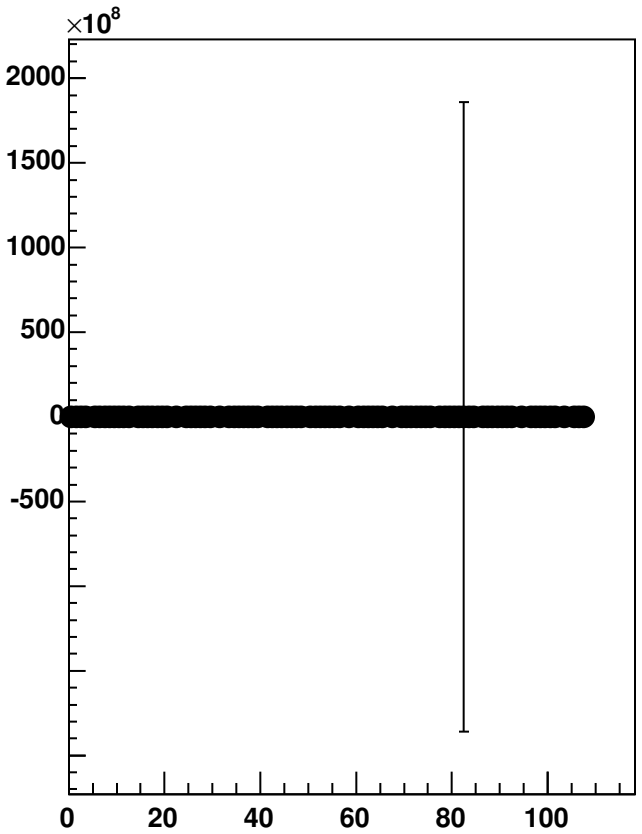
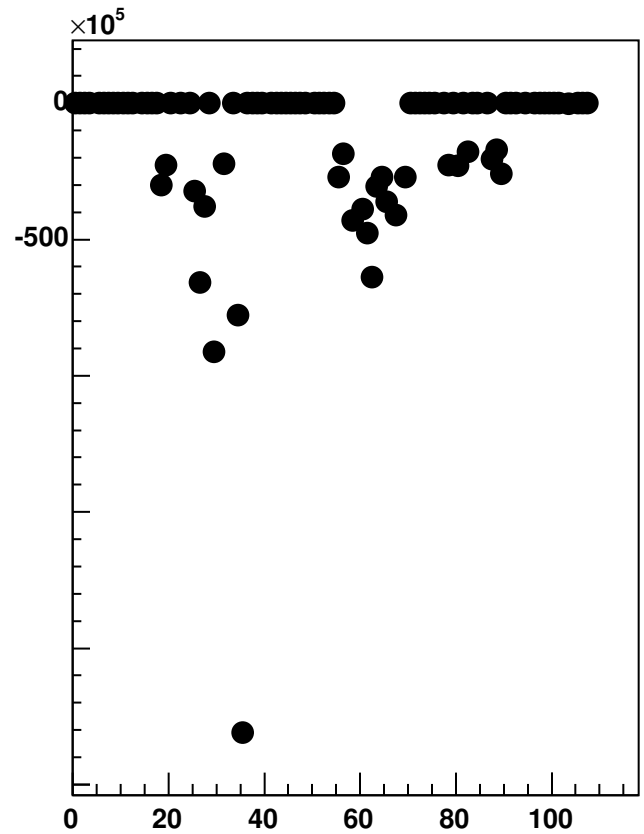


Disabled 4, DAC=4000, Fit Baseline vs 18*Chip+Chan

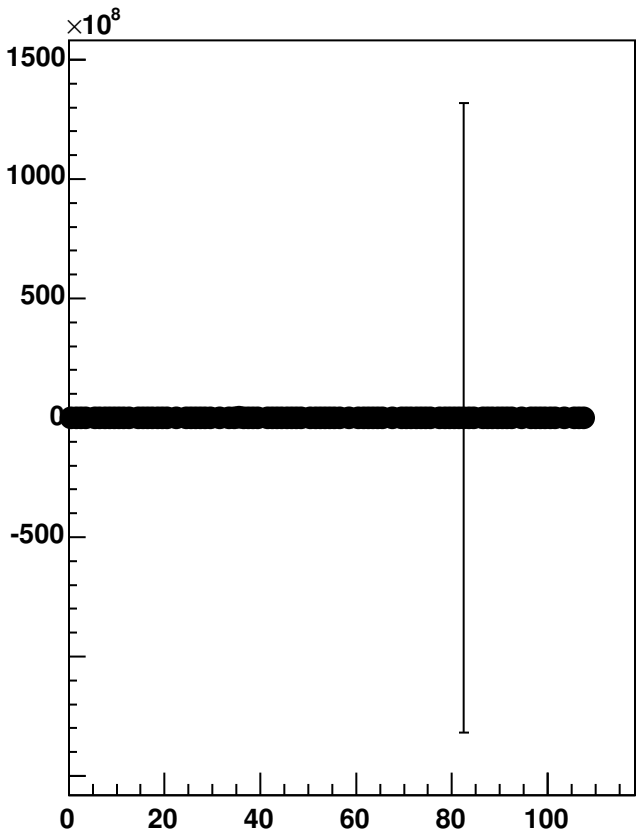




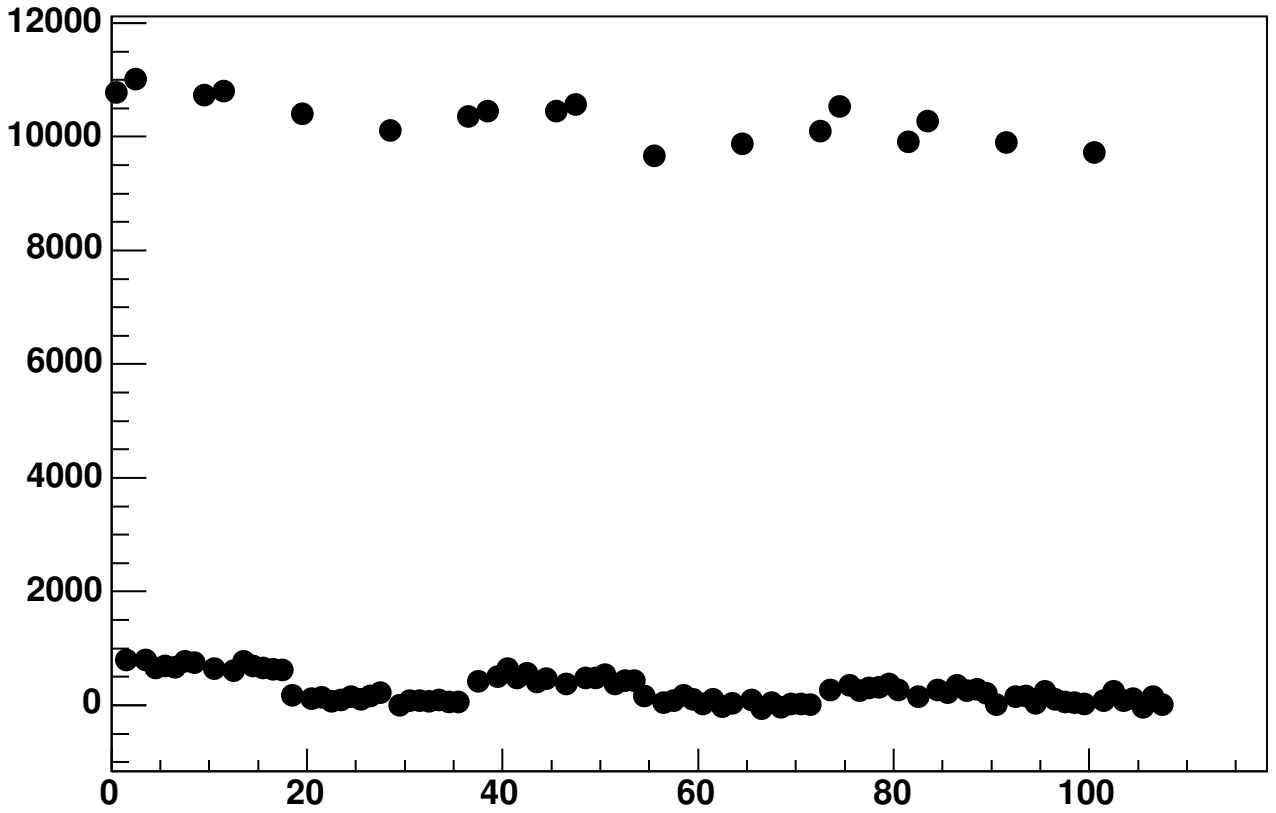
Disabled 4, DAC=4000, Fit Normalisation vs 18*Chip+Chan



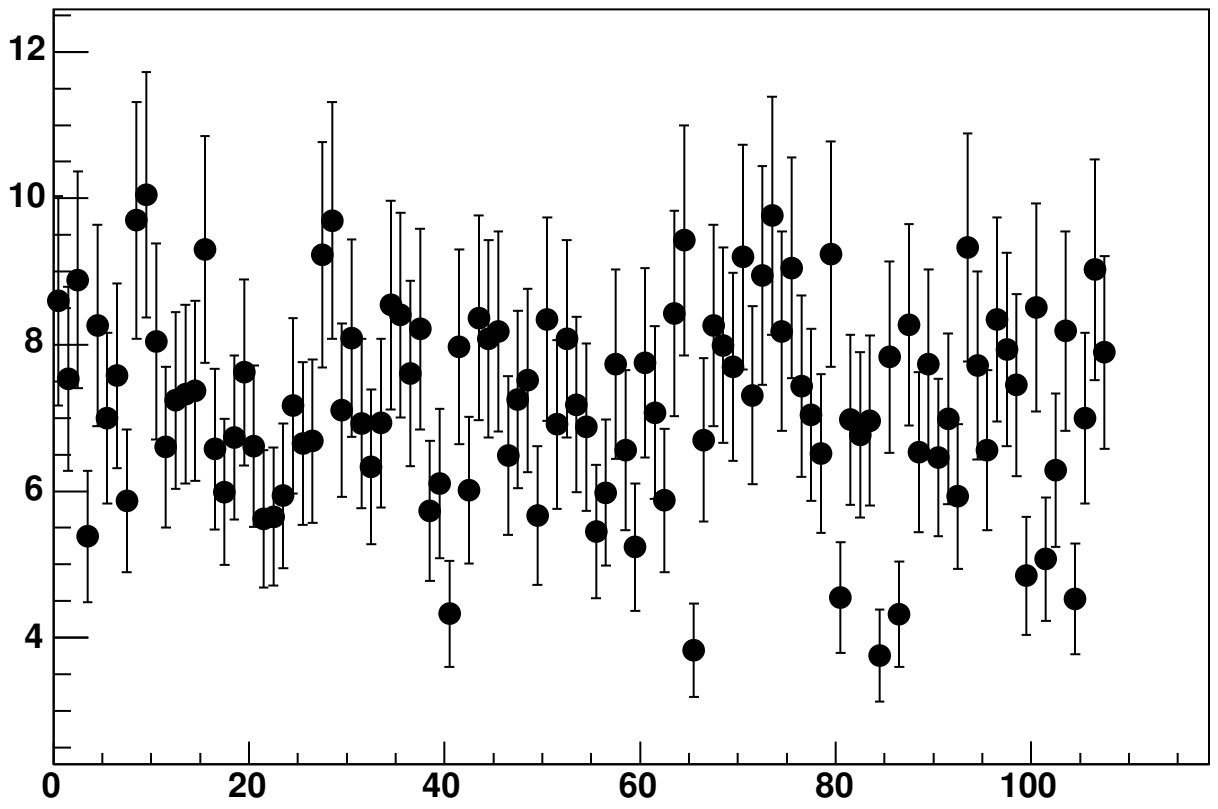
Disabled 4, DAC=4000, Fit Shaping Time vs 18*Chip+Chan



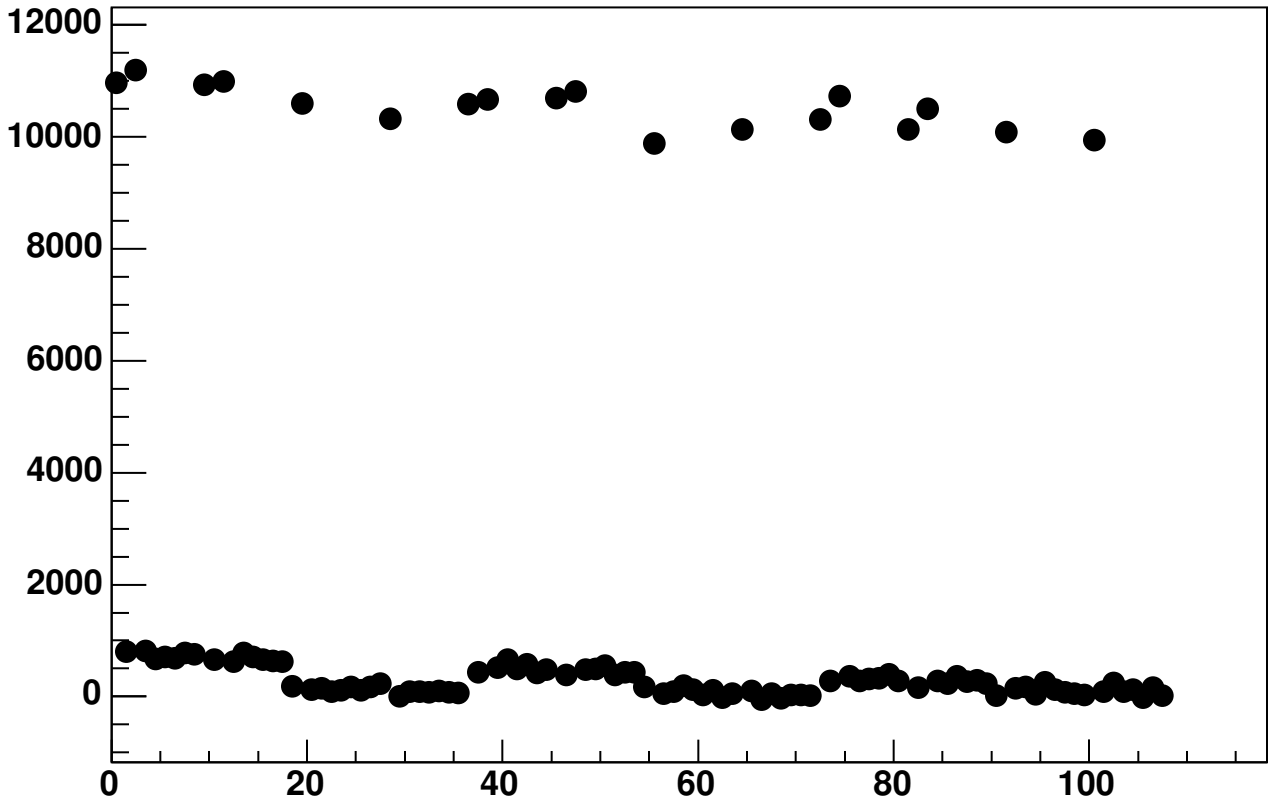
Enable 0, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



Enable 0, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



Enable 0, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



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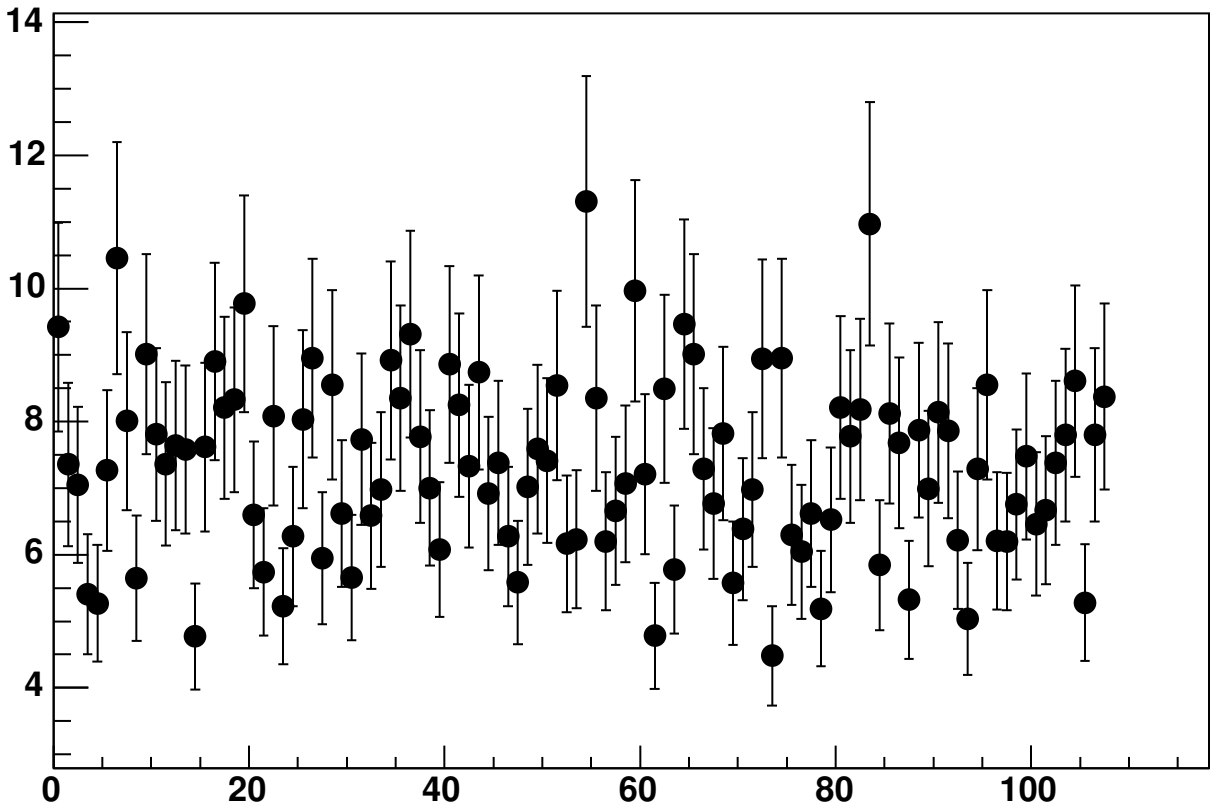
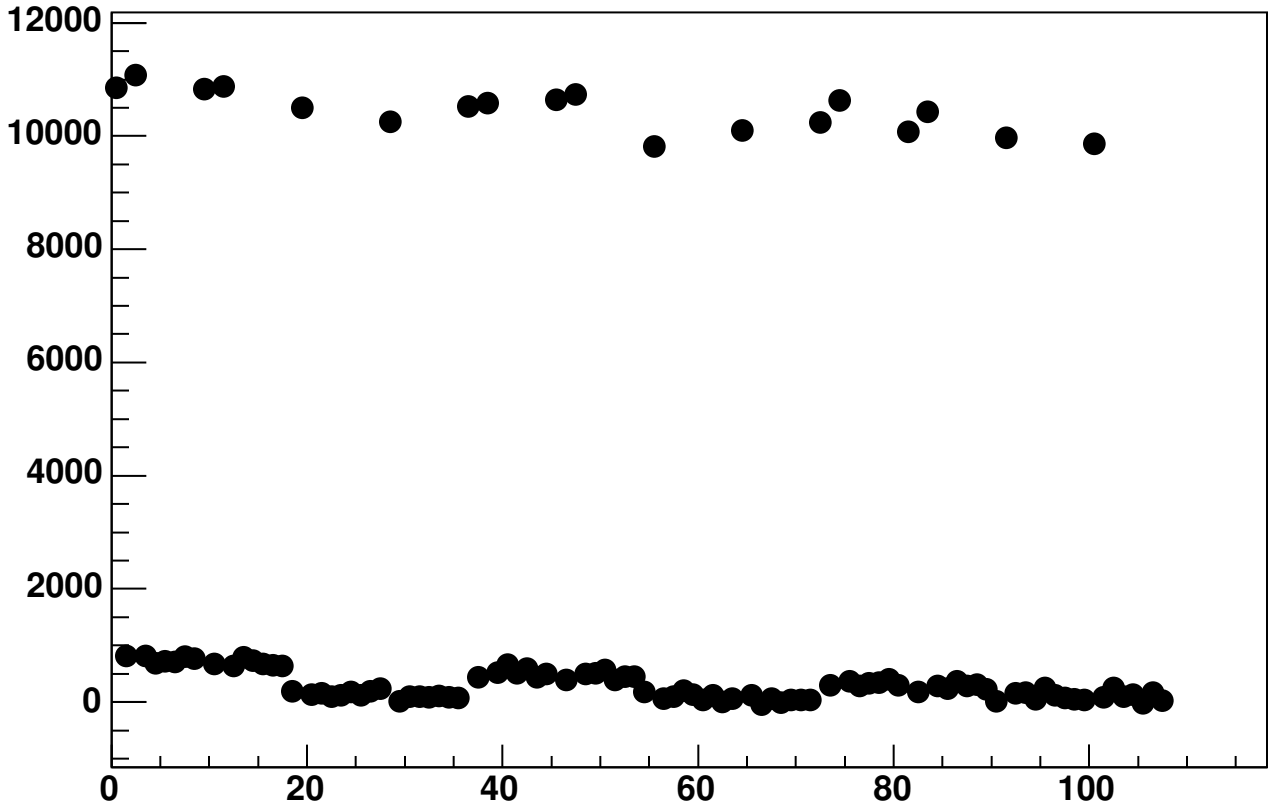
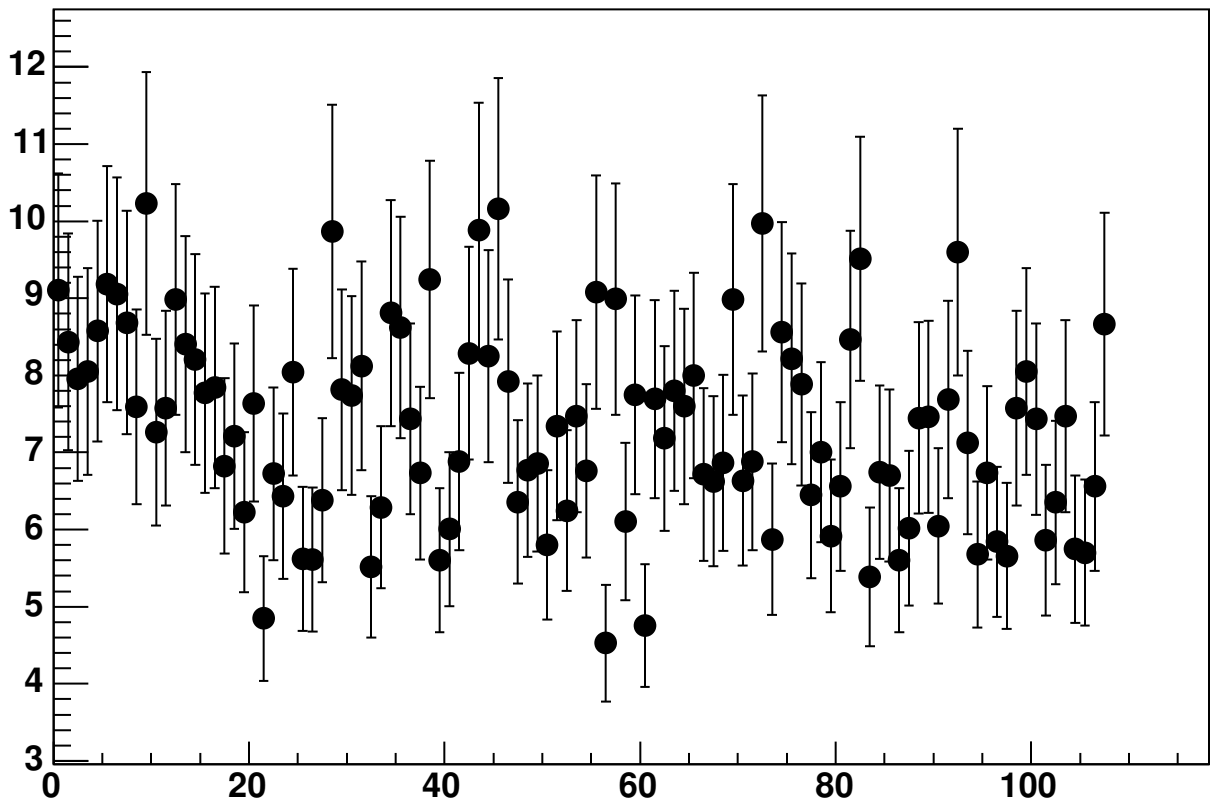


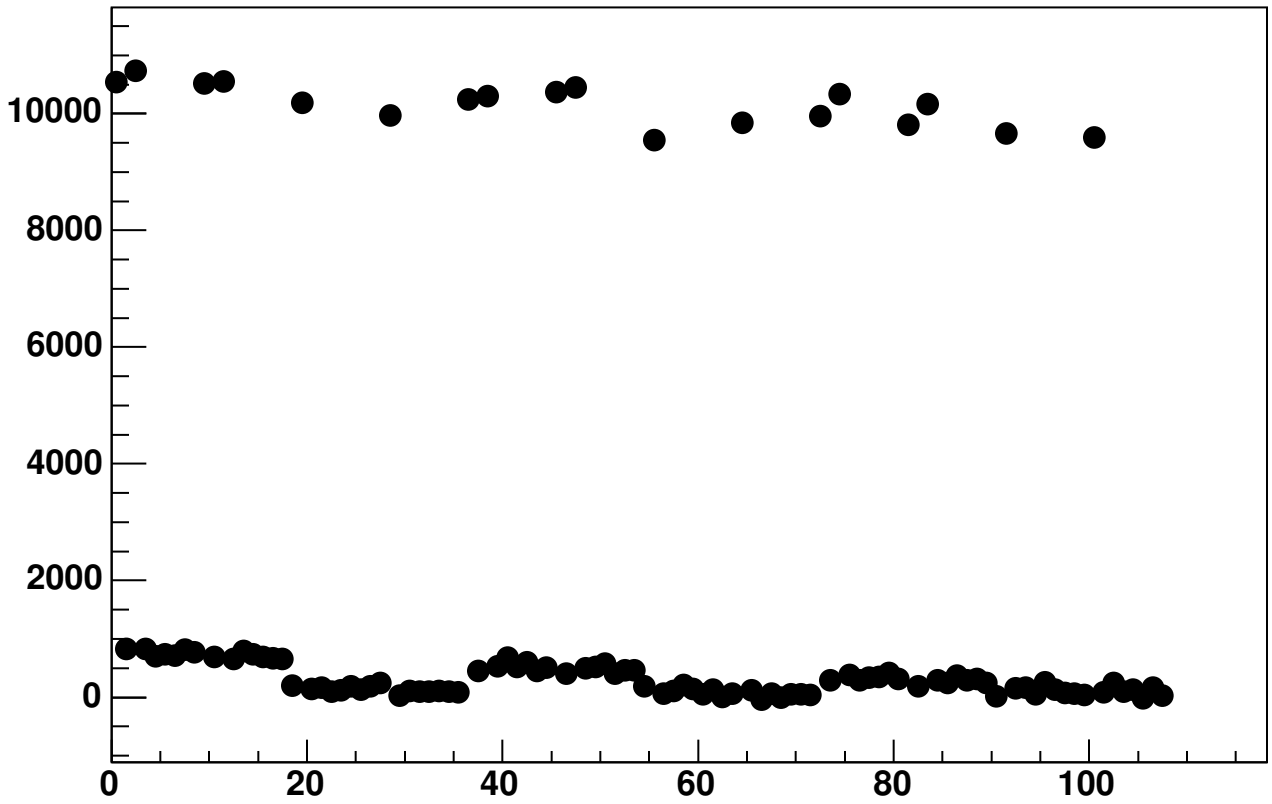
Enable 0, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



Enable 0, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



Enable 0, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



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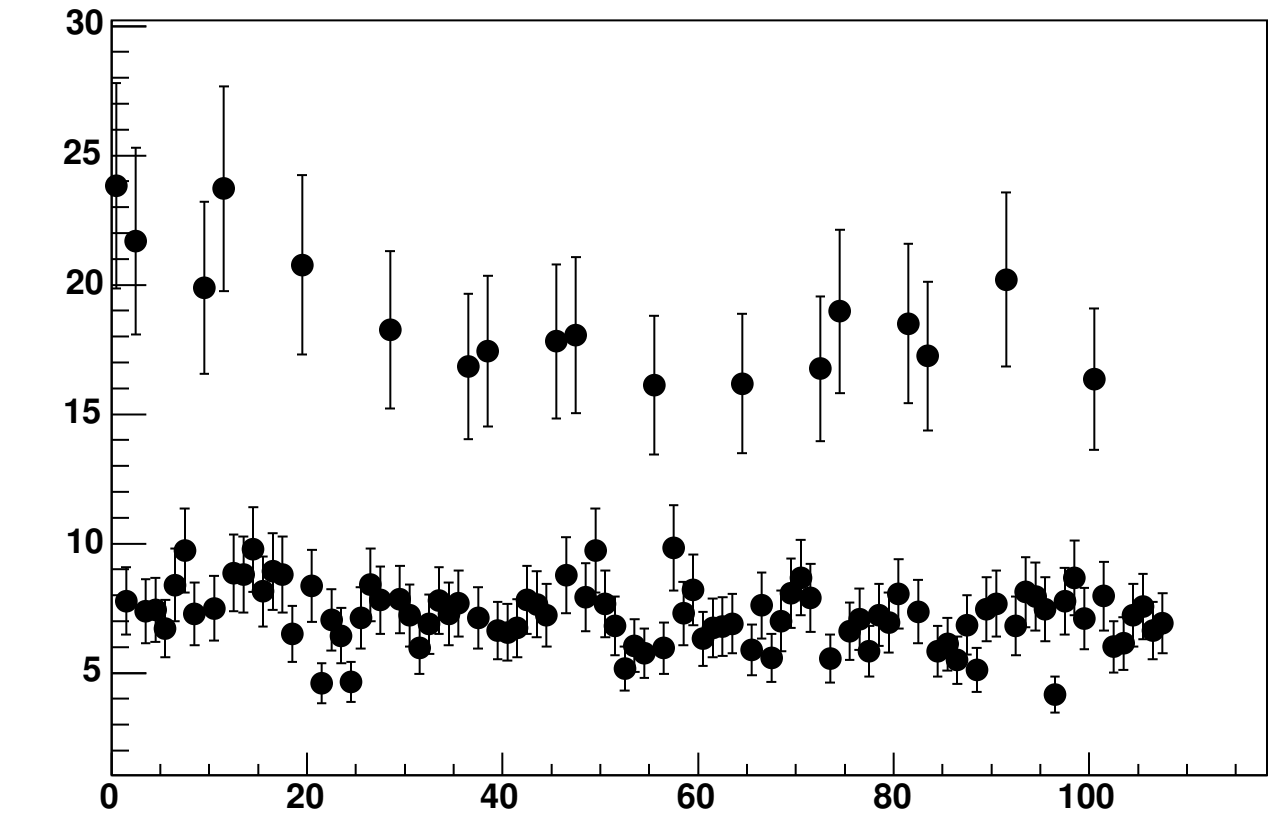
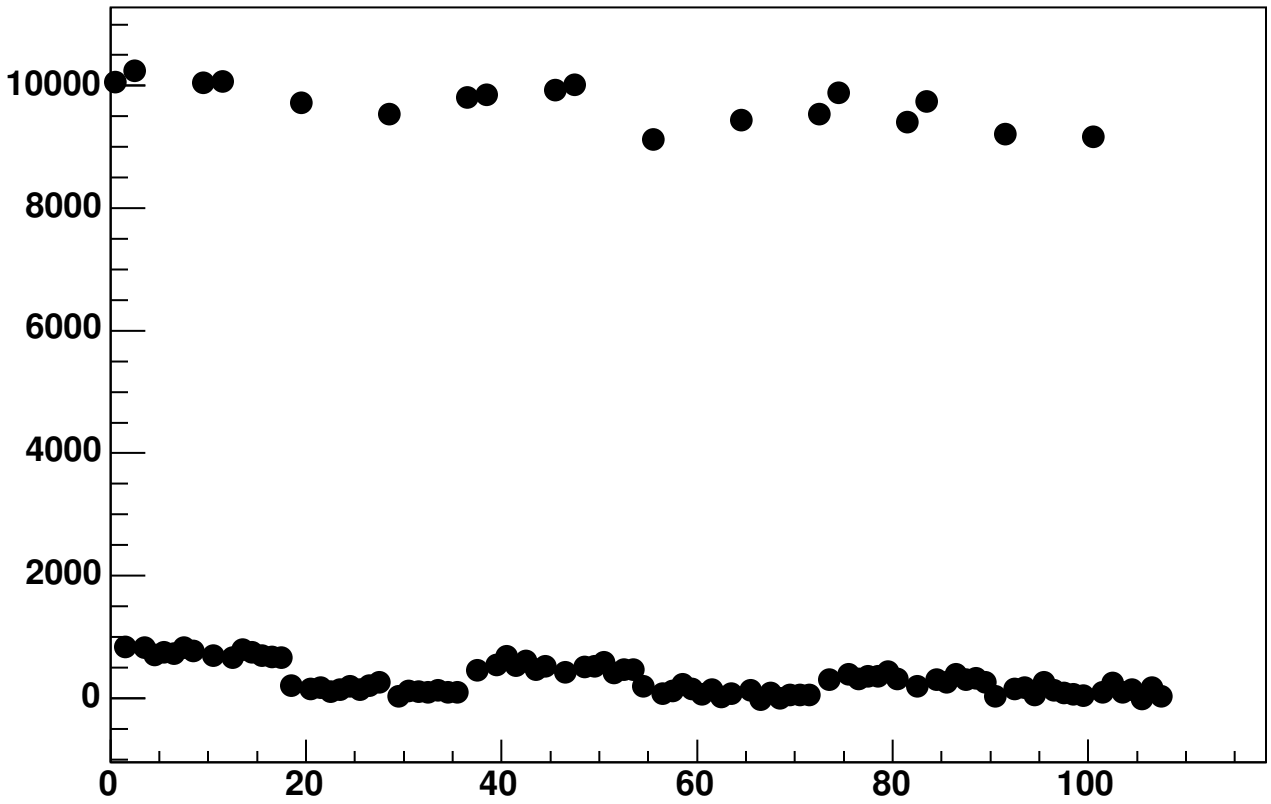
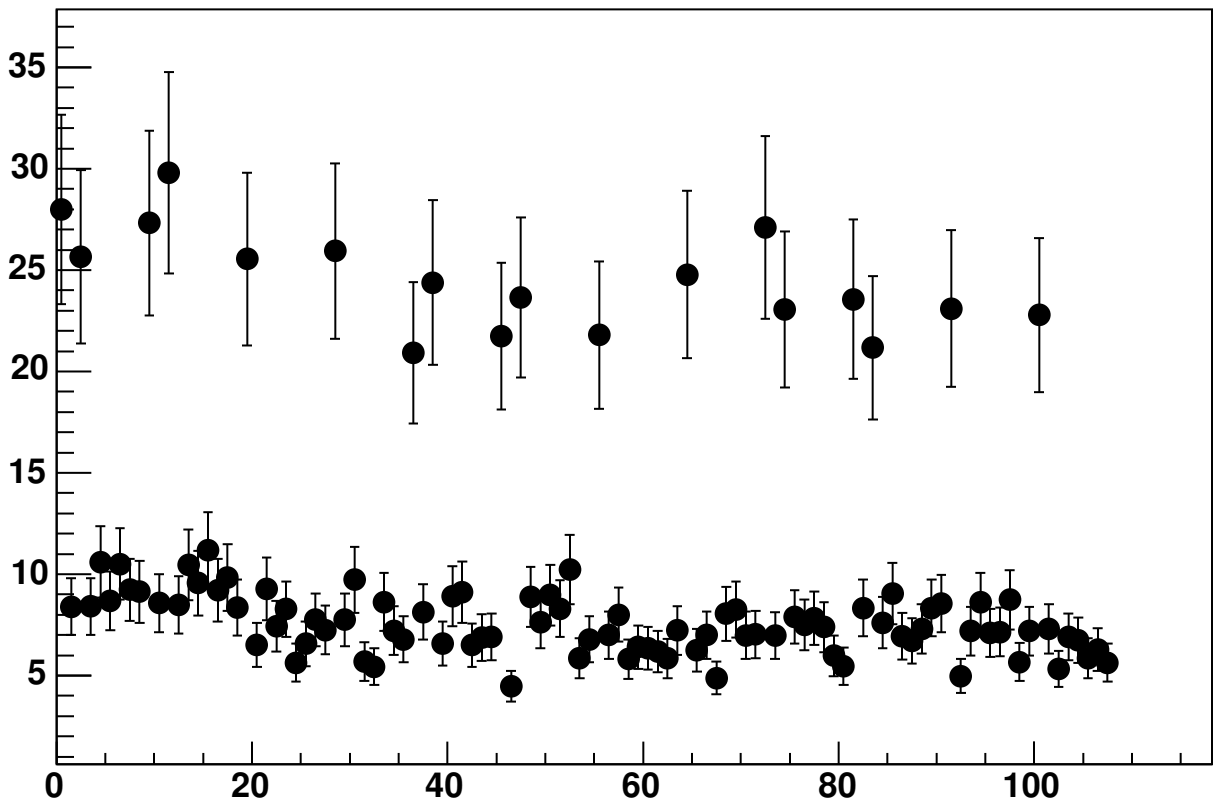


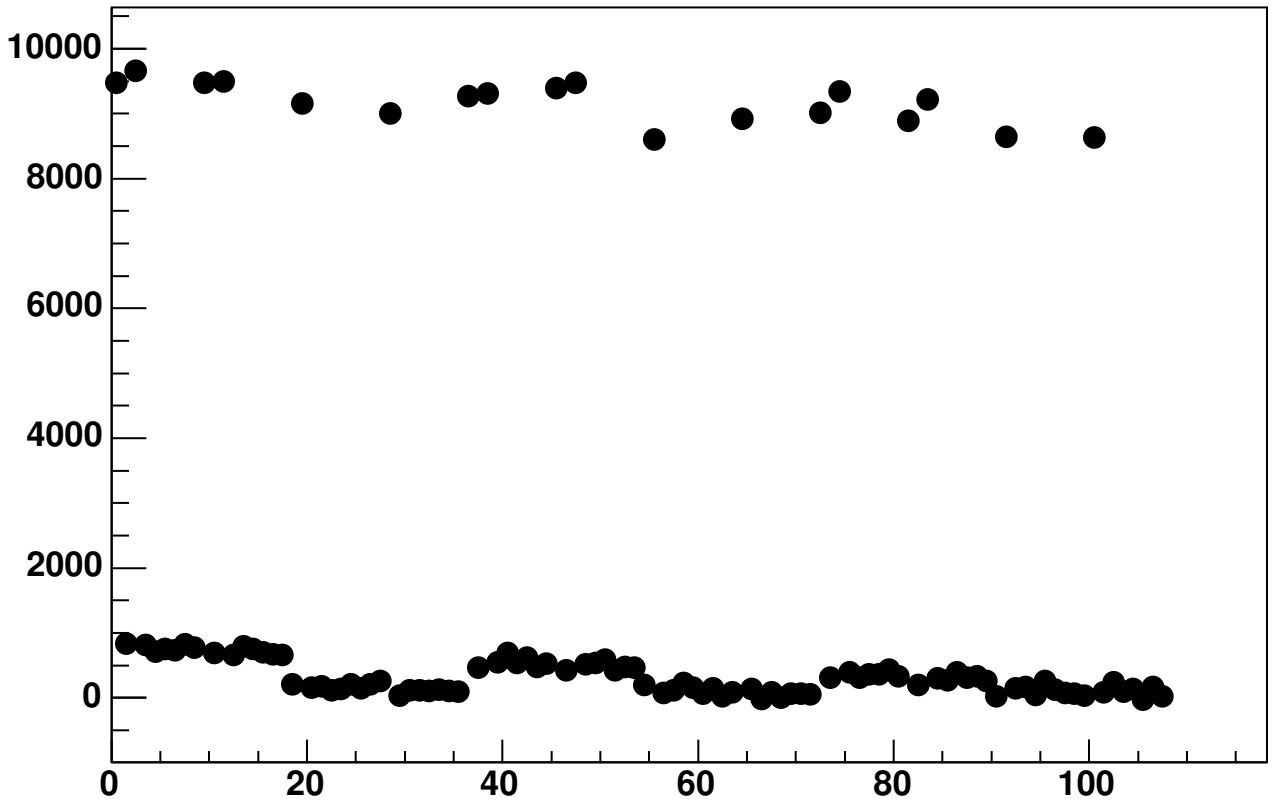
Enable 0, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



Enable 0, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



Enable 0, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



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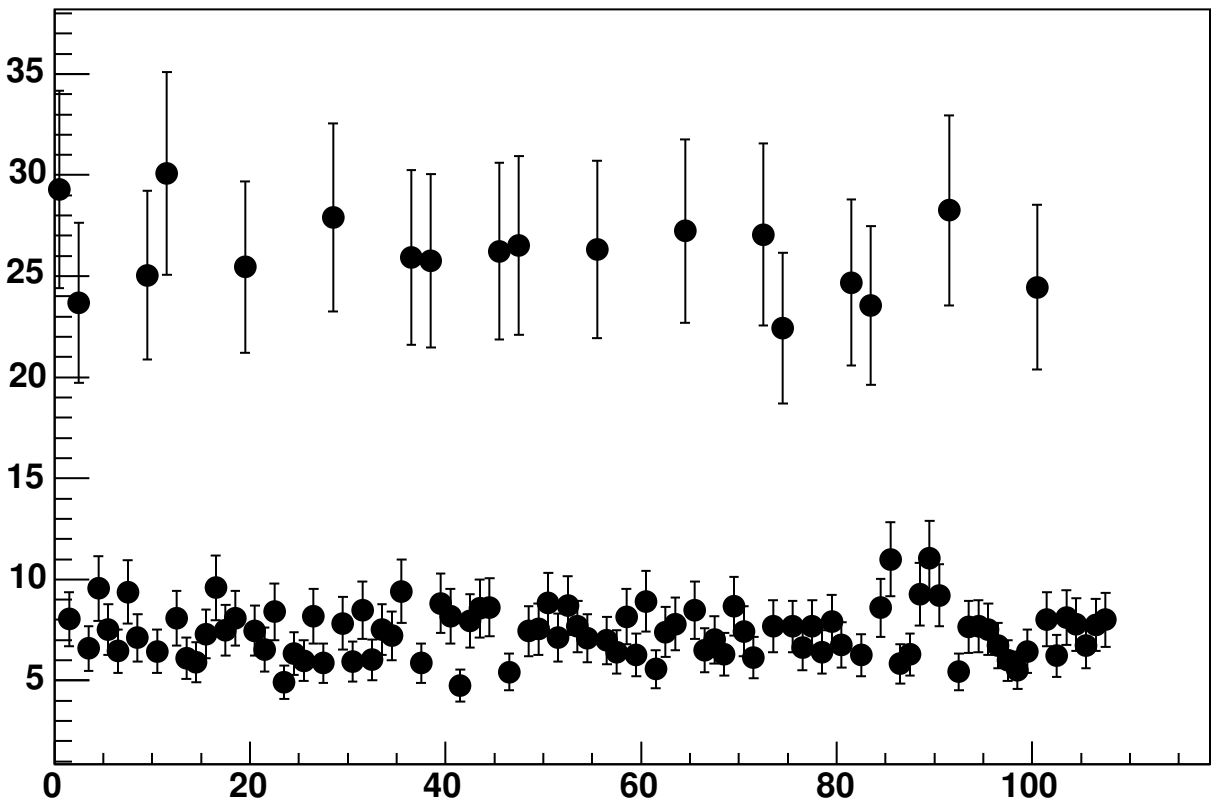
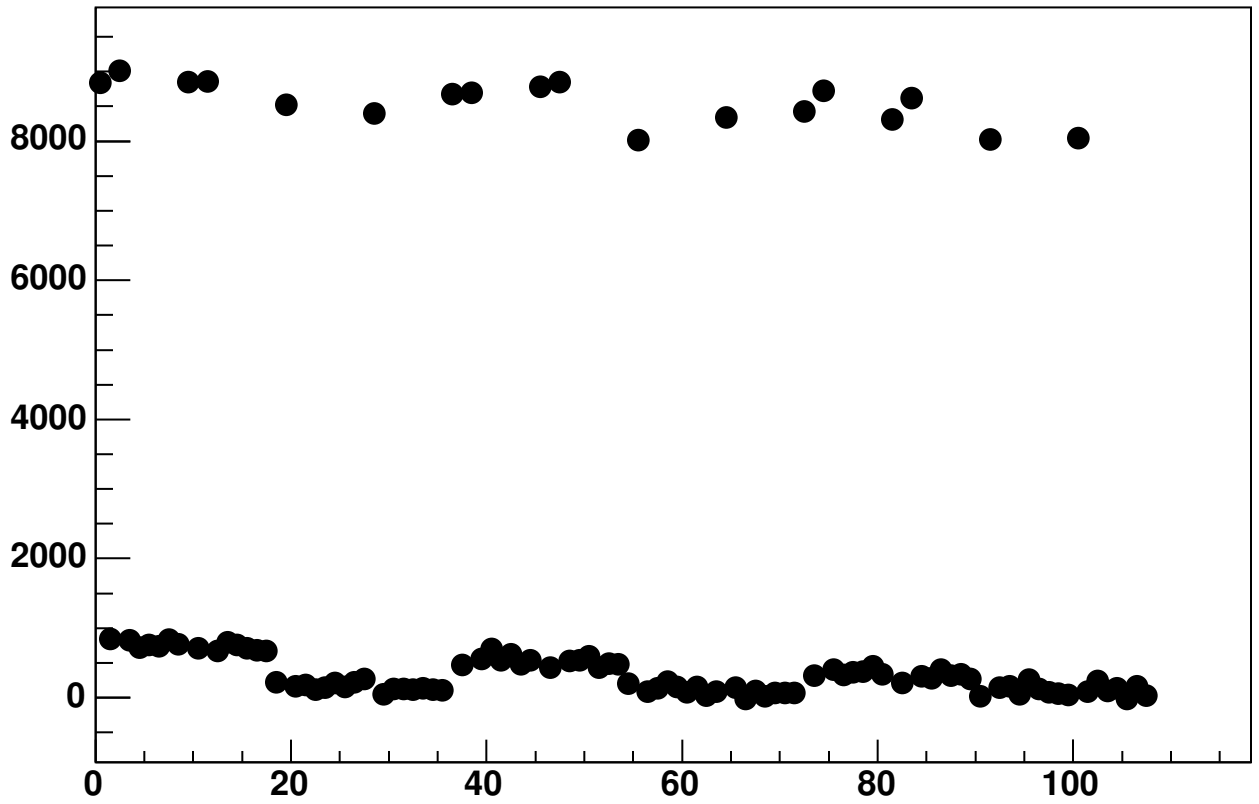


Enable 0, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



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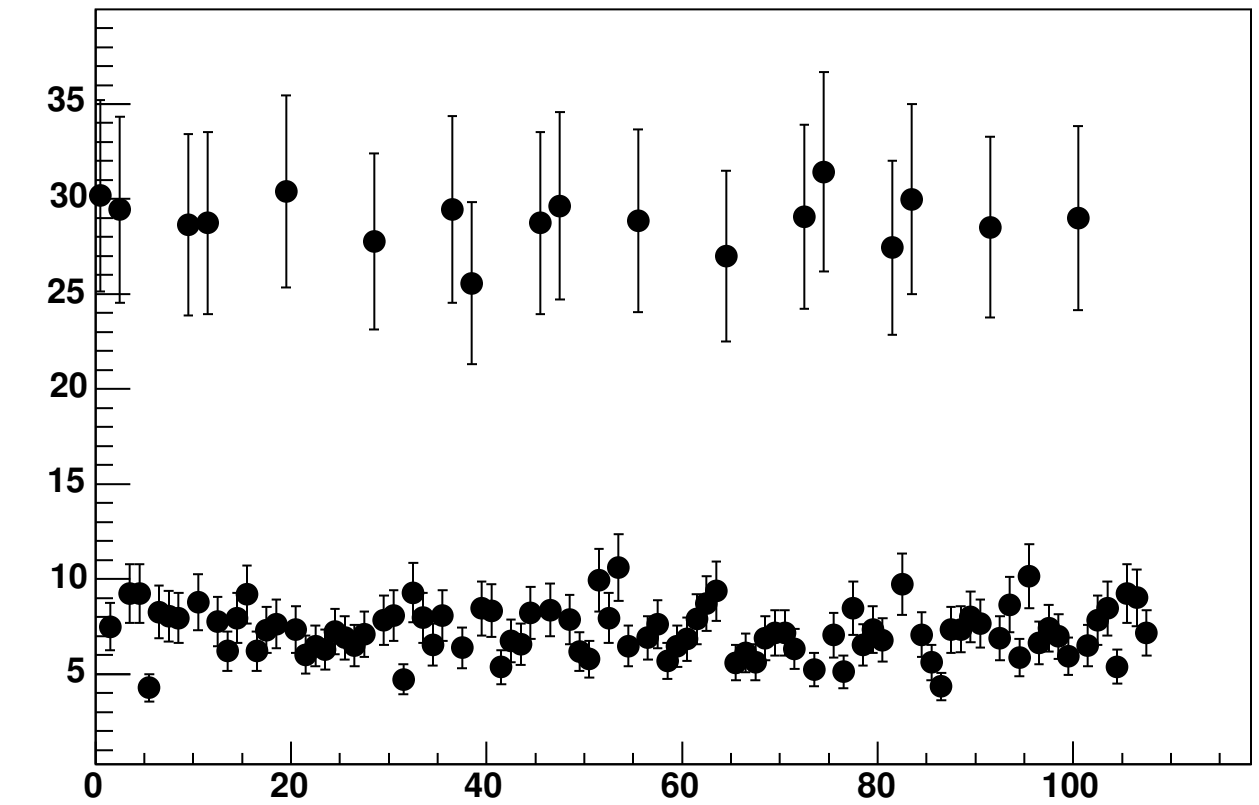
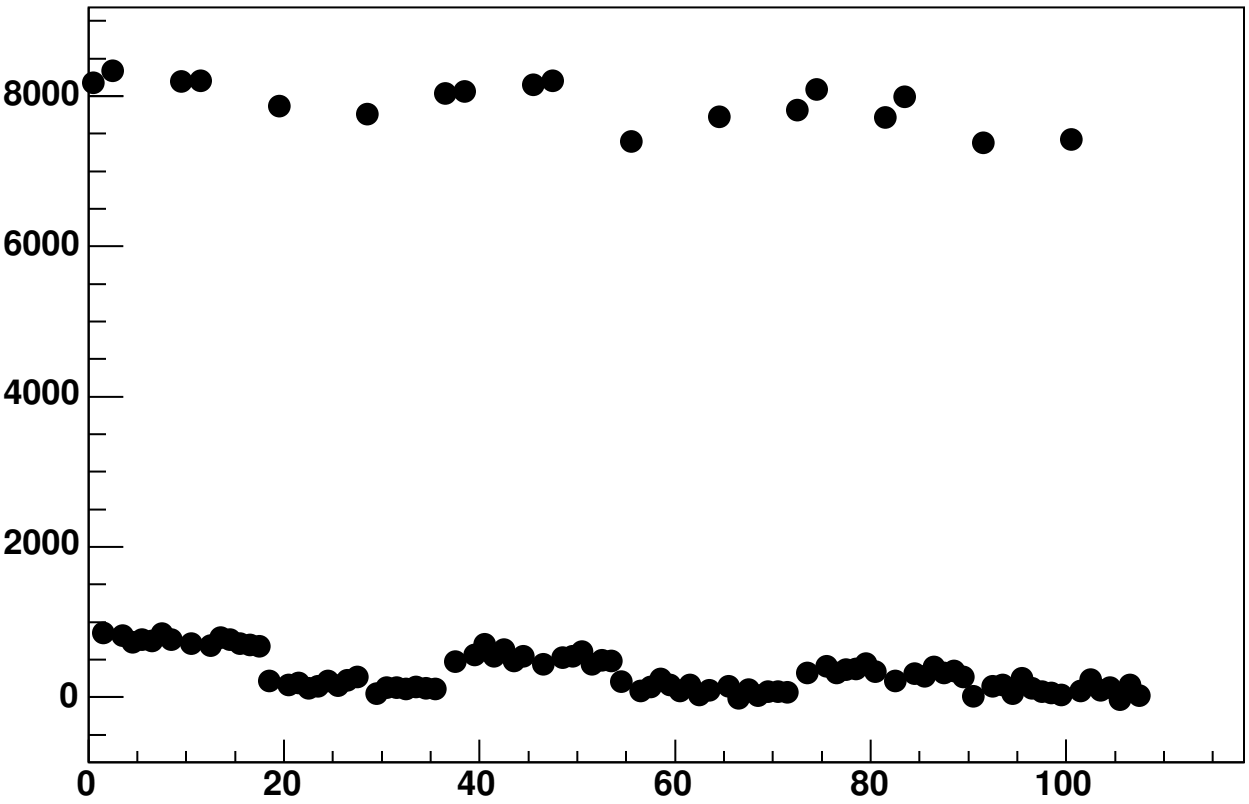


Enable 0, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



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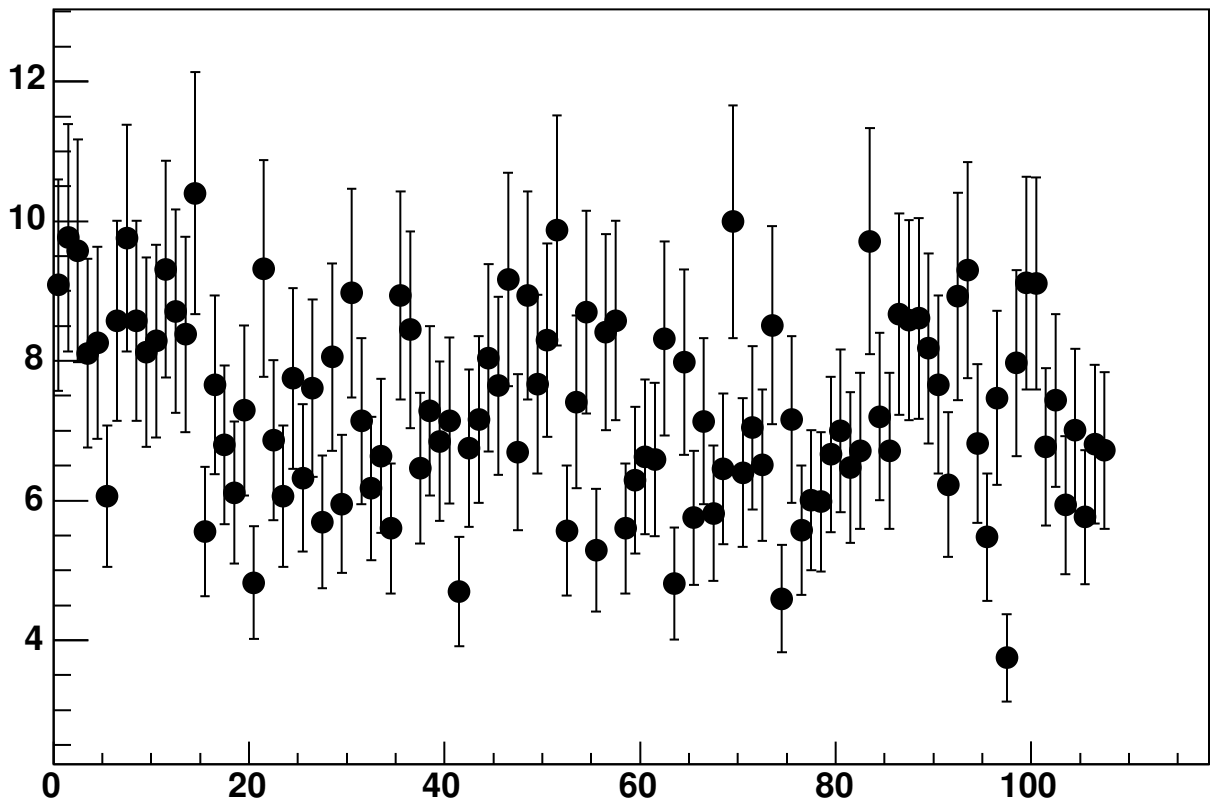
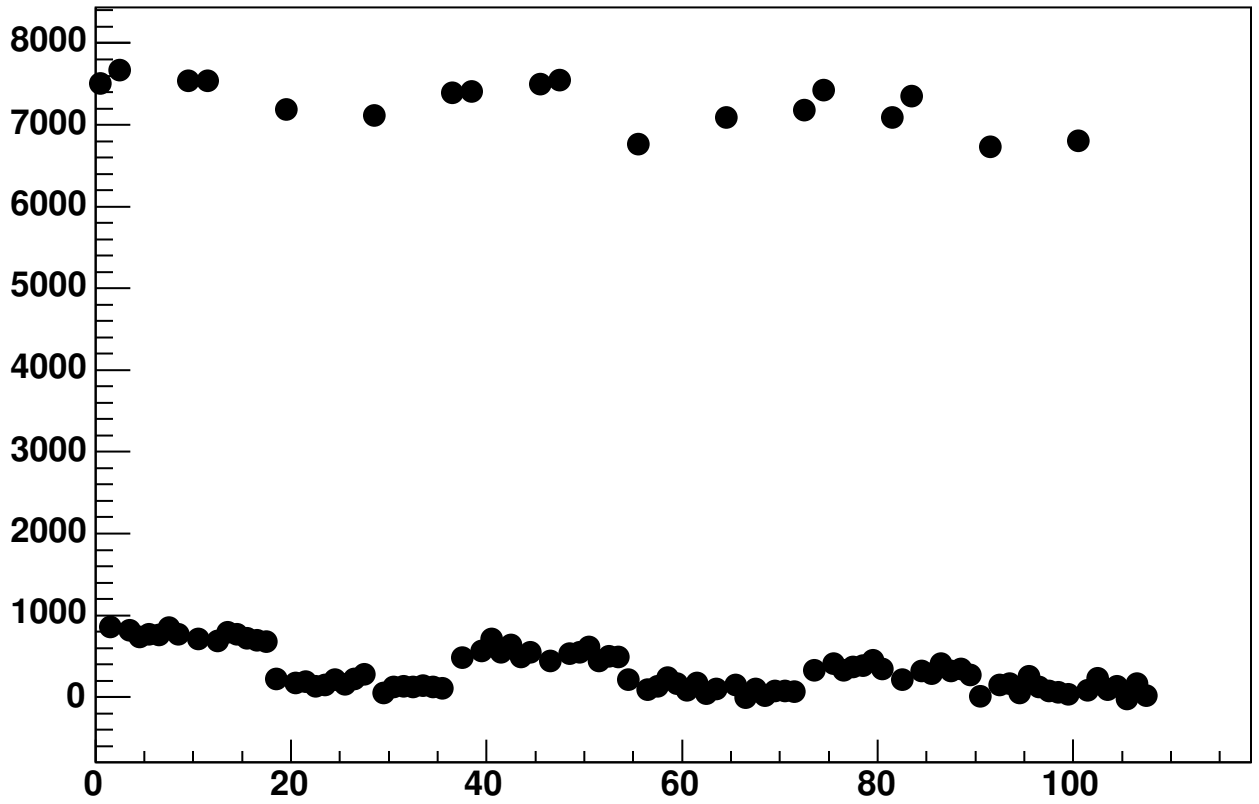
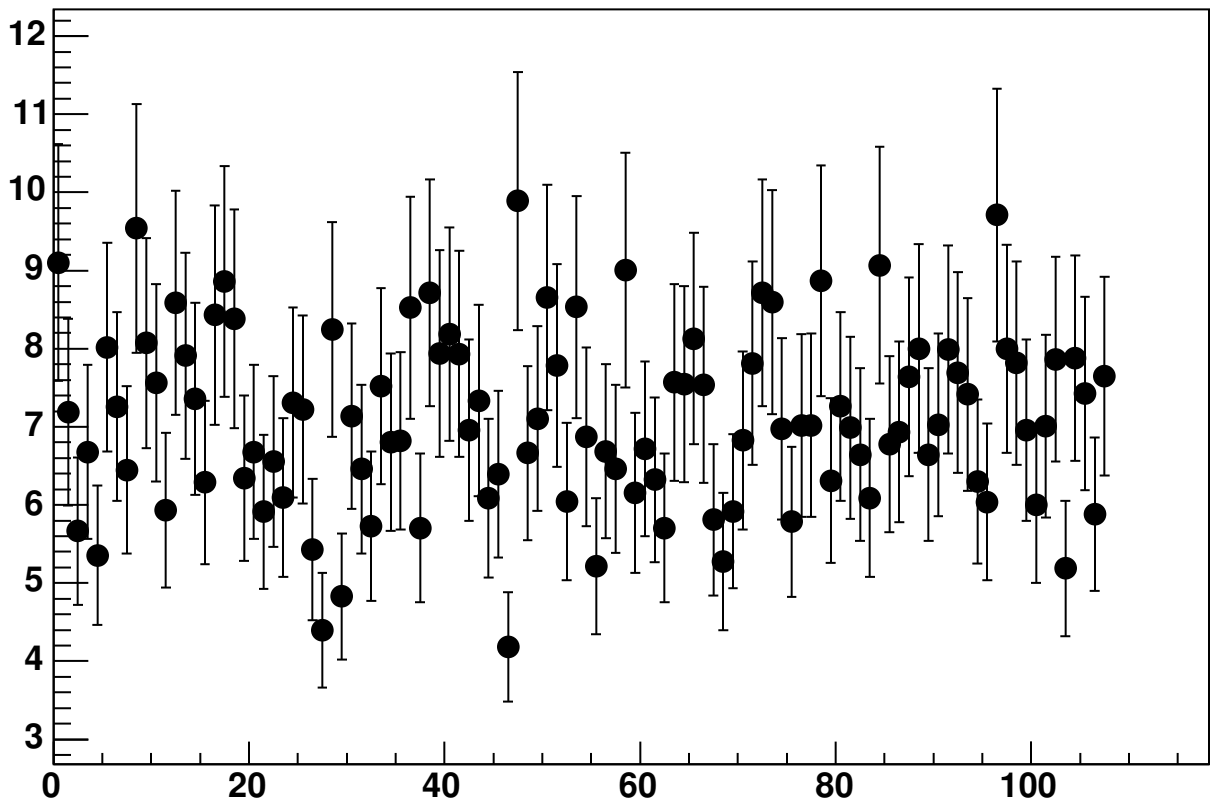


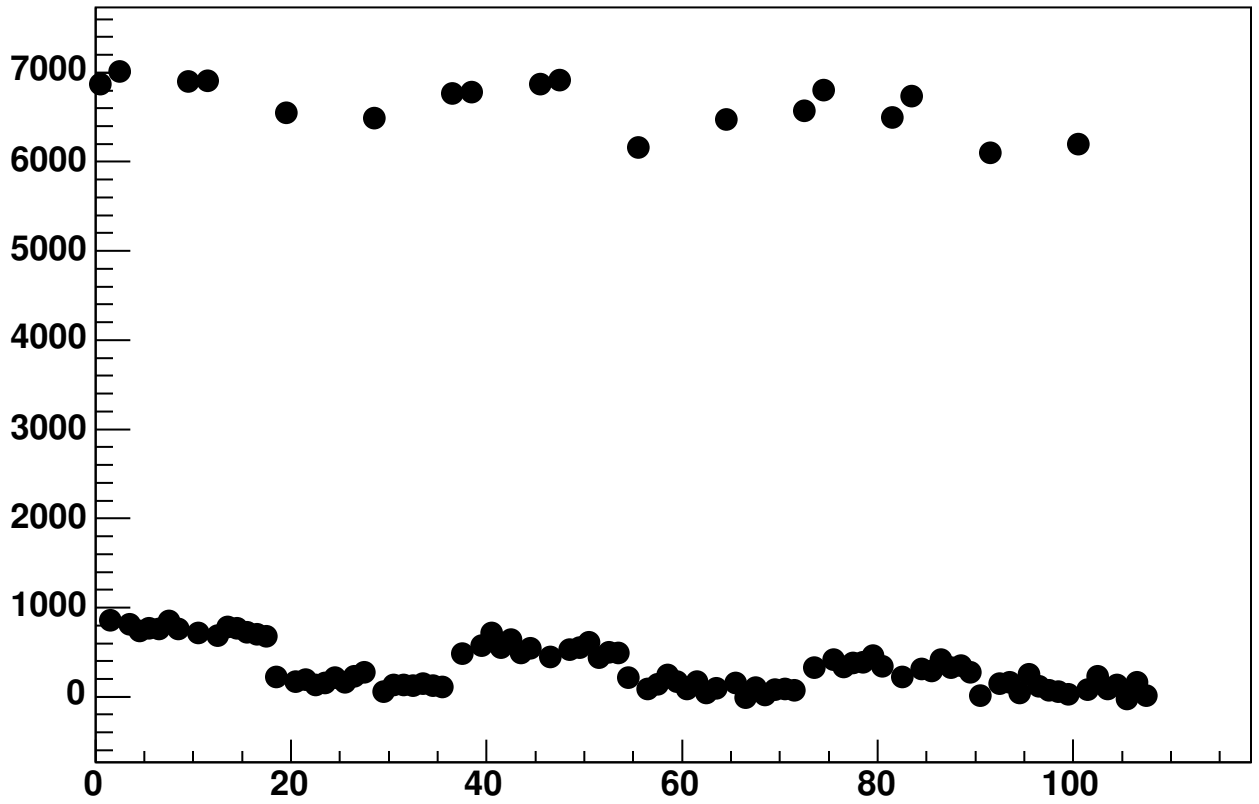
Enable 0, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



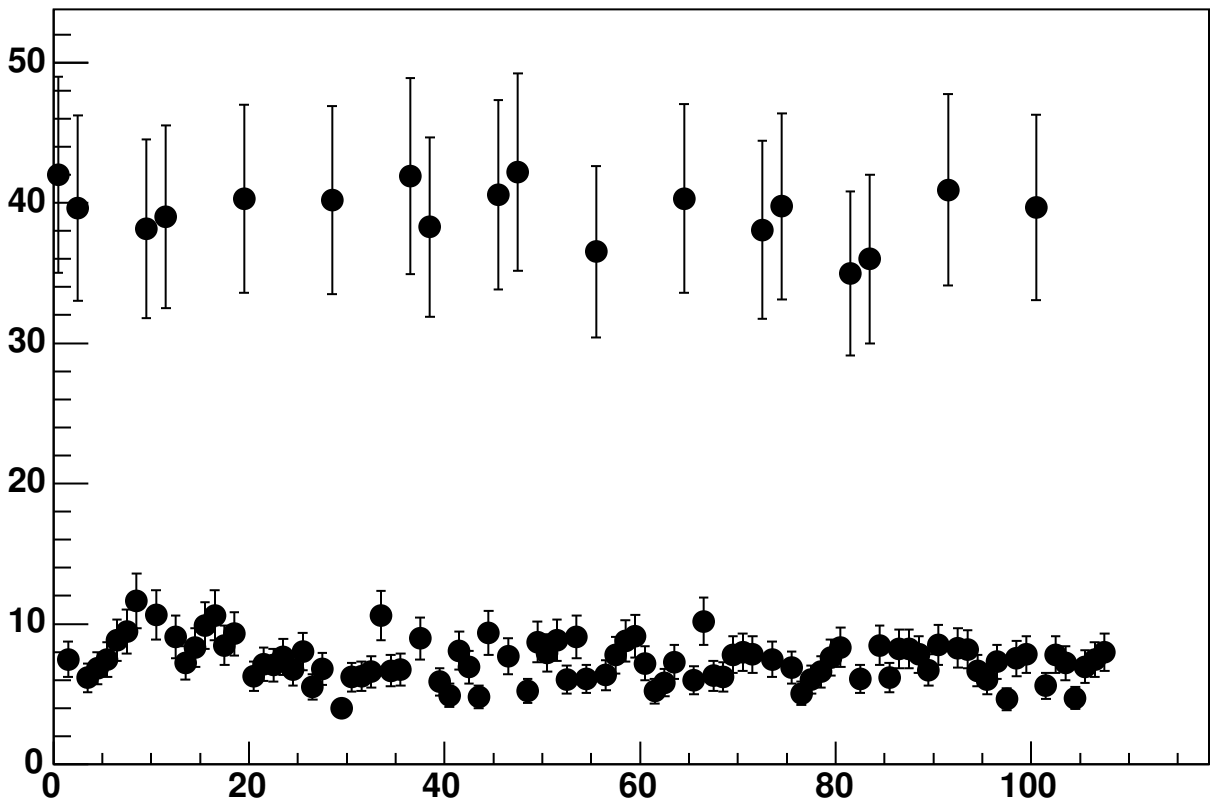
Enable 0, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



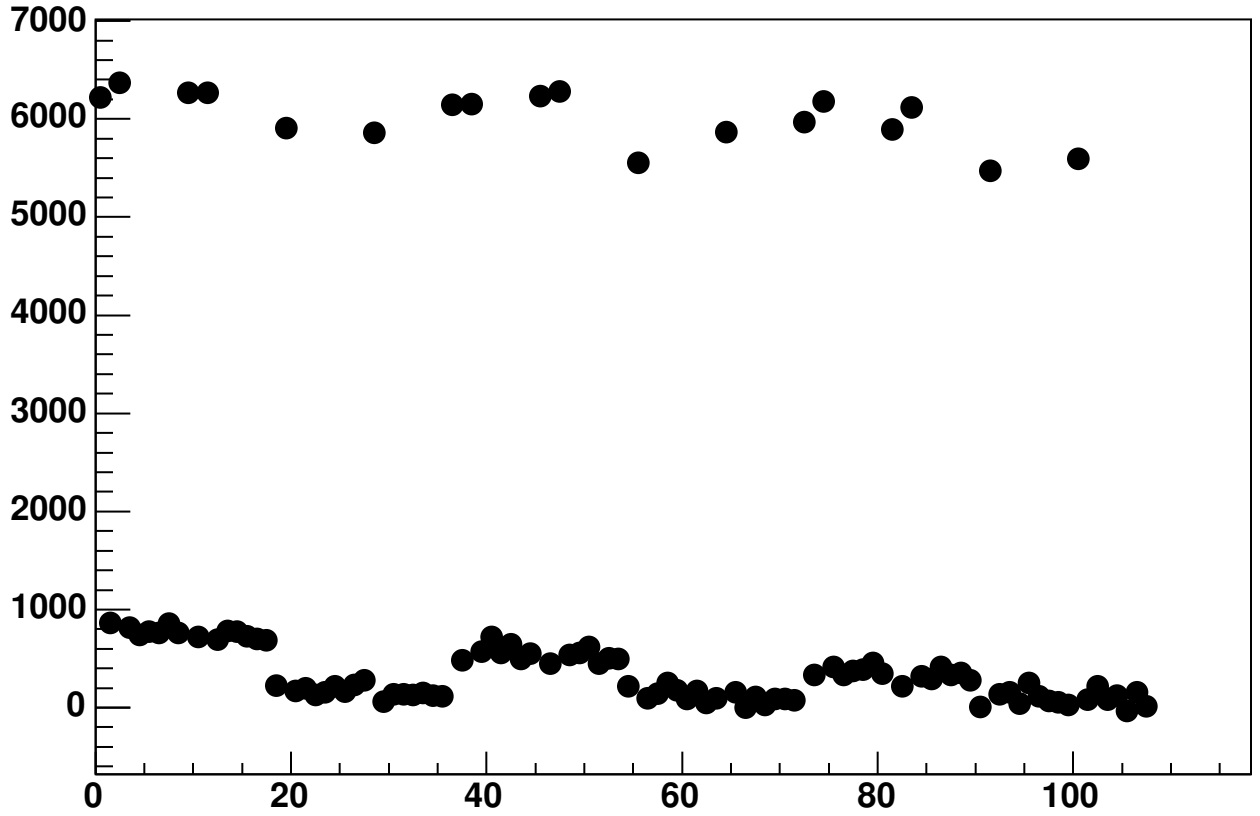
Enable 0, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



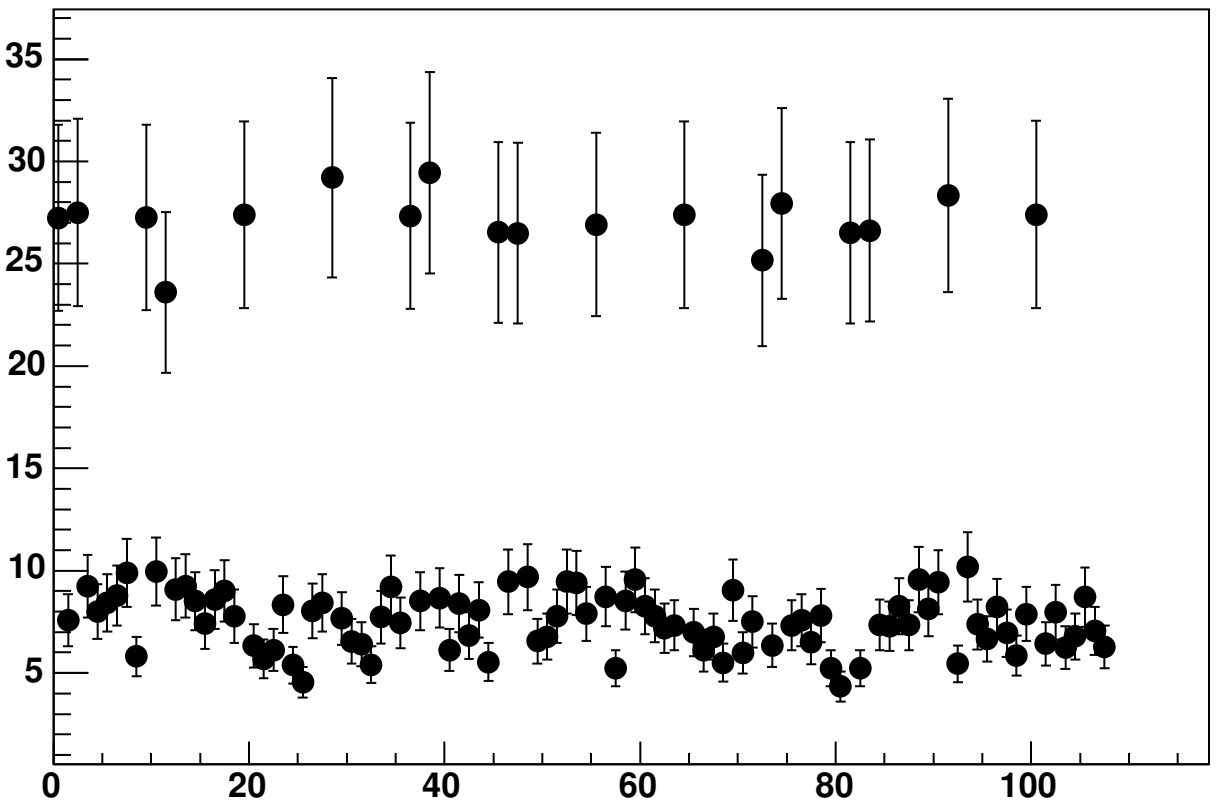
Enable 0, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



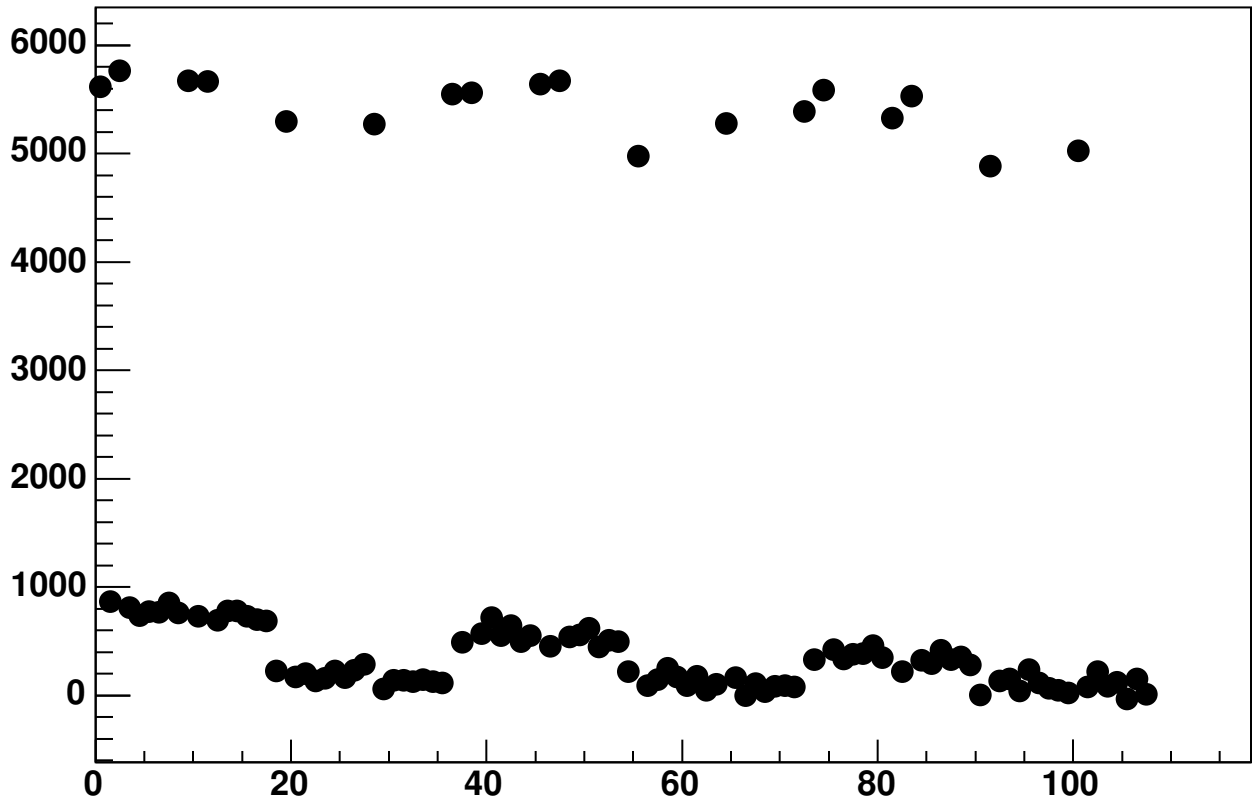
Enable 0, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



Enable 0, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



Enable 0, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



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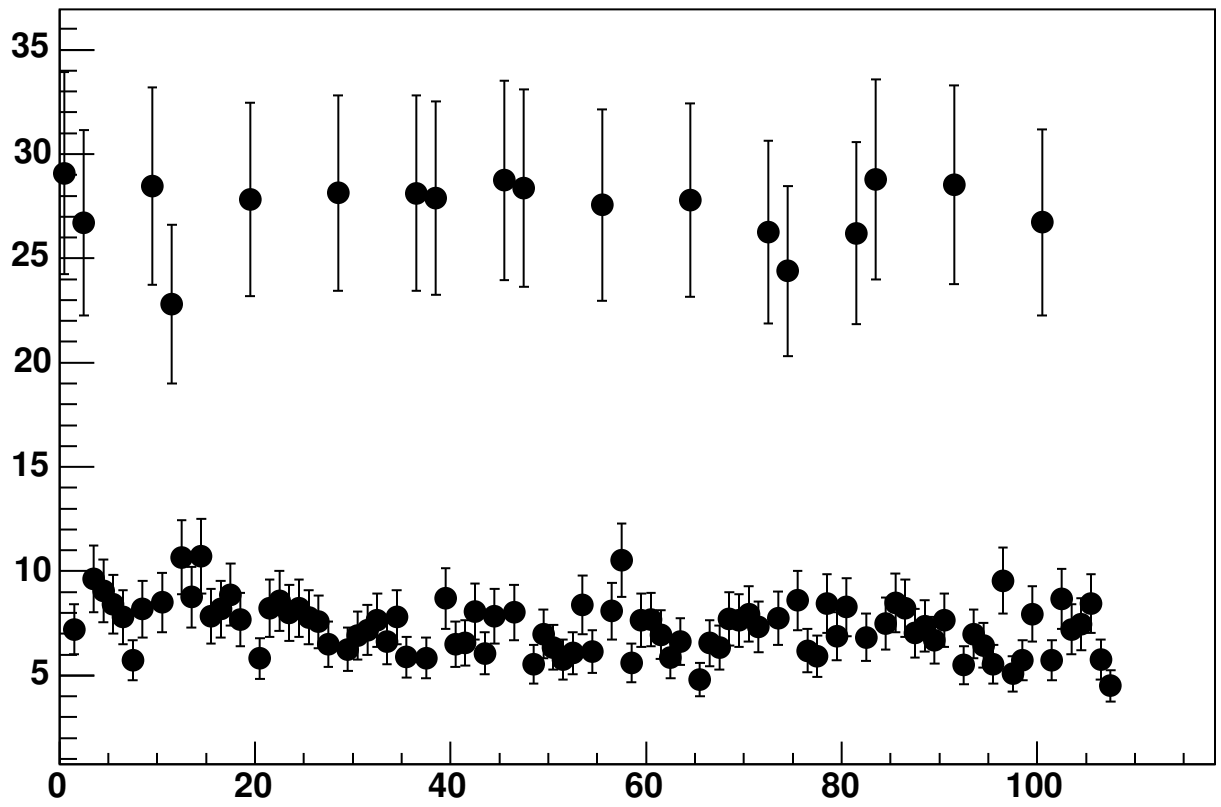
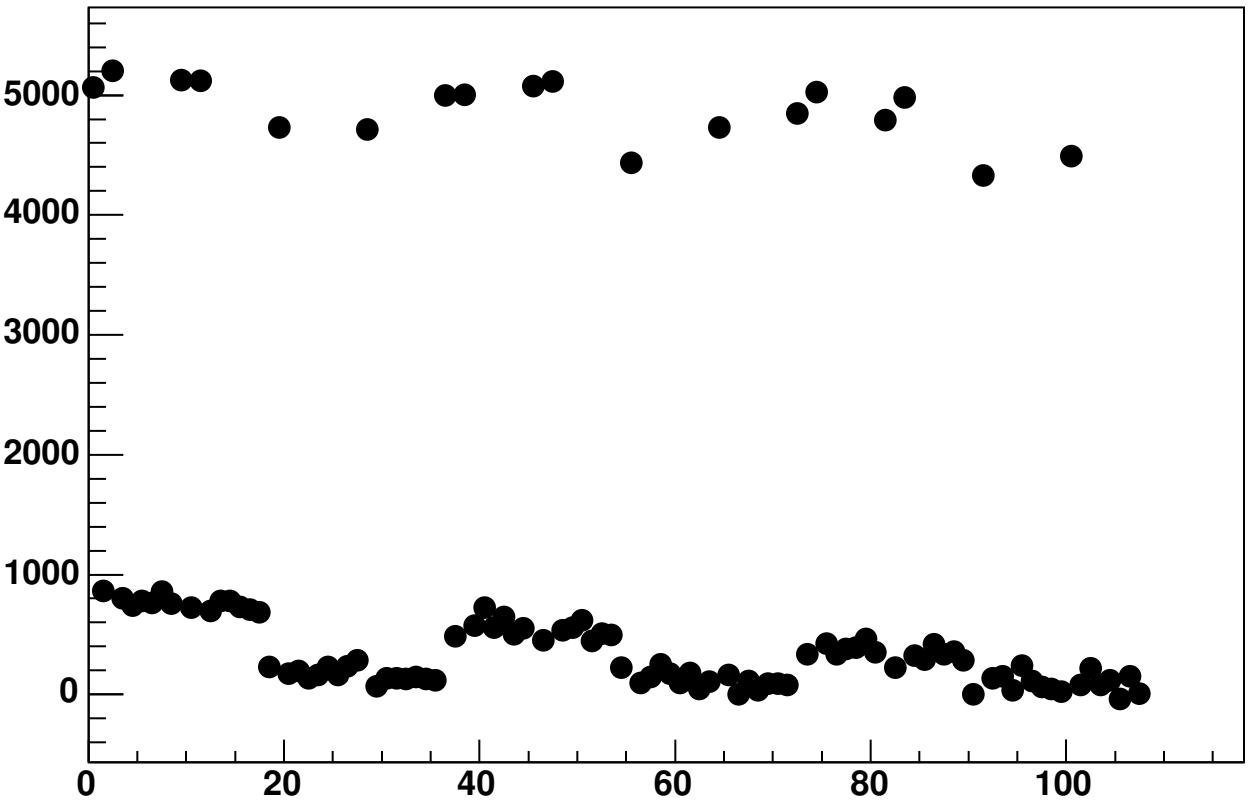
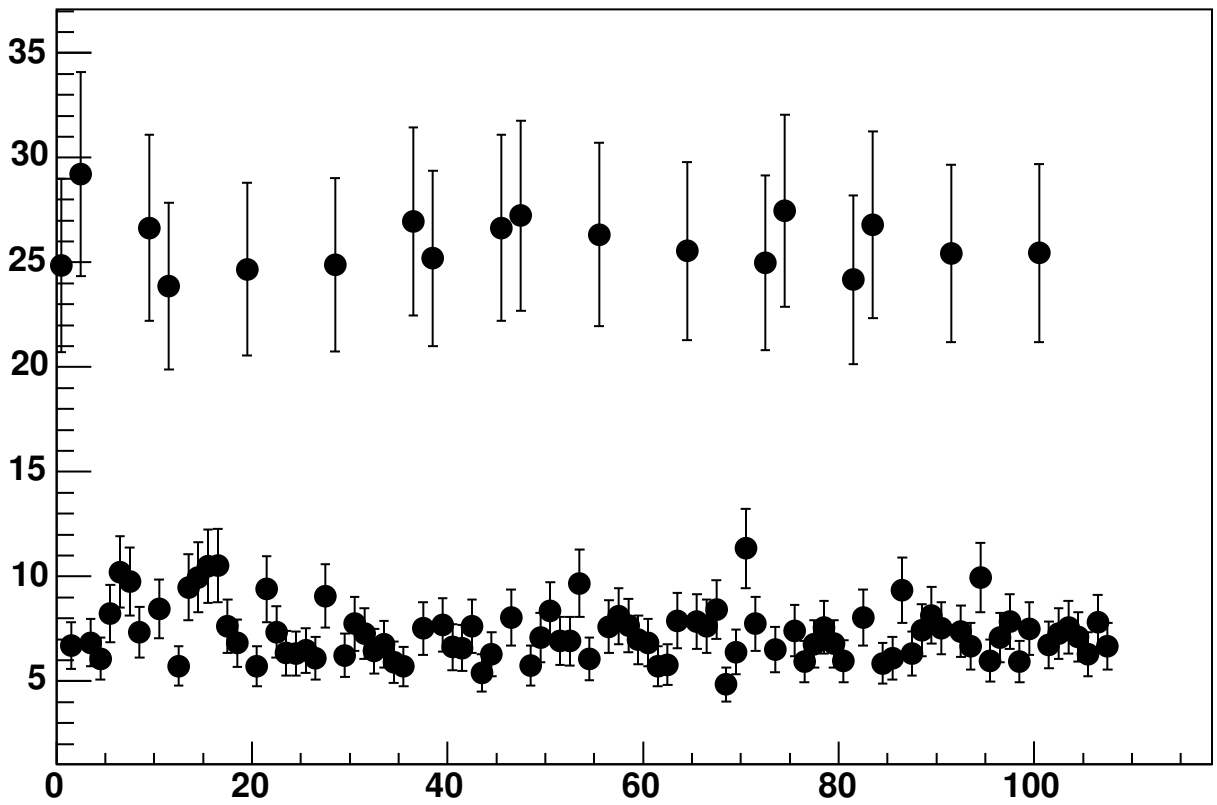


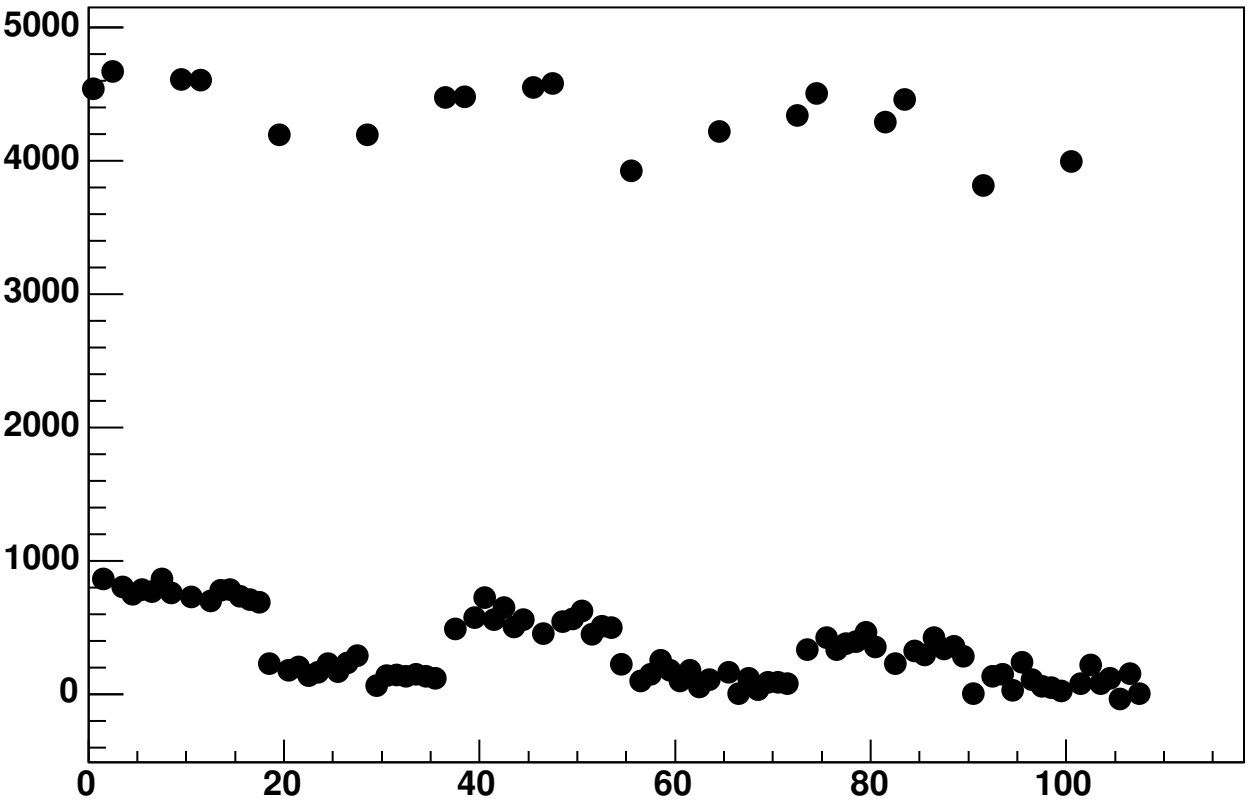
Enable 0, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



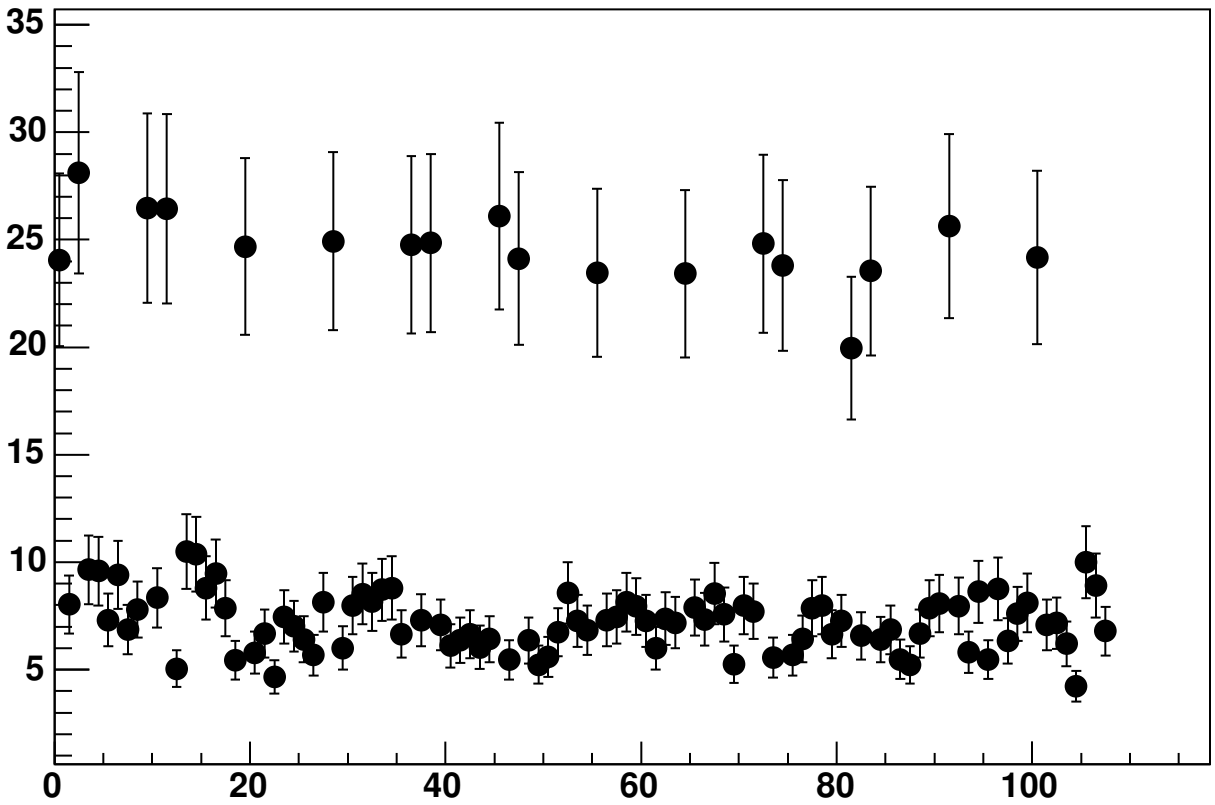
Enable 0, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



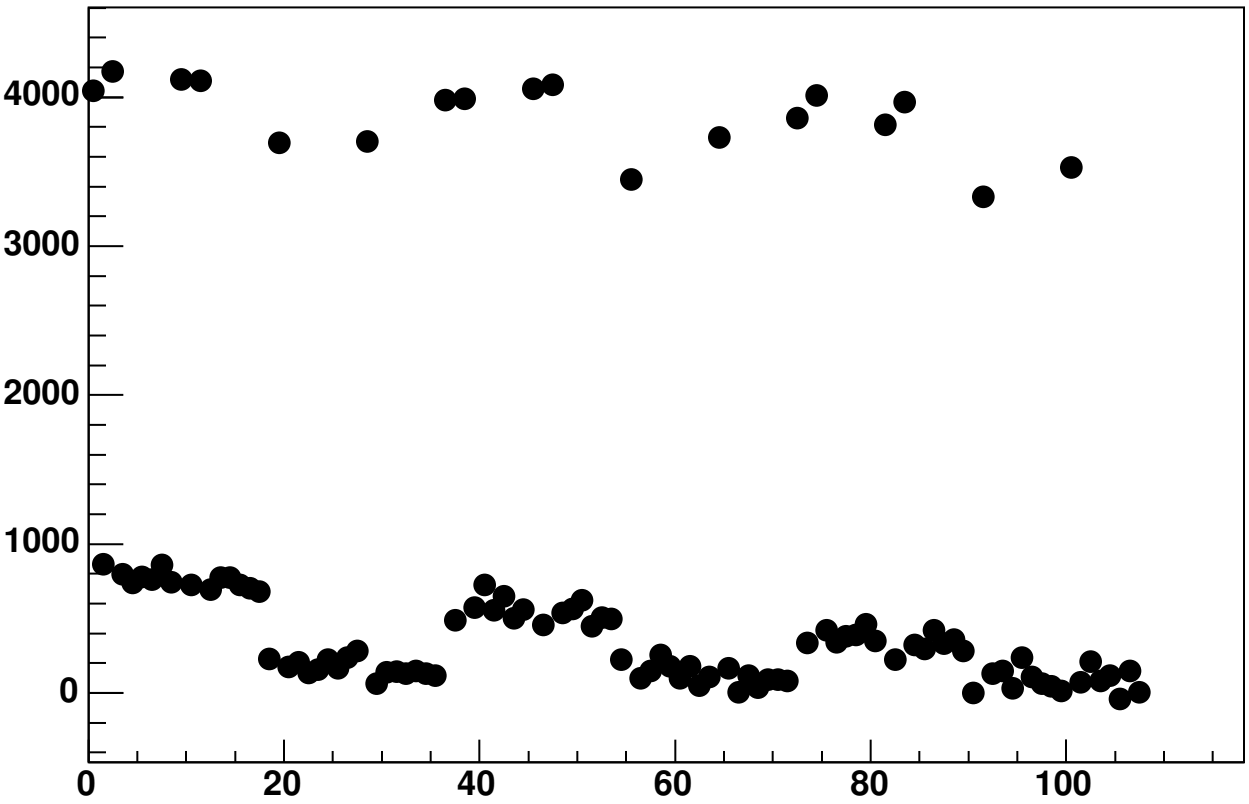
Enable 0, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



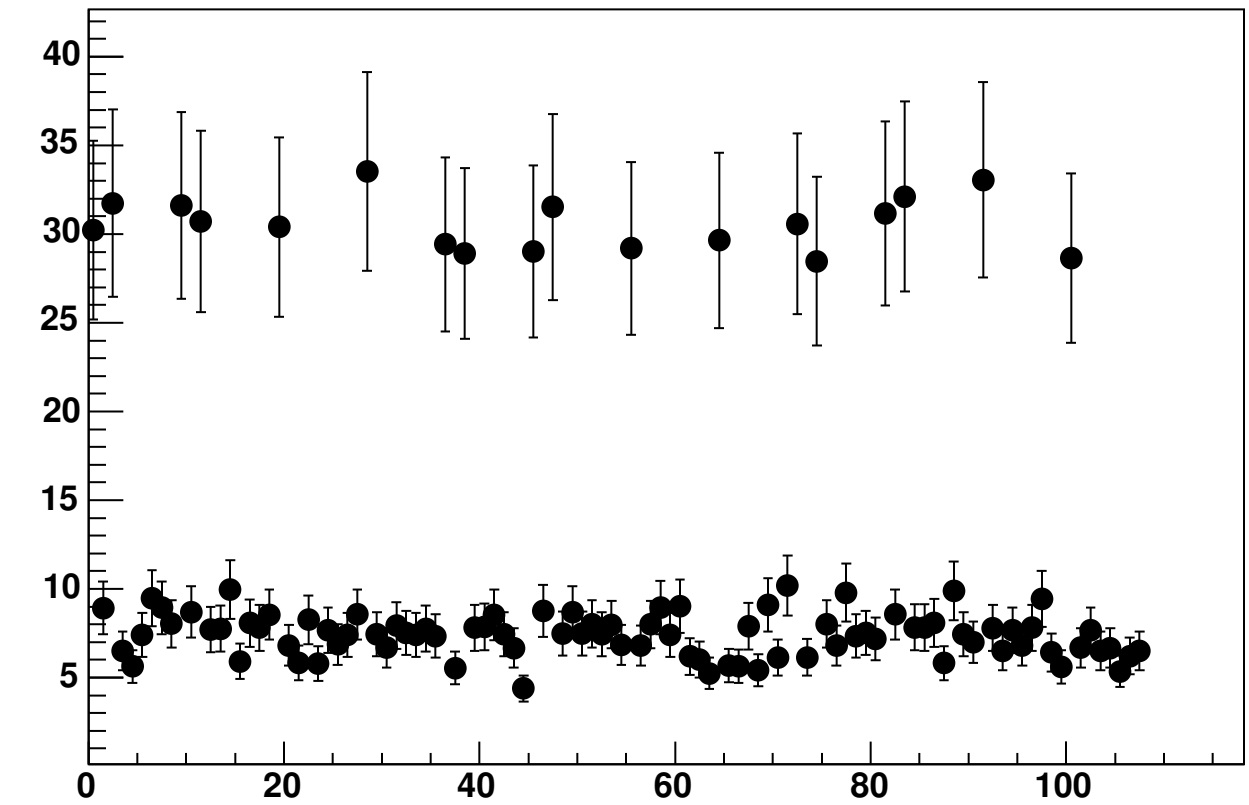
Enable 0, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



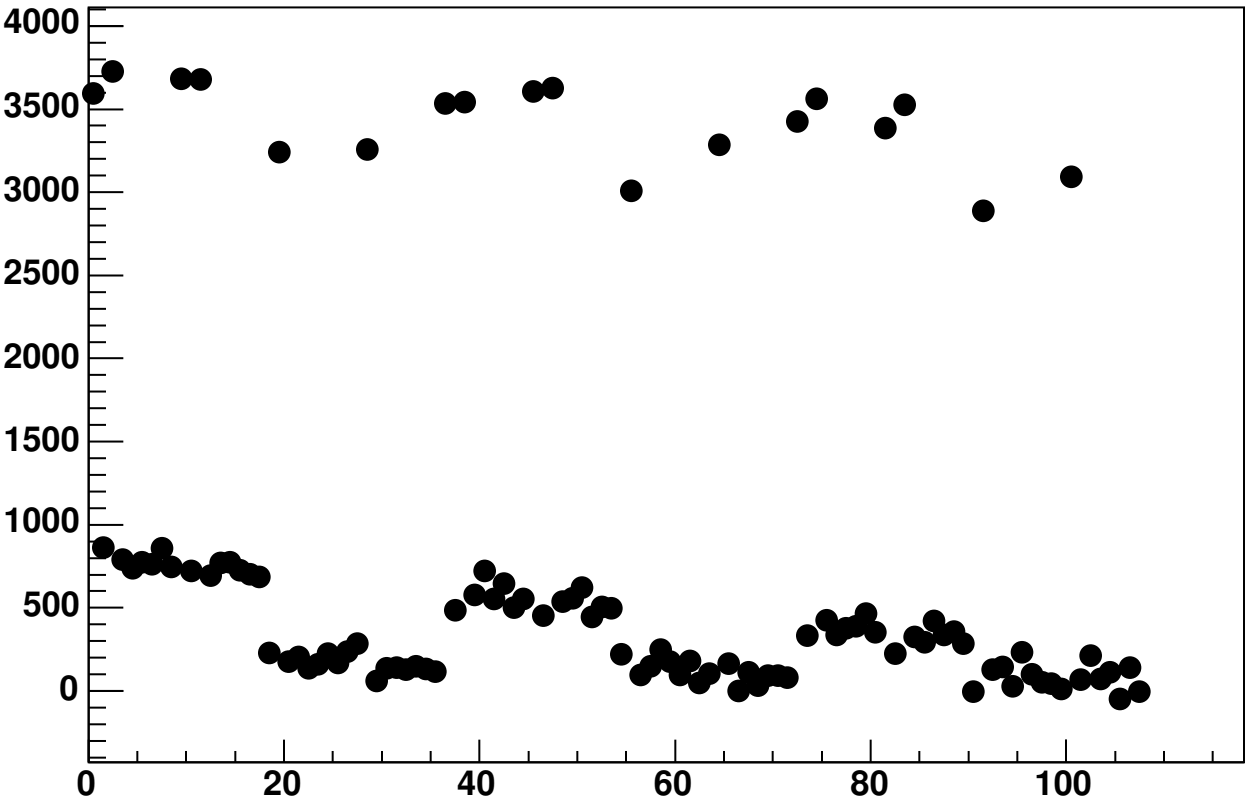
Enable 0, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



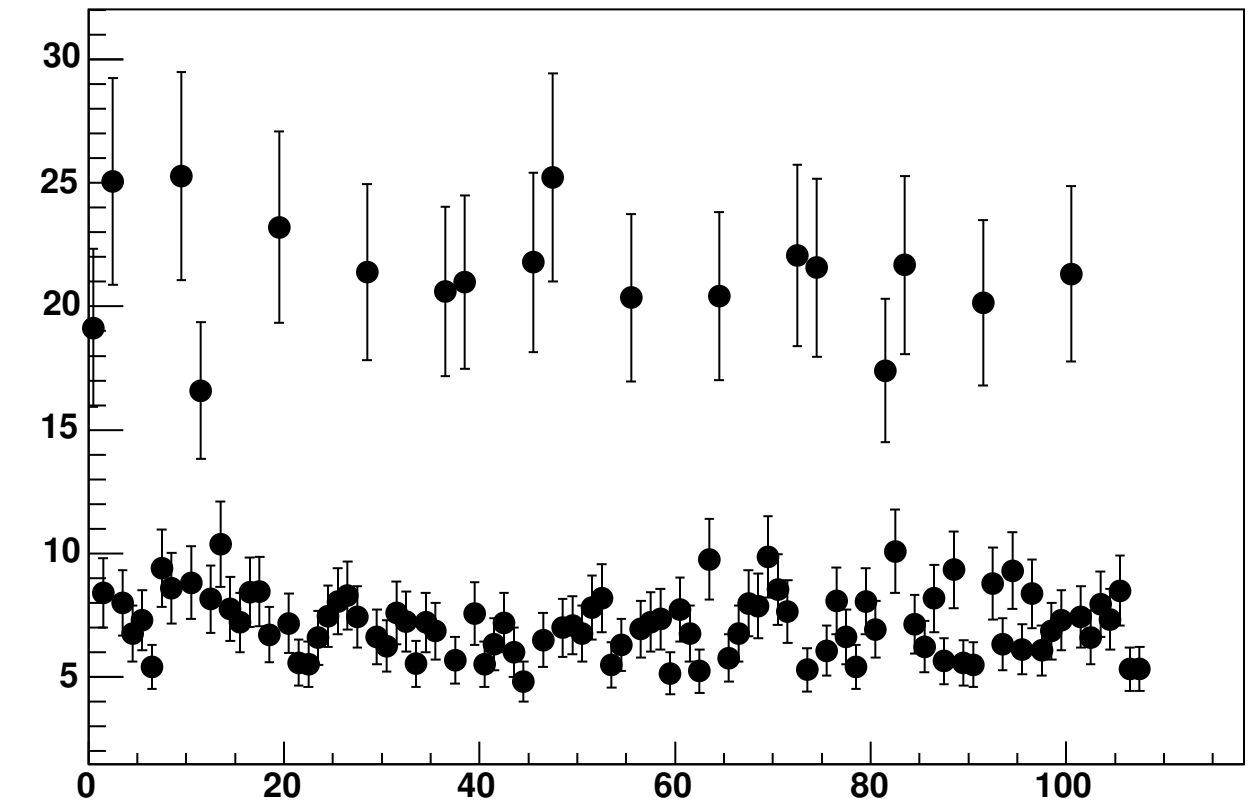
Enable 0, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



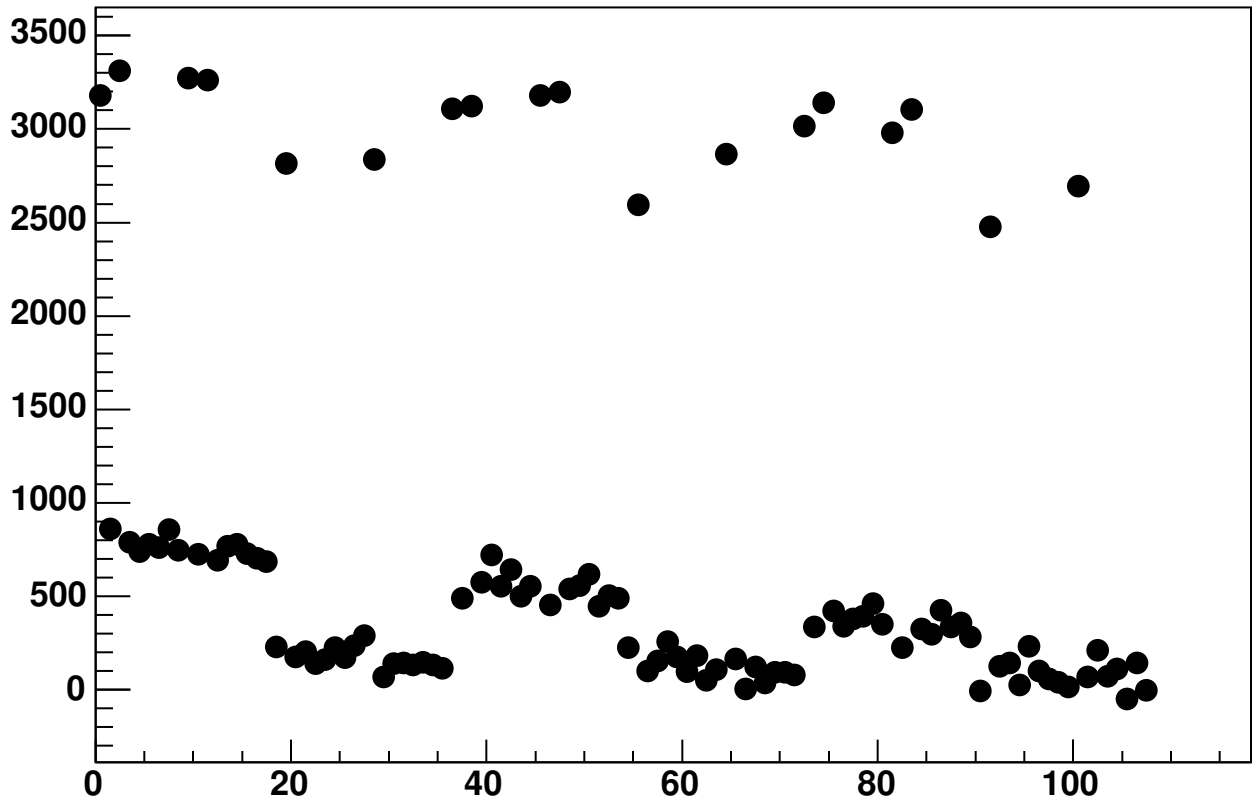
Enable 0, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



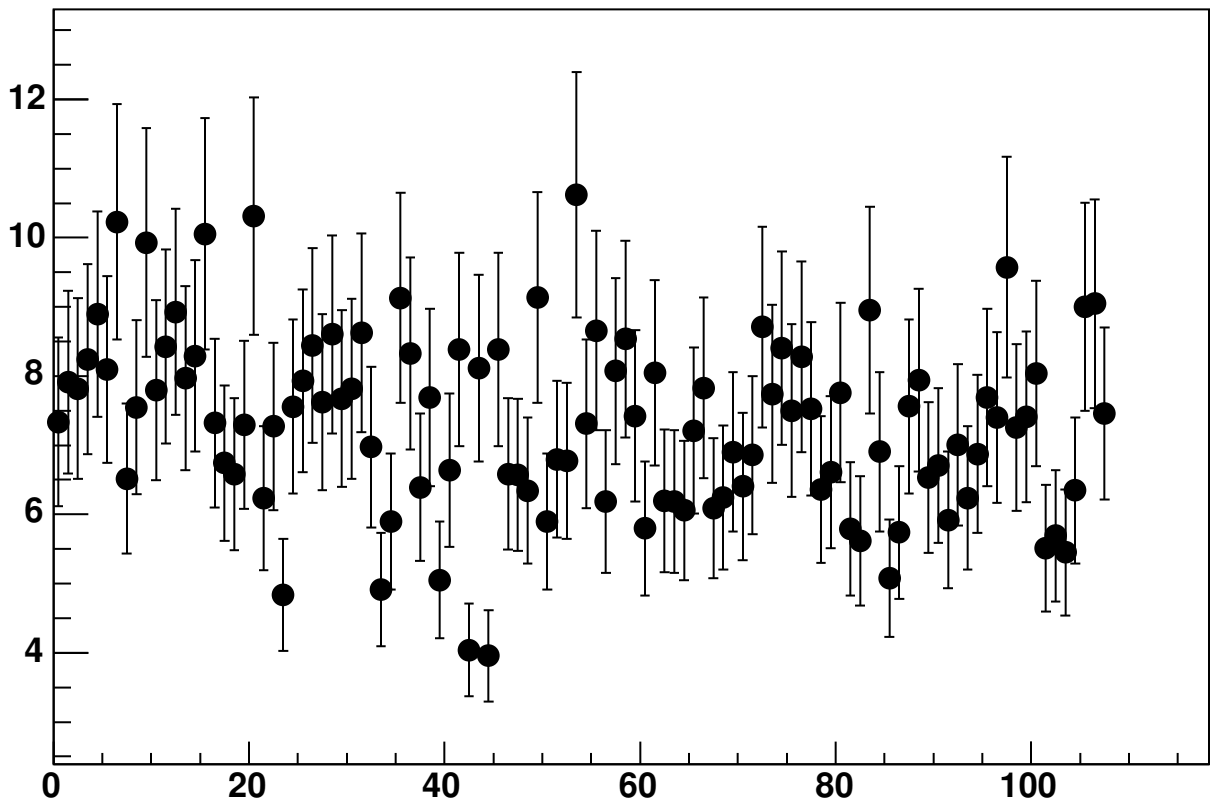
Enable 0, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



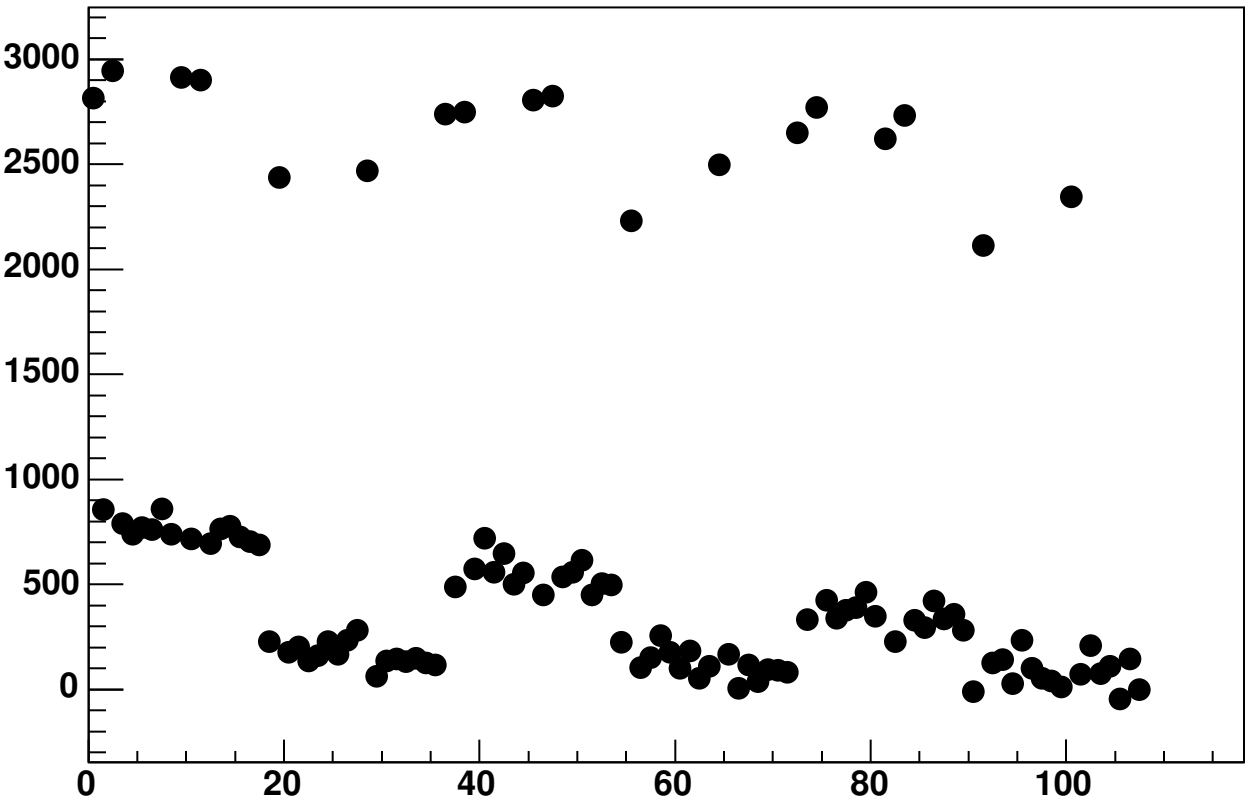
Enable 0, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



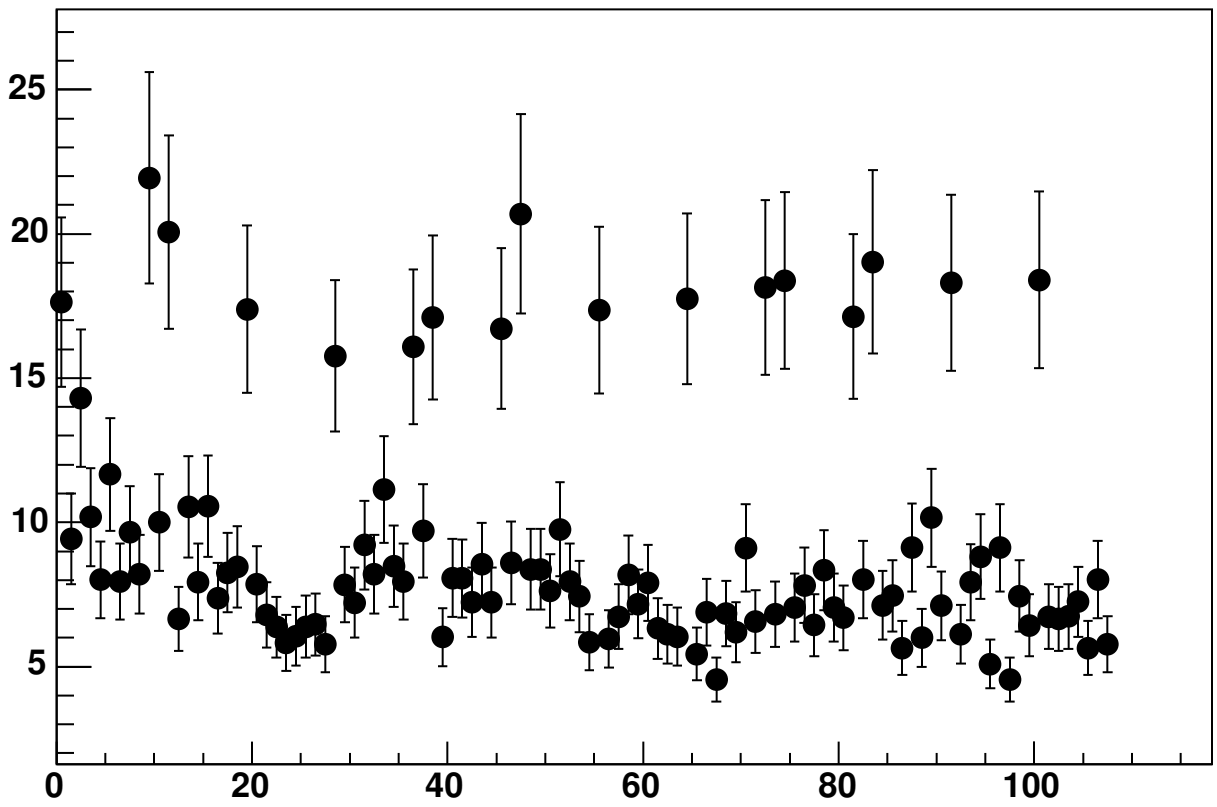
Enable 0, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



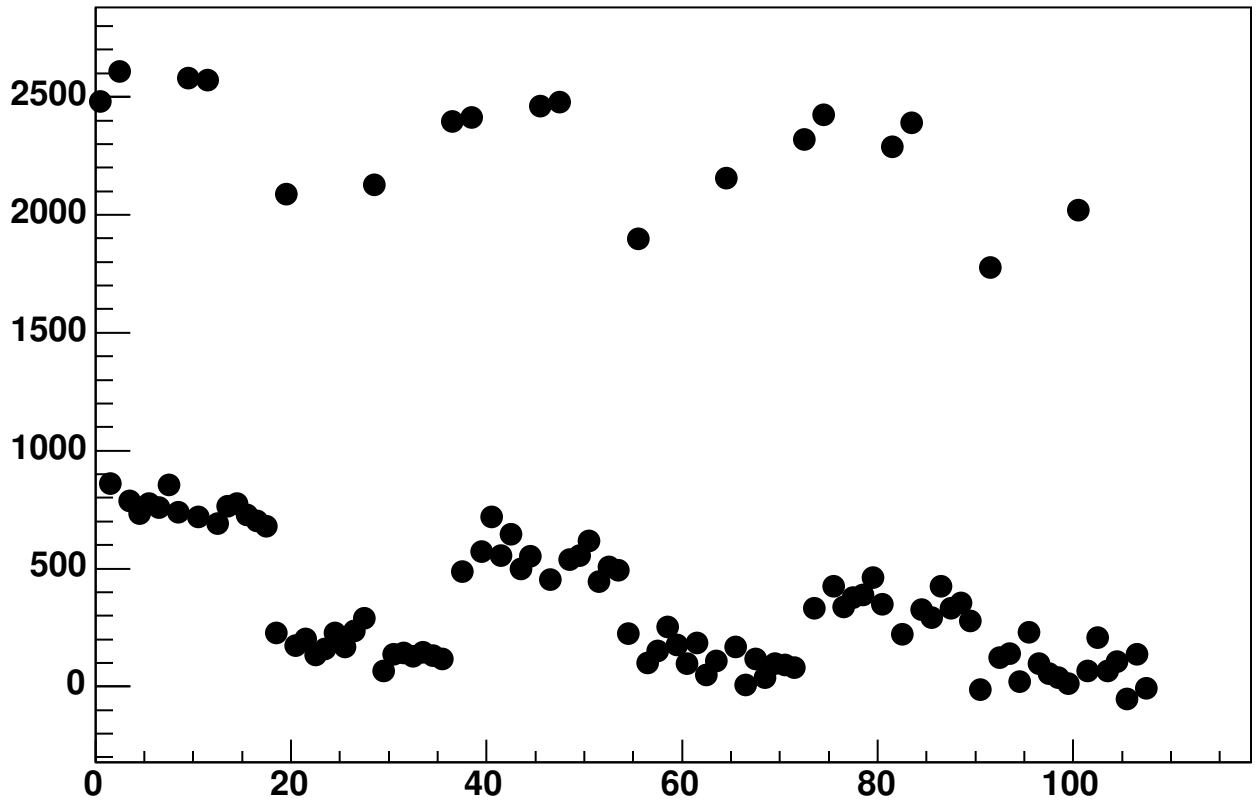
Enable 0, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



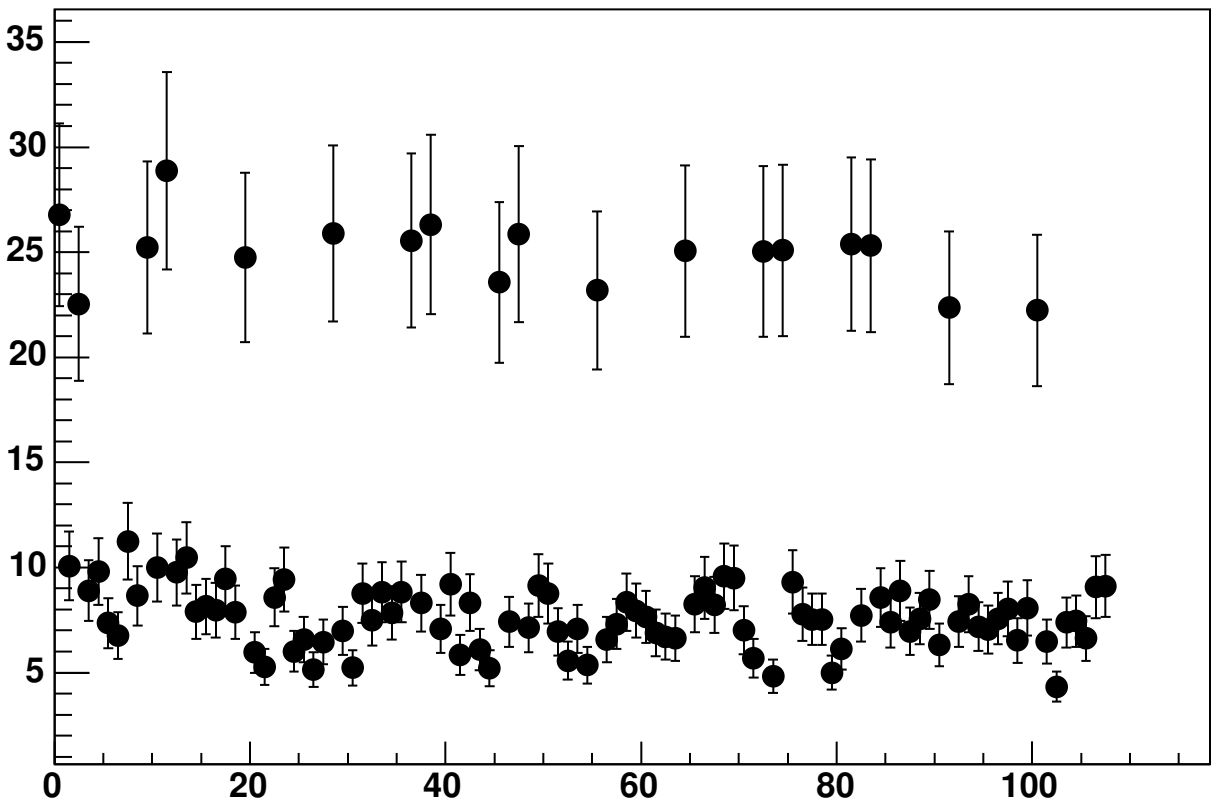
Enable 0, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



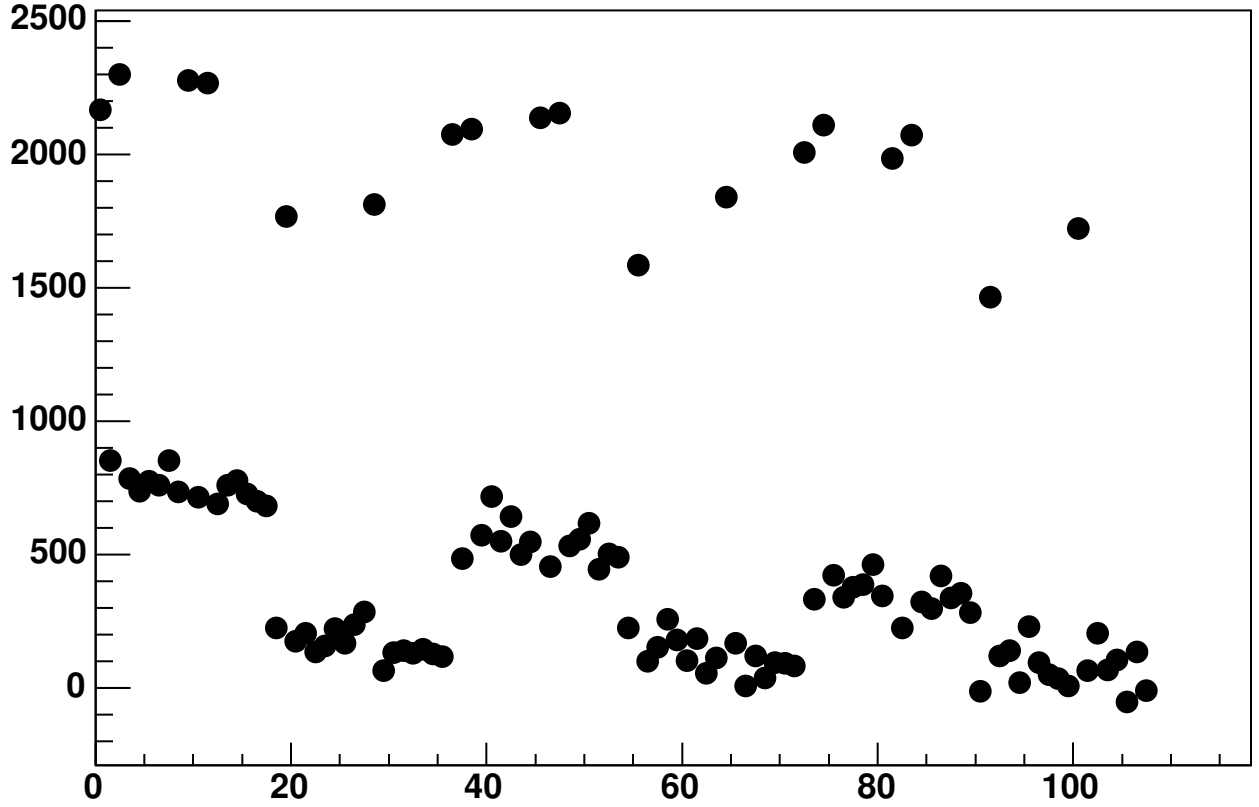
Enable 0, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



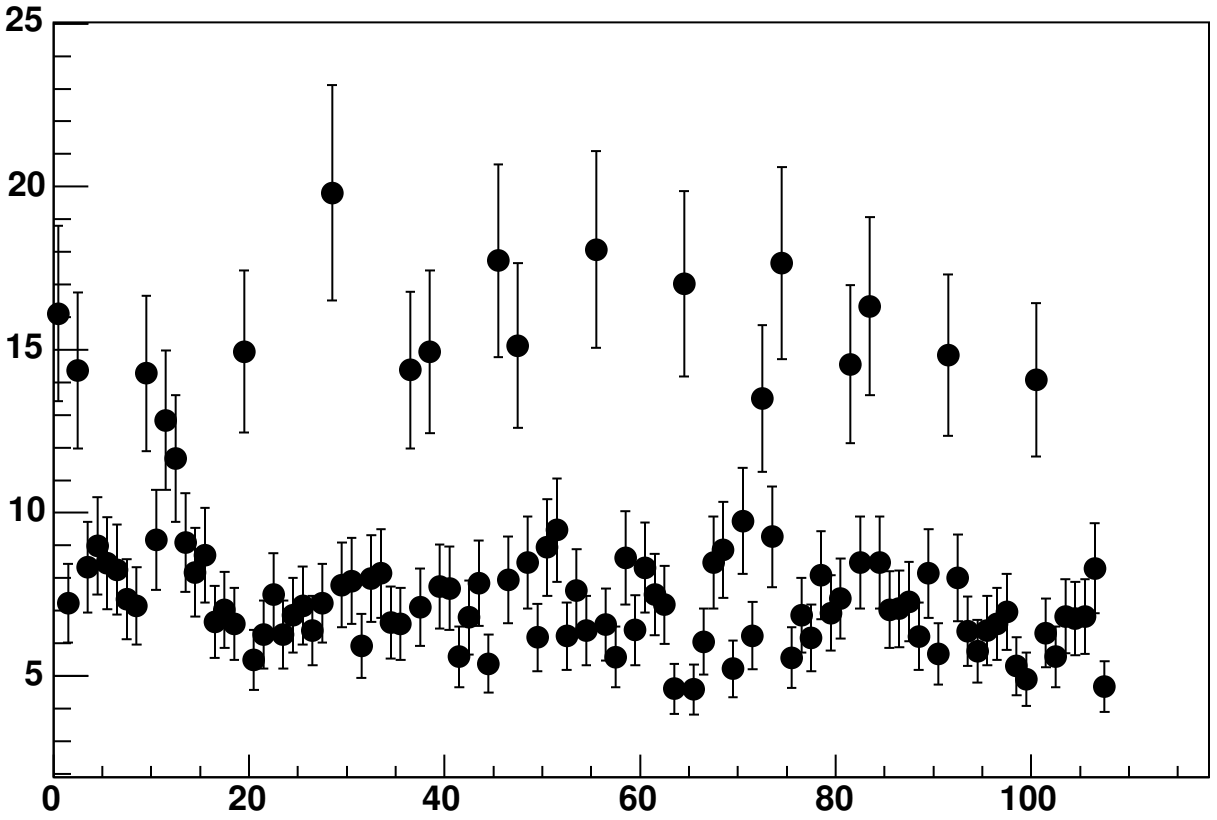
Enable 0, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



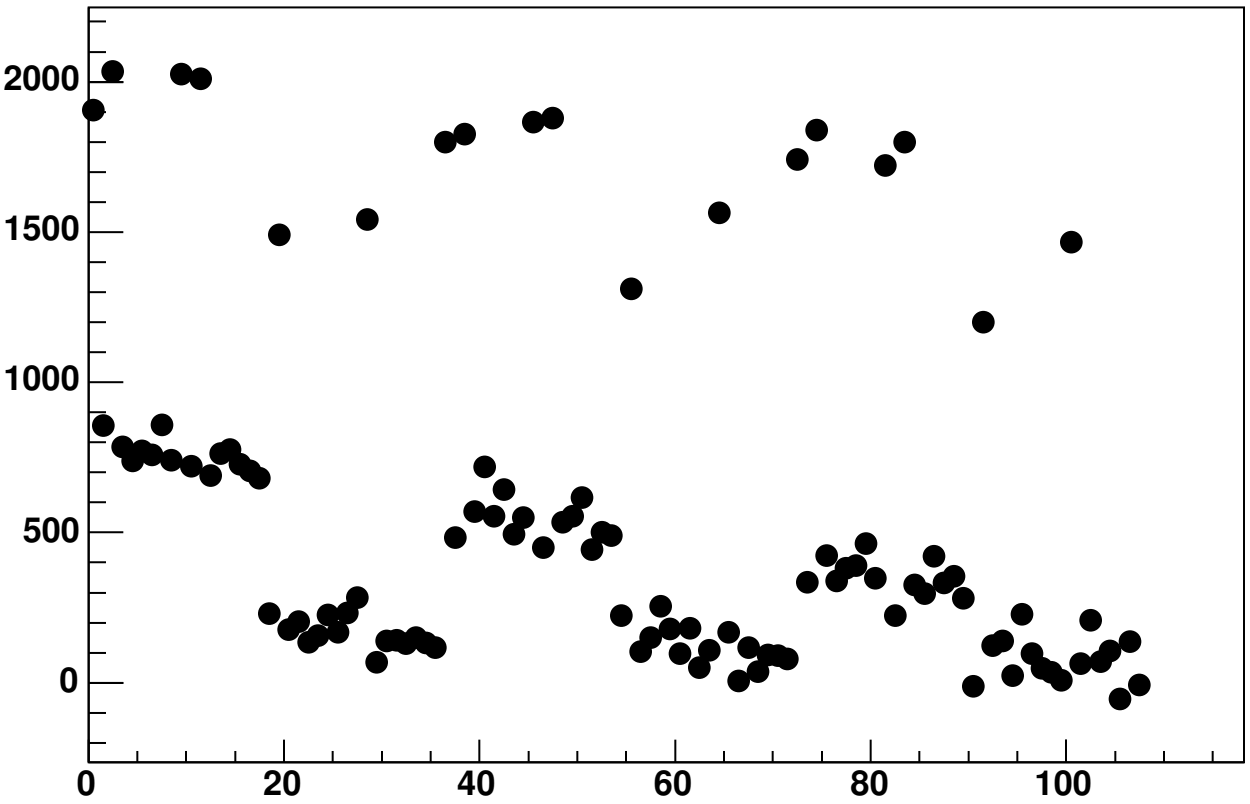
Enable 0, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



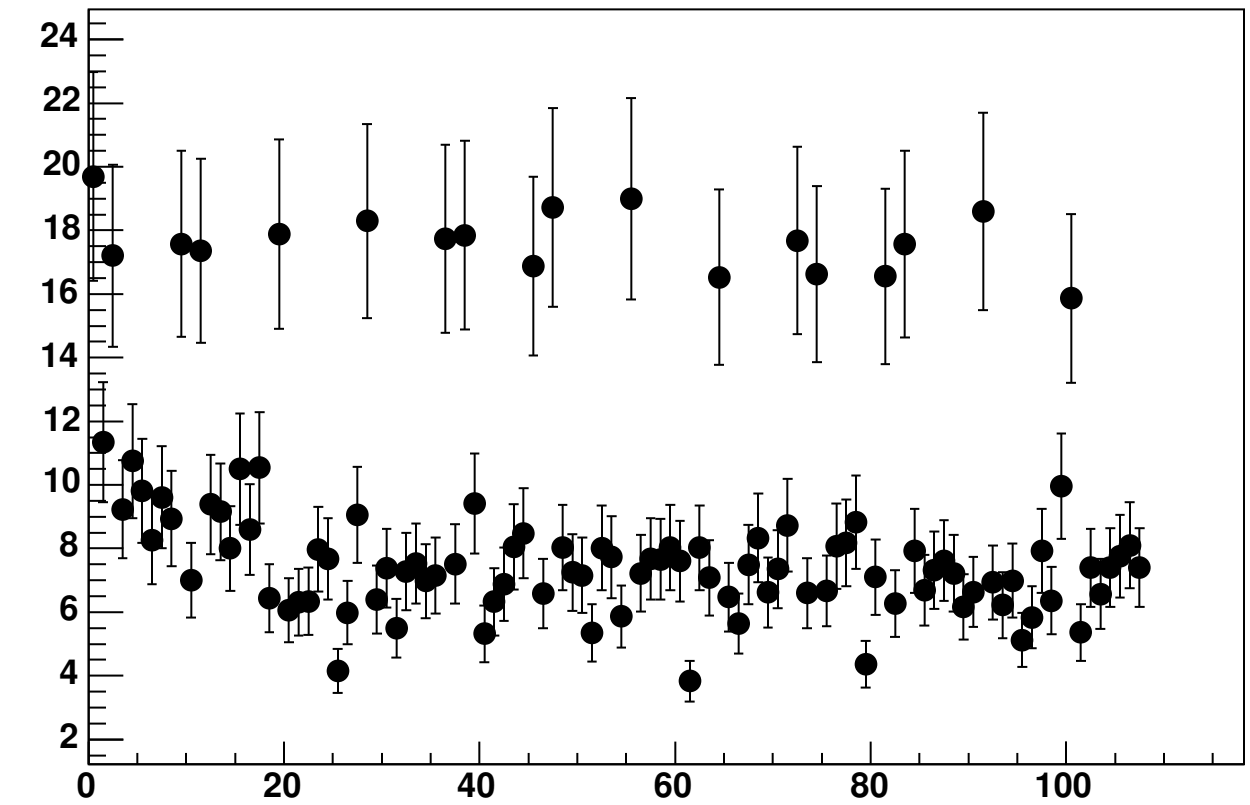
Enable 0, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



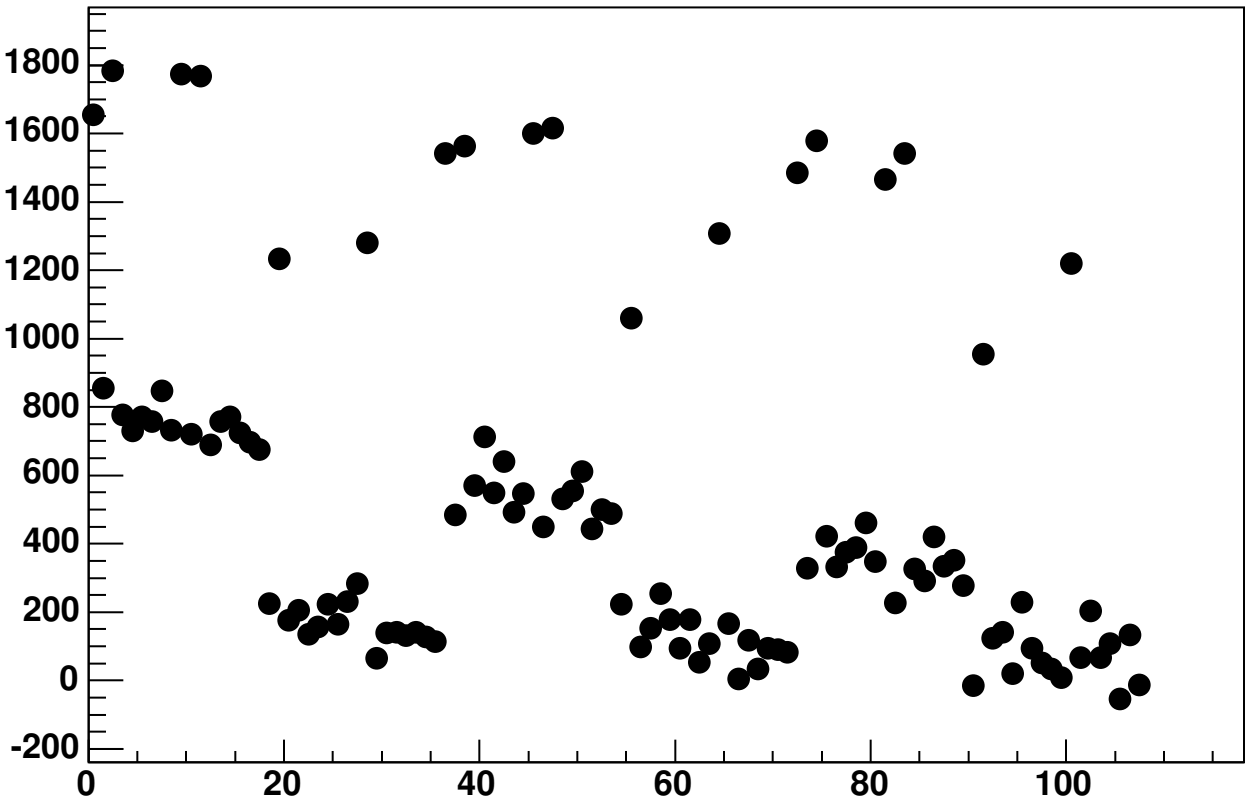
Enable 0, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



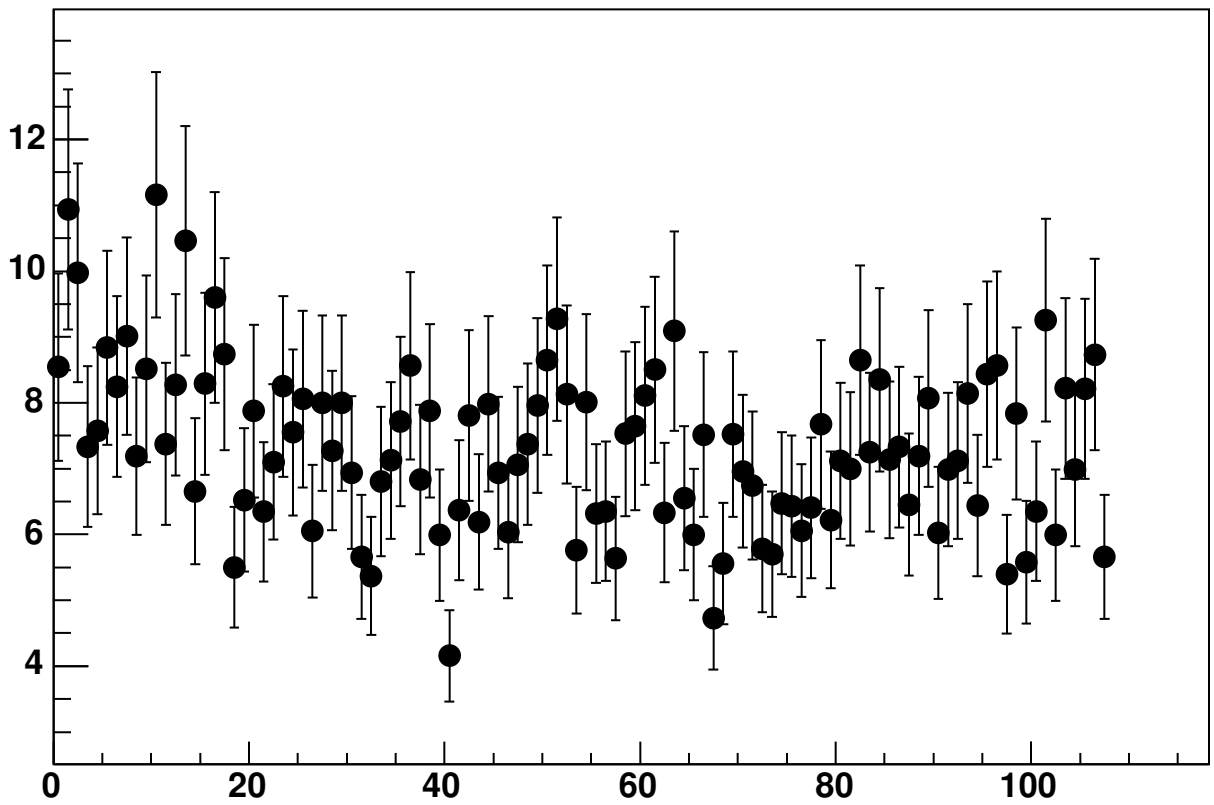
Enable 0, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



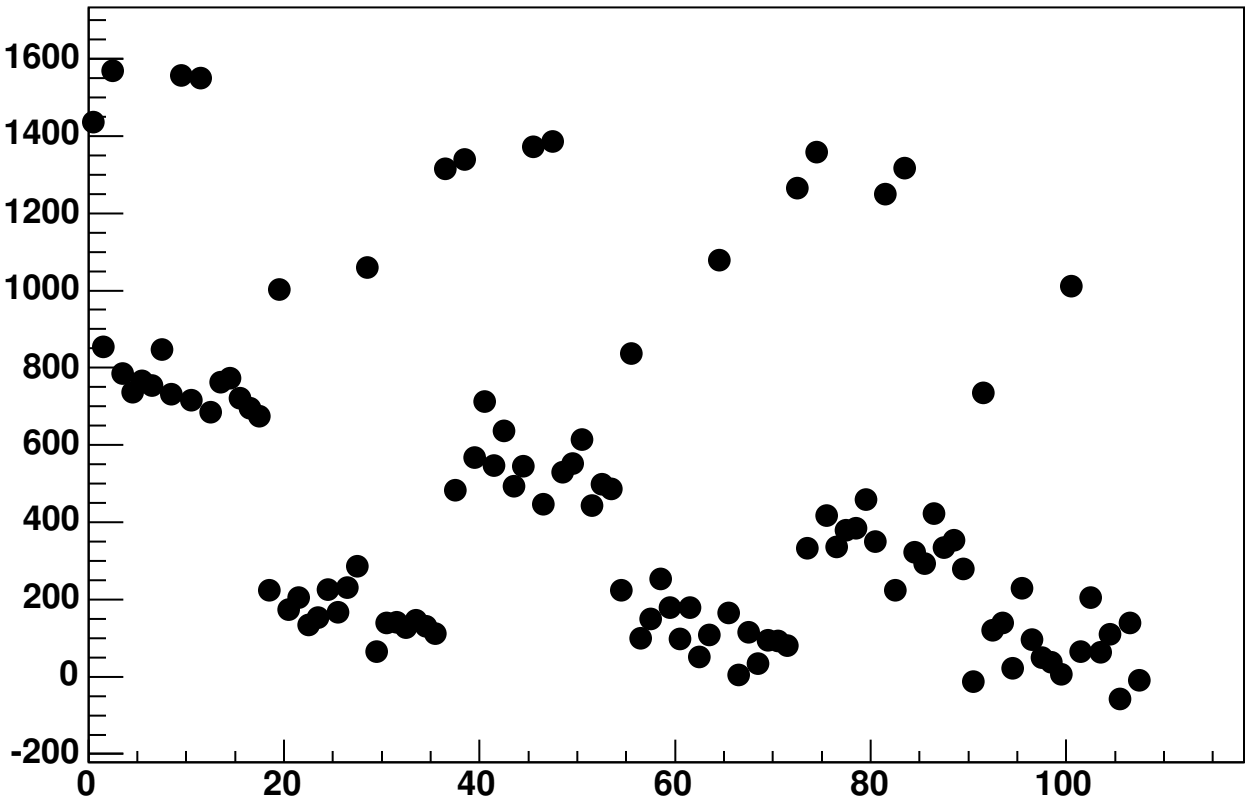
Enable 0, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



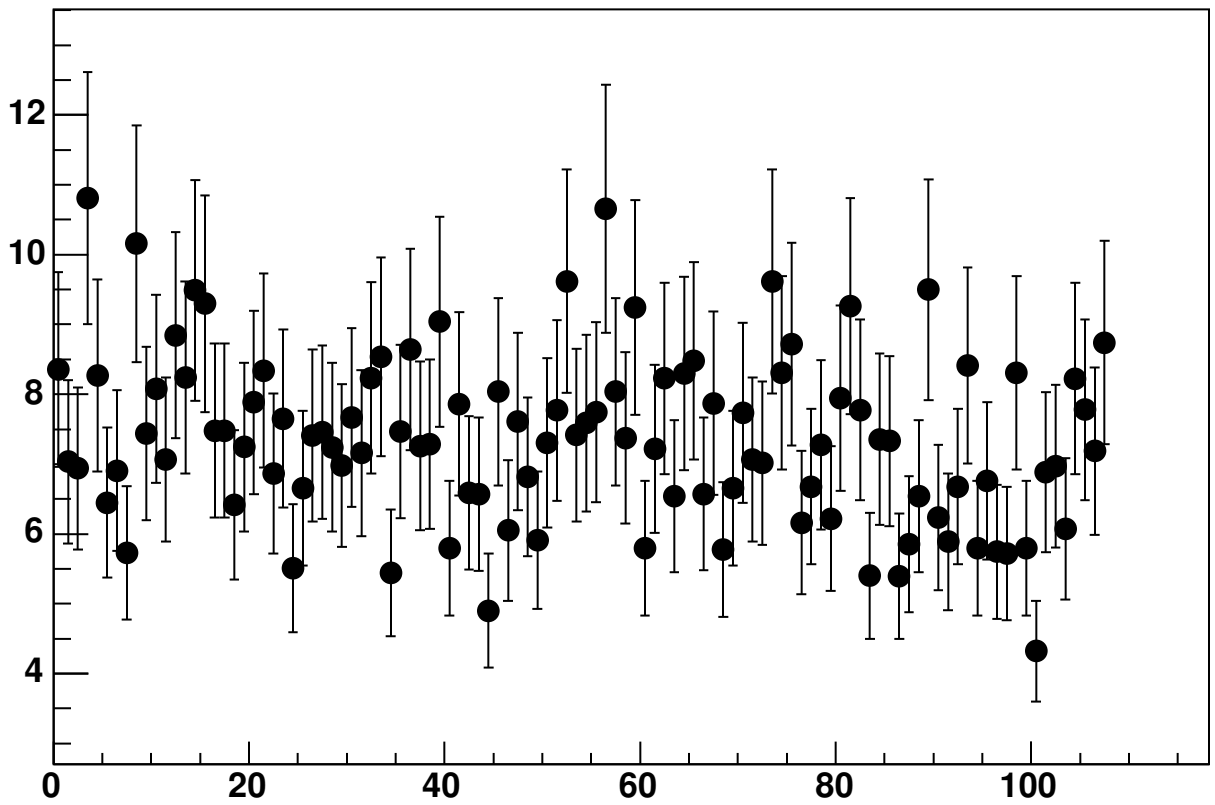
Enable 0, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



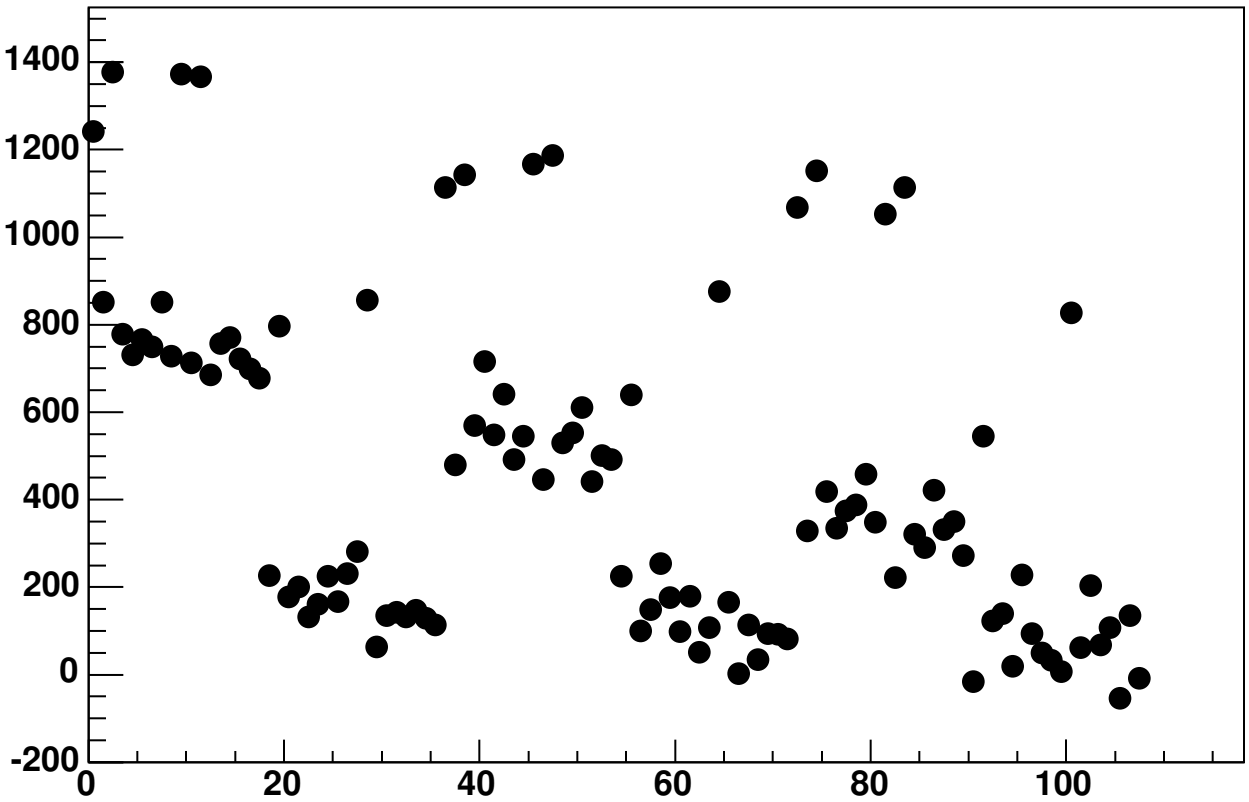
Enable 0, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



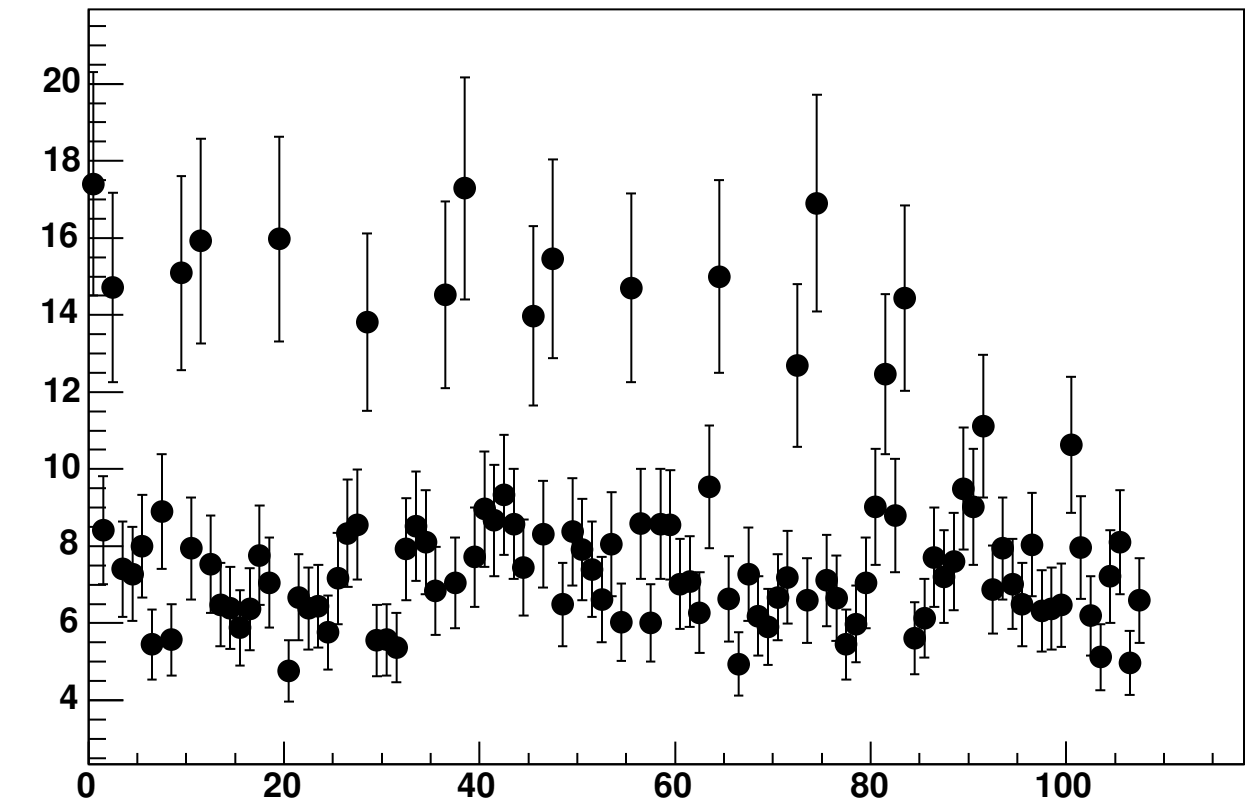
Enable 0, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



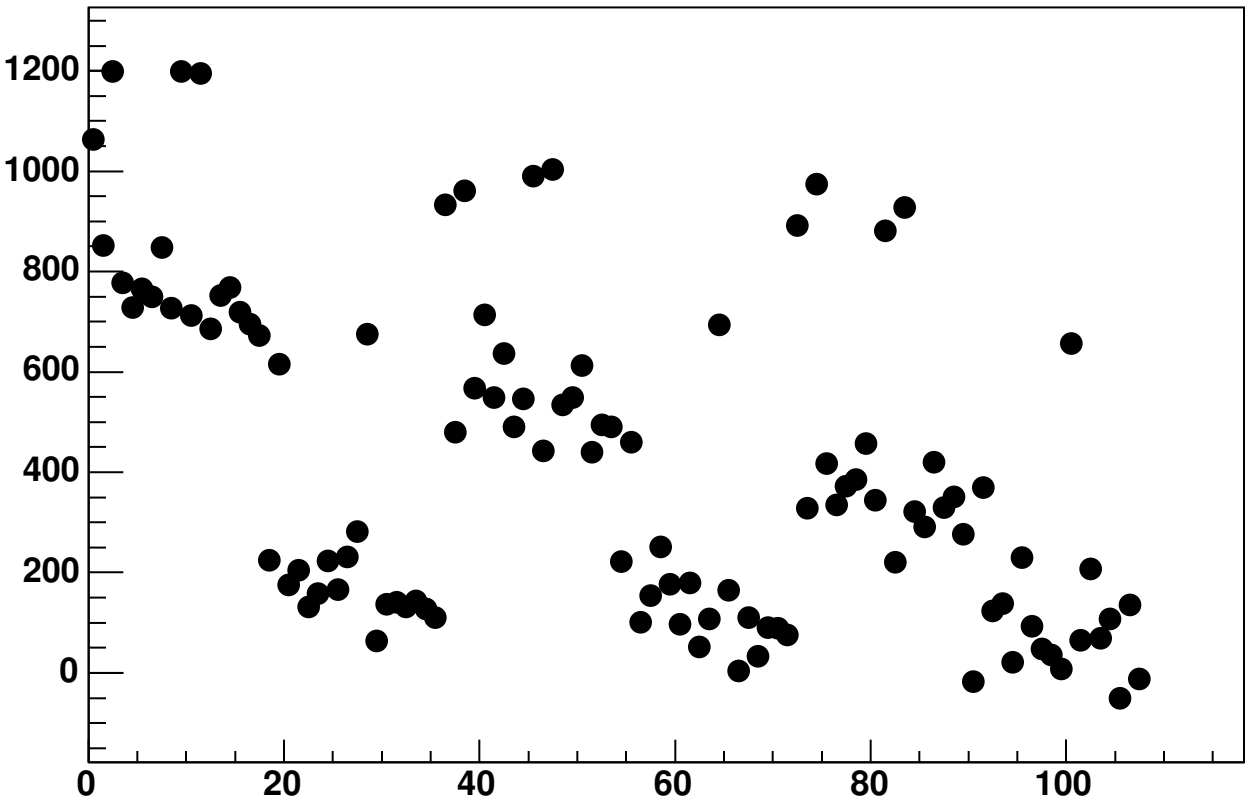
Enable 0, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



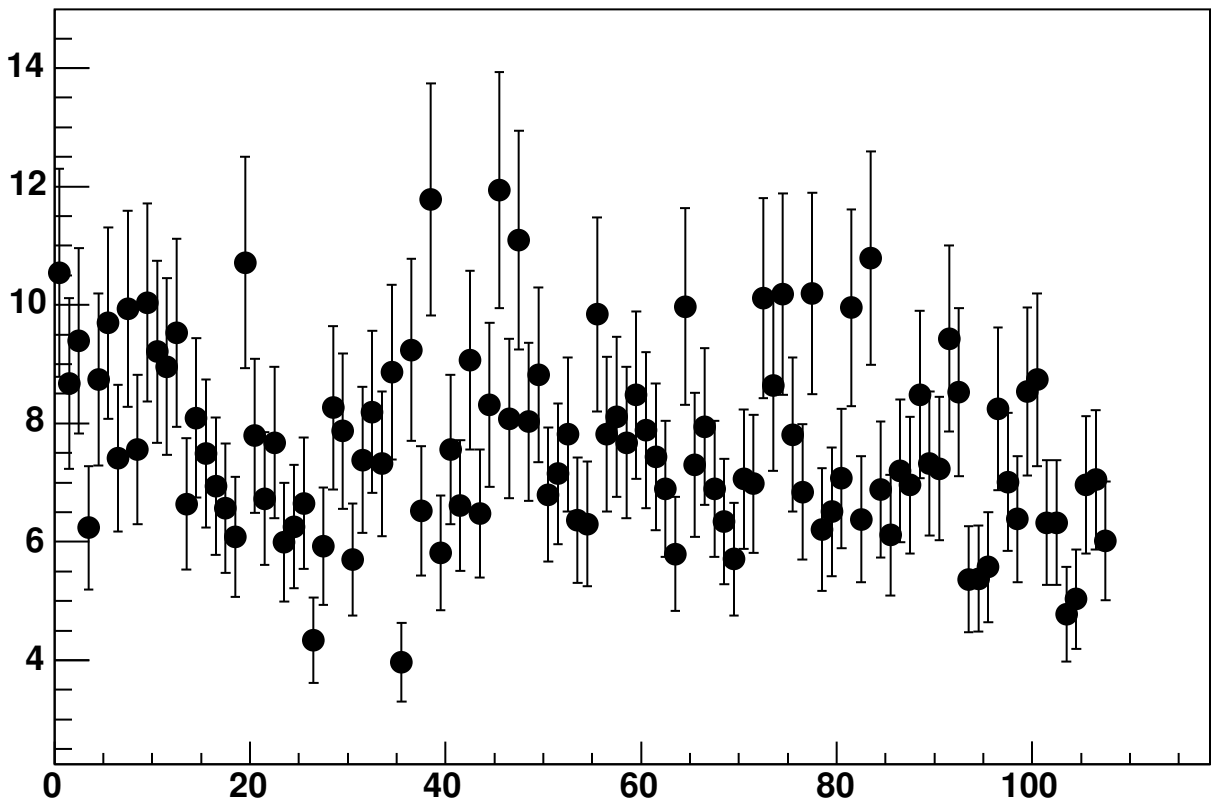
Enable 0, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



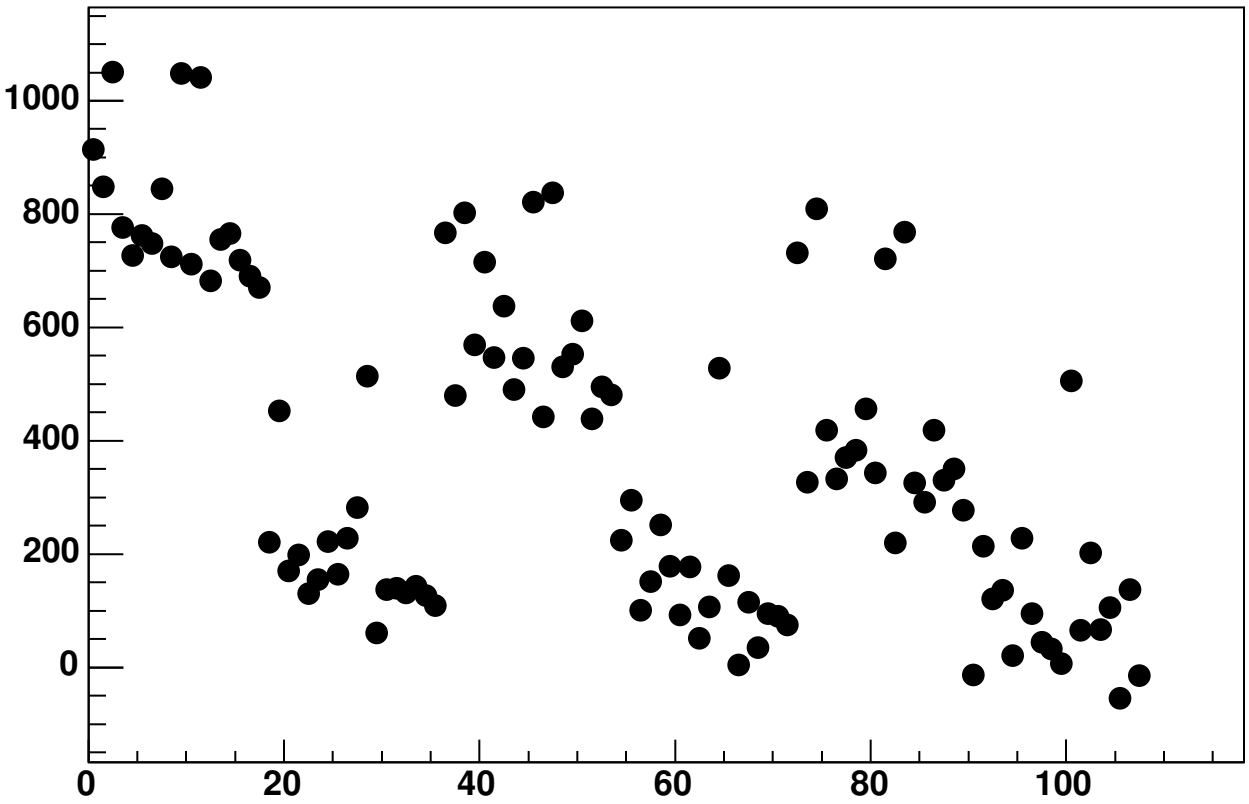
Enable 0, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



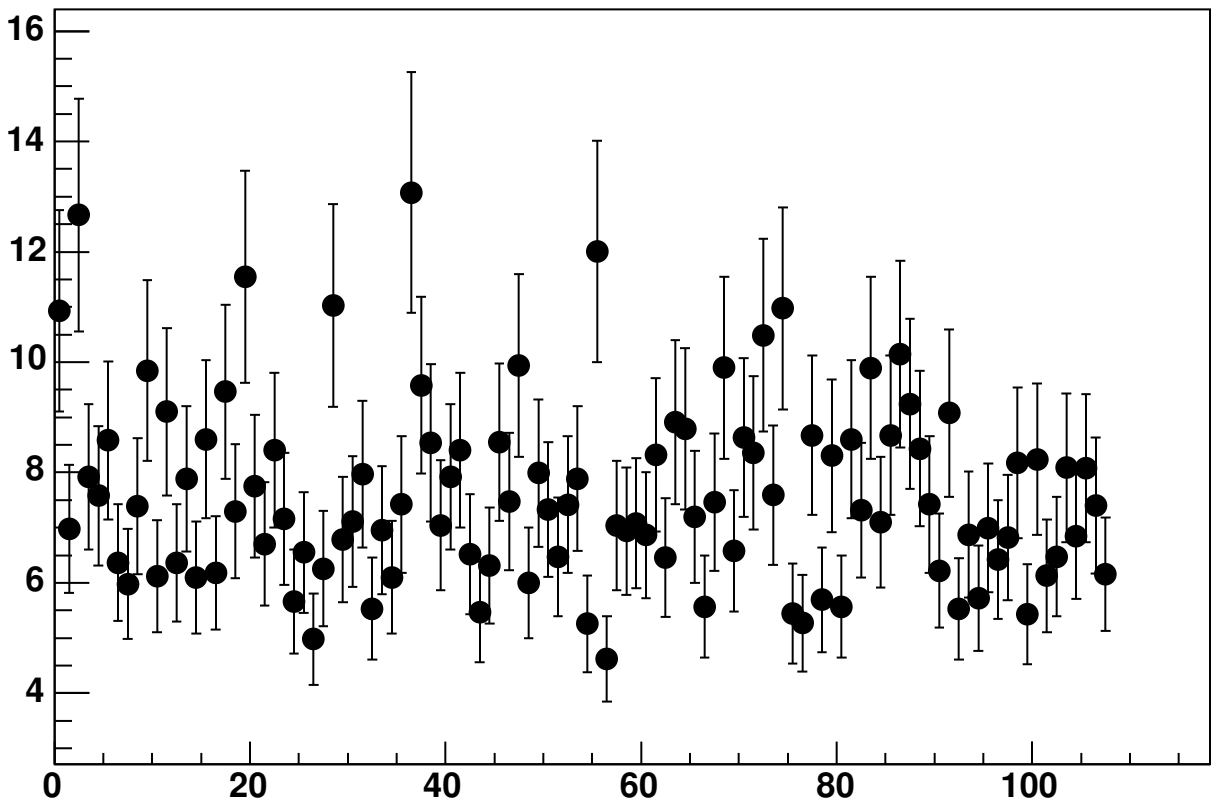
Enable 0, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



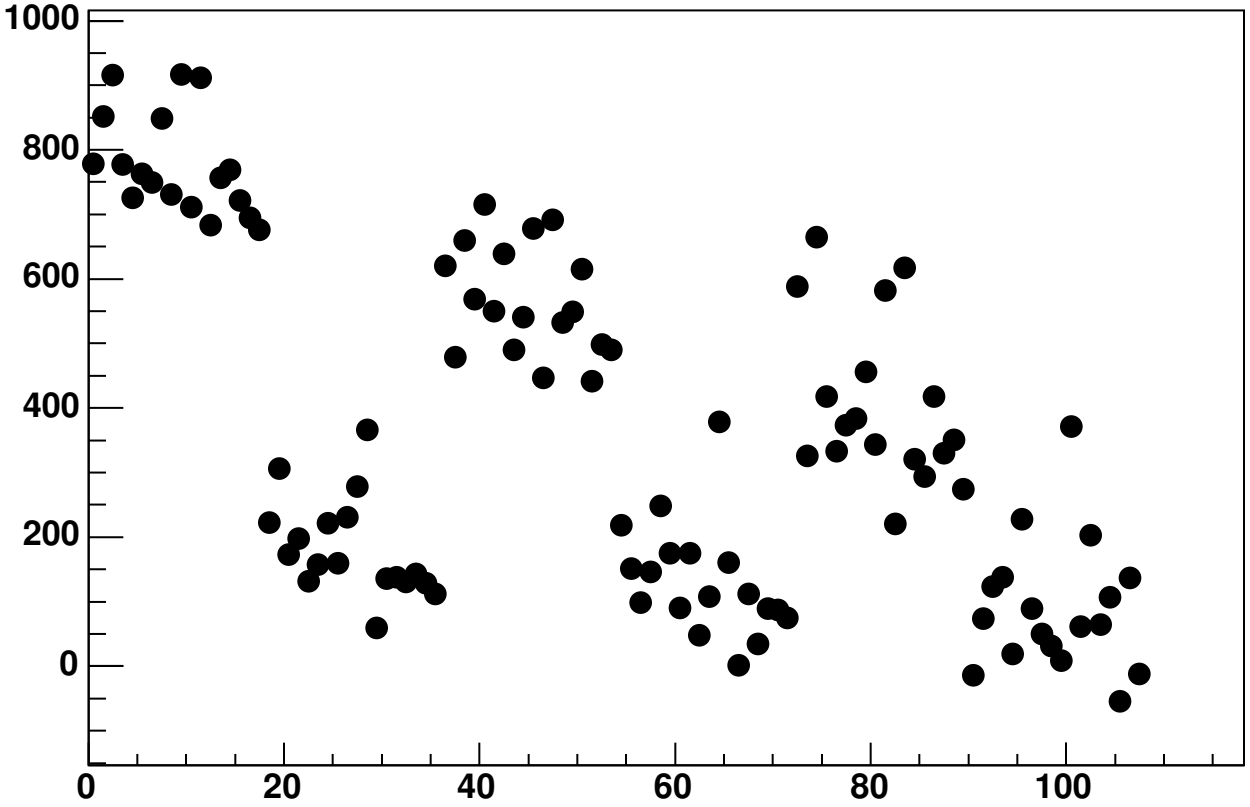
Enable 0, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



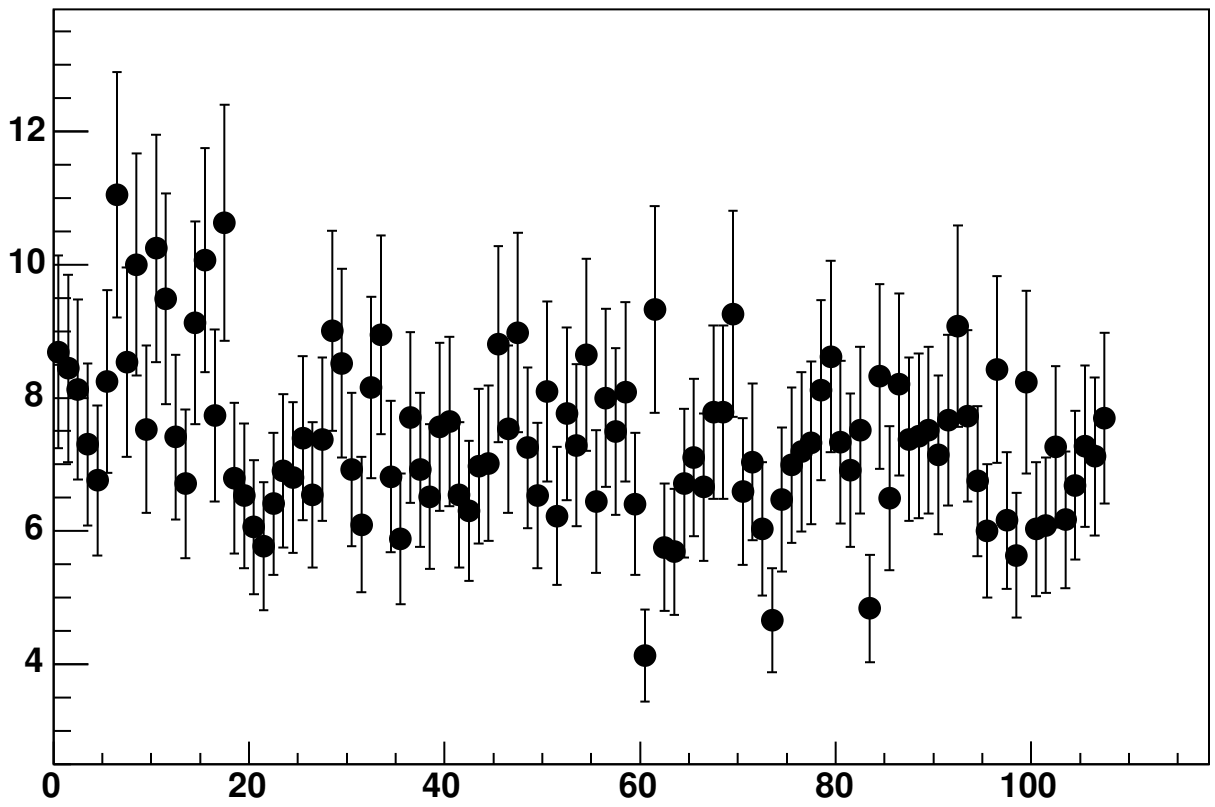
Enable 0, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



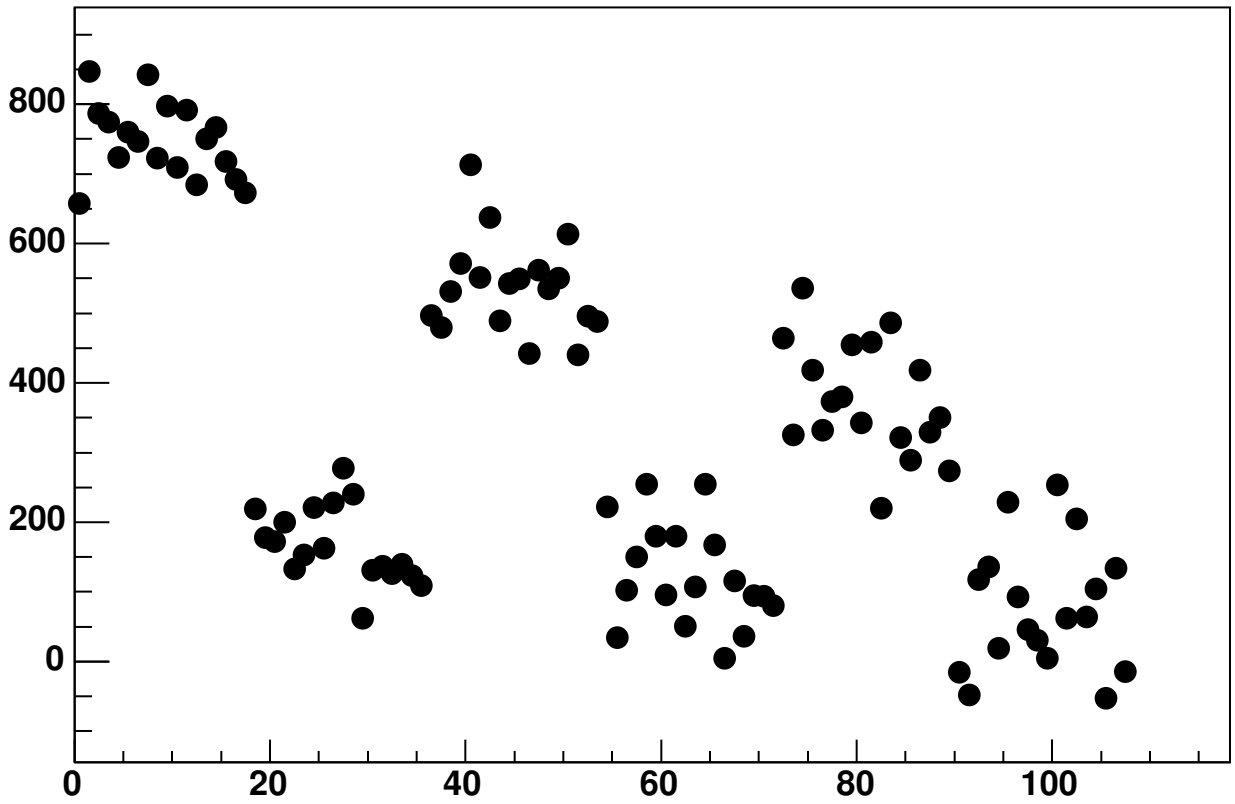
Enable 0, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



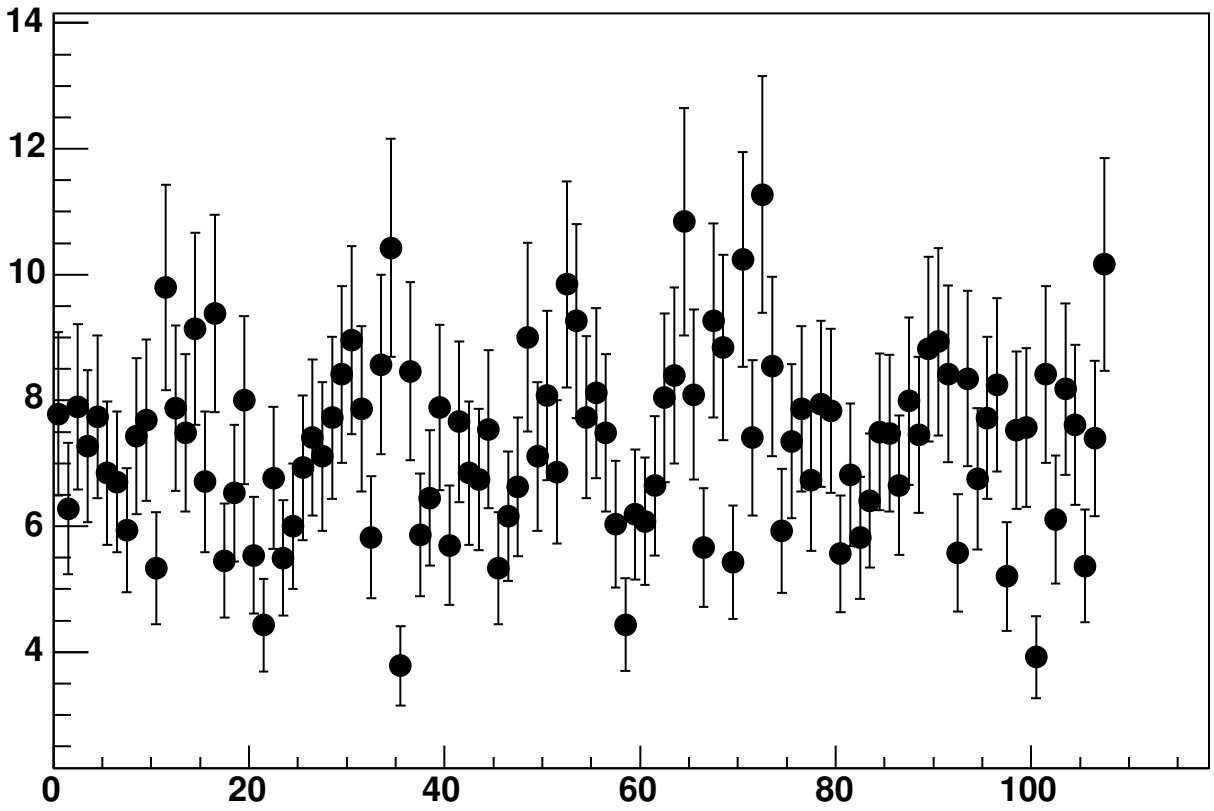
Enable 0, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



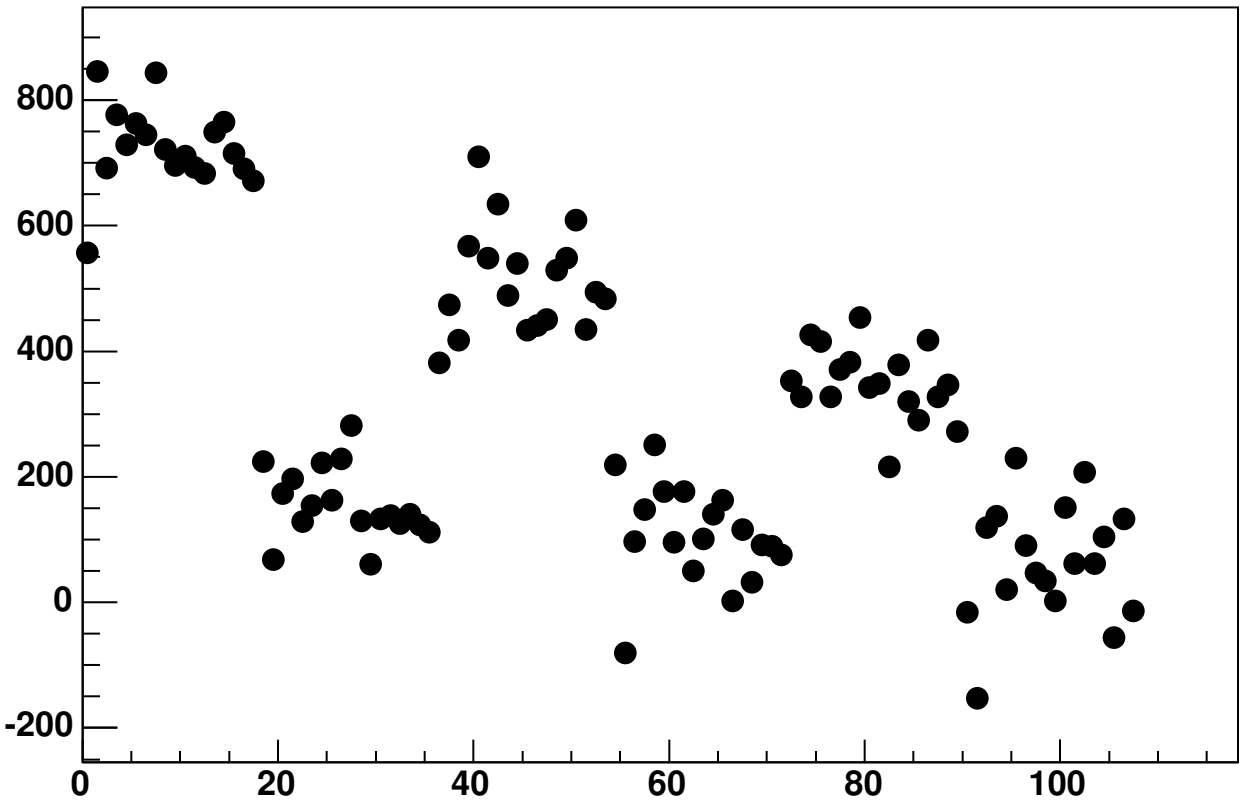
Enable 0, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



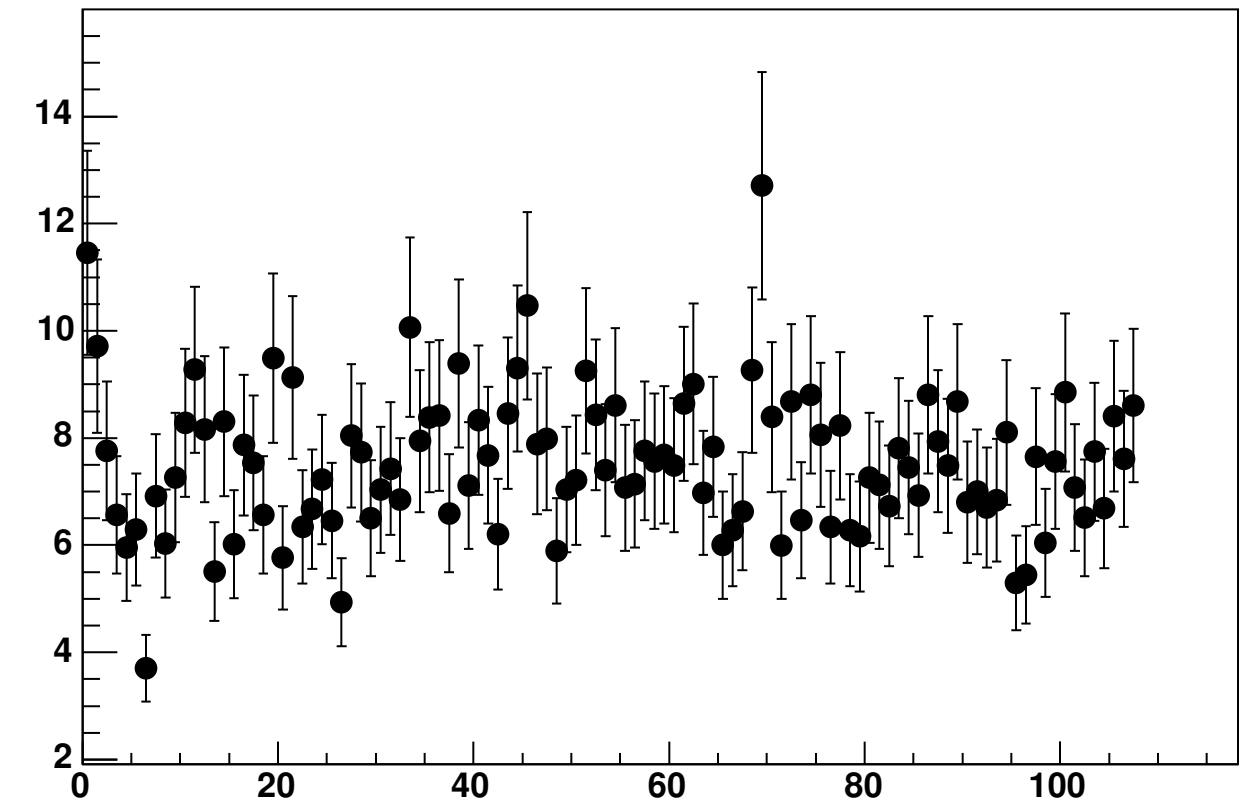
Enable 0, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



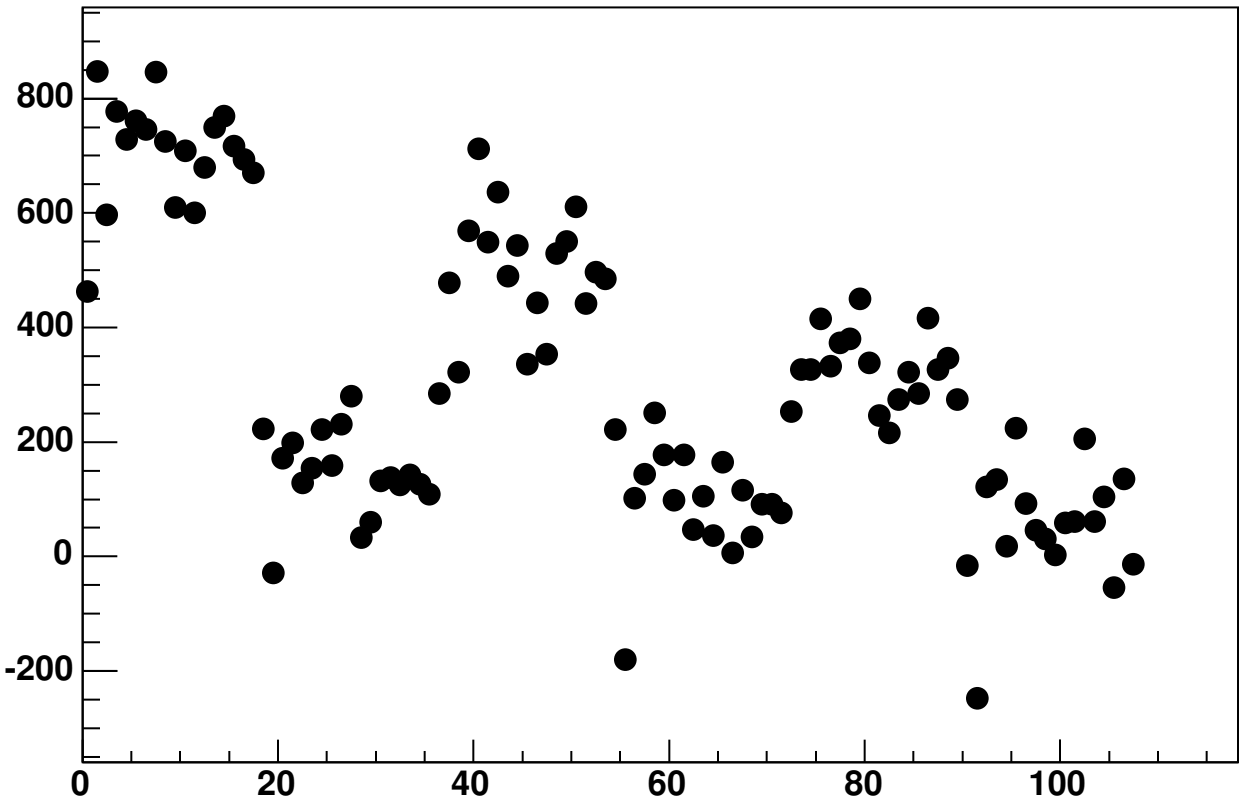
Enable 0, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



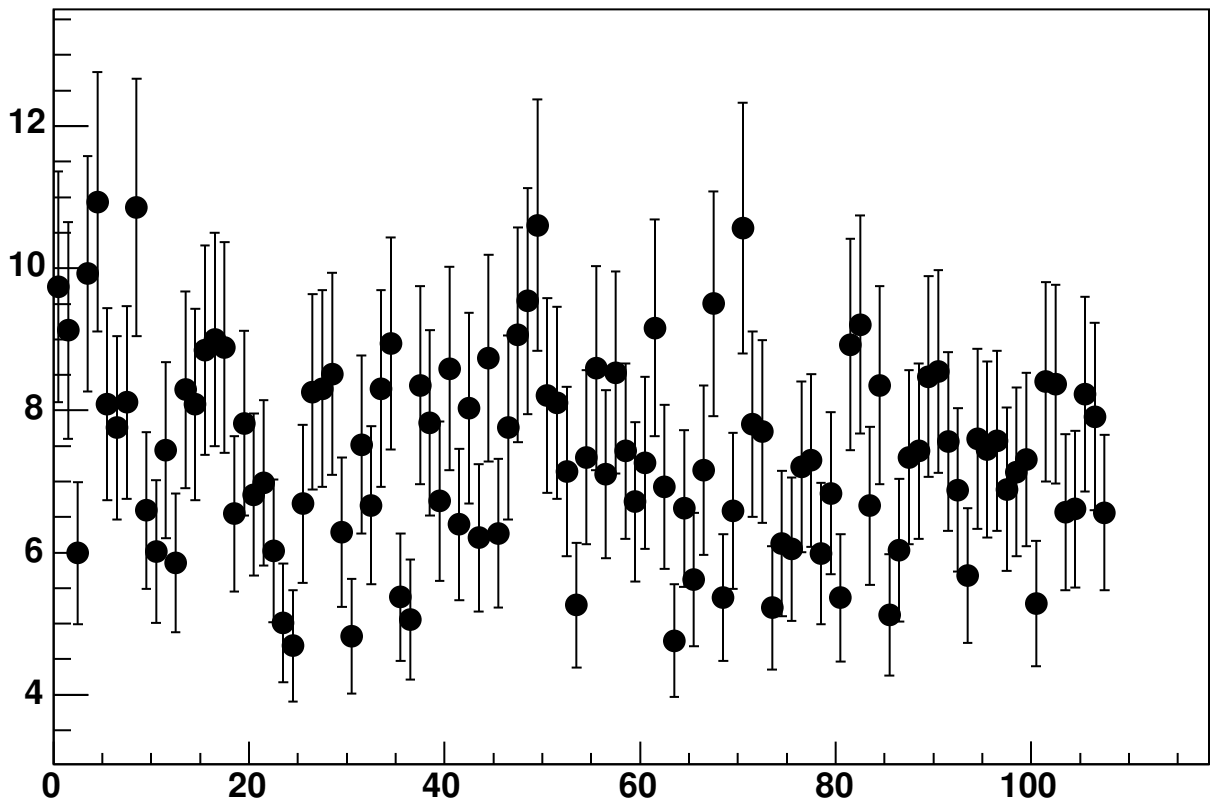
Enable 0, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



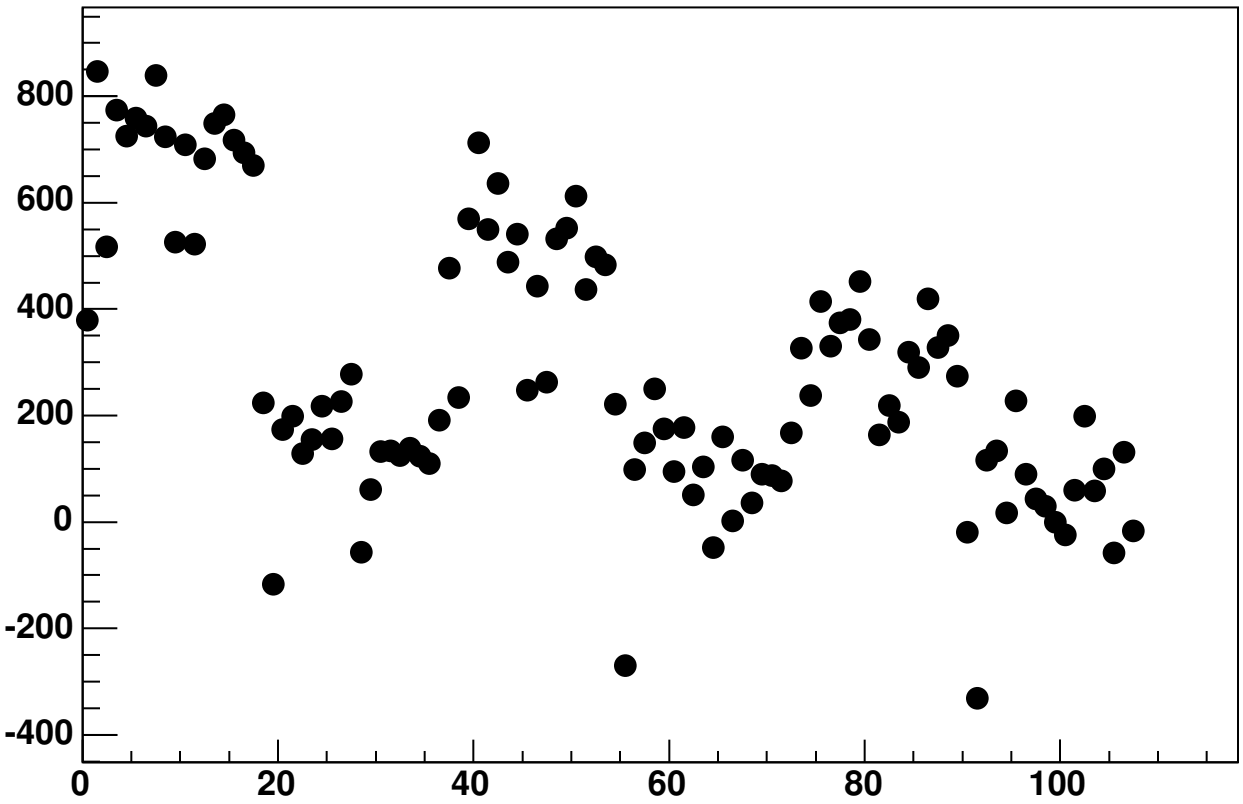
Enable 0, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



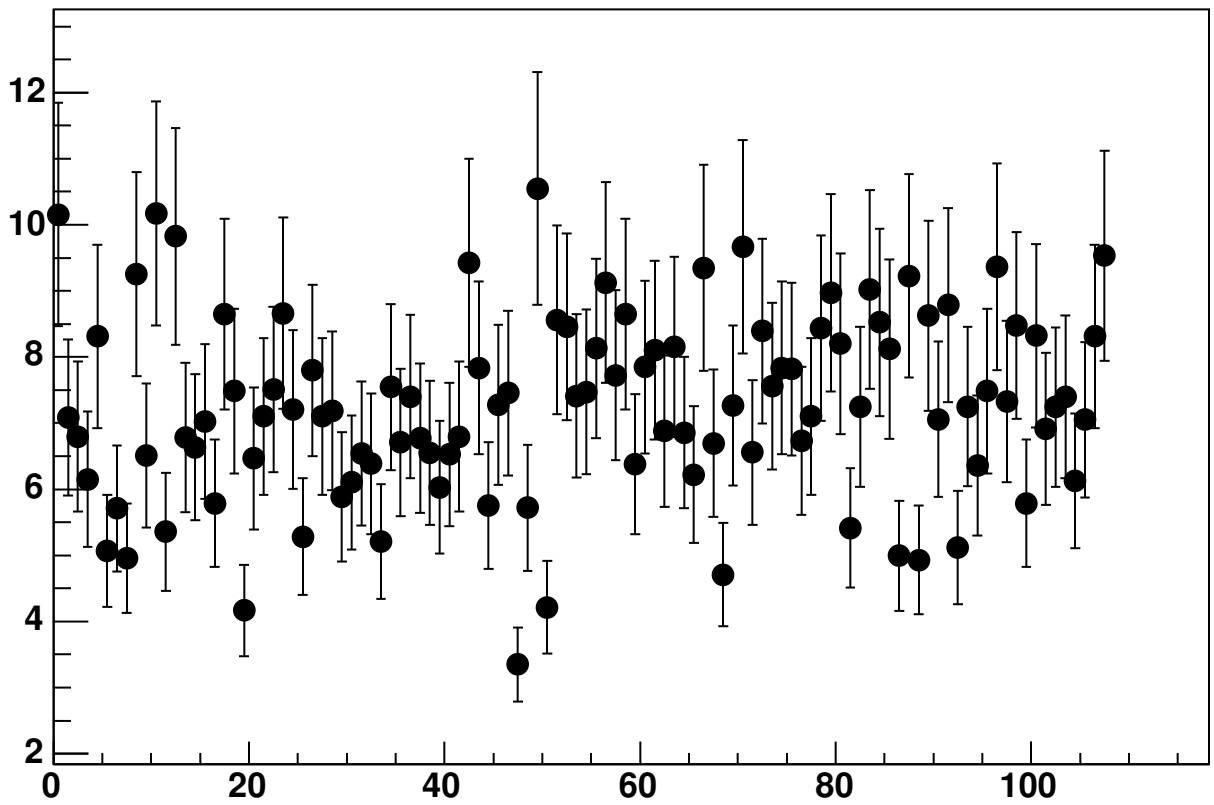
Enable 0, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



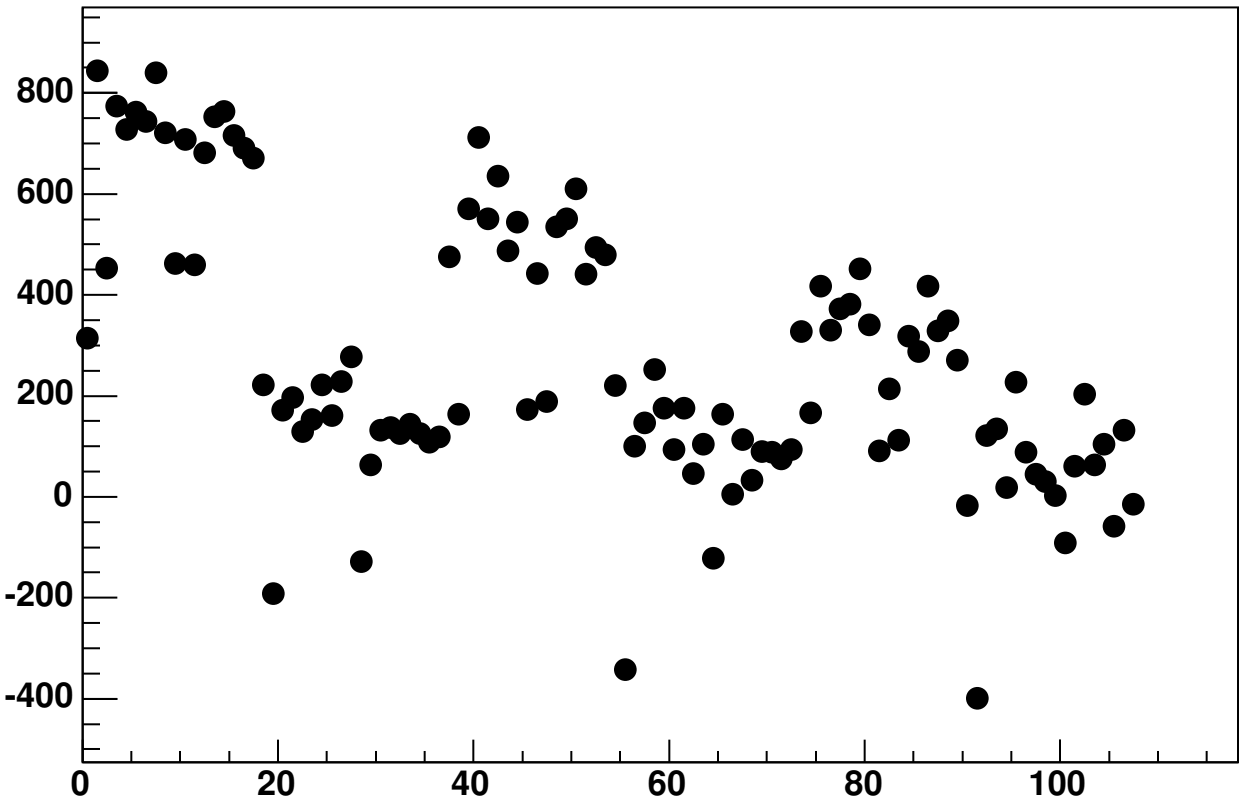
Enable 0, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



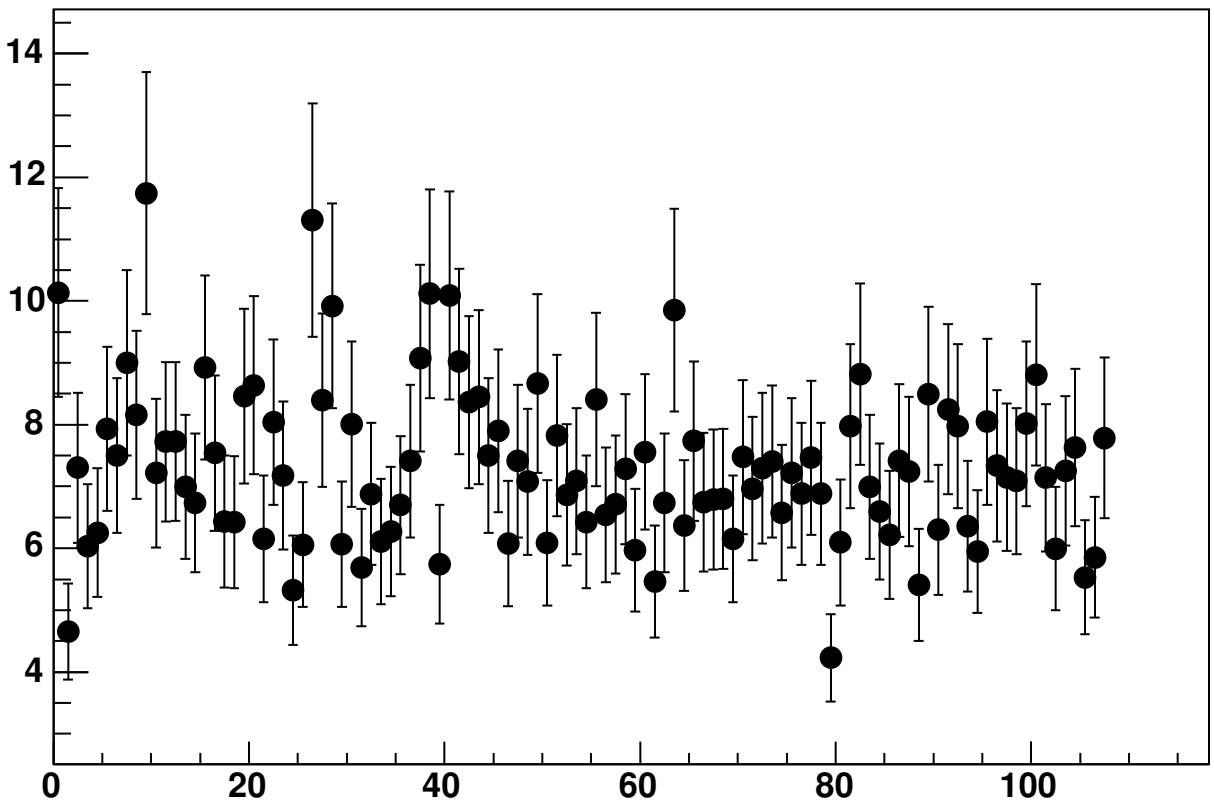
Enable 0, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



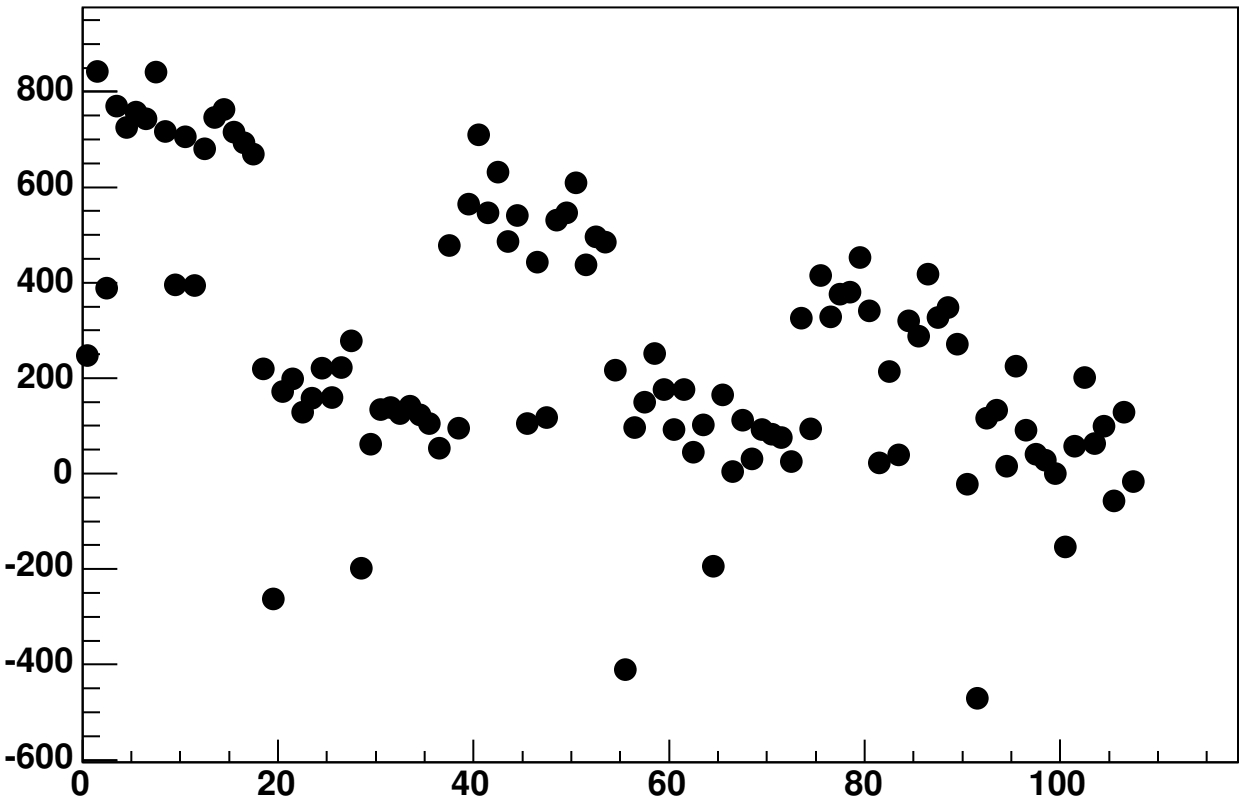
Enable 0, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



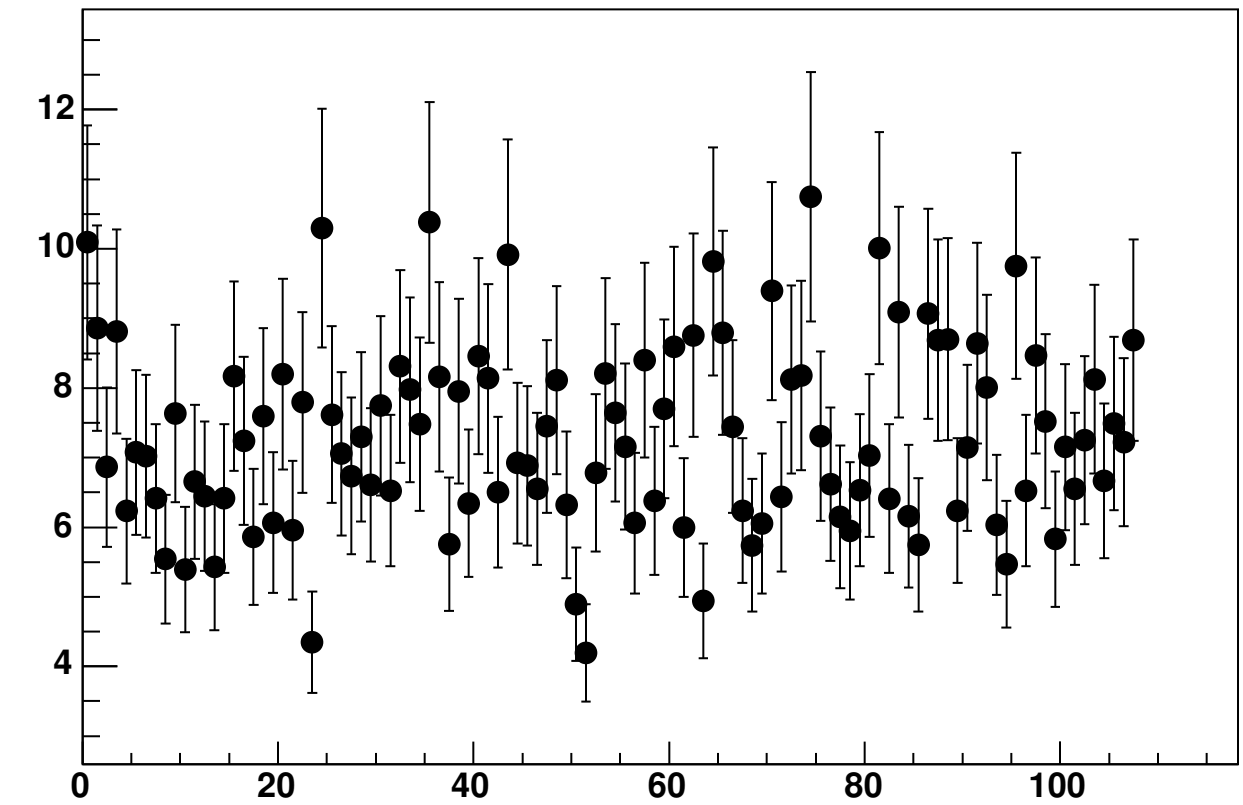
Enable 0, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



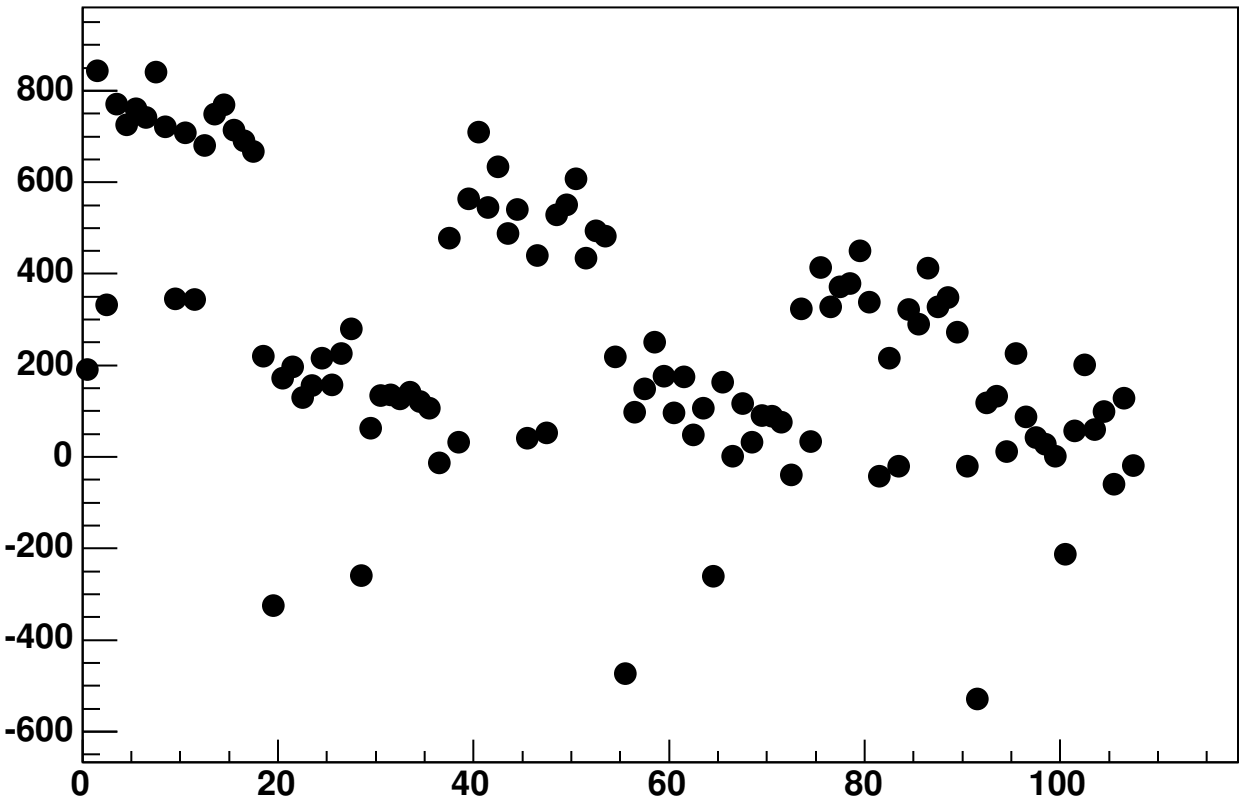
Enable 0, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



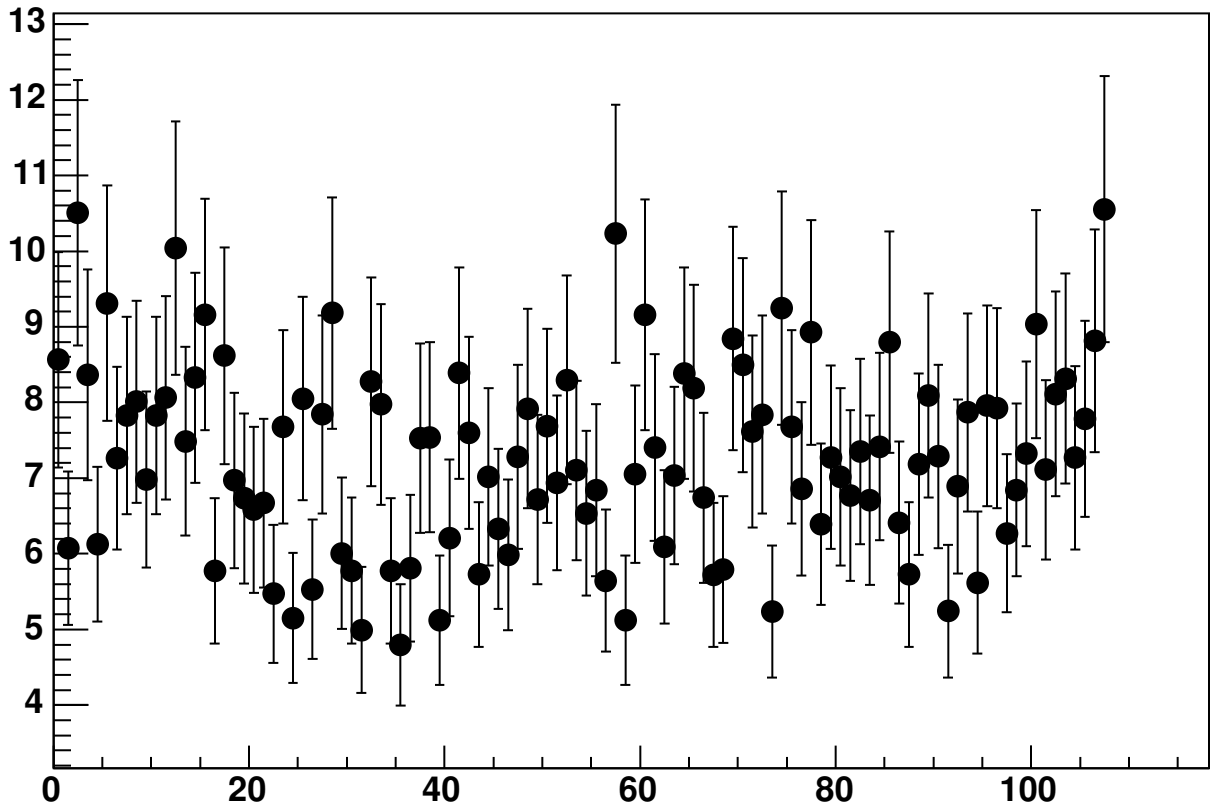
Enable 0, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



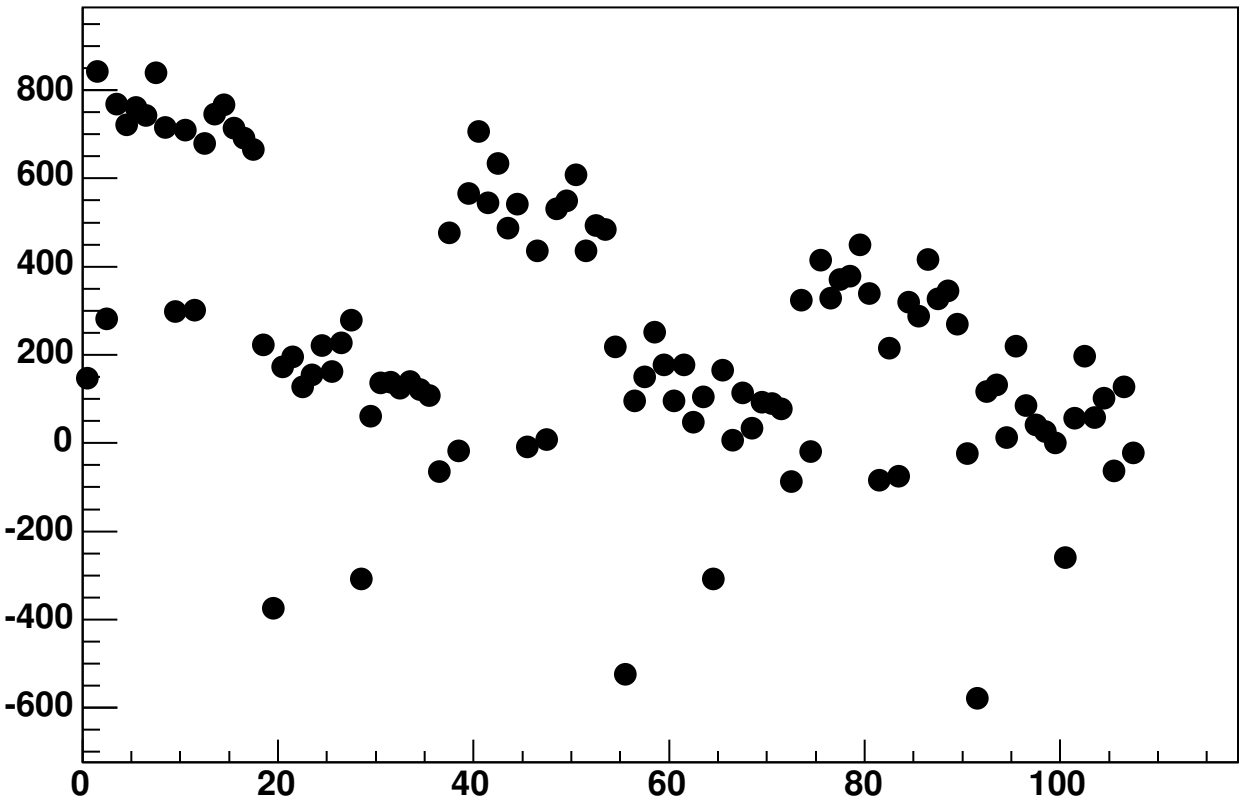
Enable 0, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



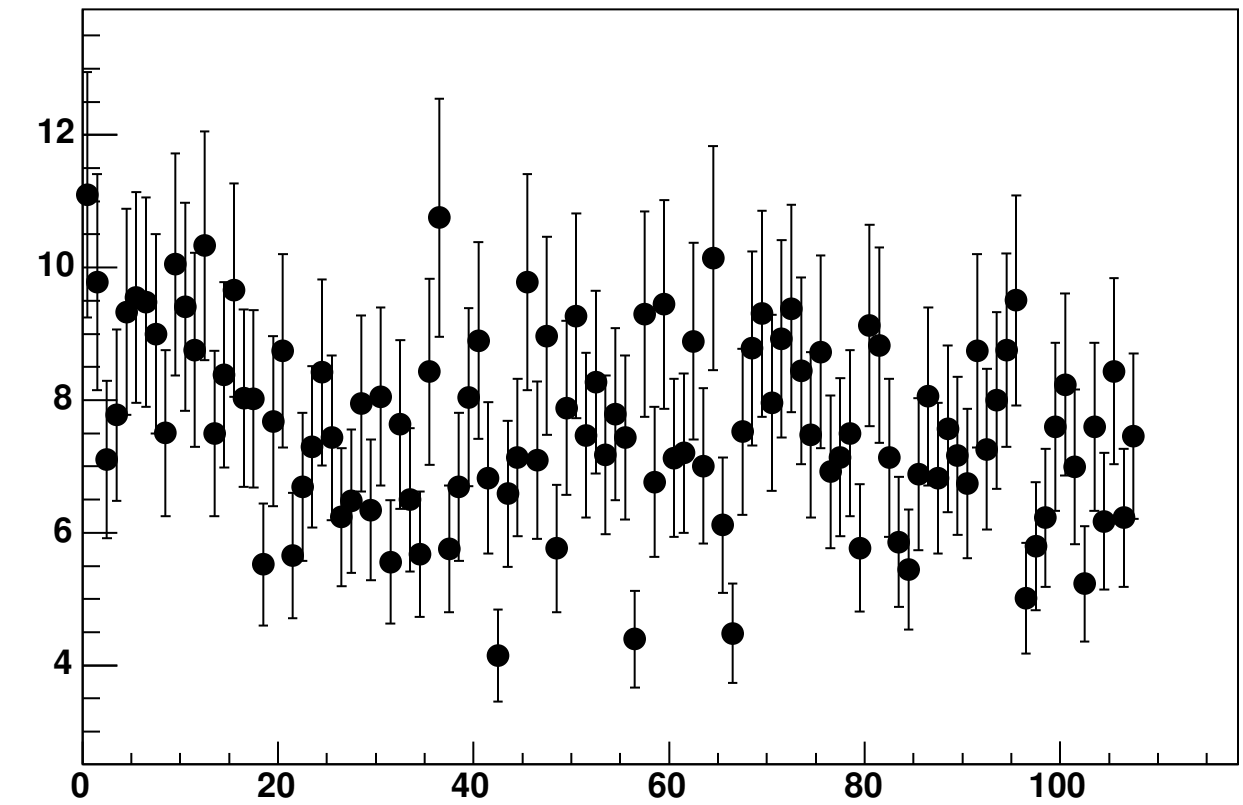
Enable 0, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



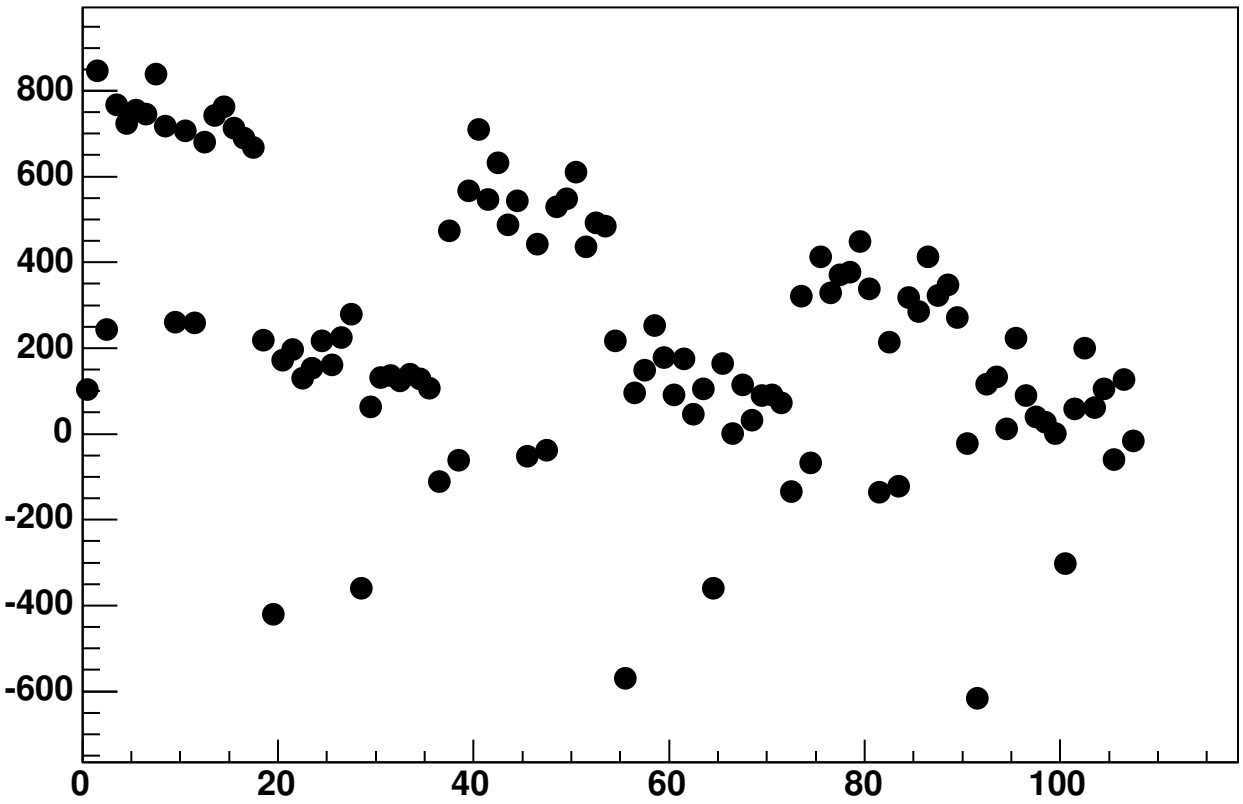
Enable 0, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



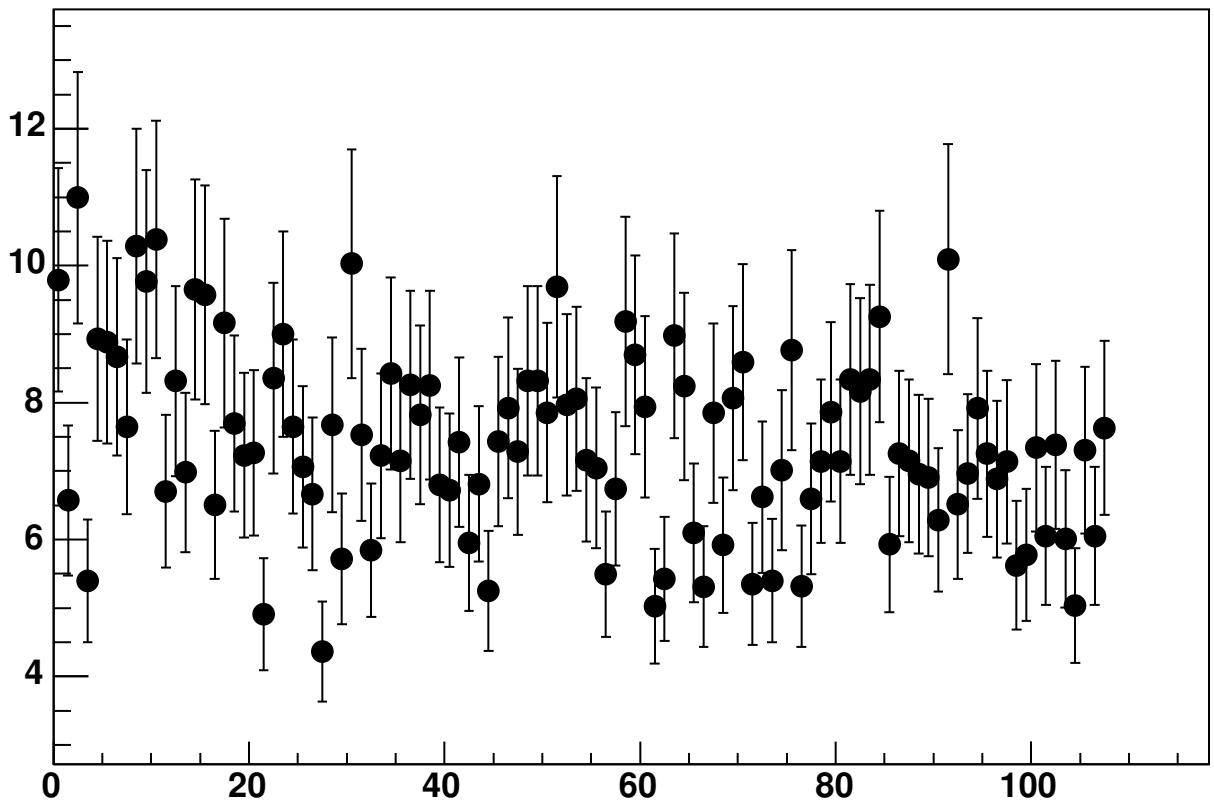
Enable 0, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



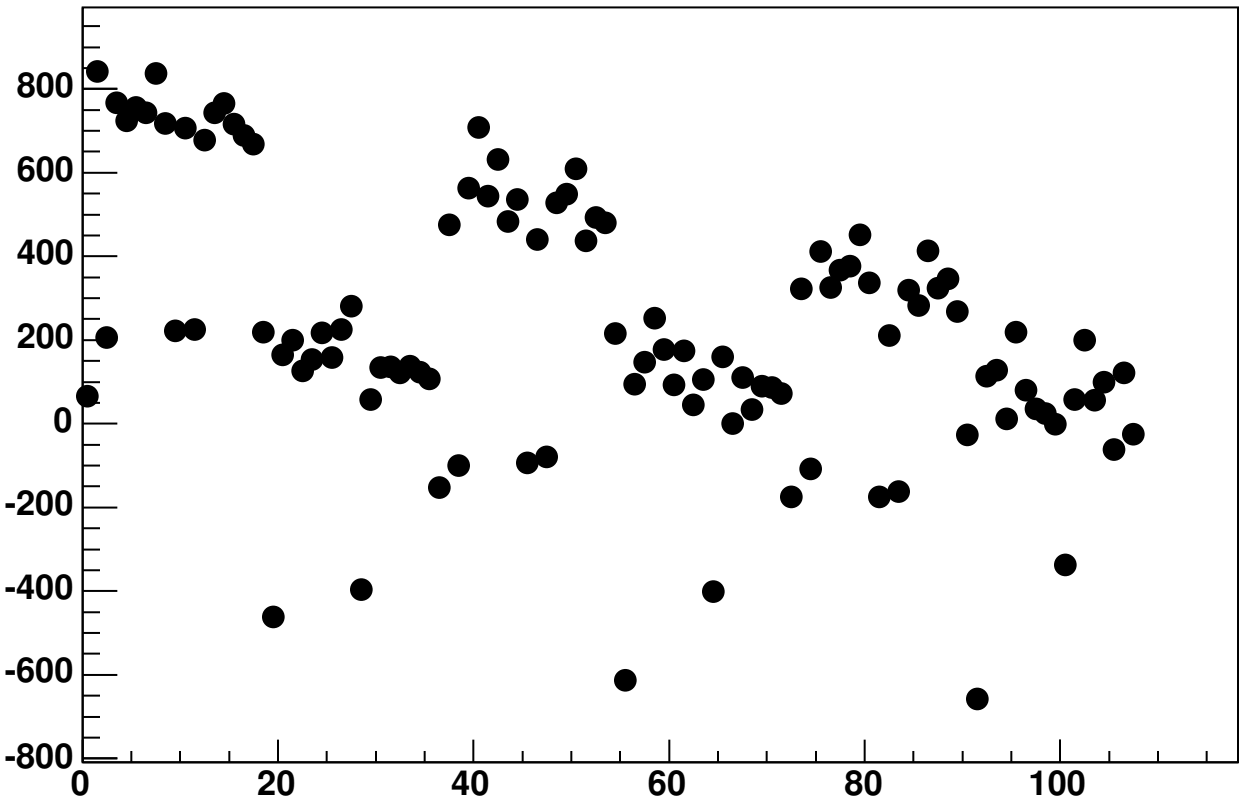
Enable 0, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



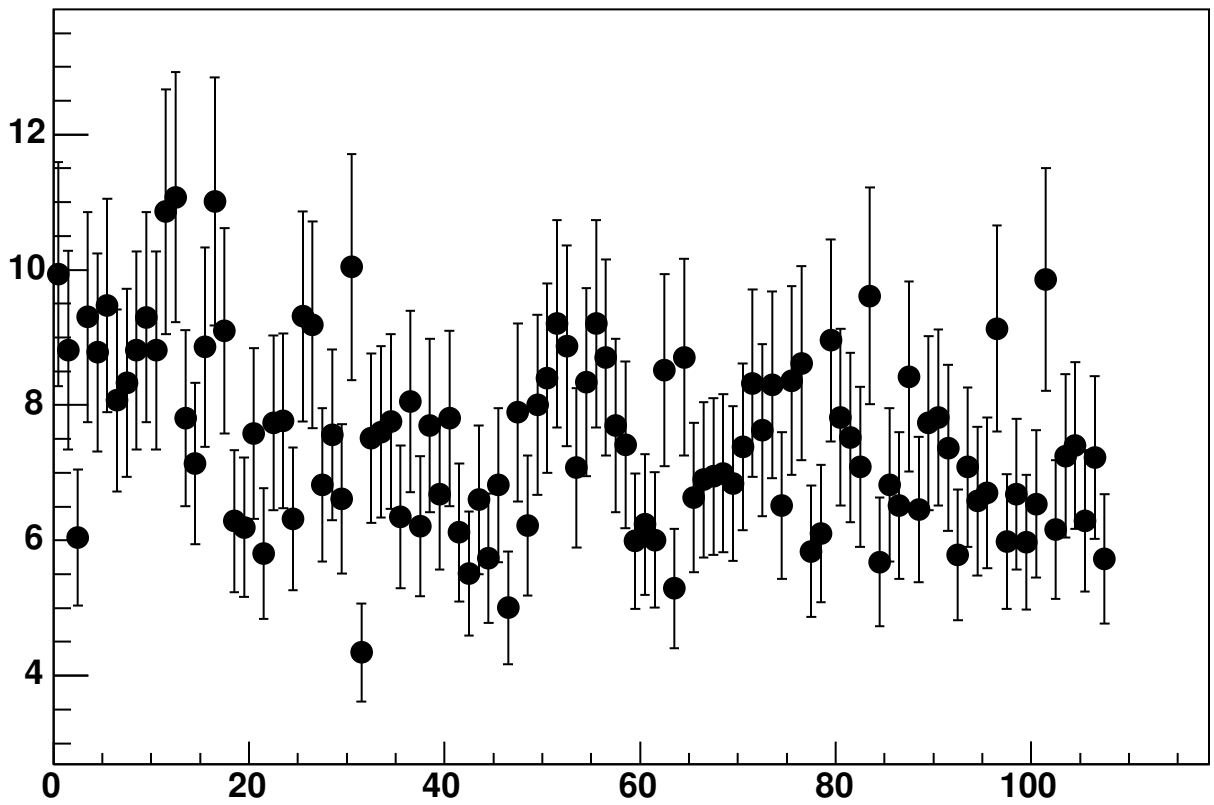
Enable 0, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



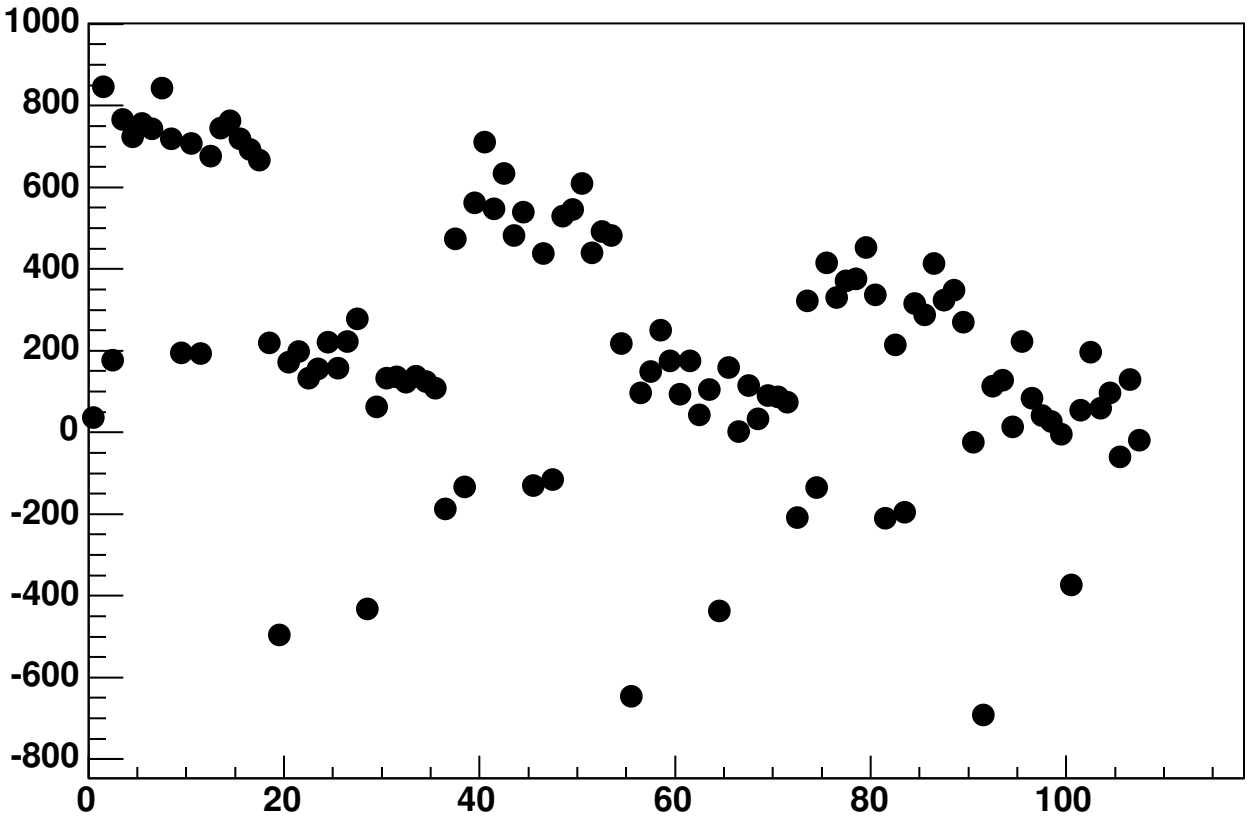
Enable 0, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



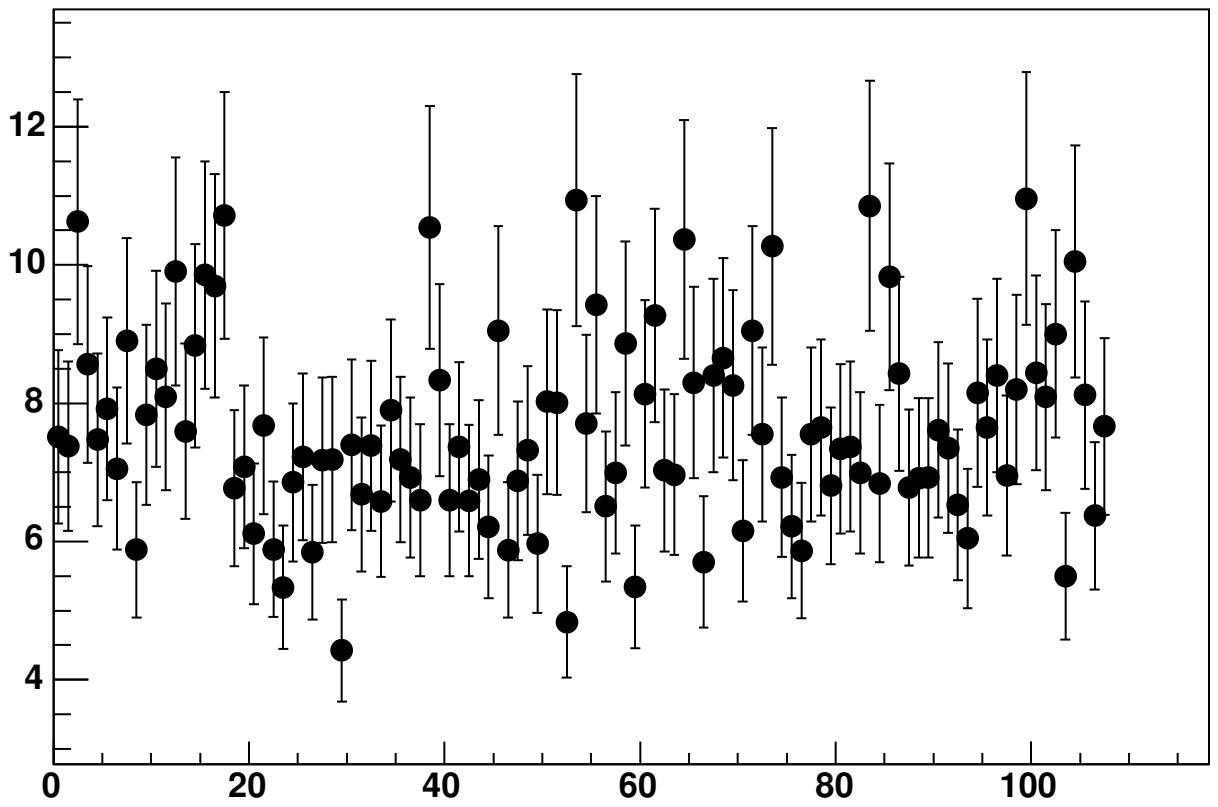
Enable 0, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



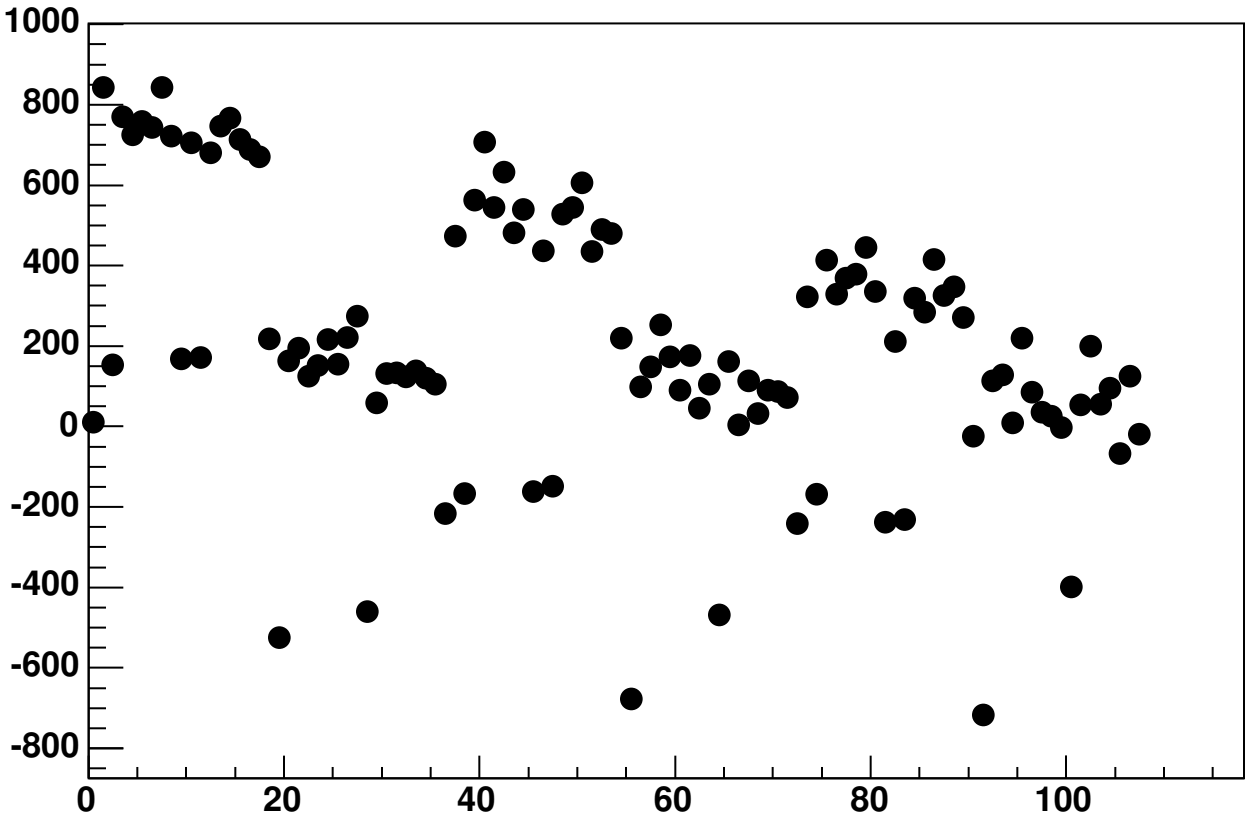
Enable 0, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



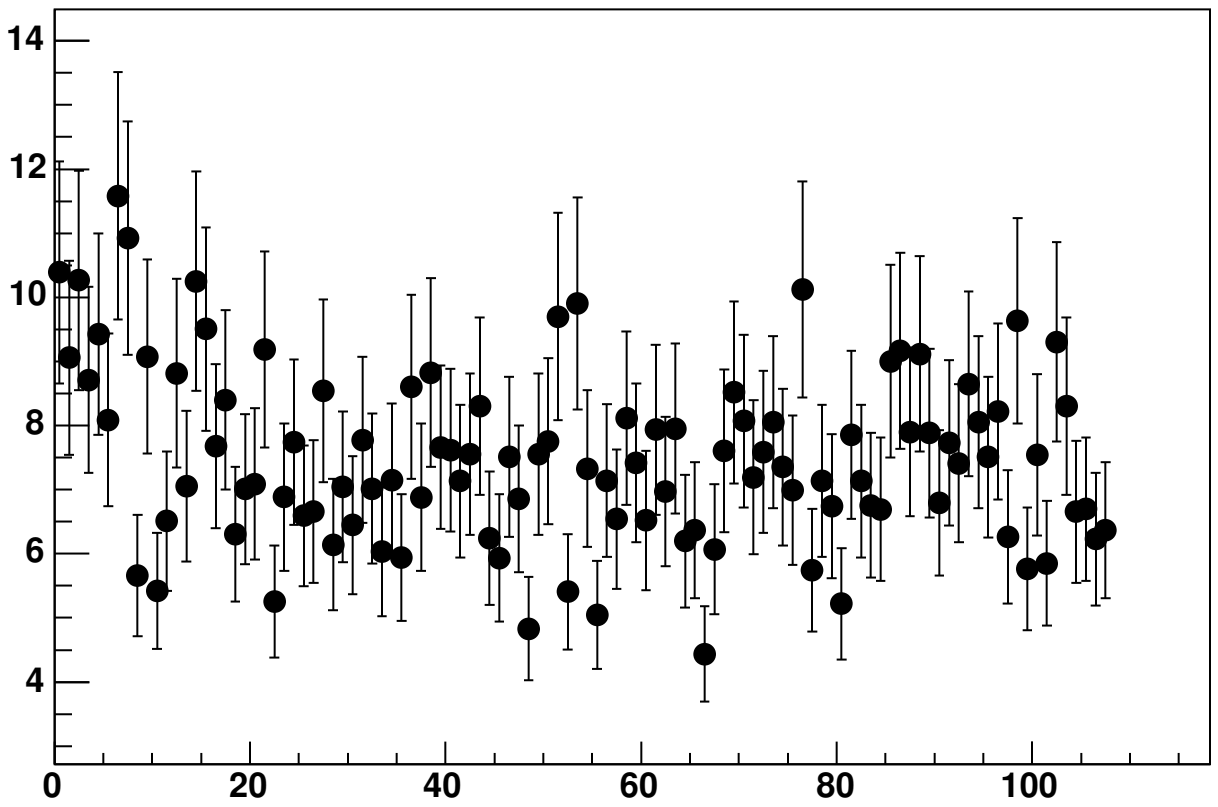
Enable 0, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



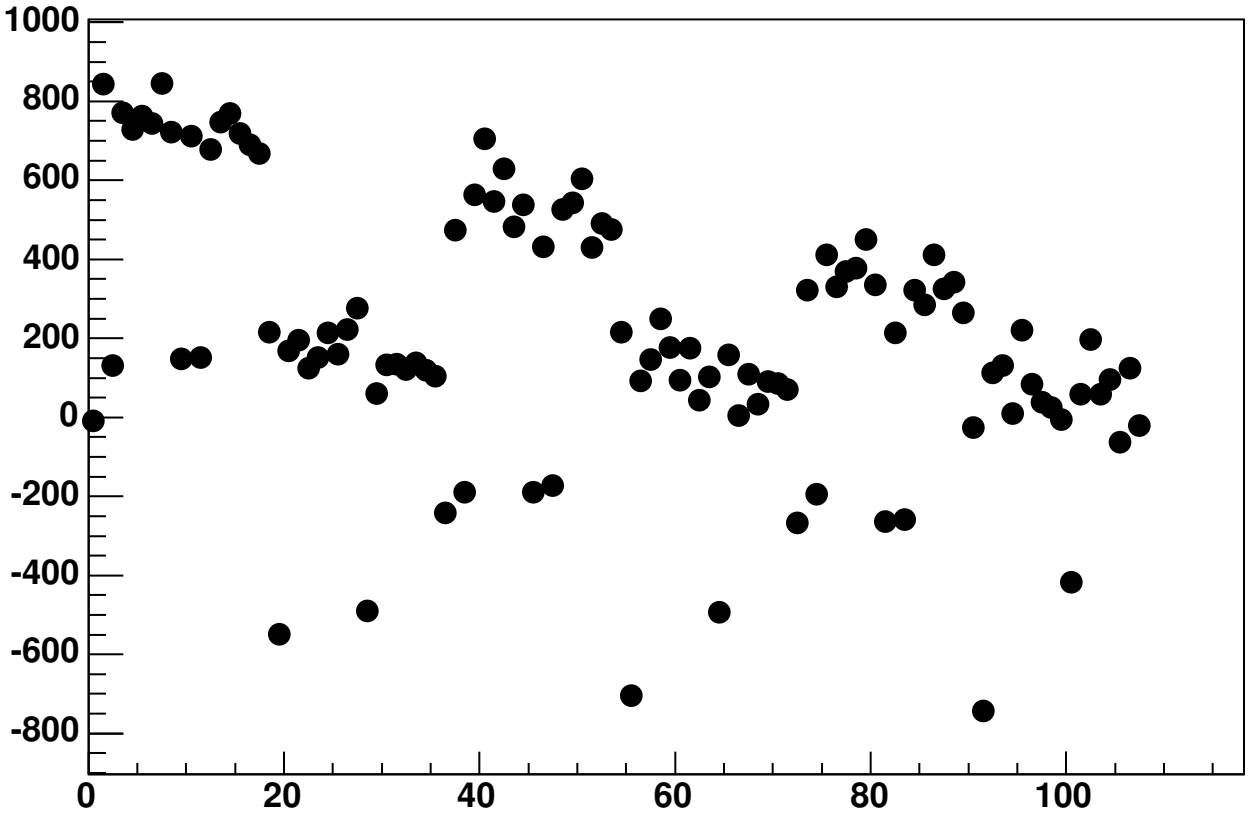
Enable 0, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



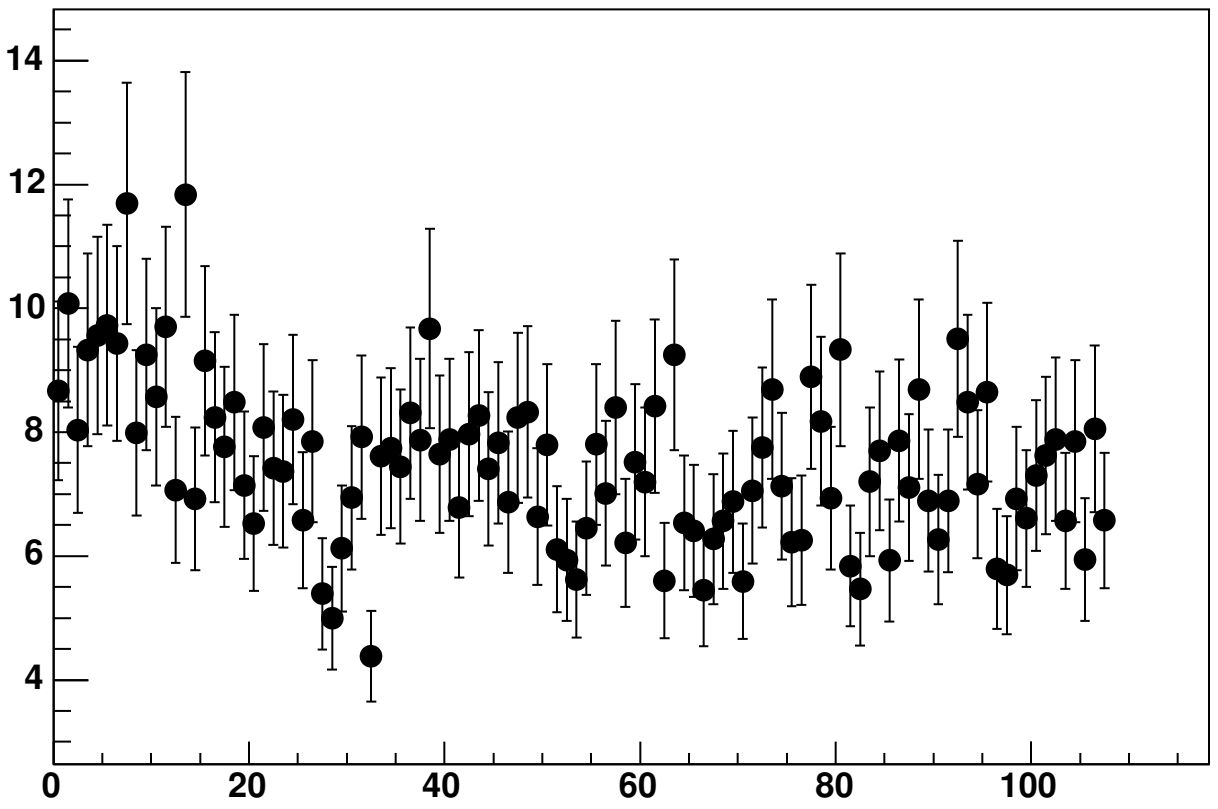
Enable 0, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



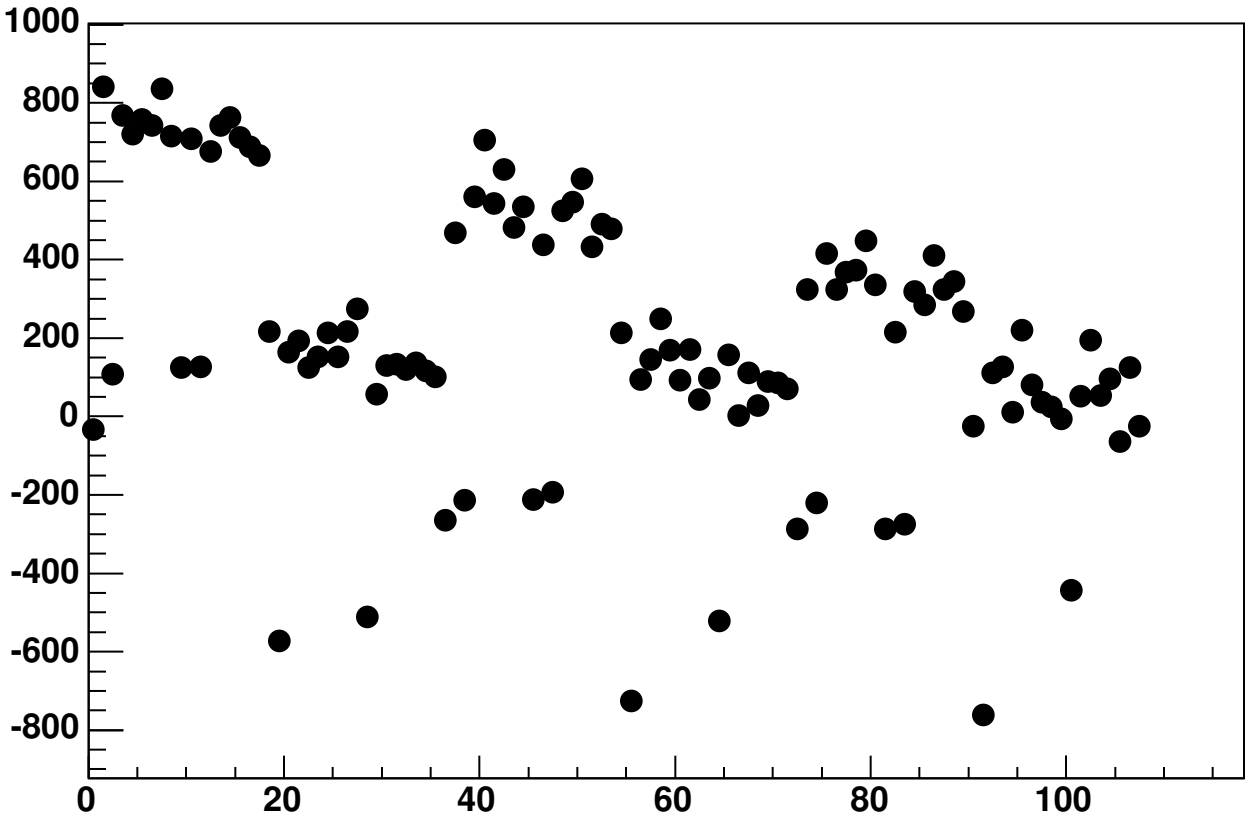
Enable 0, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



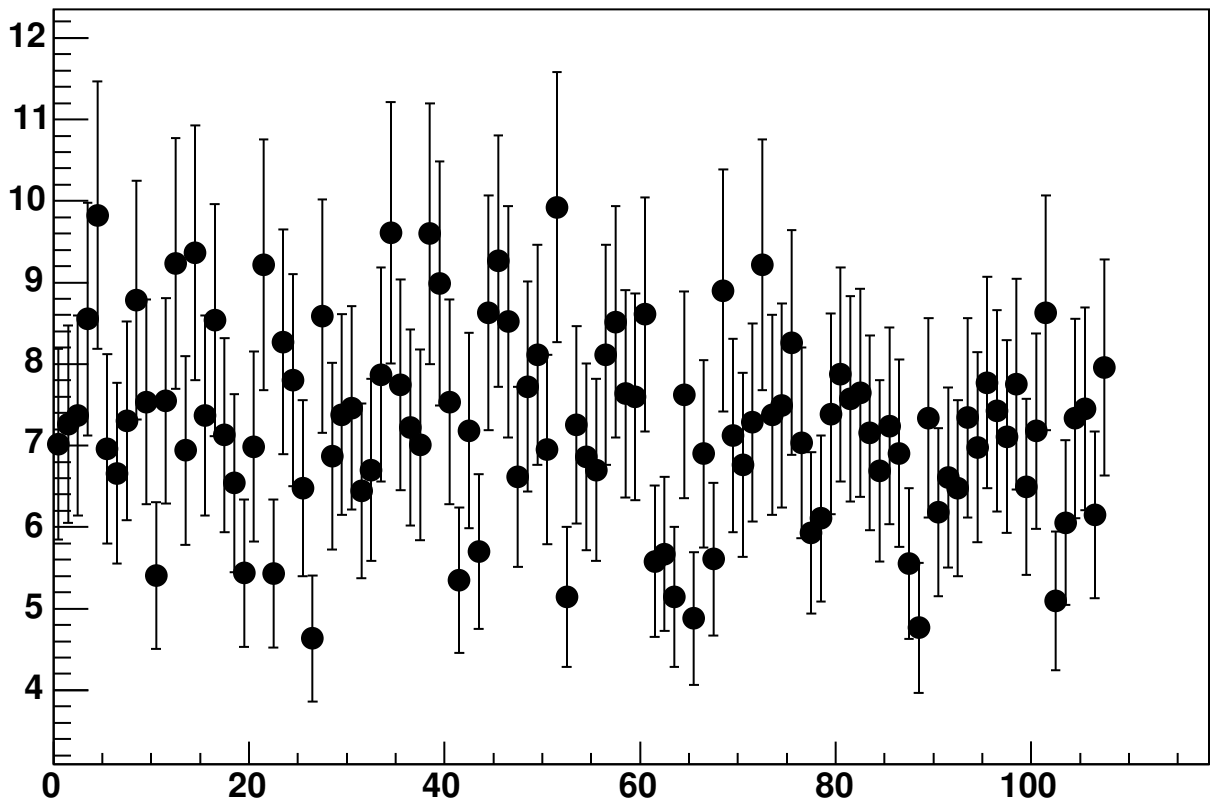
Enable 0, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



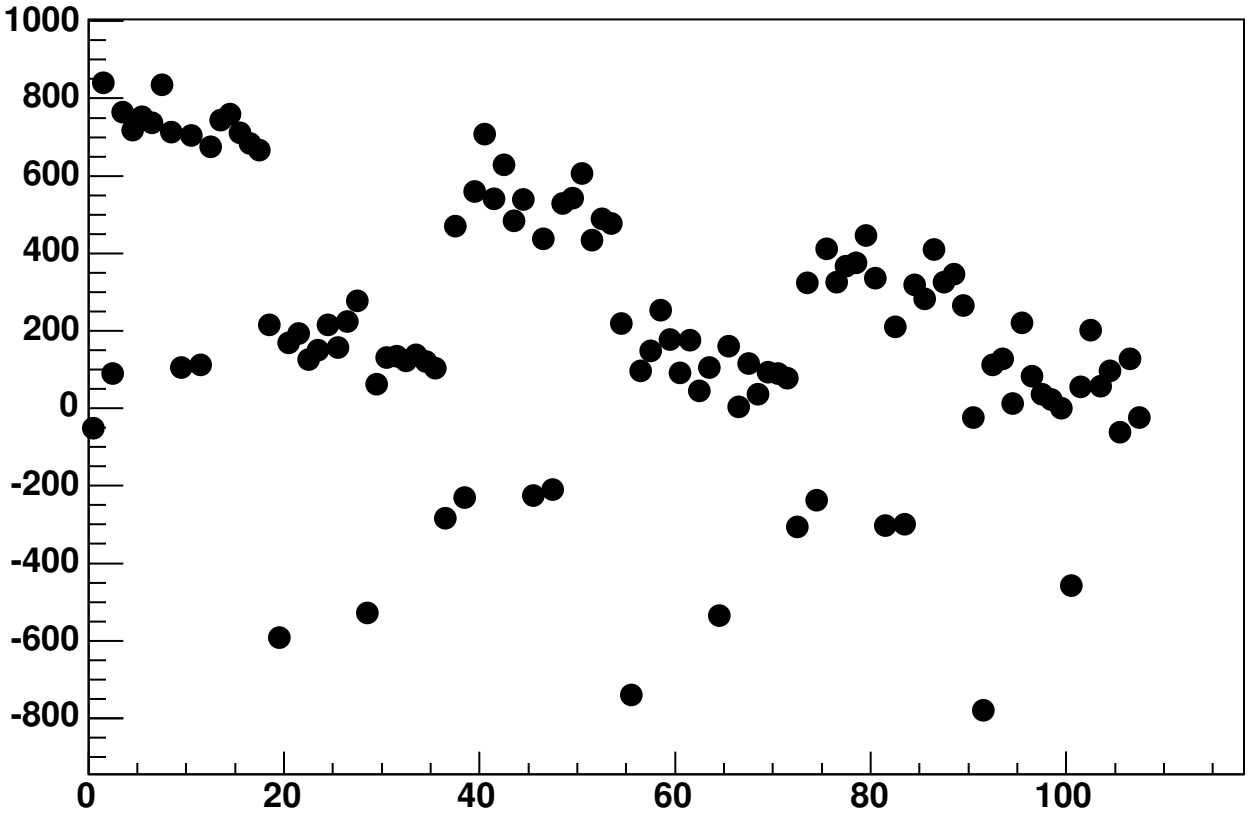
Enable 0, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



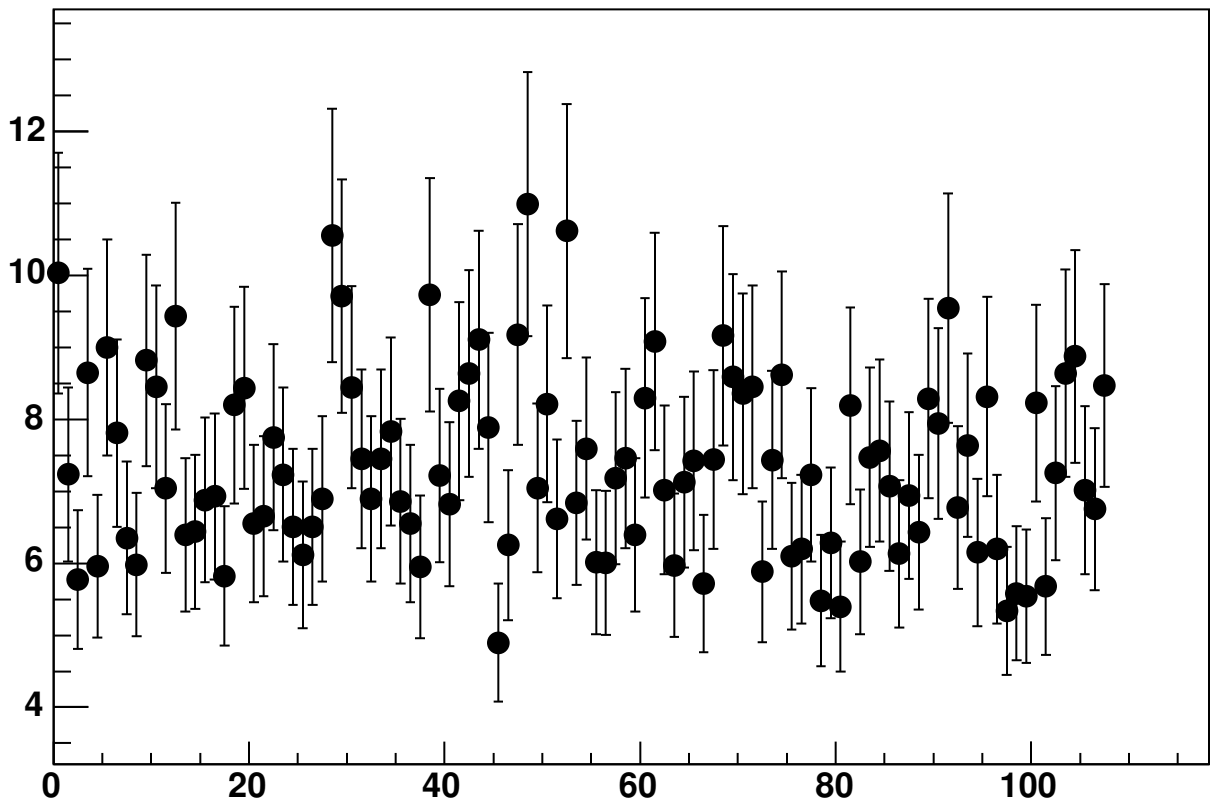
Enable 0, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



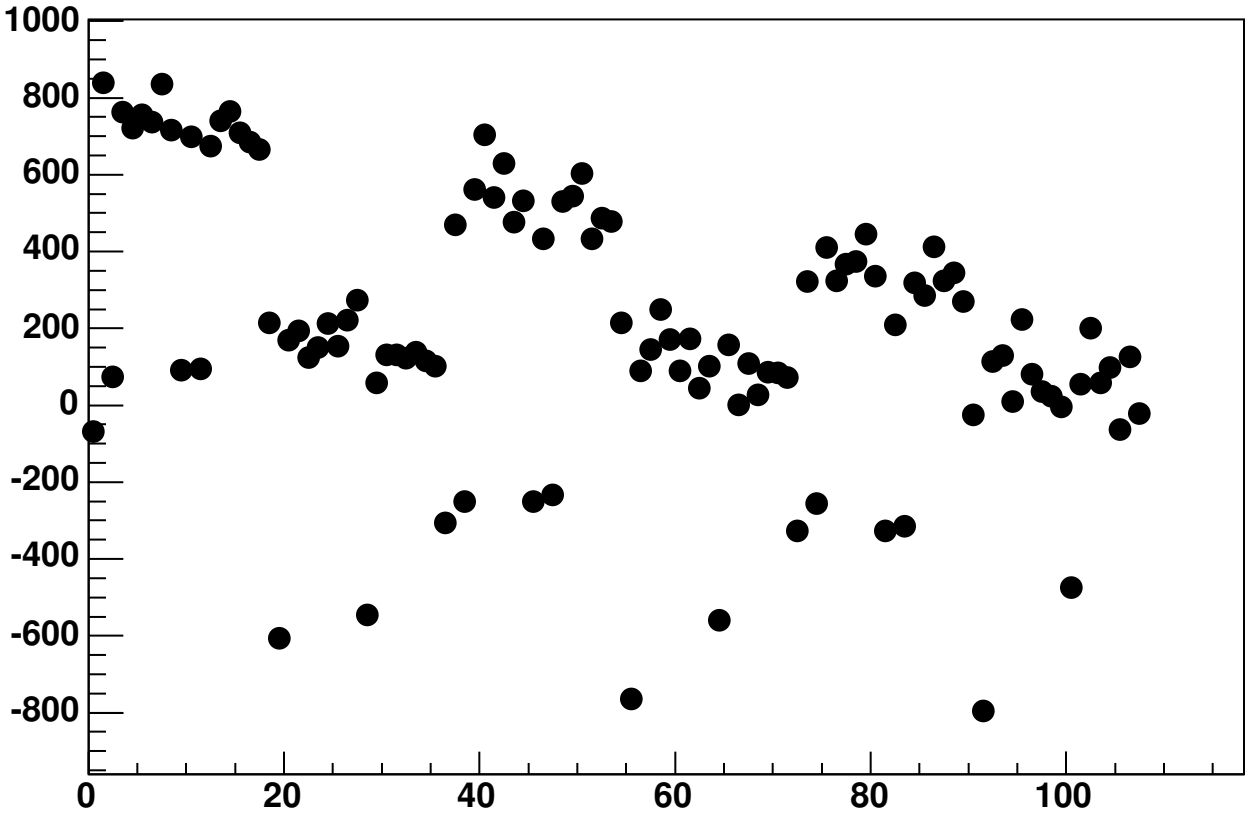
Enable 0, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



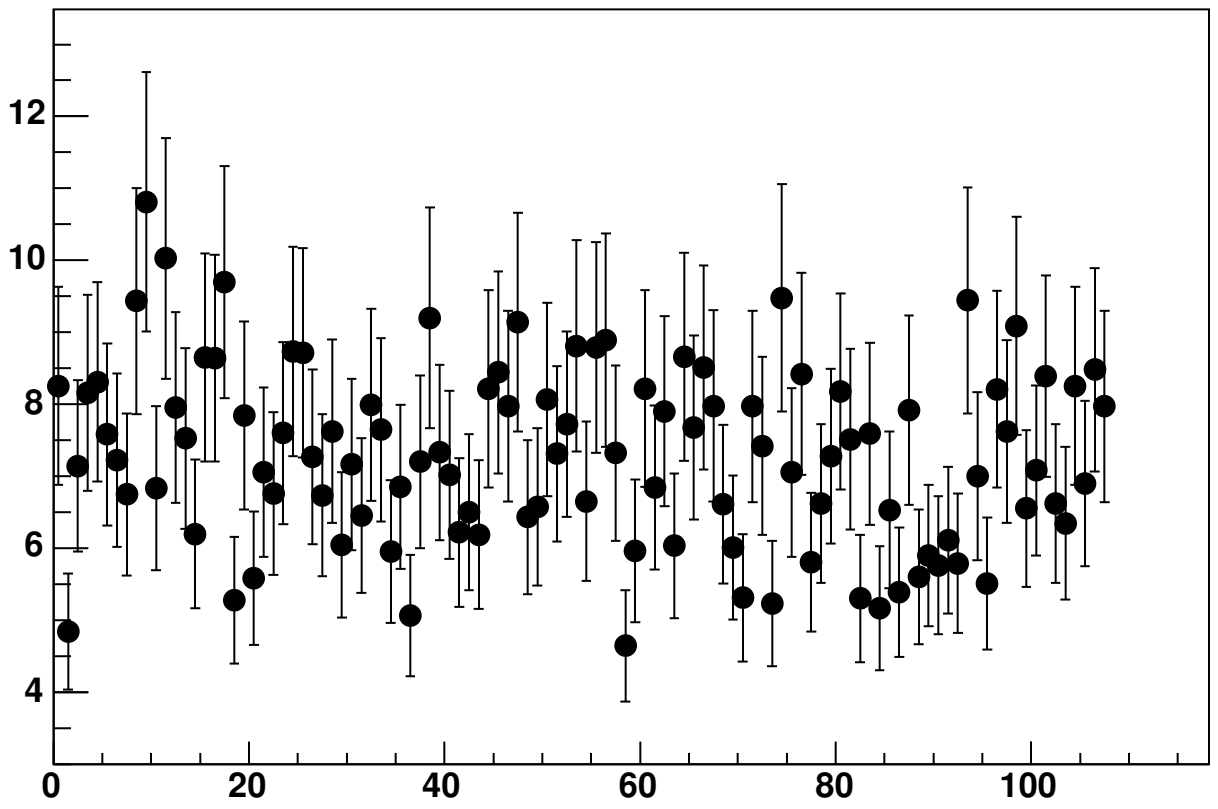
Enable 0, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



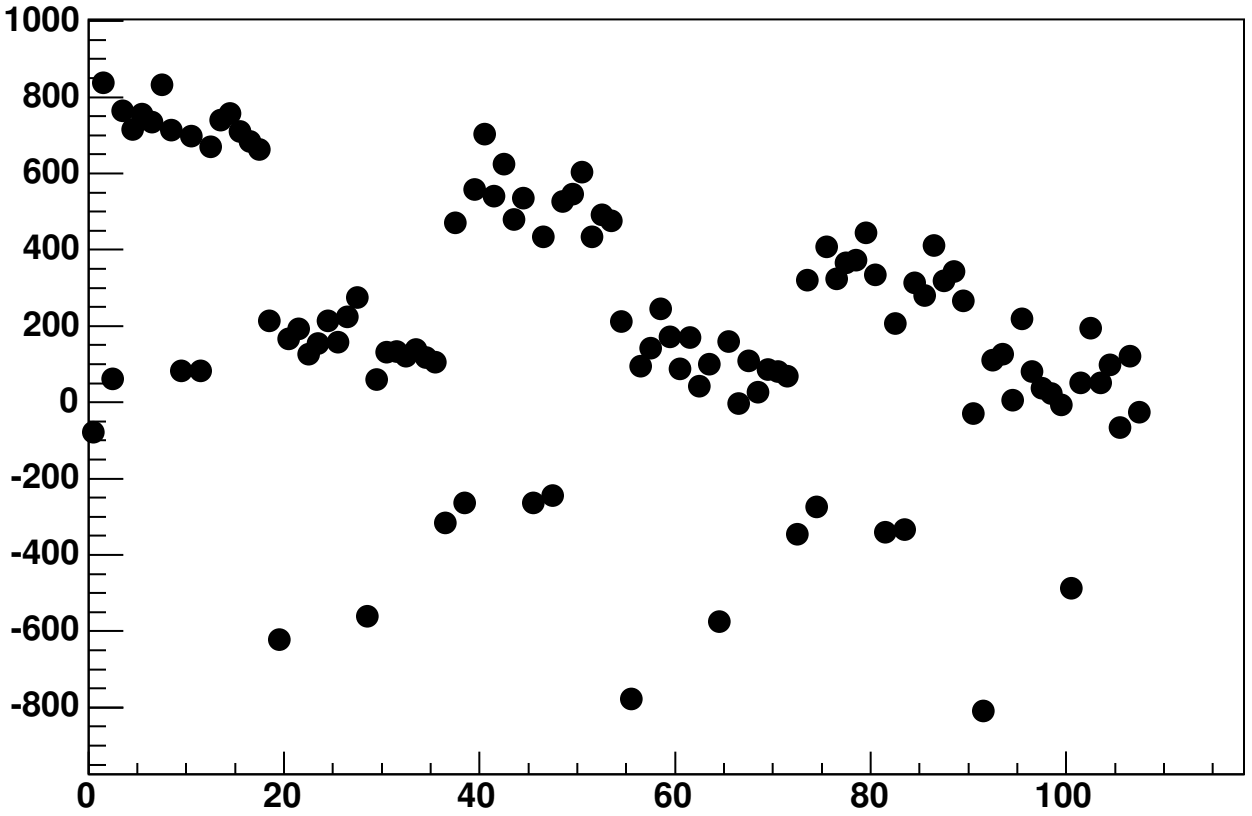
Enable 0, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



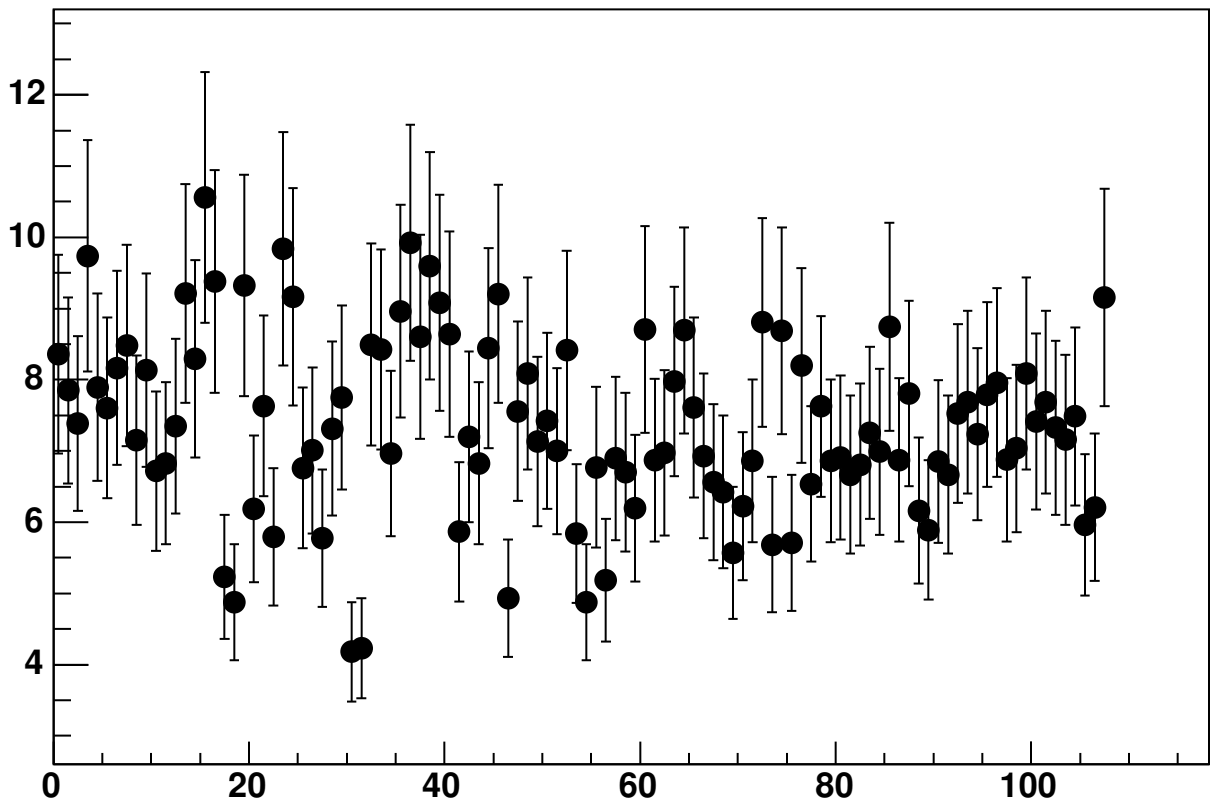
Enable 0, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan



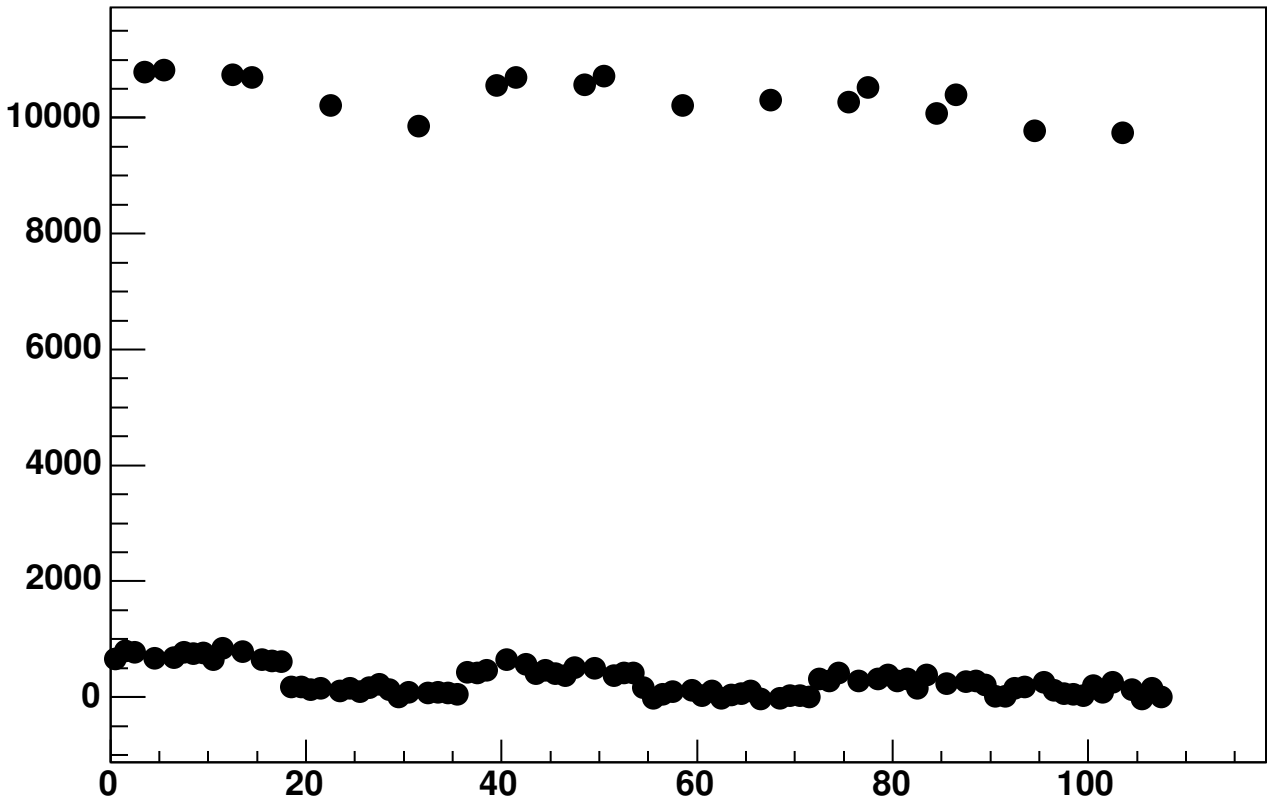
Enable 0, DAC=4000, Hold=245, ADC Mean vs 18*Chip+Chan



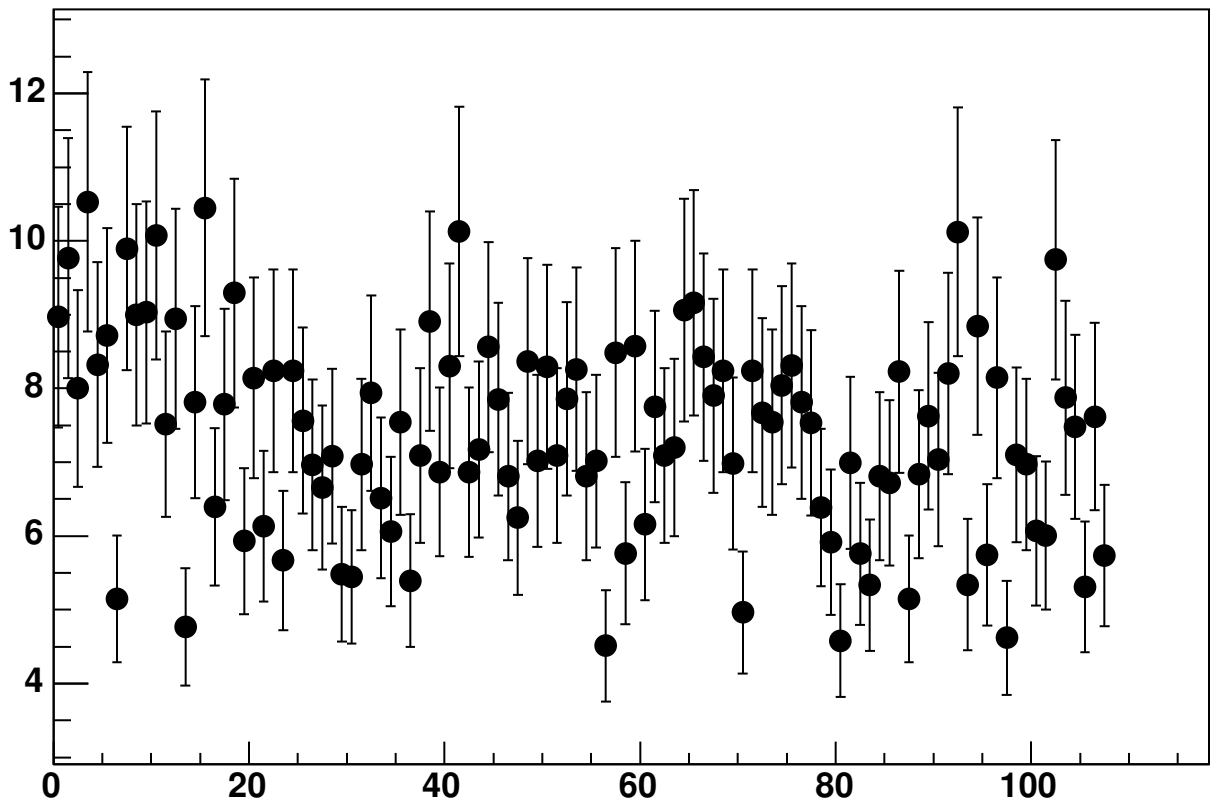
Enable 0, DAC=4000, Hold=245, ADC Noise vs 18*Chip+Chan



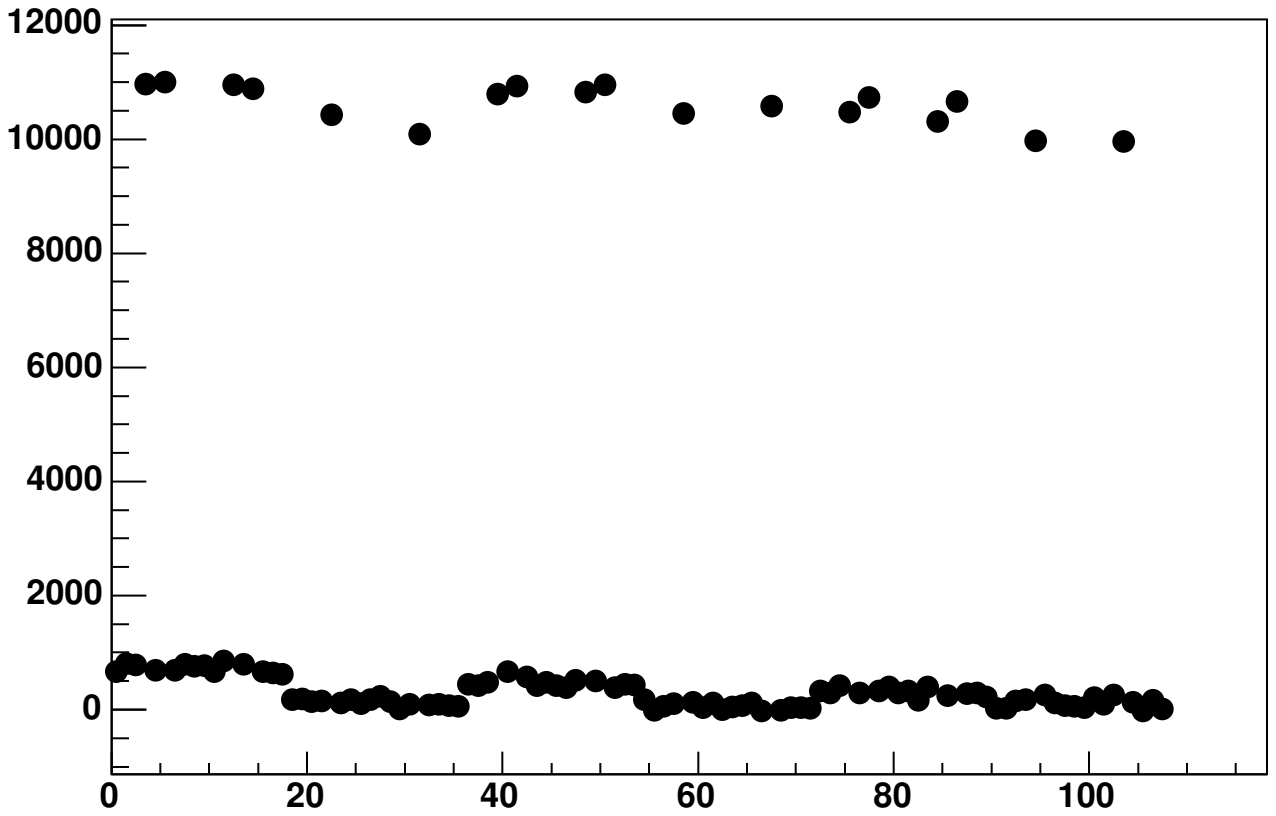
Enable 1, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



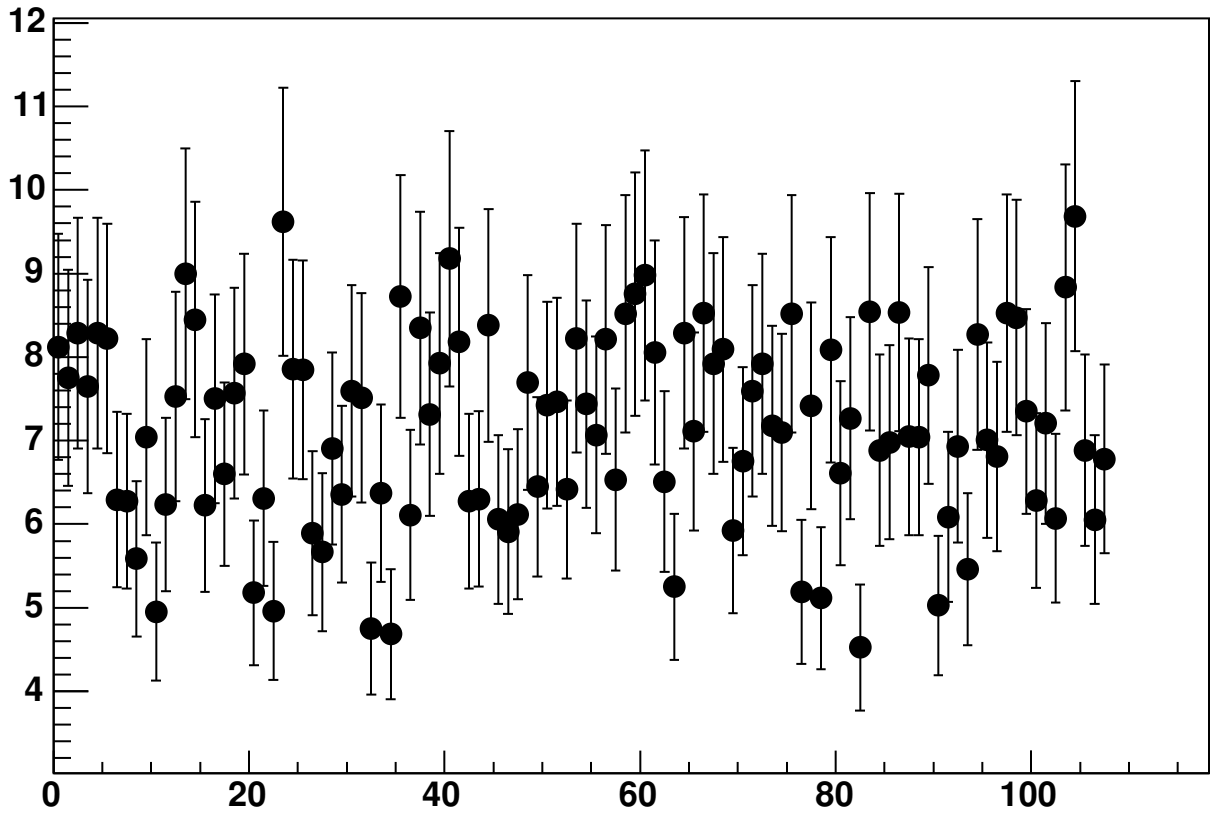
Enable 1, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



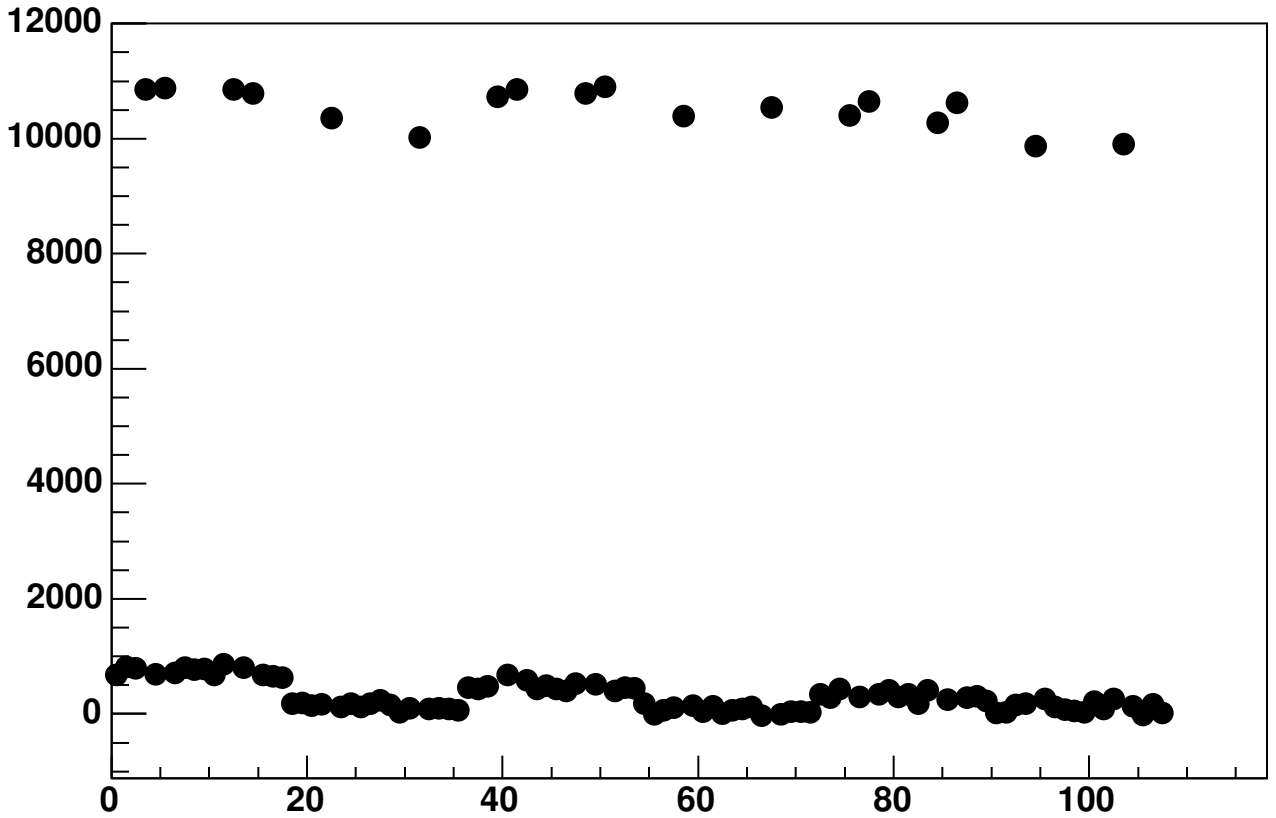
Enable 1, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



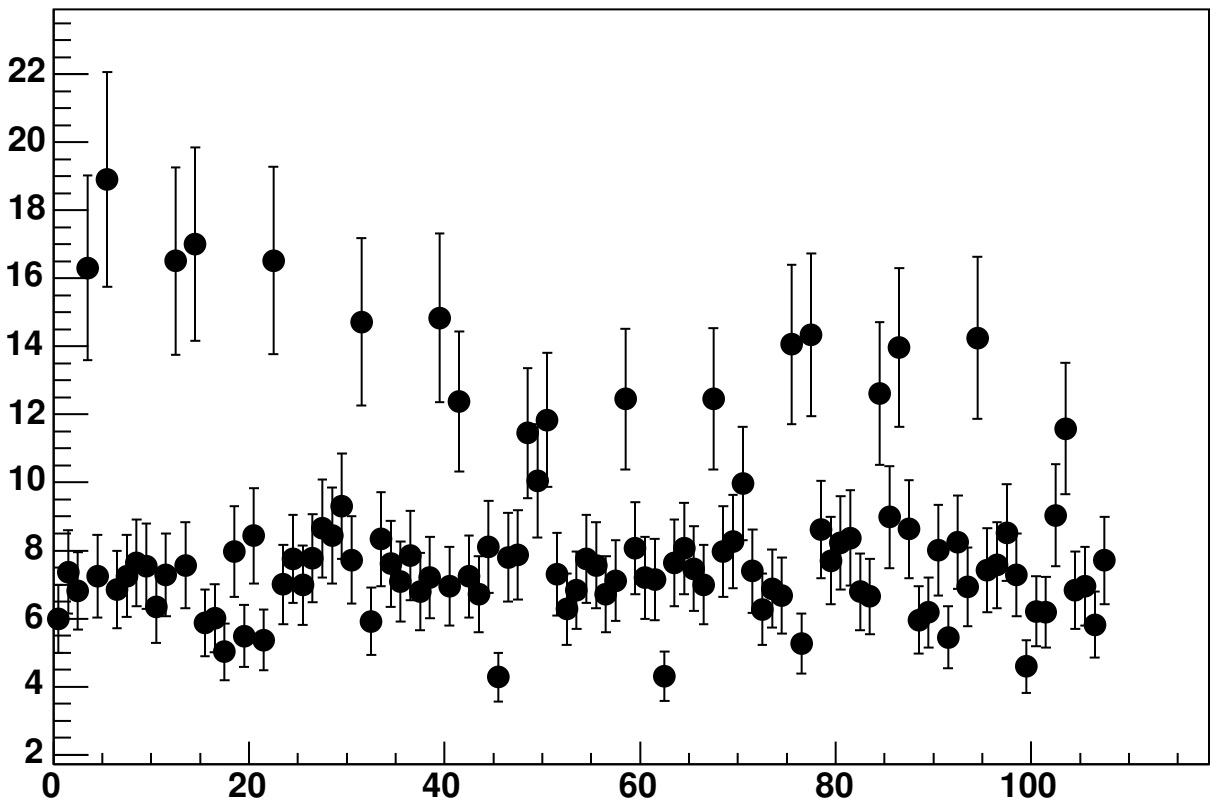
Enable 1, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



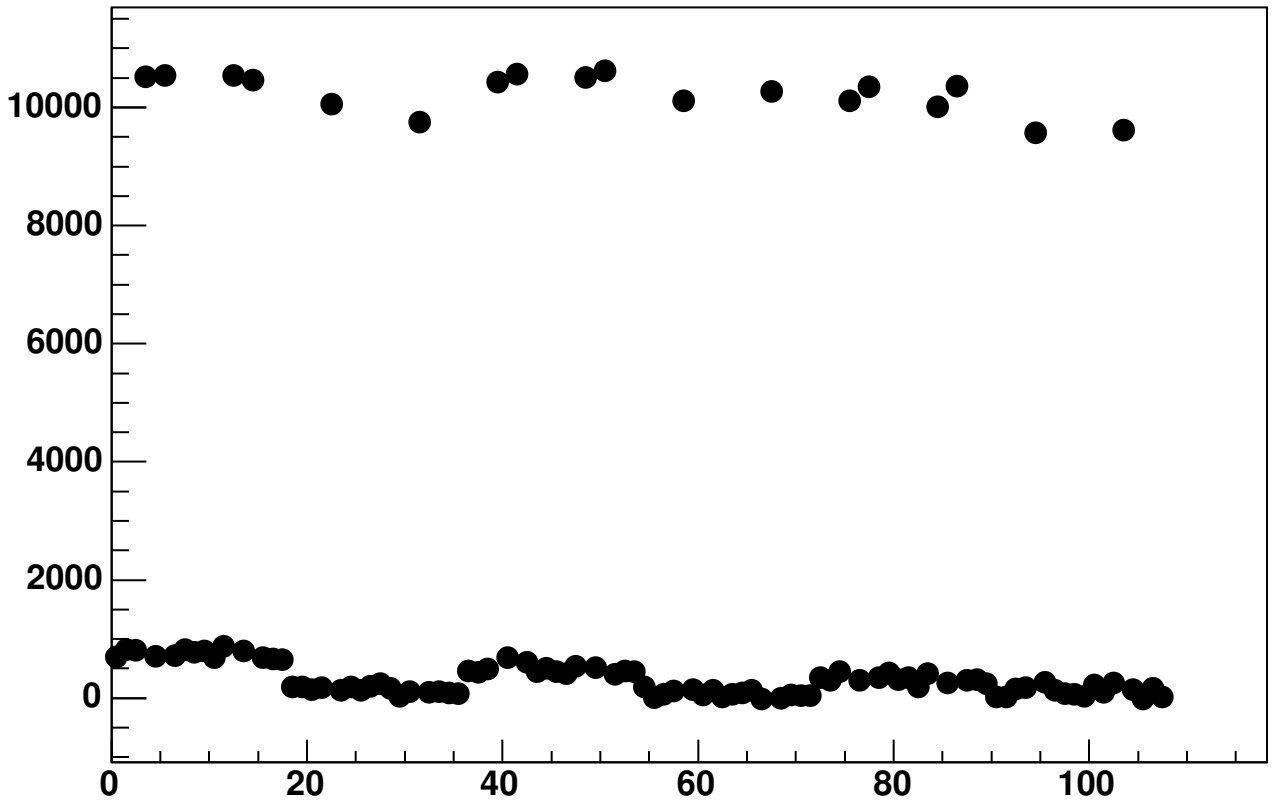
Enable 1, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



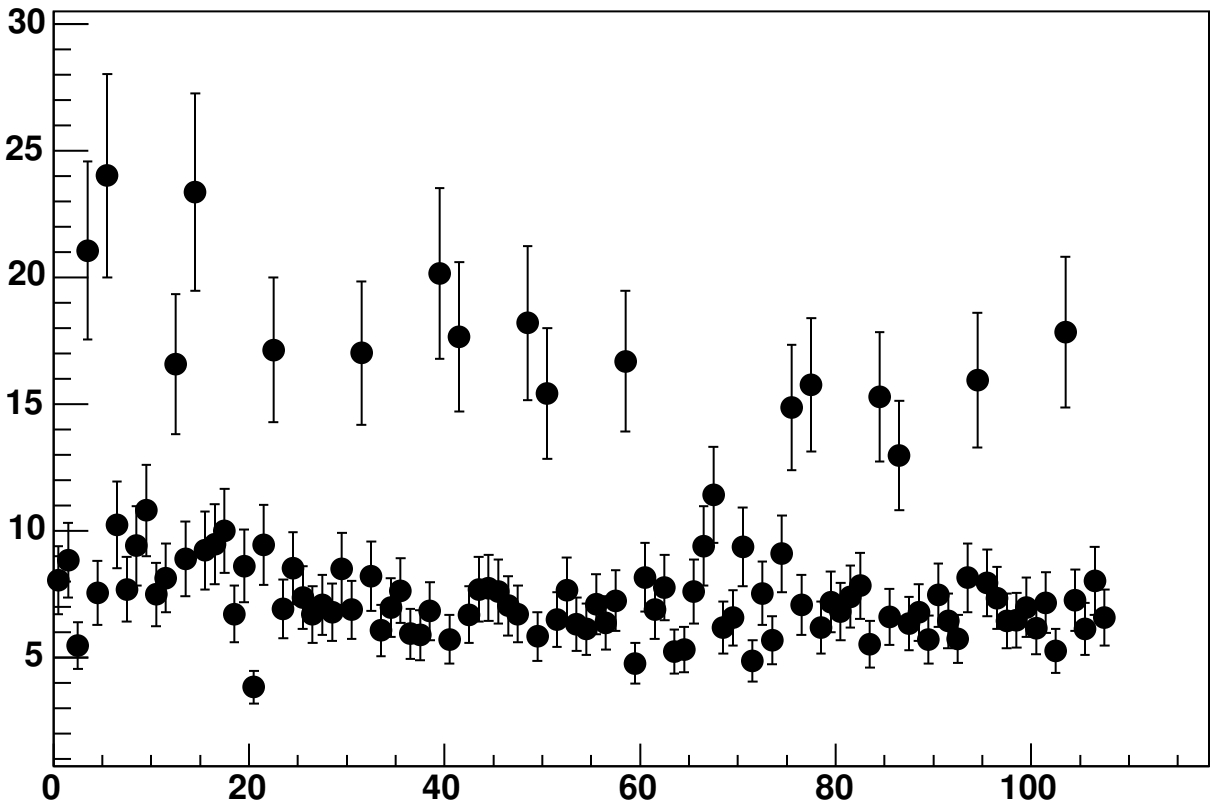
Enable 1, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



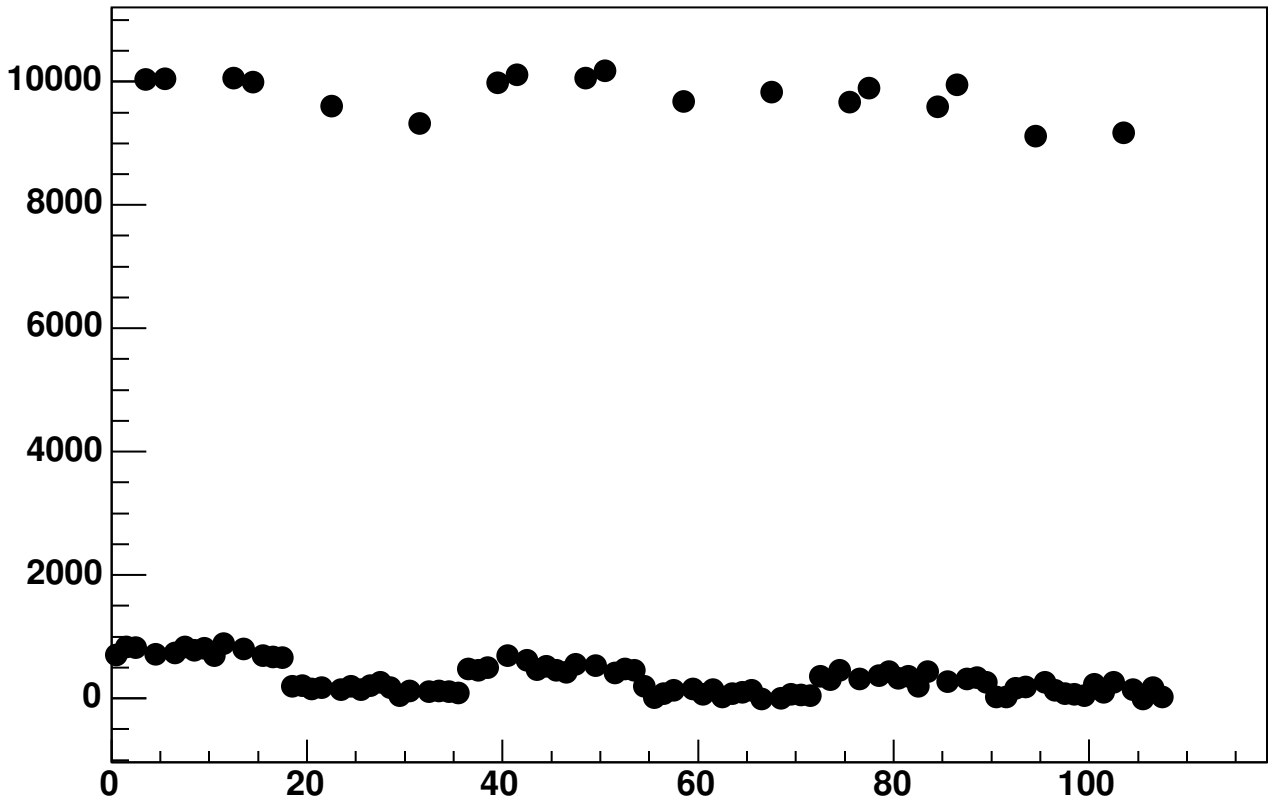
Enable 1, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



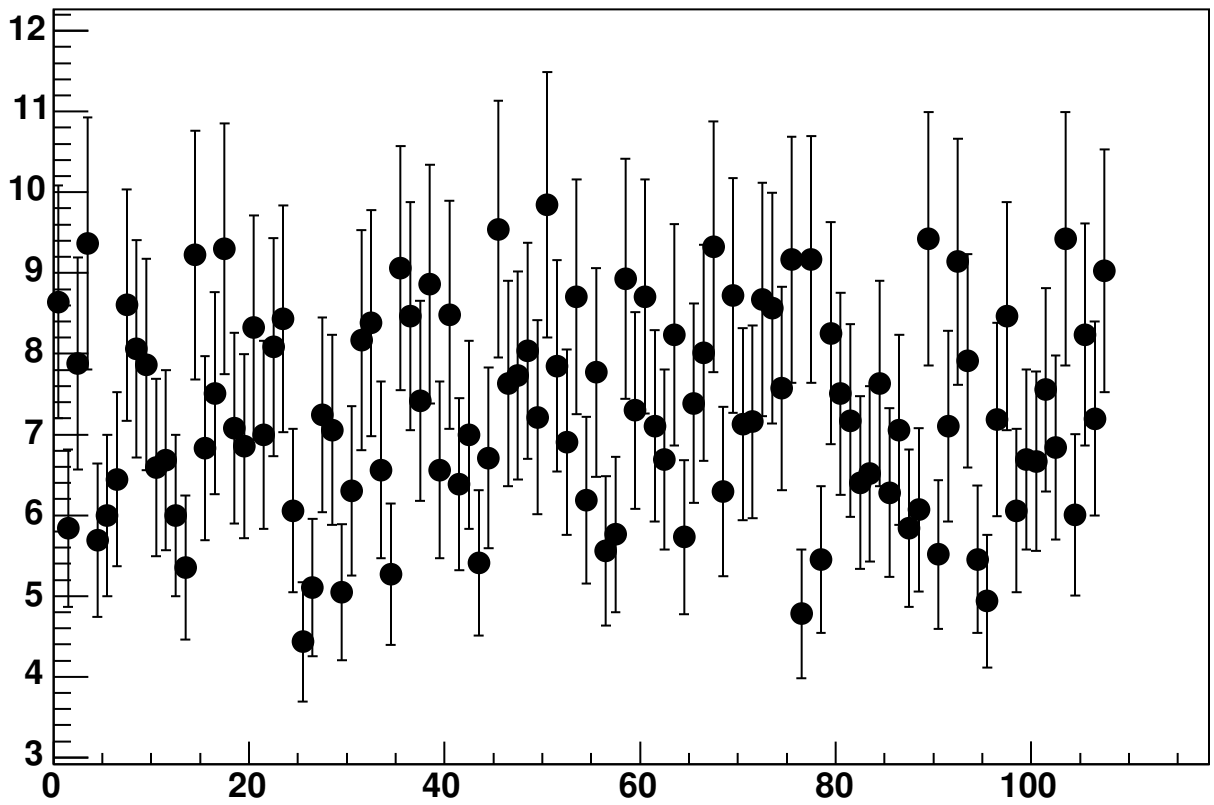
Enable 1, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



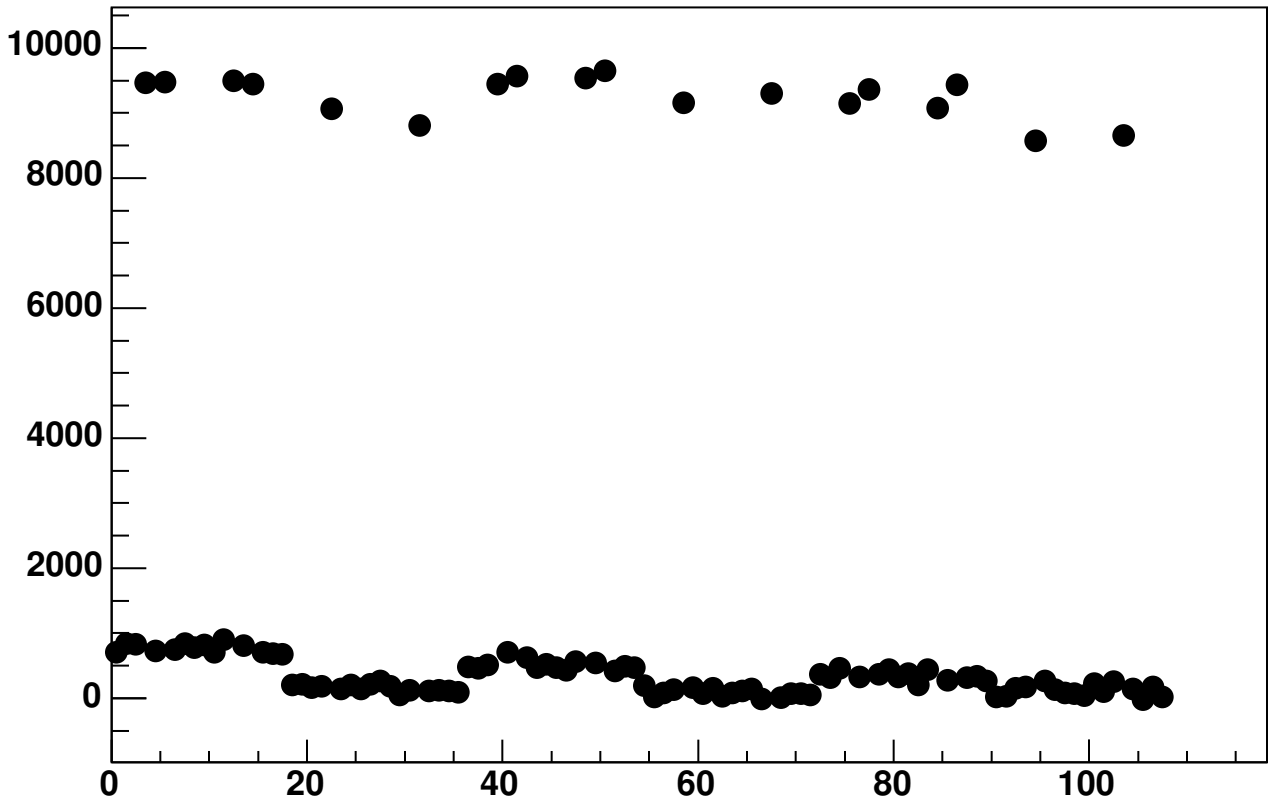
Enable 1, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



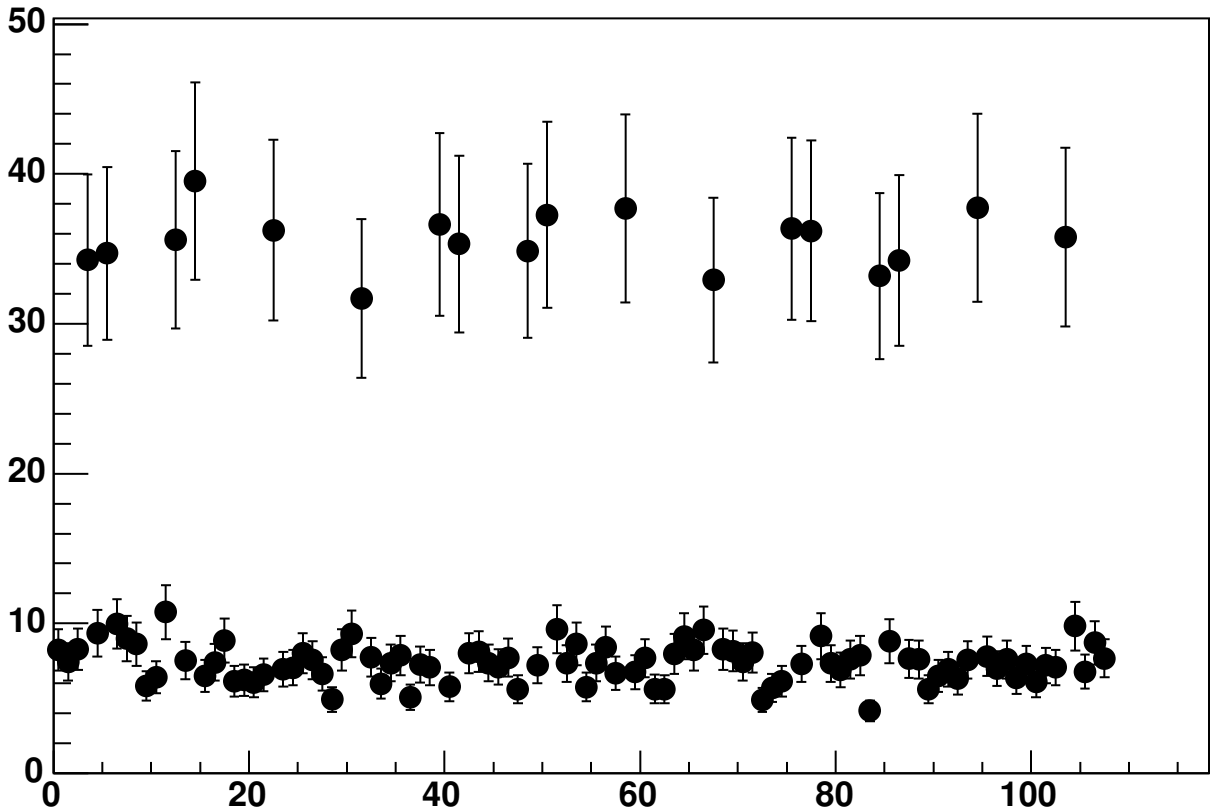
Enable 1, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



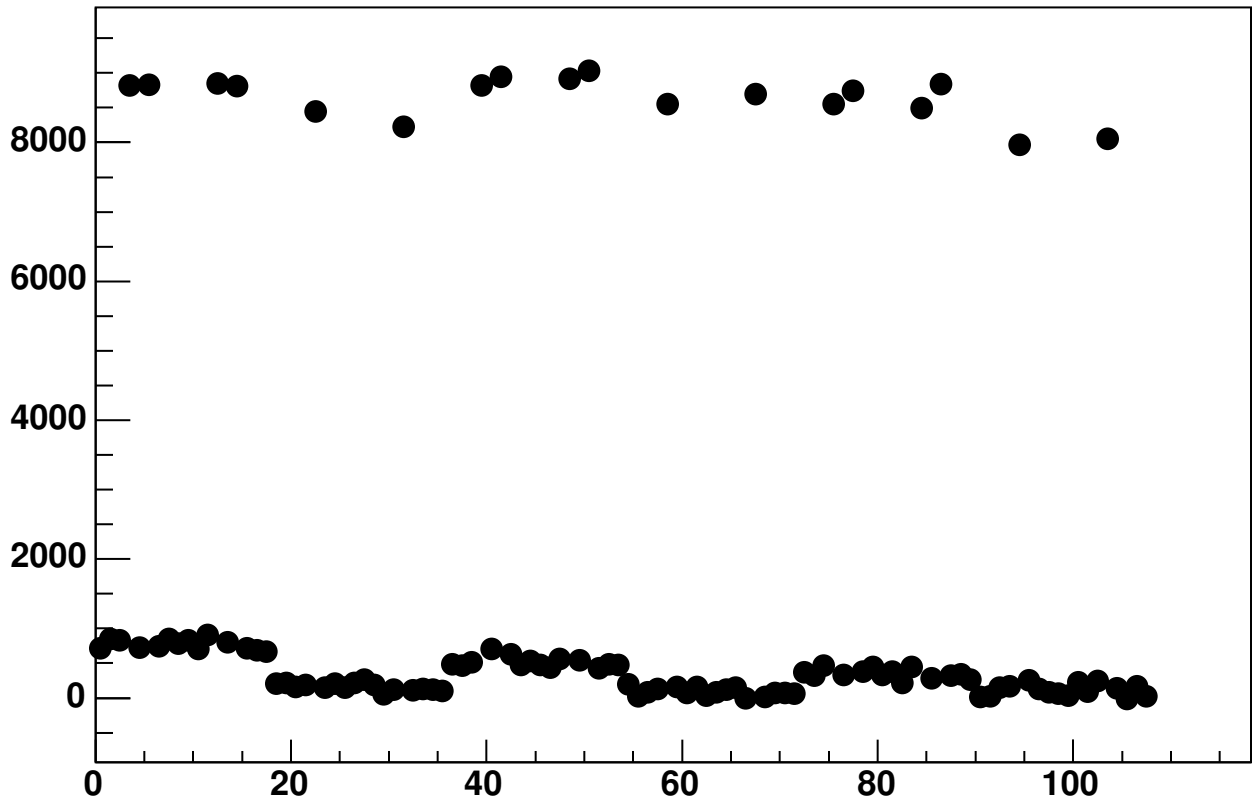
Enable 1, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



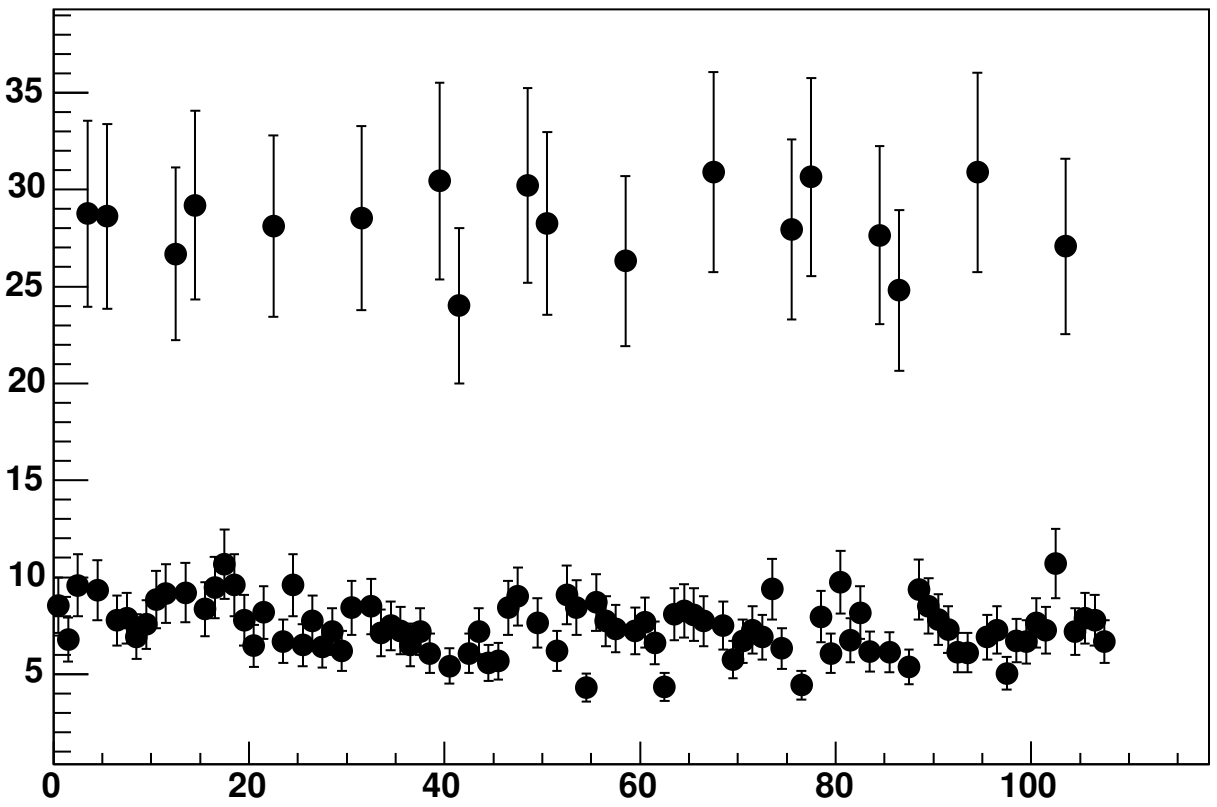
Enable 1, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



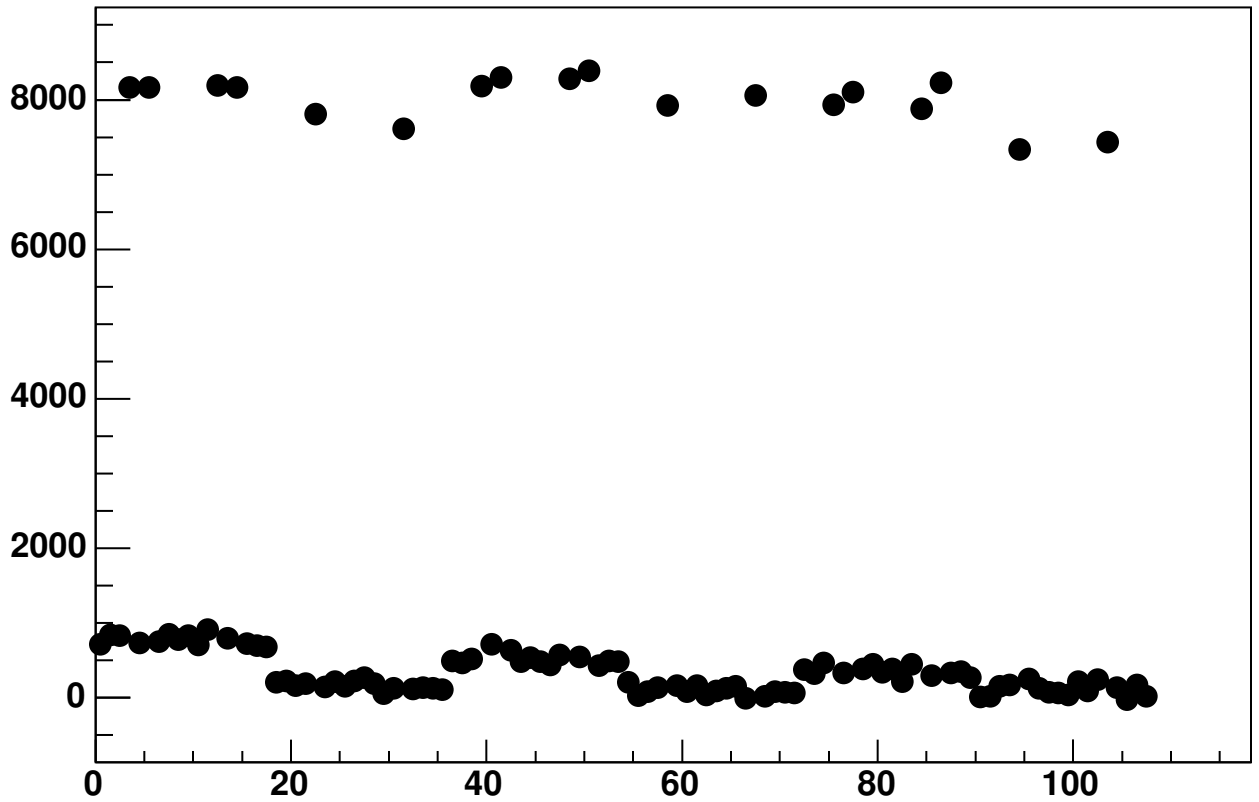
Enable 1, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



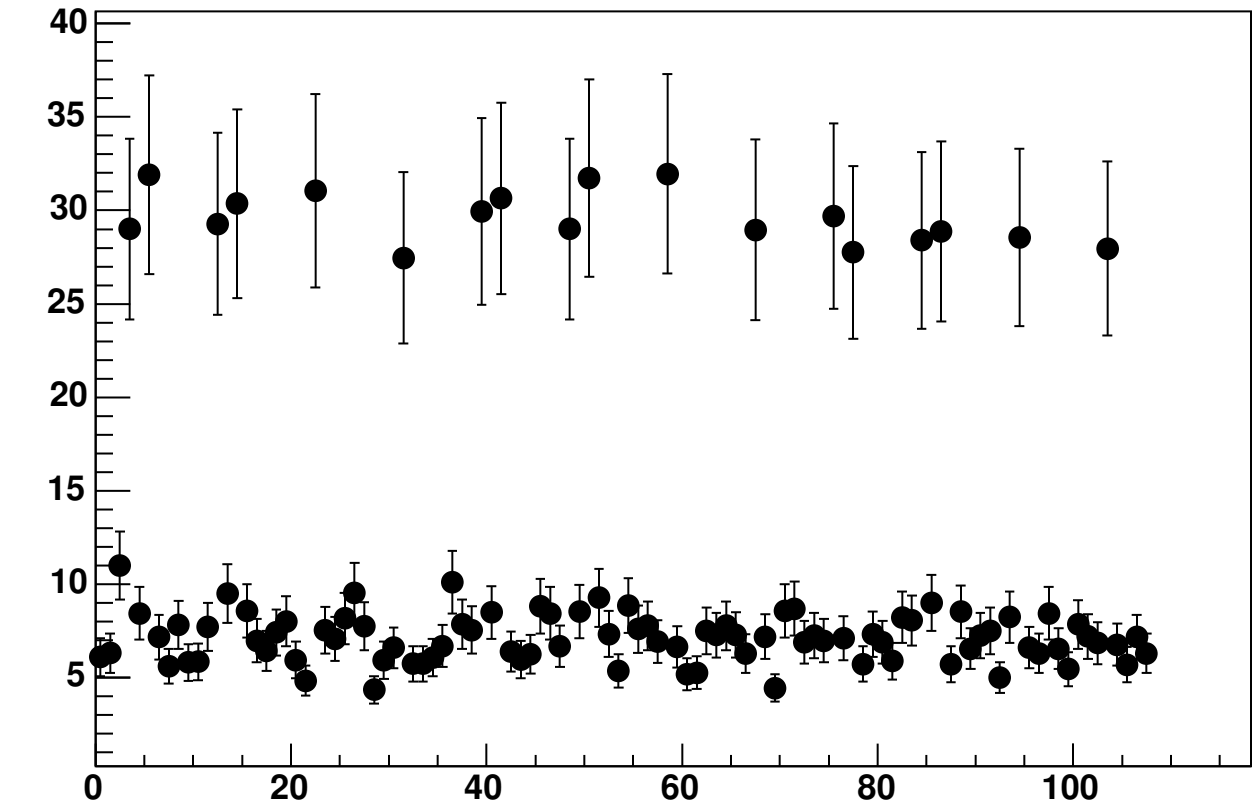
Enable 1, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



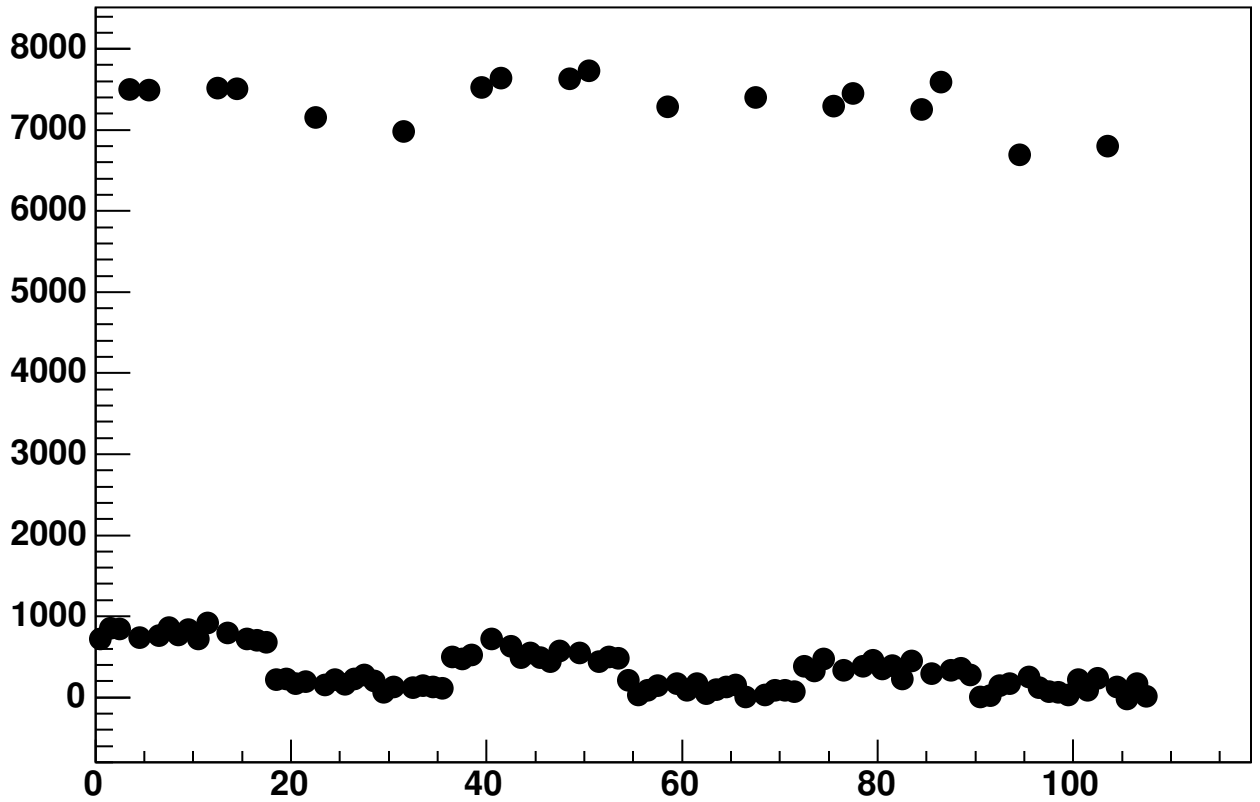
Enable 1, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



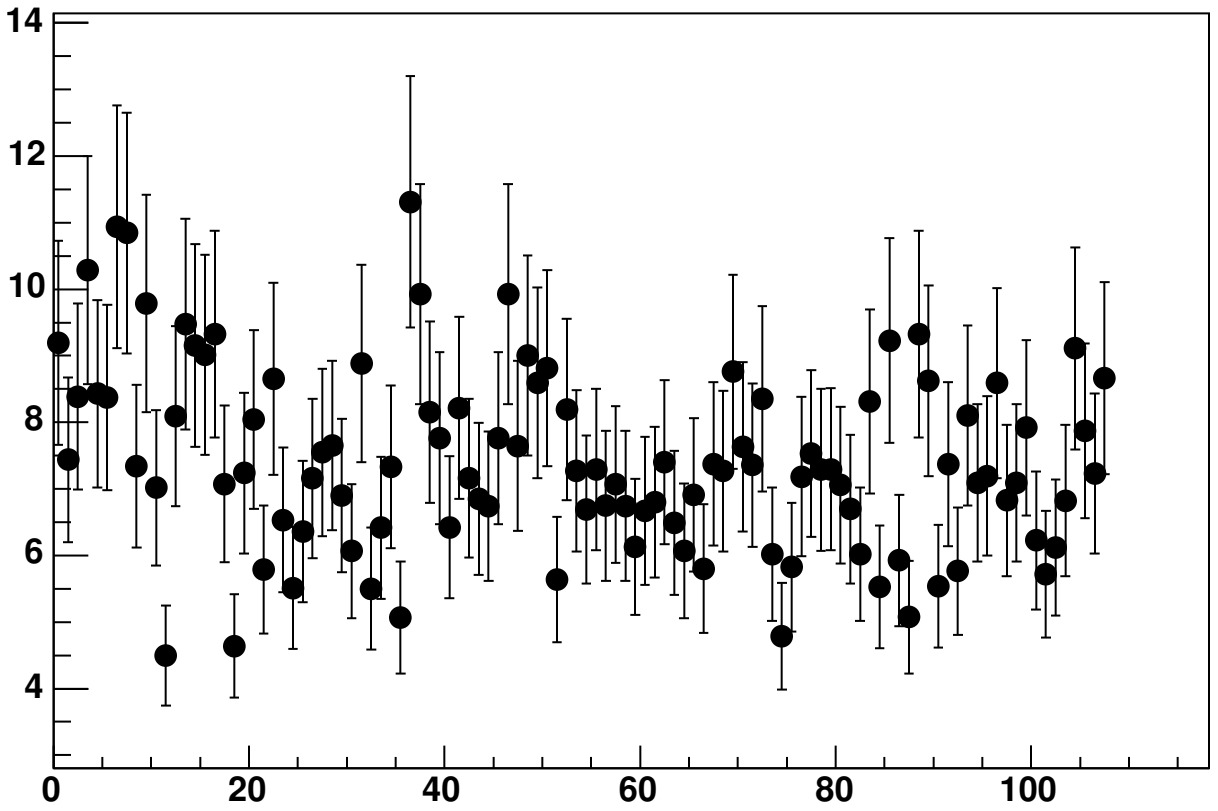
Enable 1, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



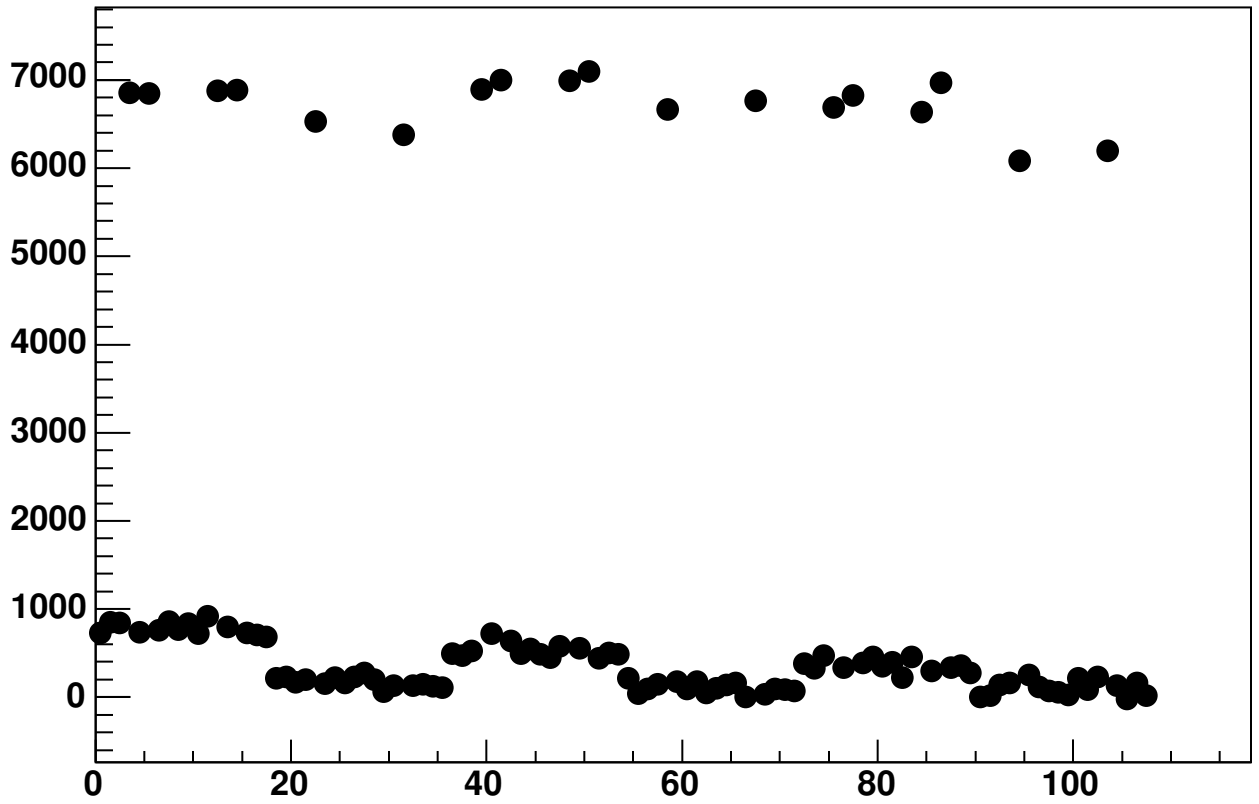
Enable 1, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



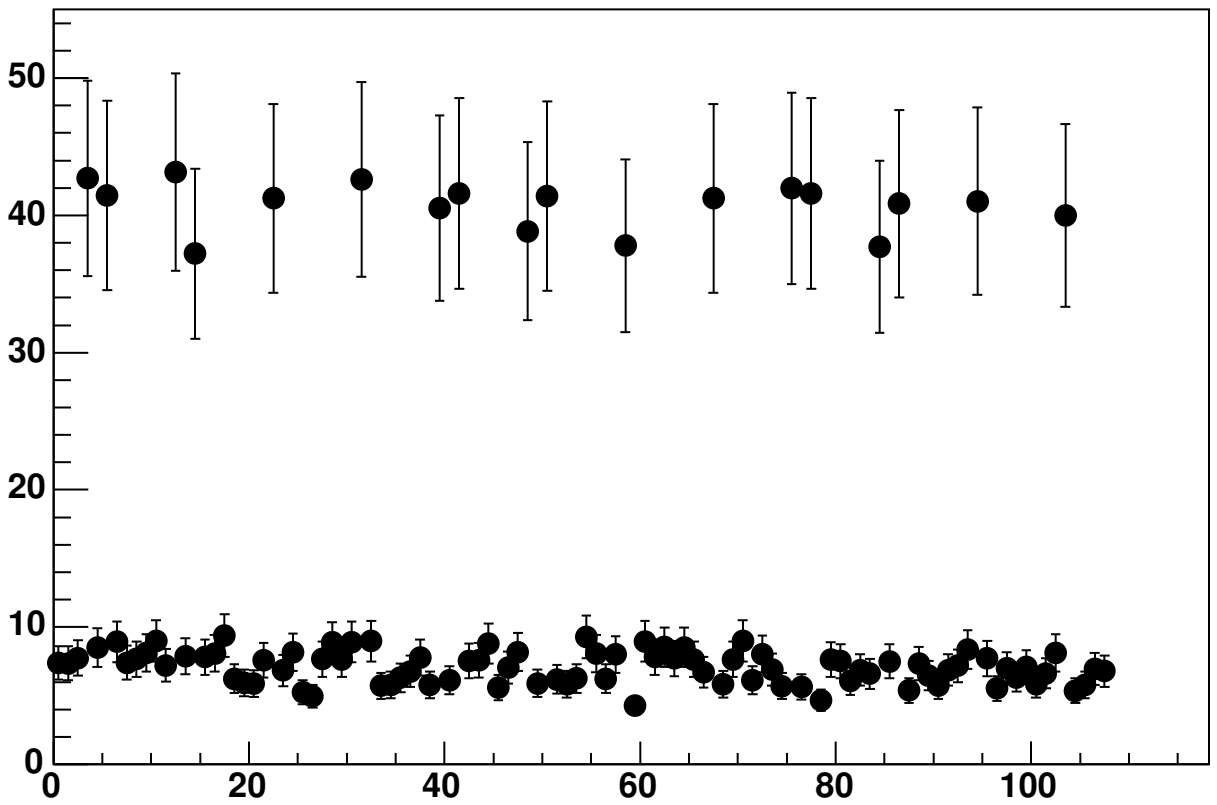
Enable 1, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



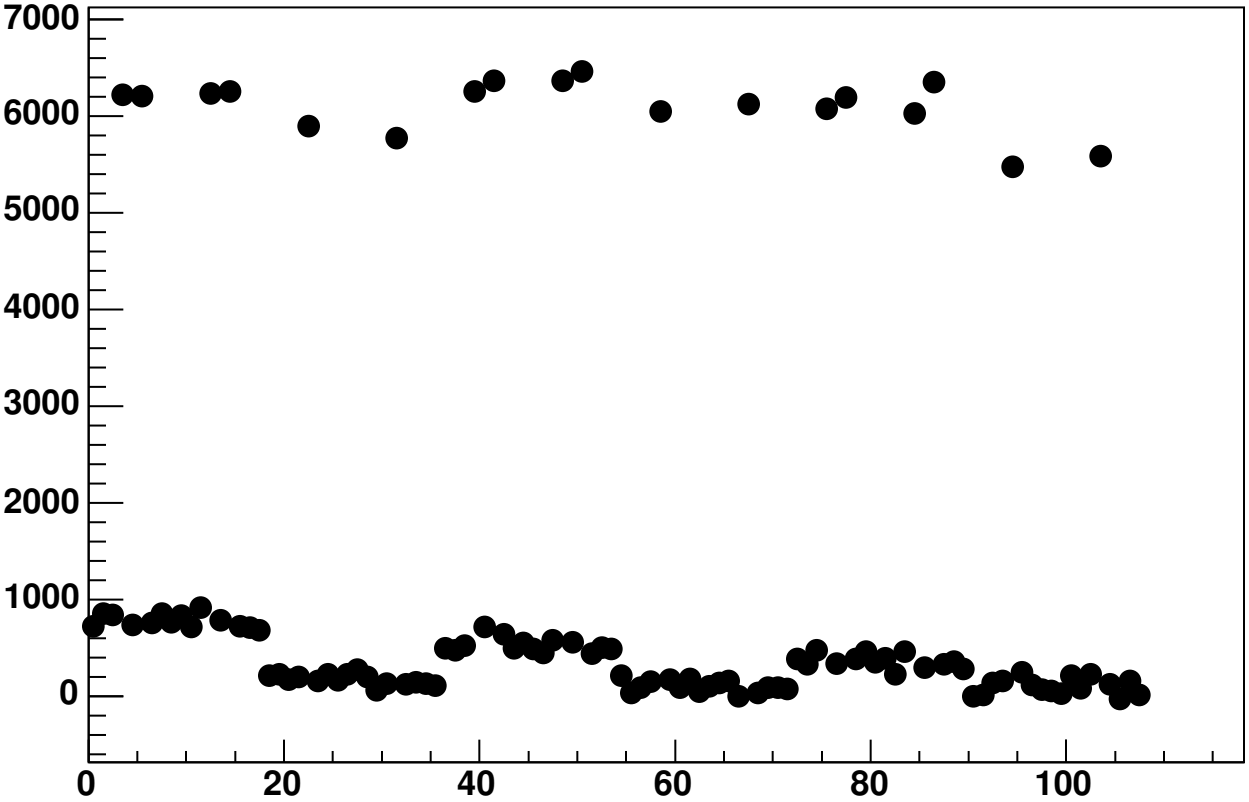
Enable 1, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



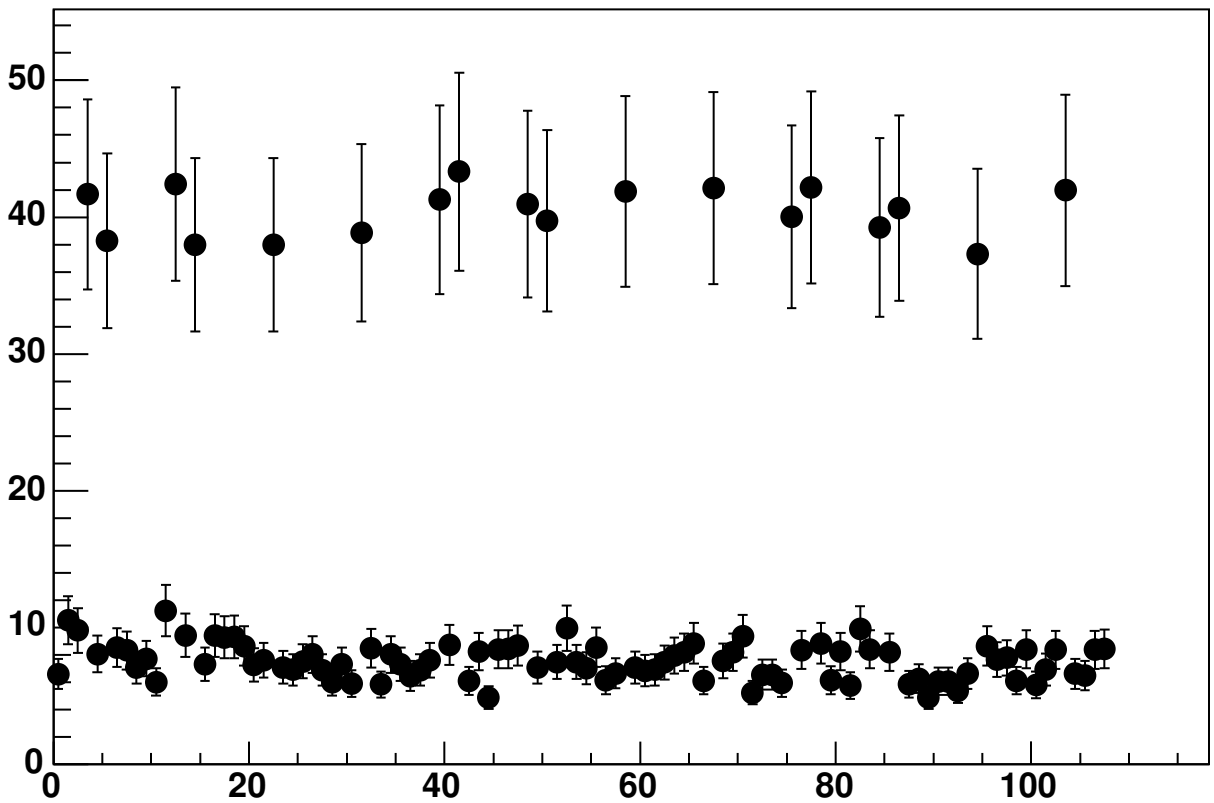
Enable 1, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



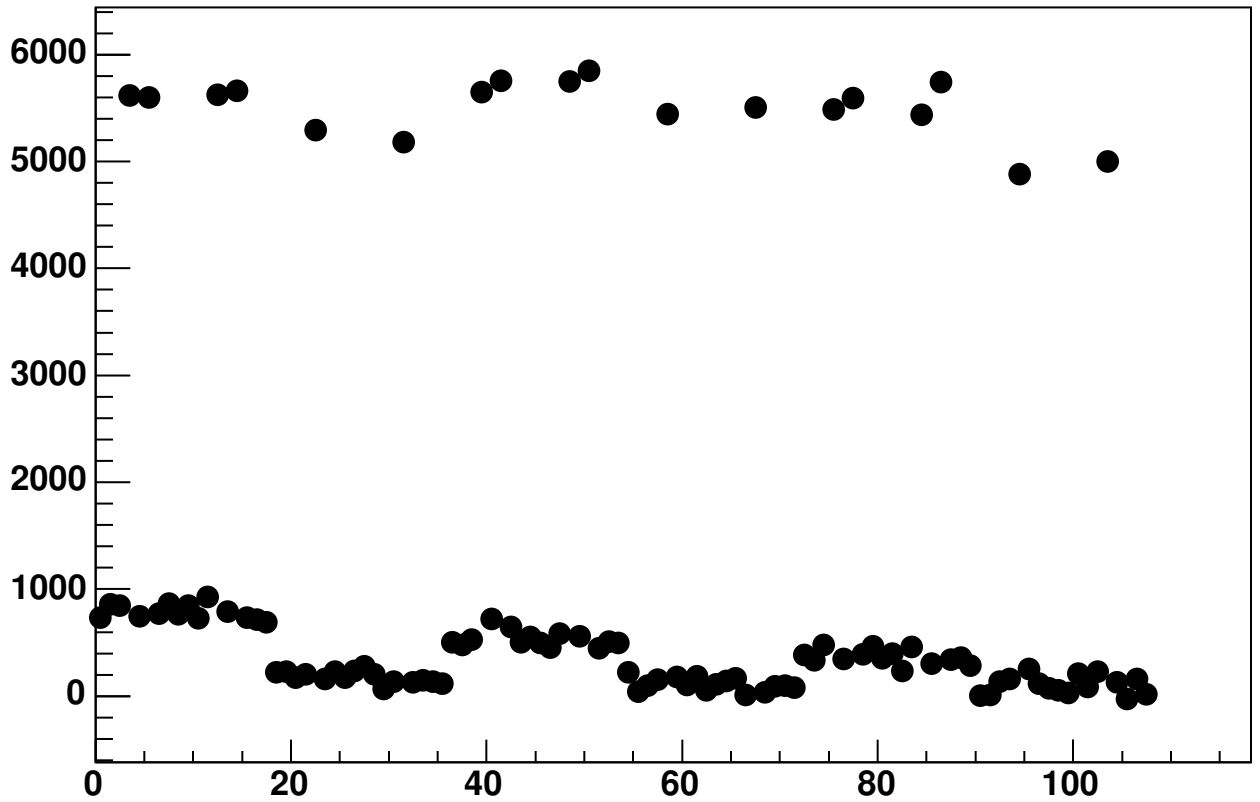
Enable 1, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



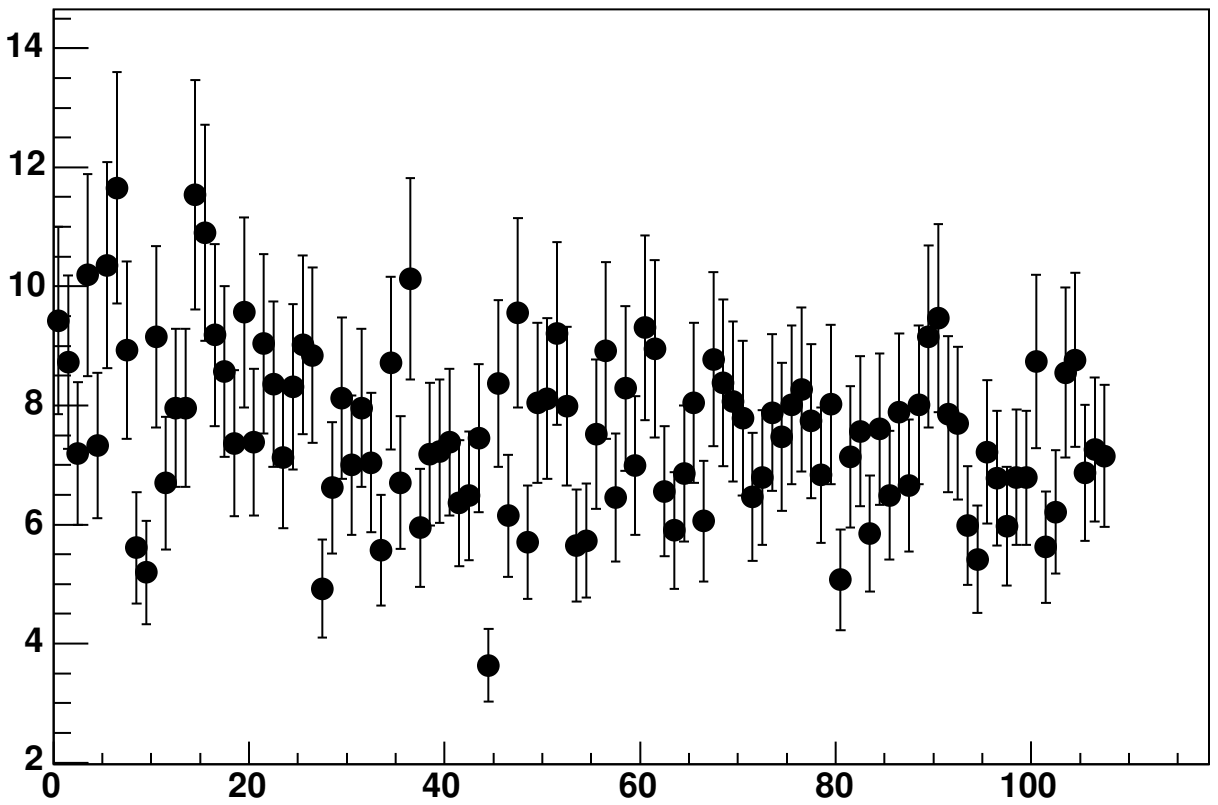
Enable 1, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



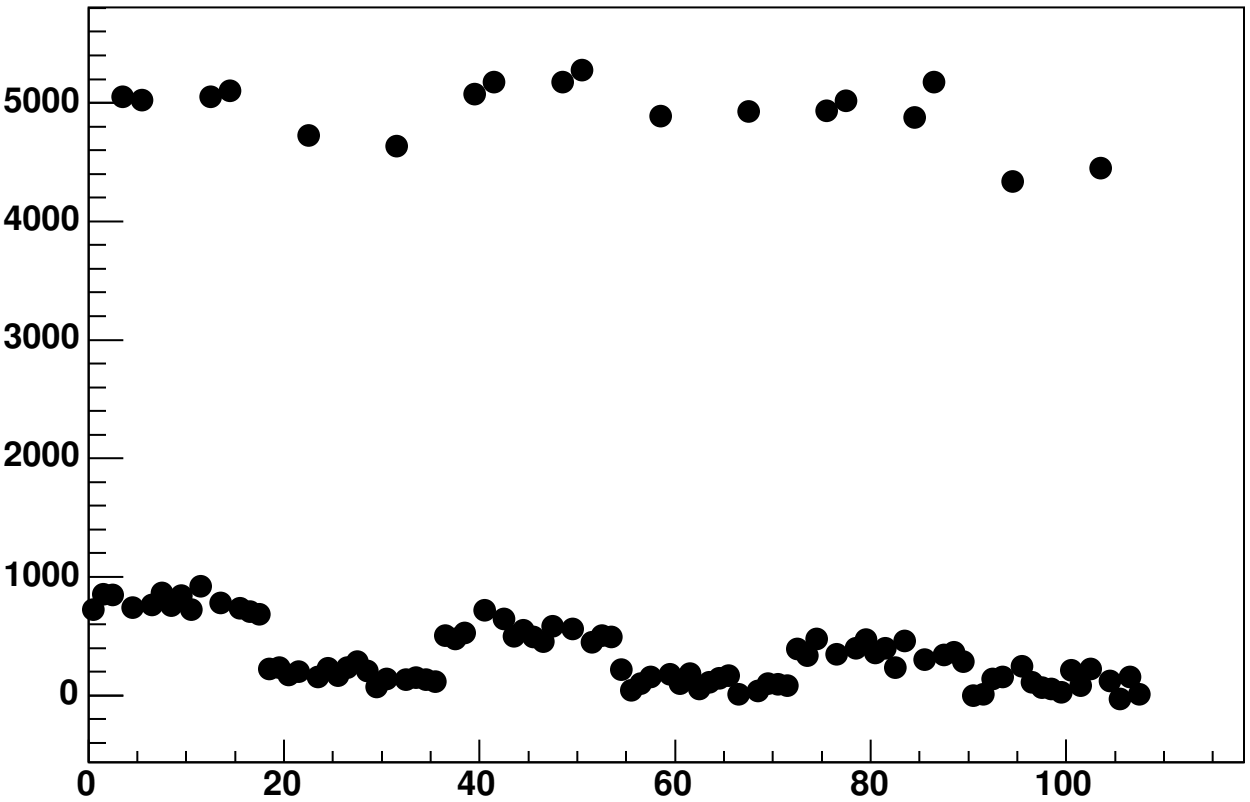
Enable 1, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



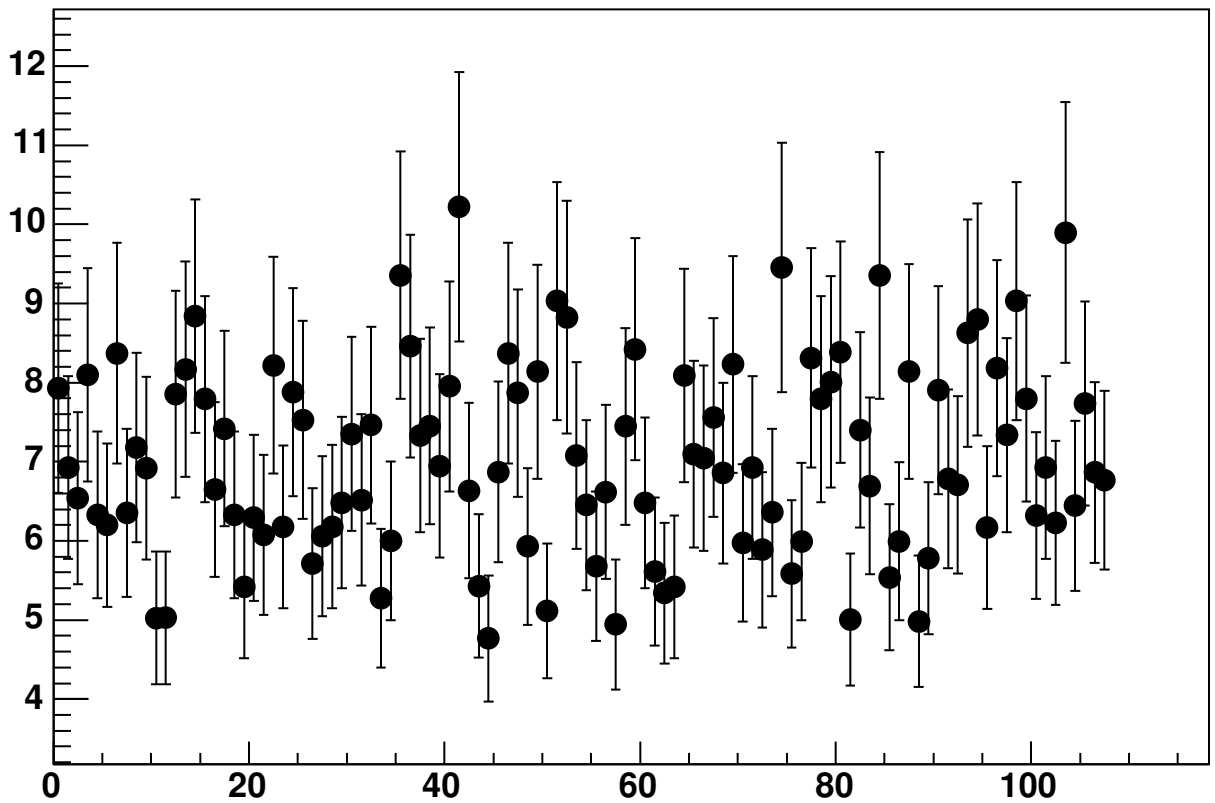
Enable 1, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



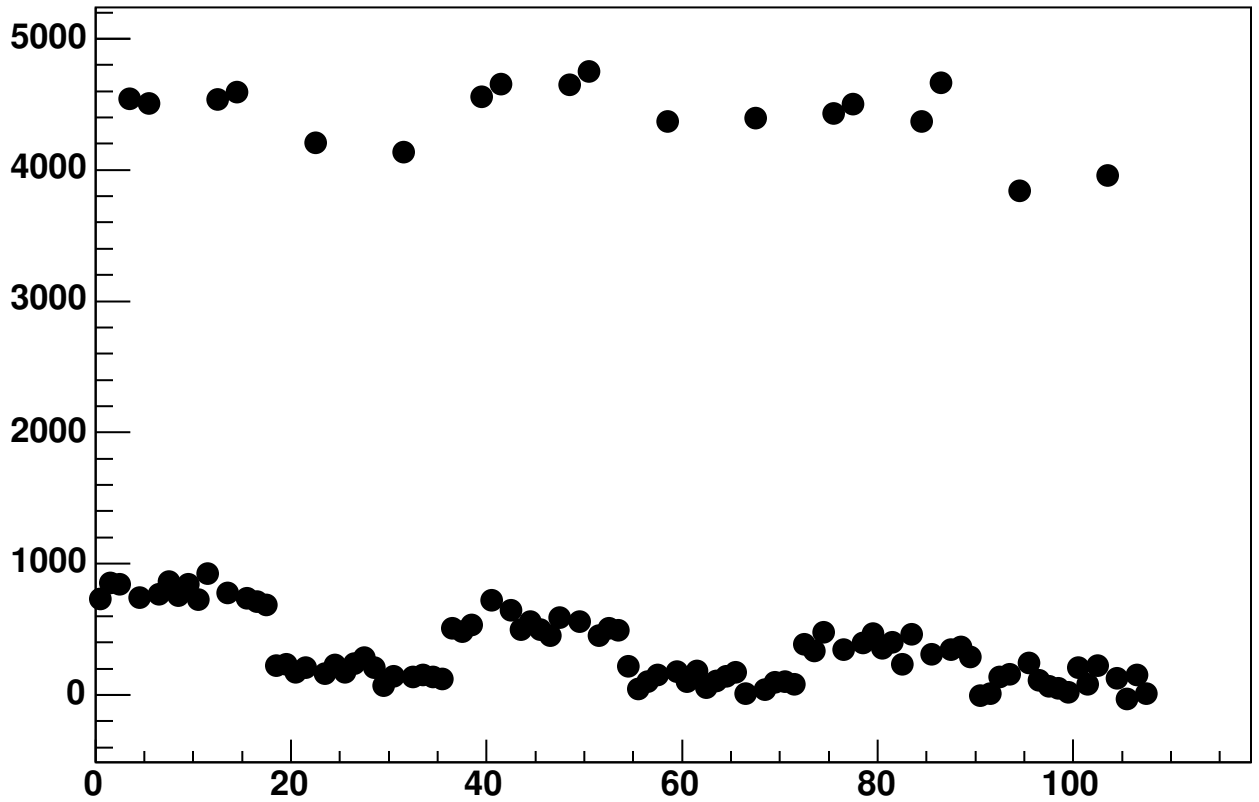
Enable 1, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



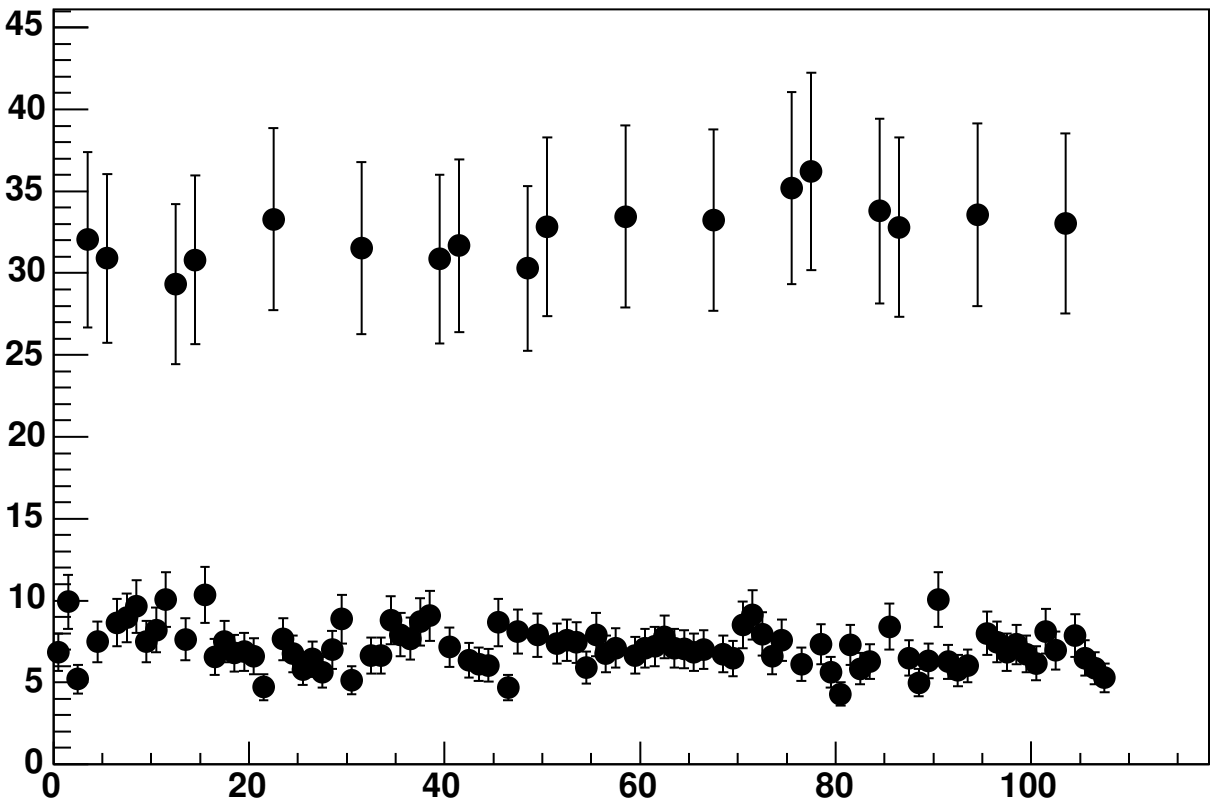
Enable 1, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



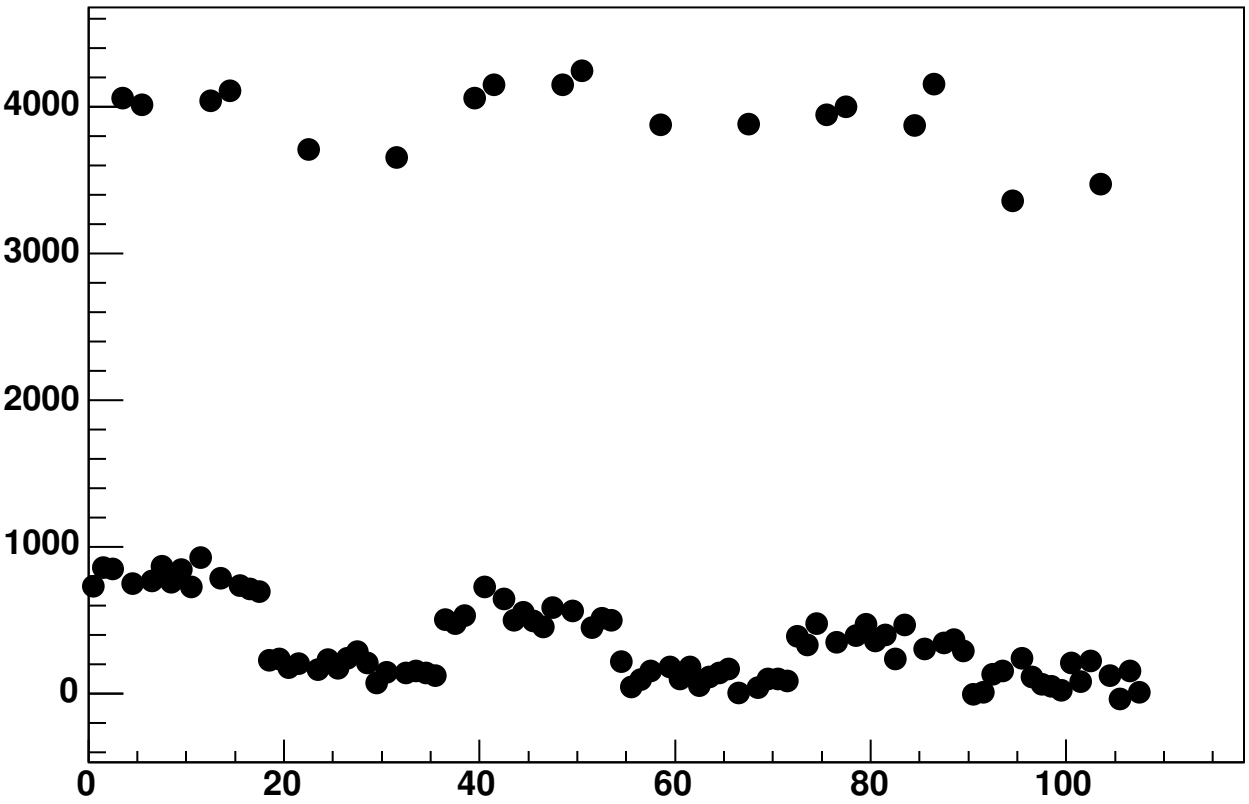
Enable 1, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



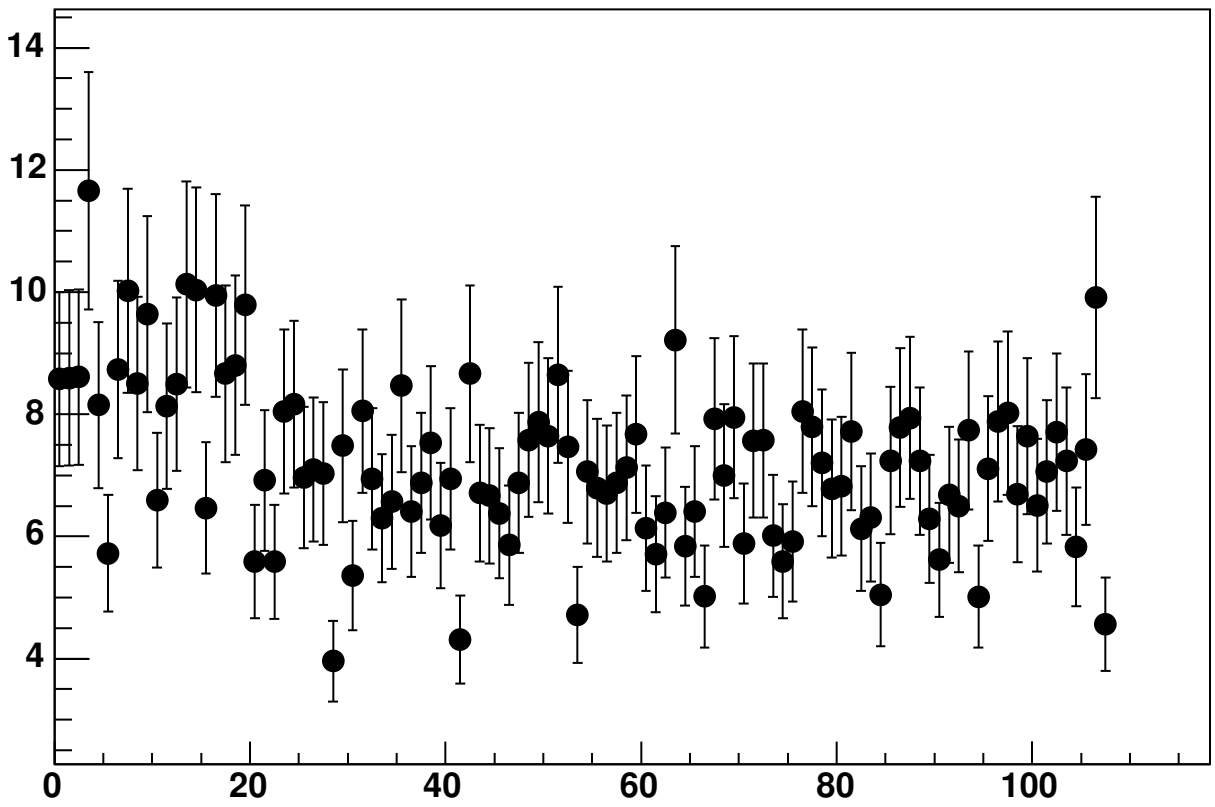
Enable 1, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



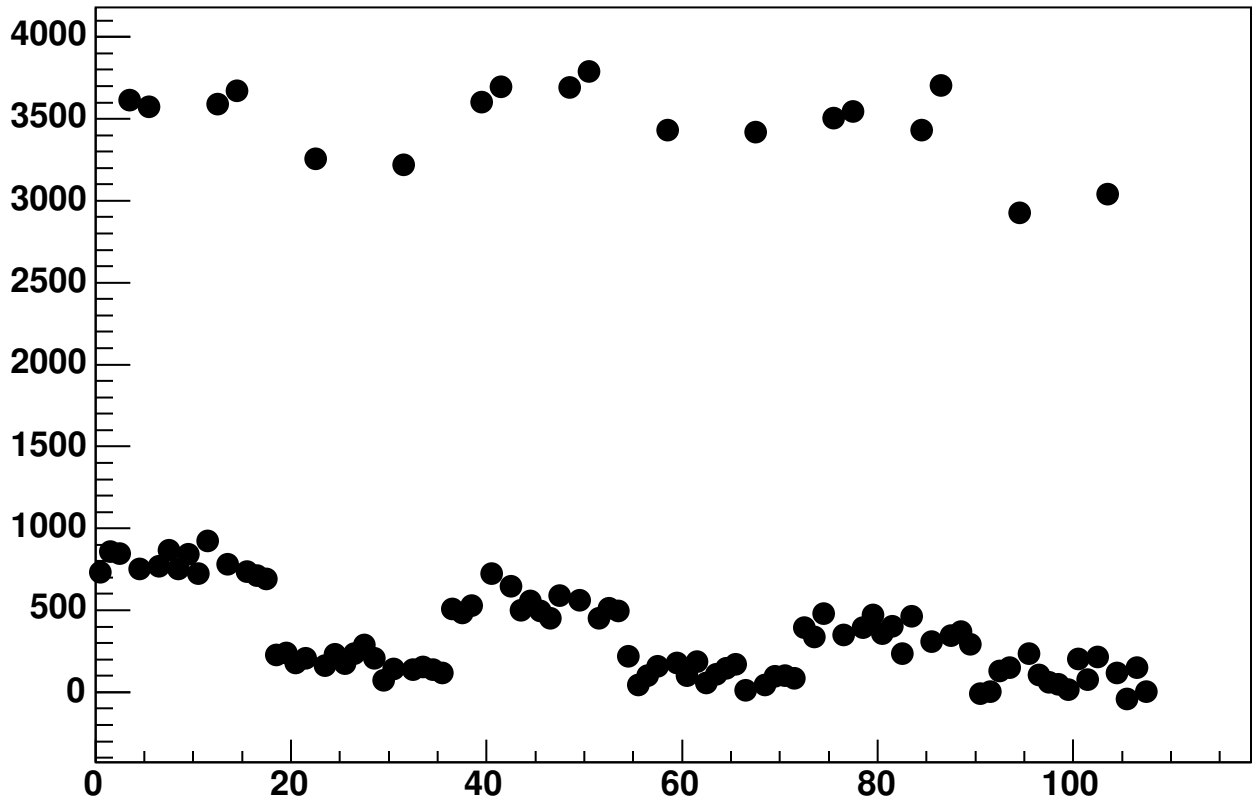
Enable 1, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



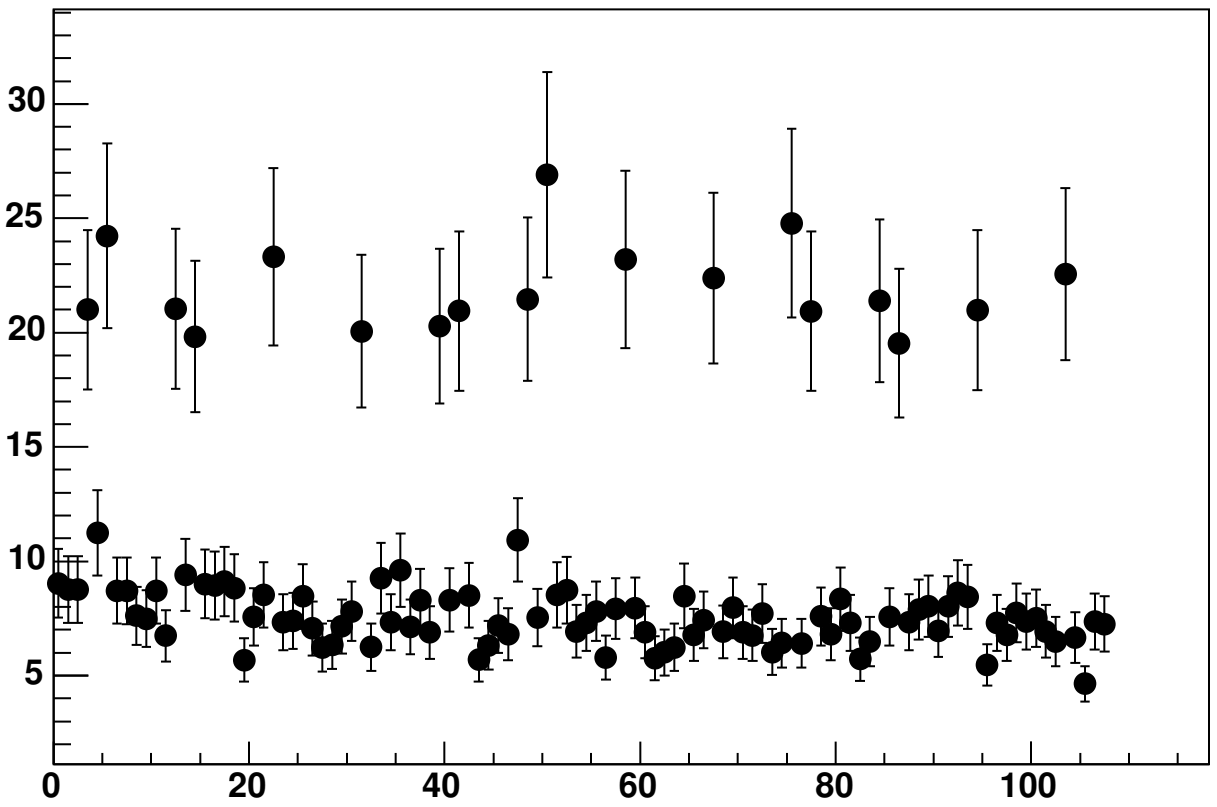
Enable 1, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



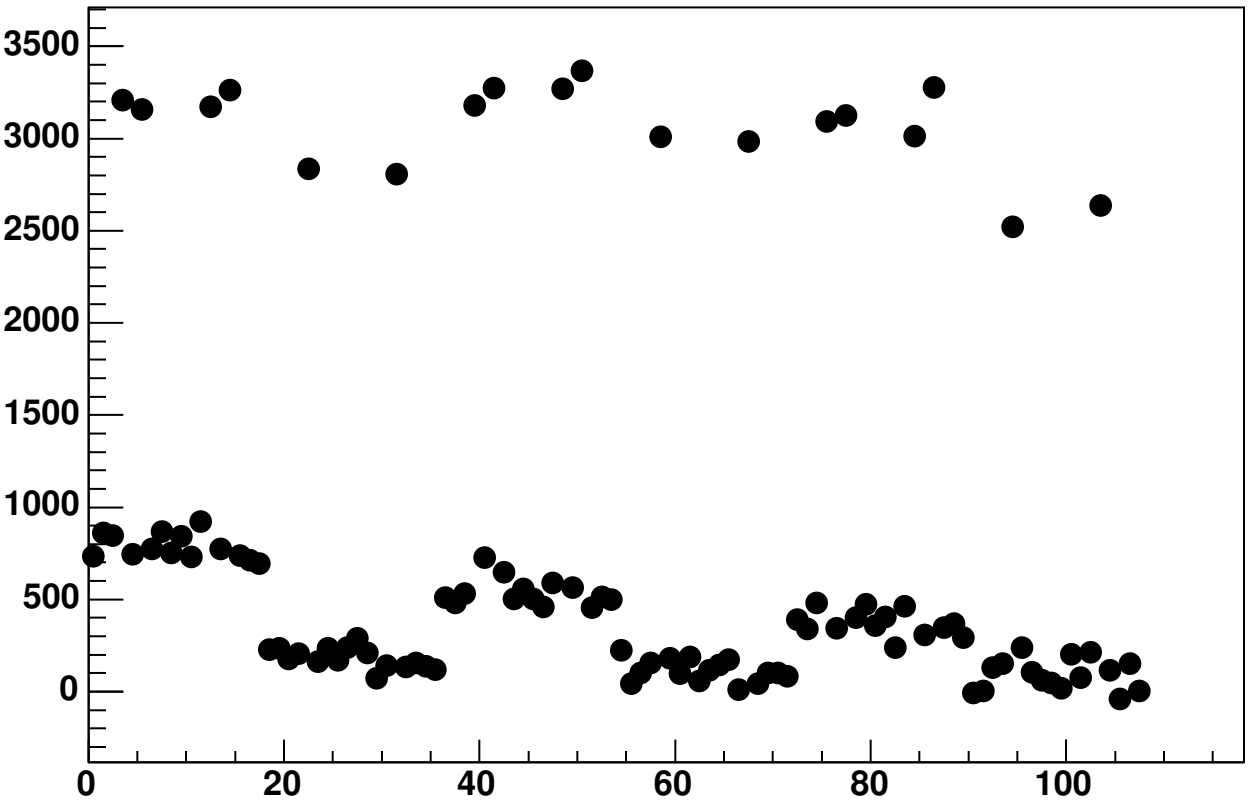
Enable 1, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



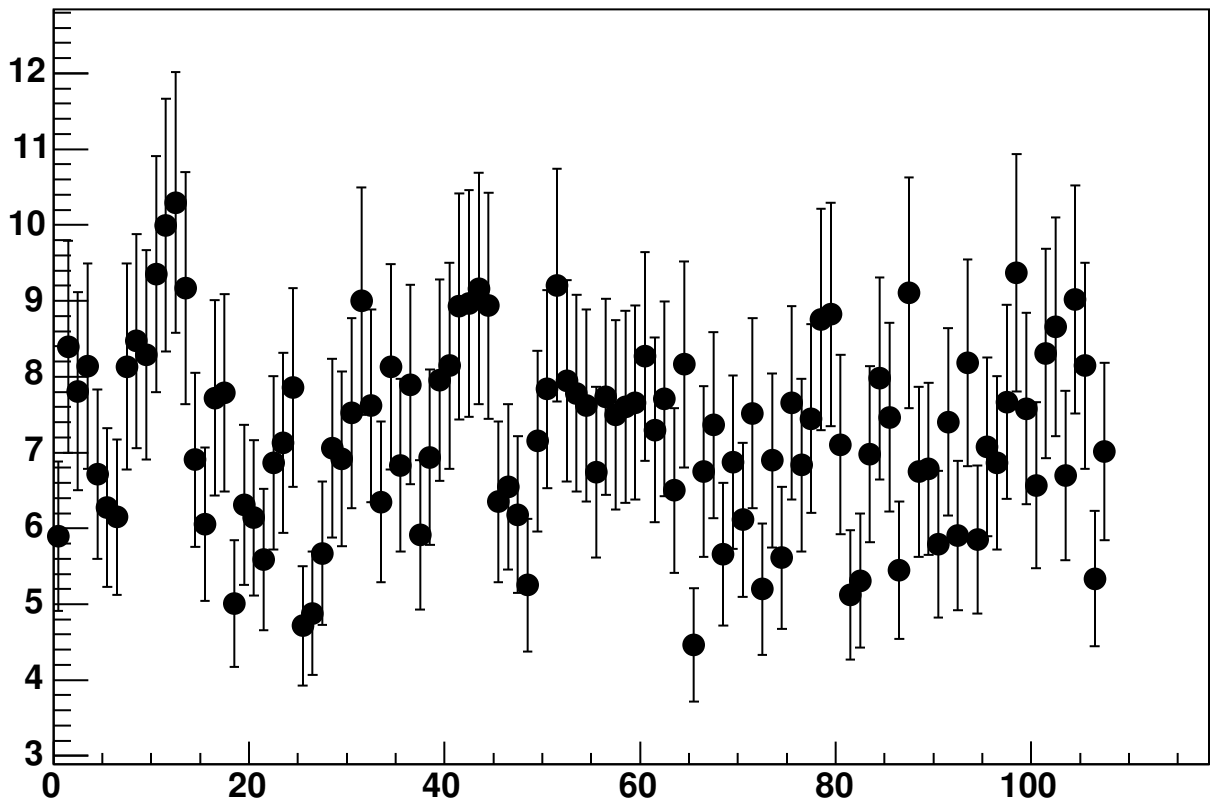
Enable 1, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



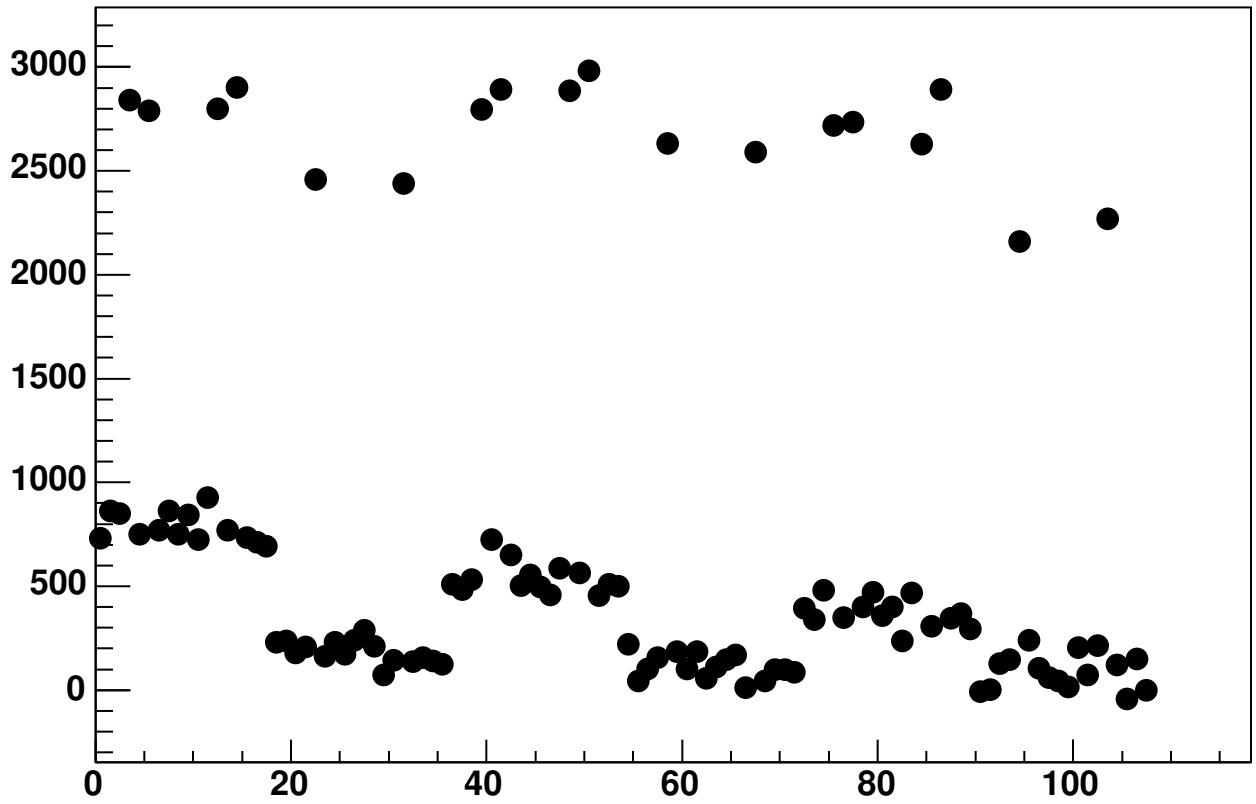
Enable 1, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



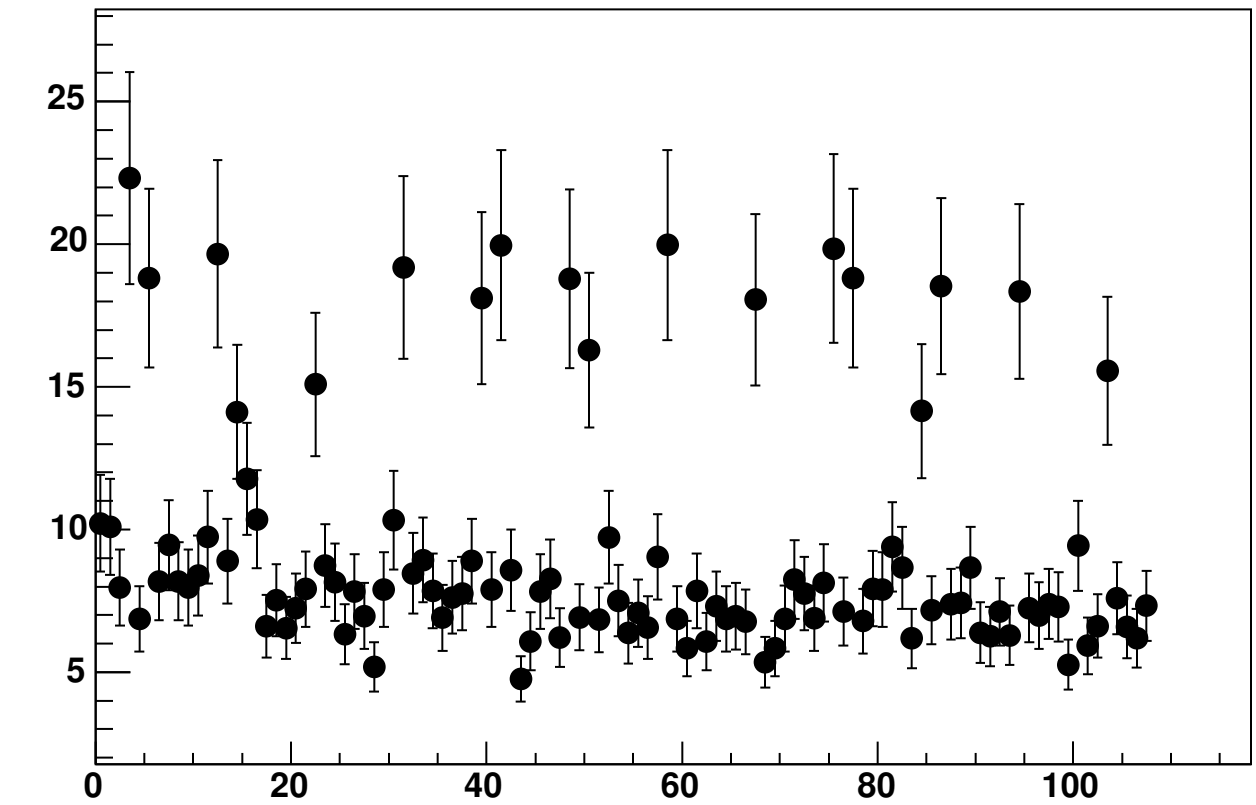
Enable 1, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



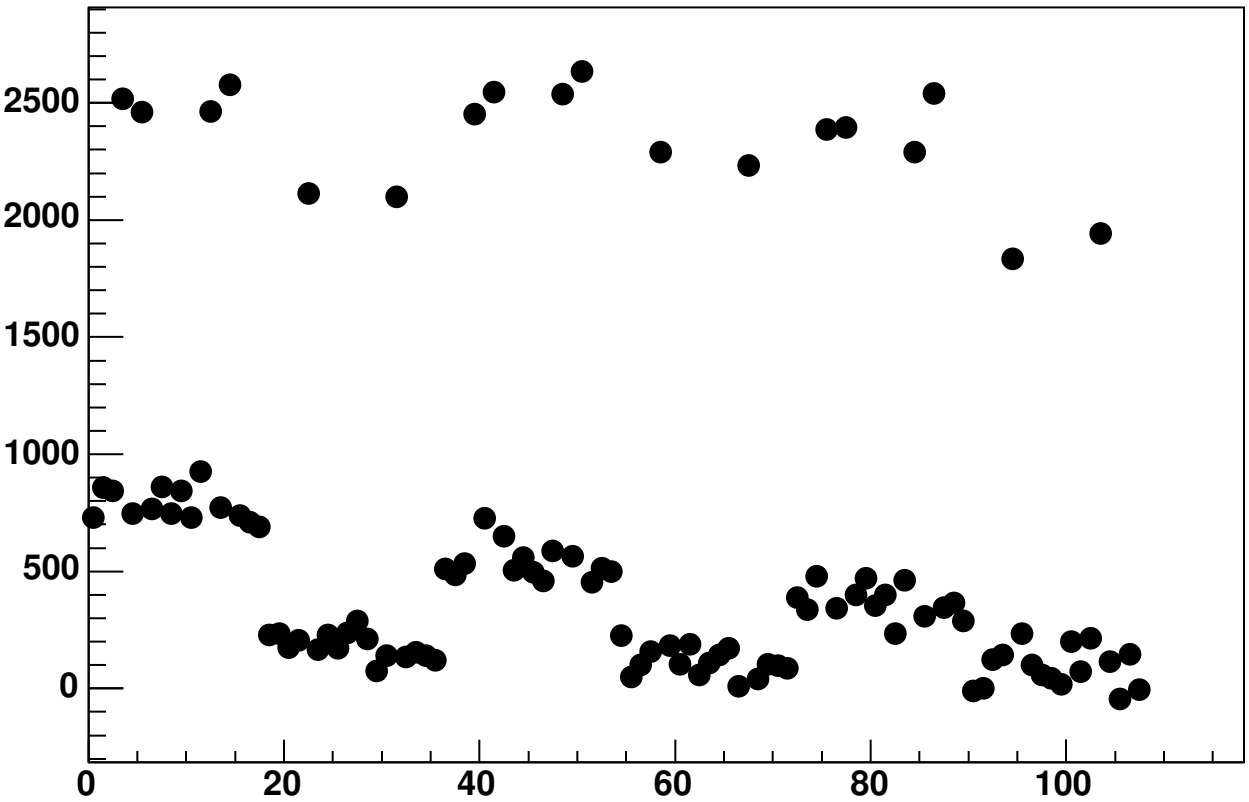
Enable 1, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



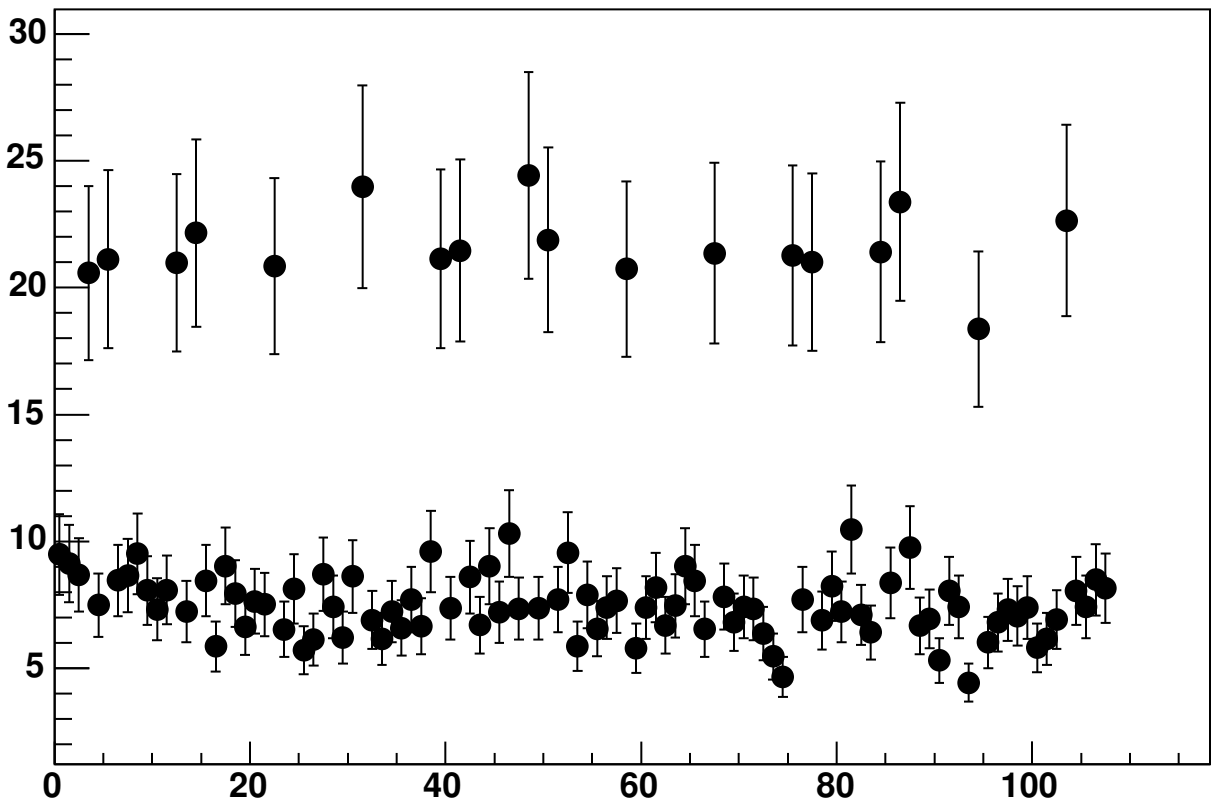
Enable 1, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



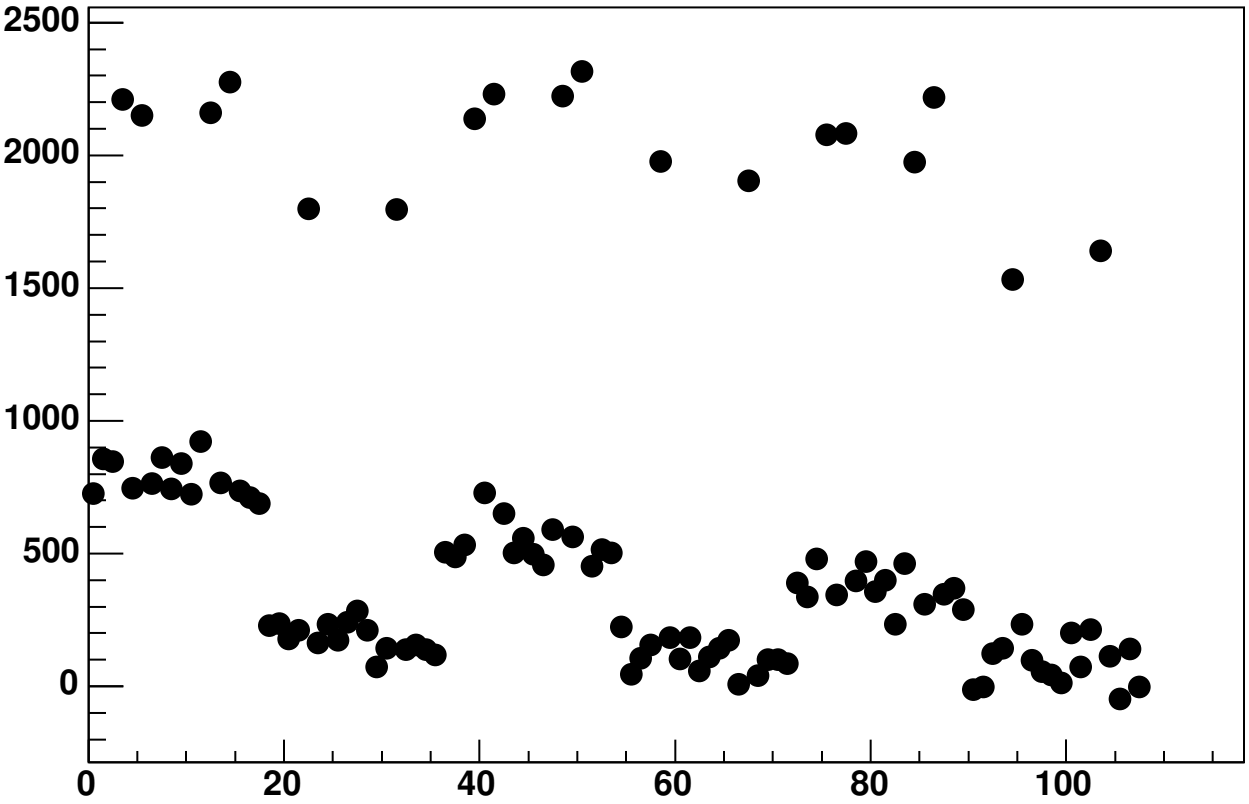
Enable 1, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



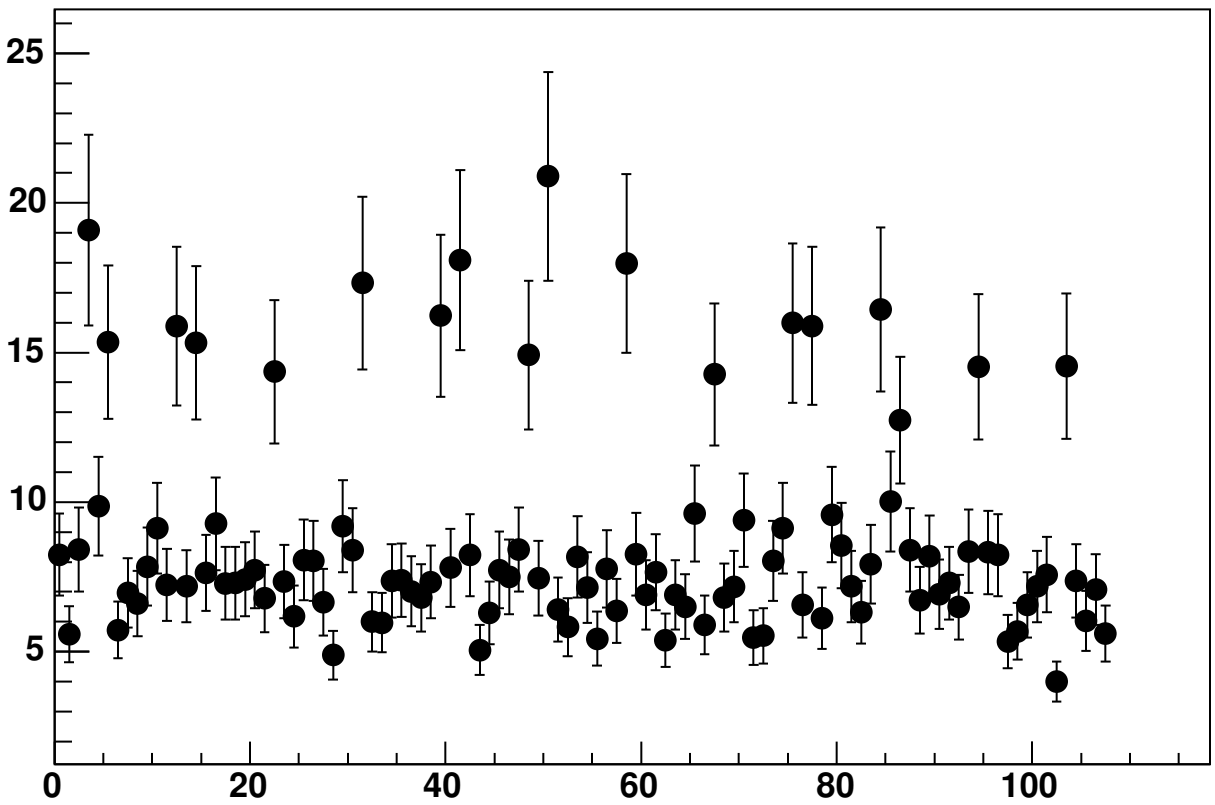
Enable 1, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



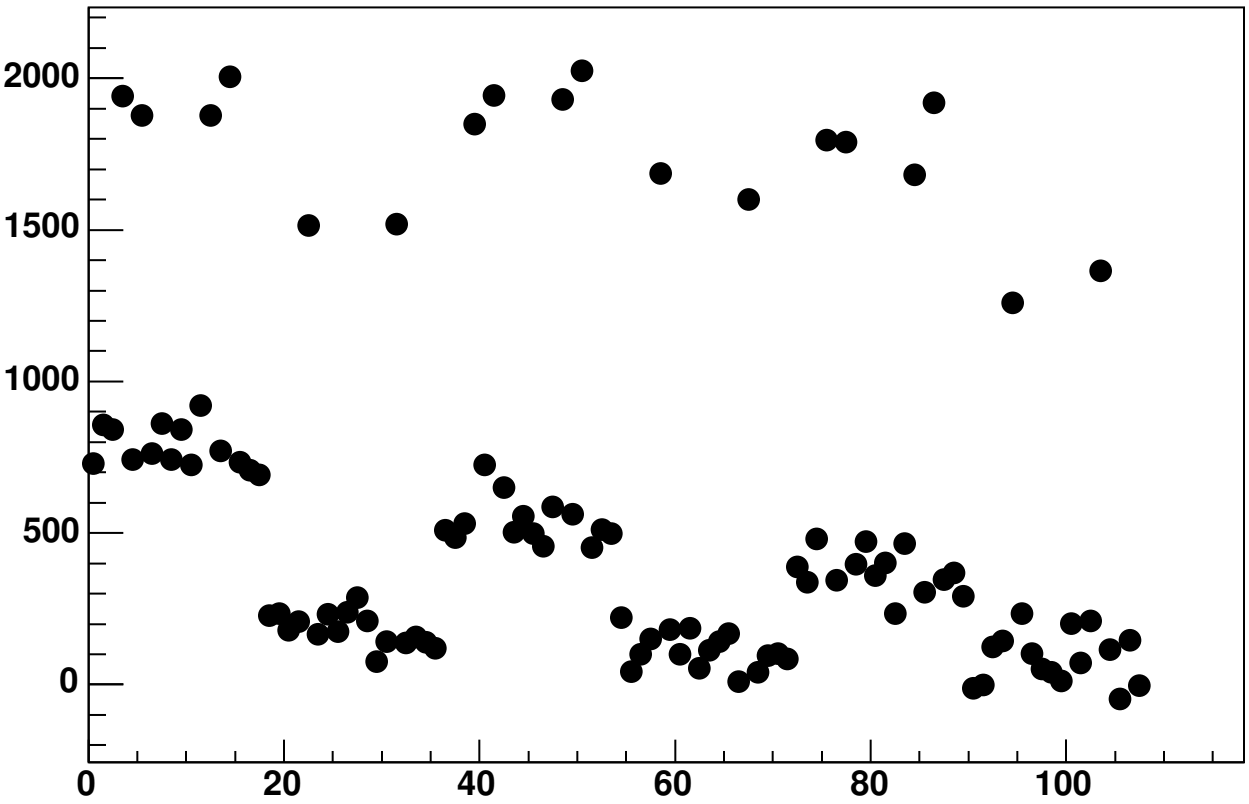
Enable 1, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



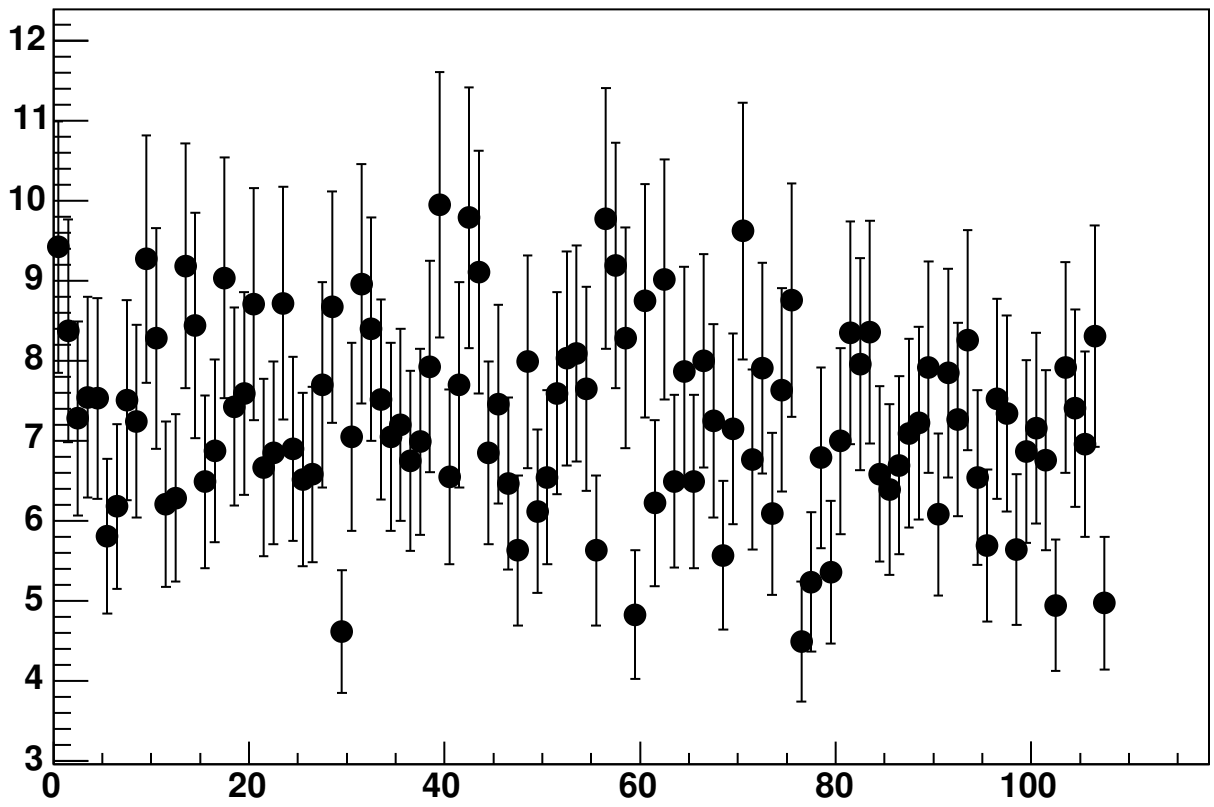
Enable 1, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



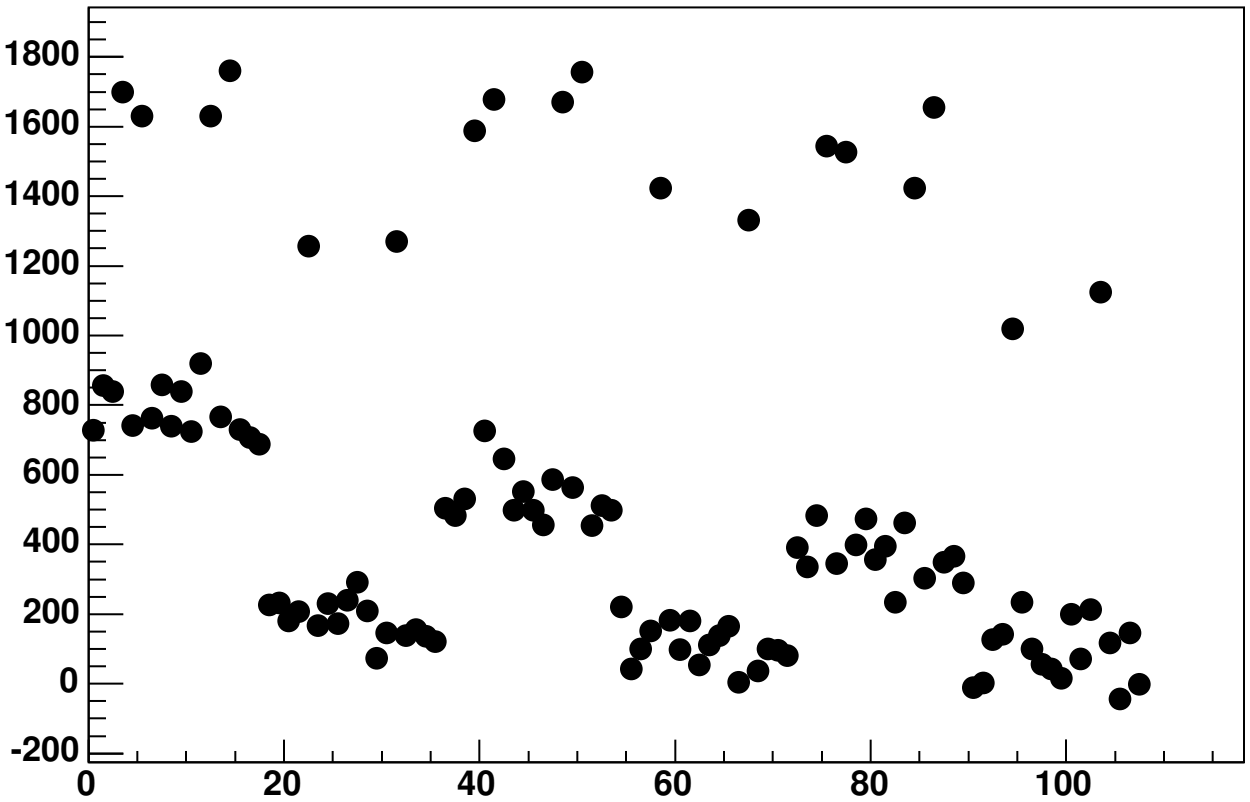
Enable 1, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



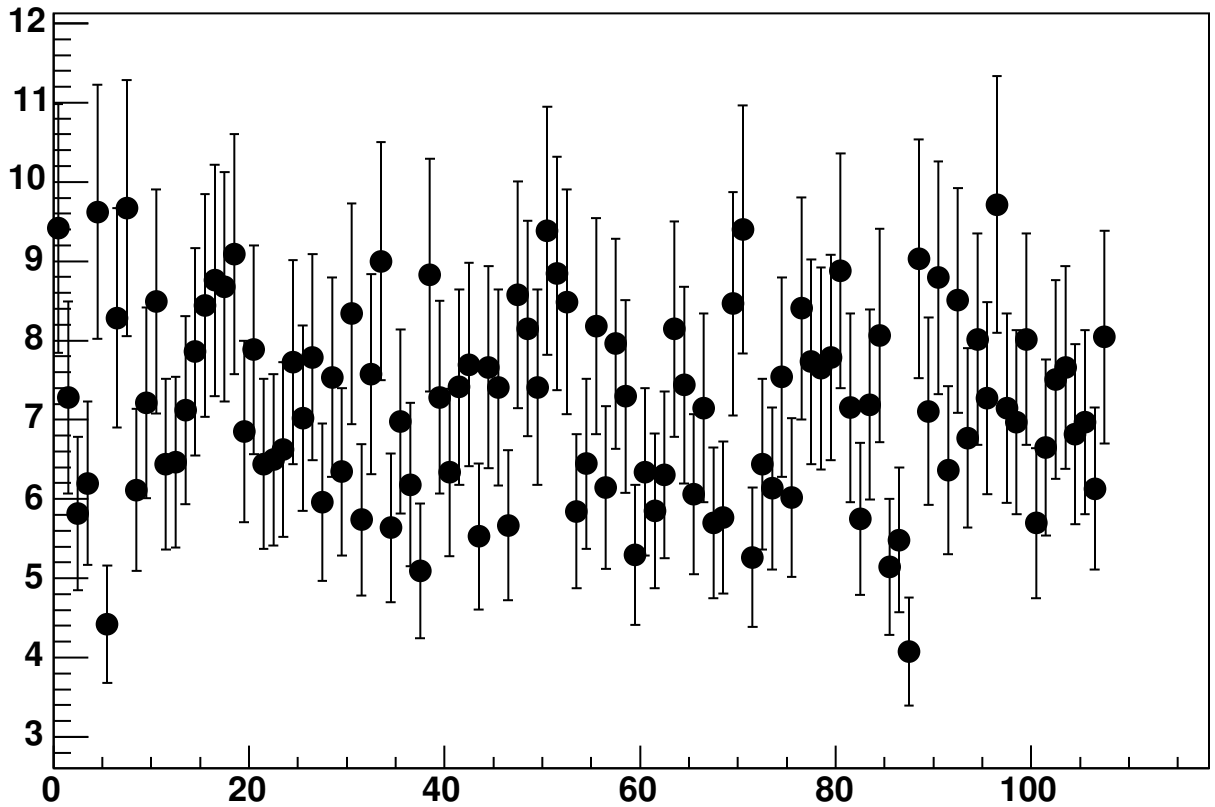
Enable 1, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



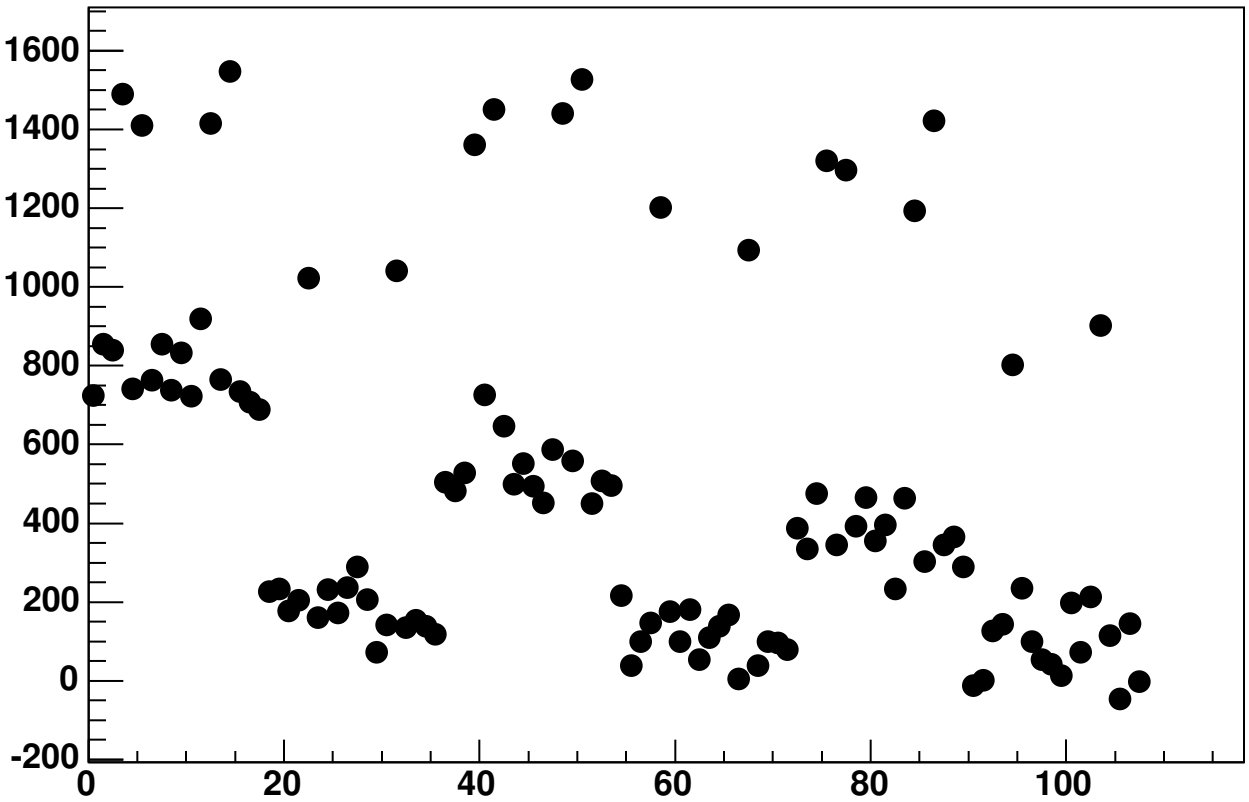
Enable 1, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



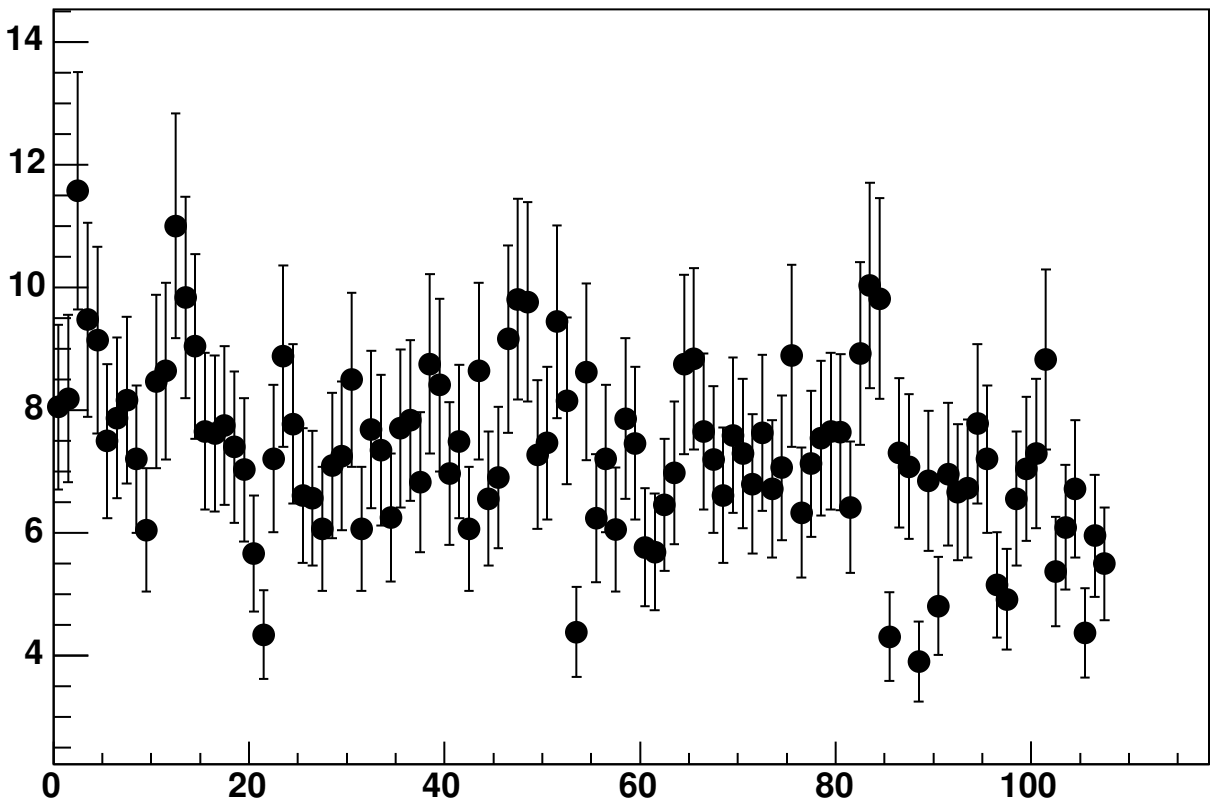
Enable 1, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



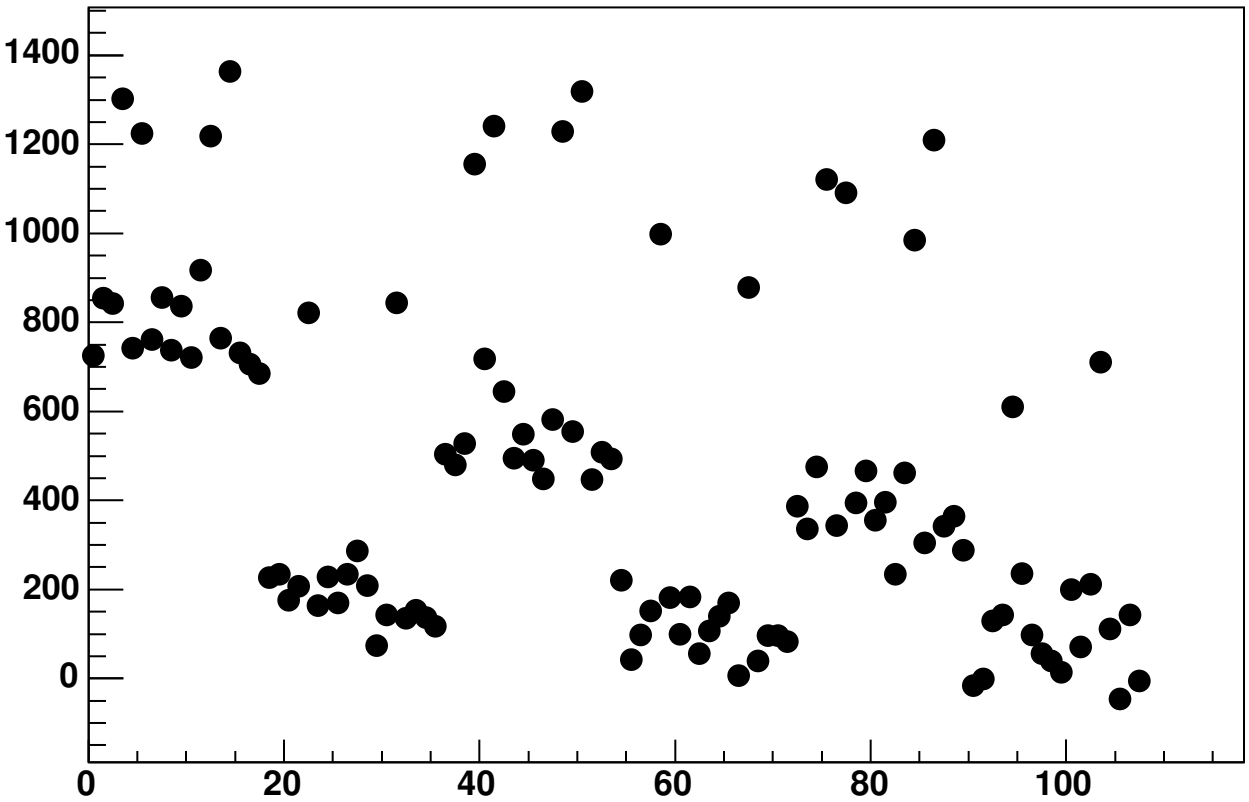
Enable 1, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



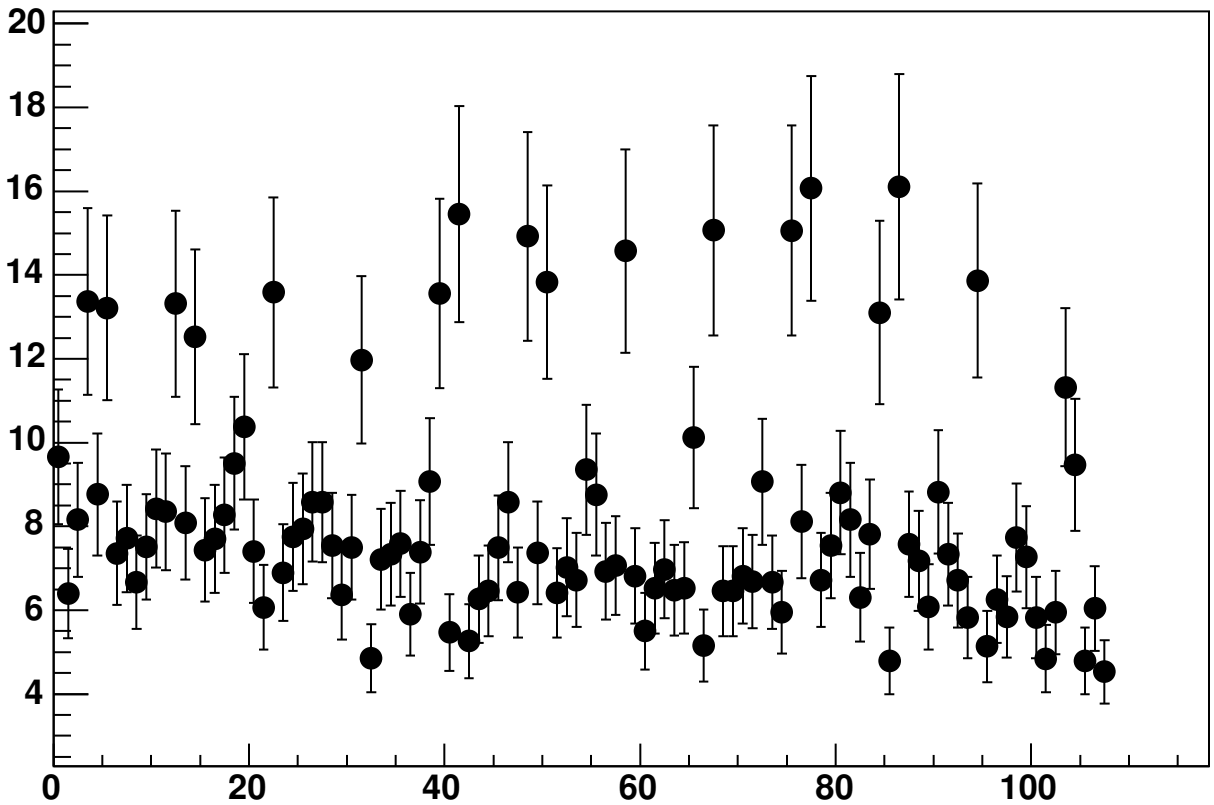
Enable 1, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



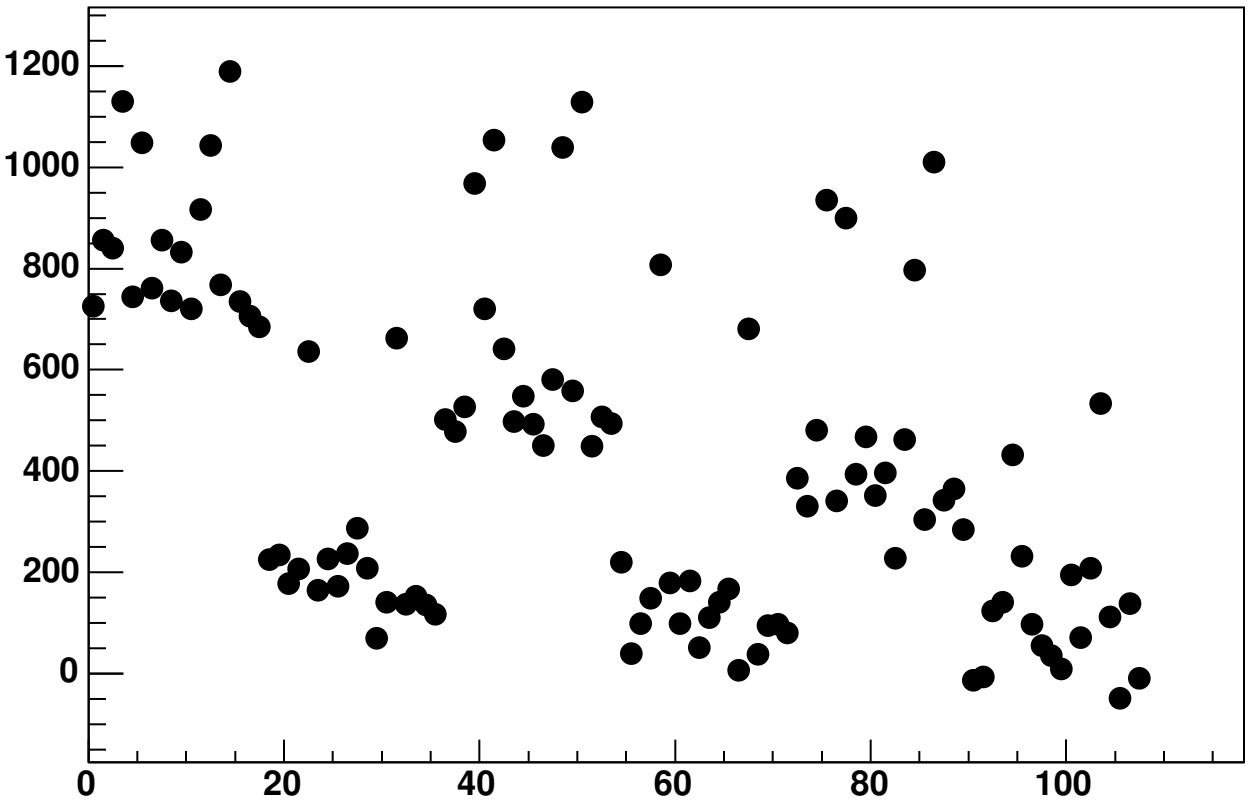
Enable 1, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



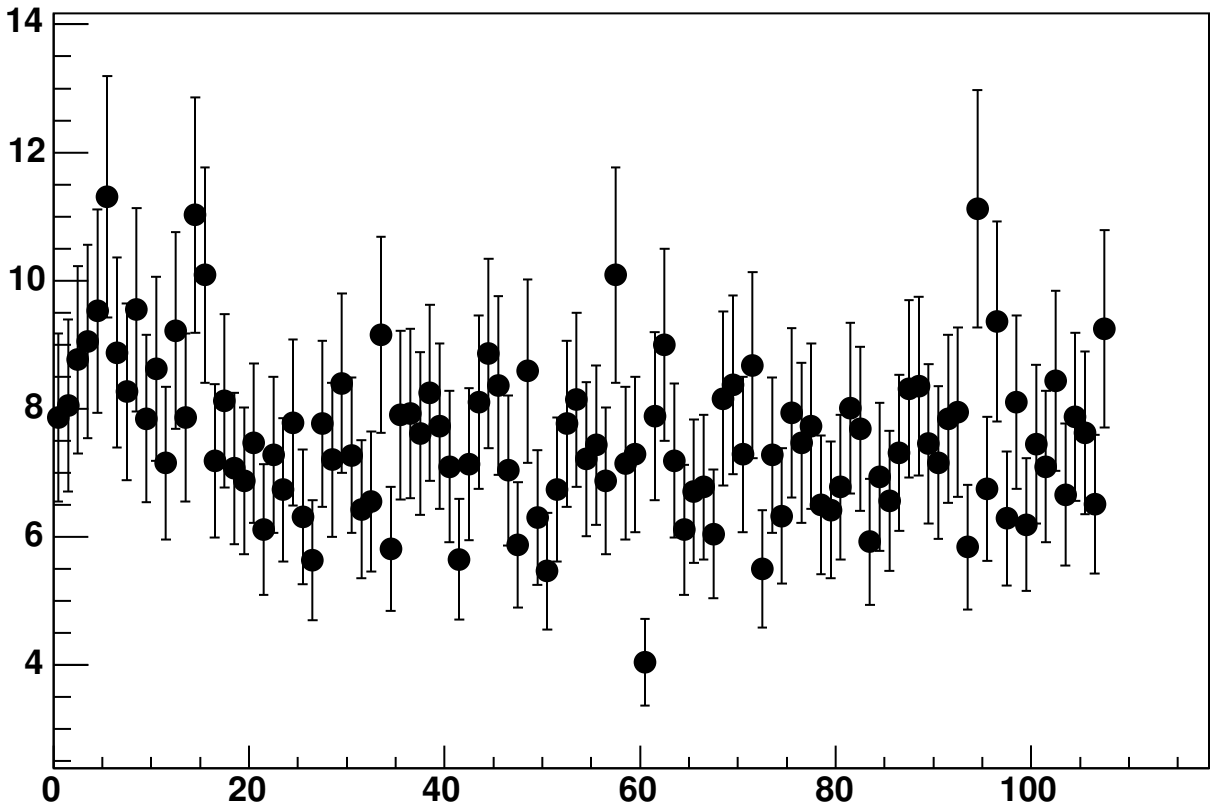
Enable 1, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



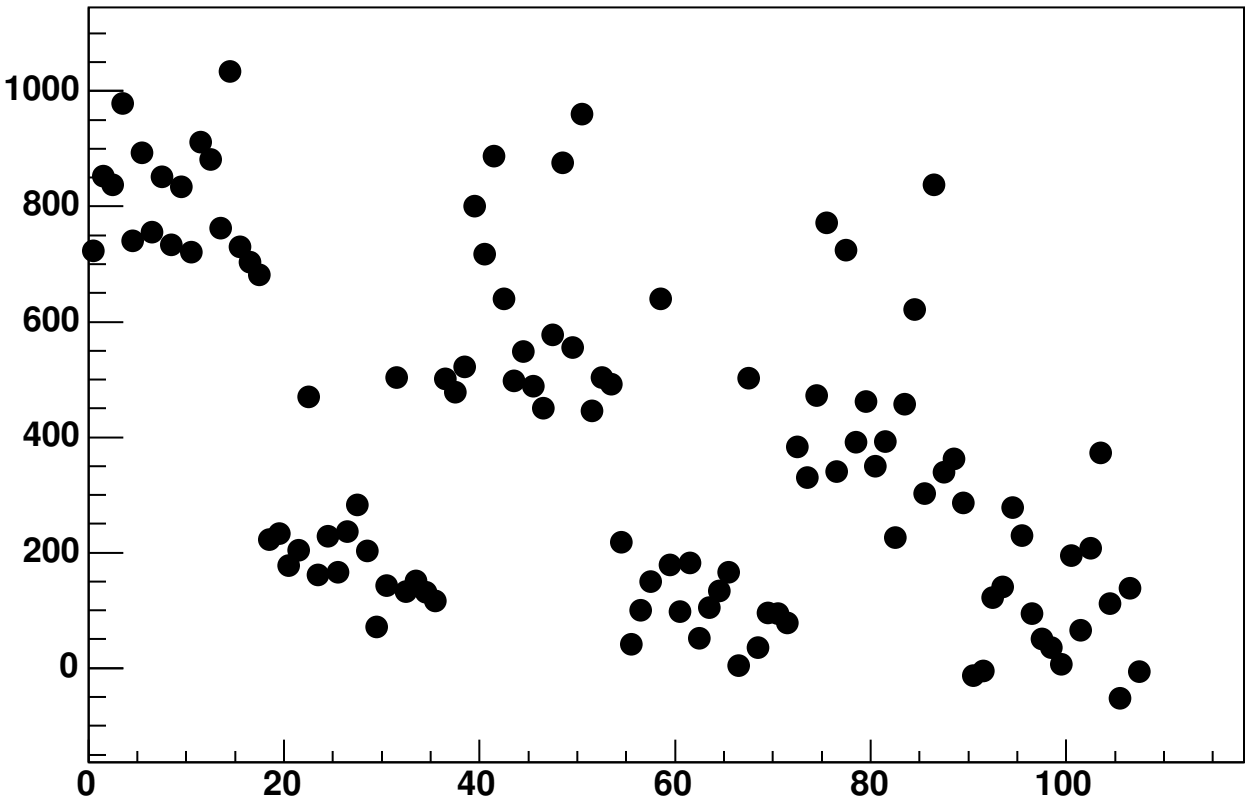
Enable 1, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



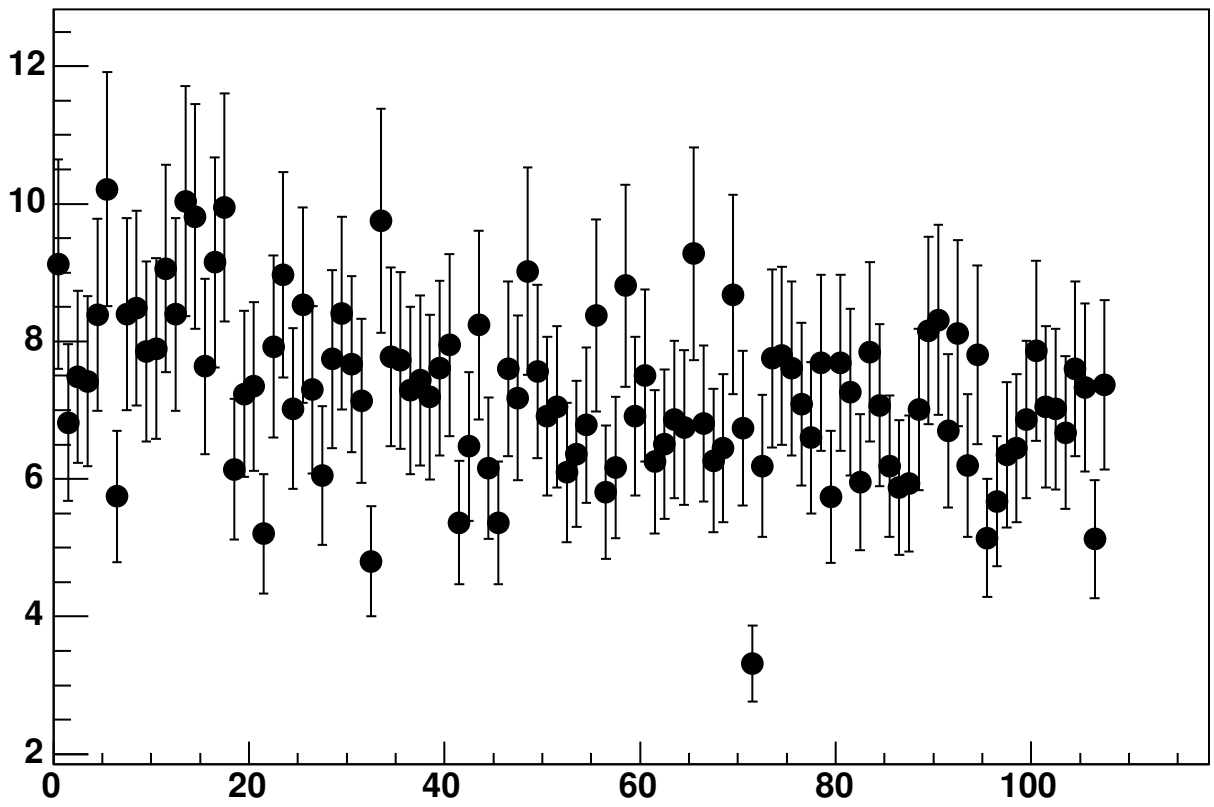
Enable 1, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



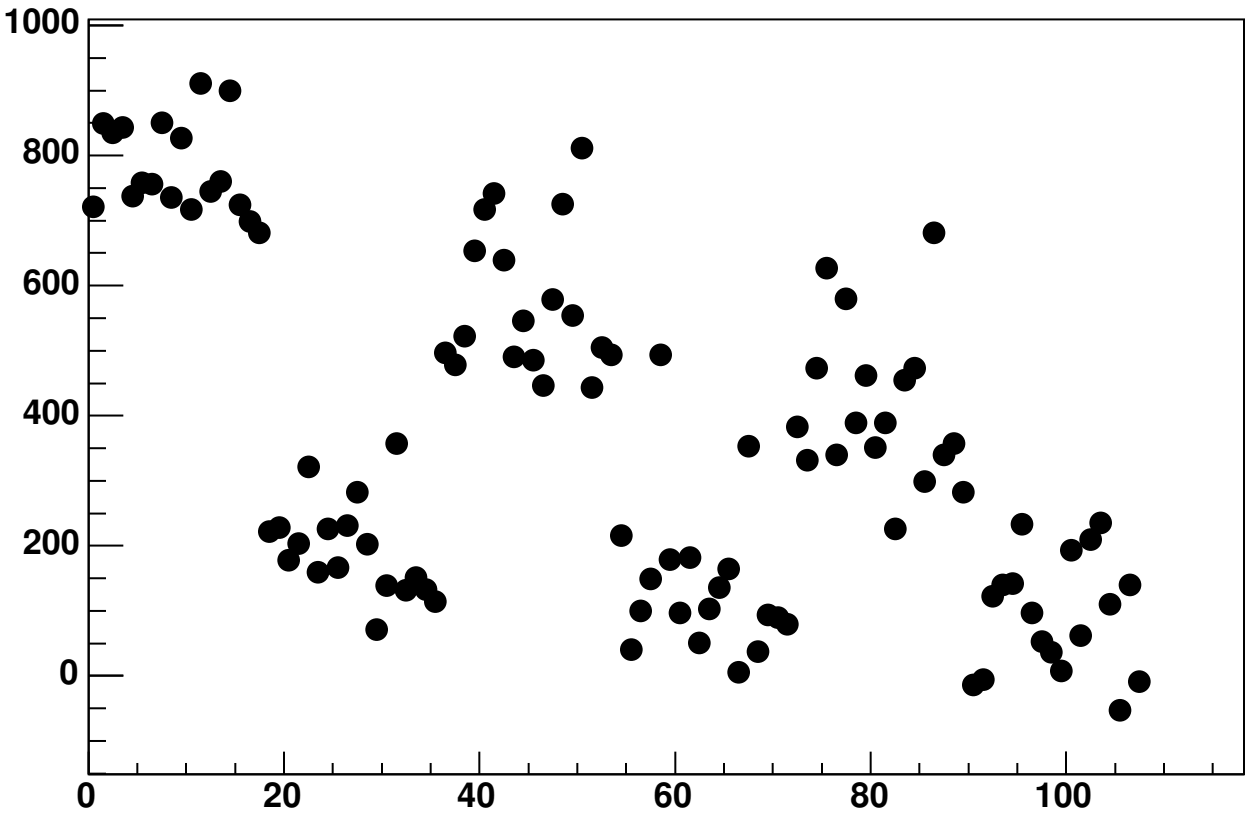
Enable 1, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



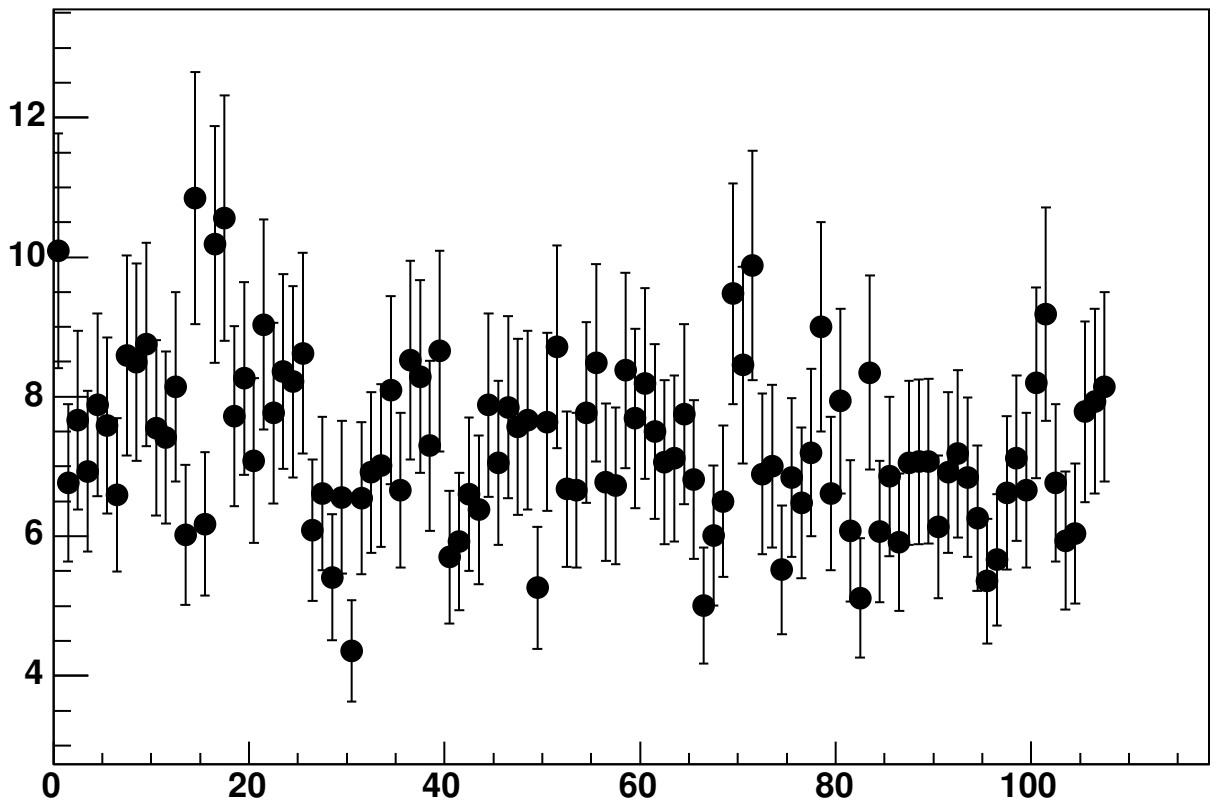
Enable 1, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



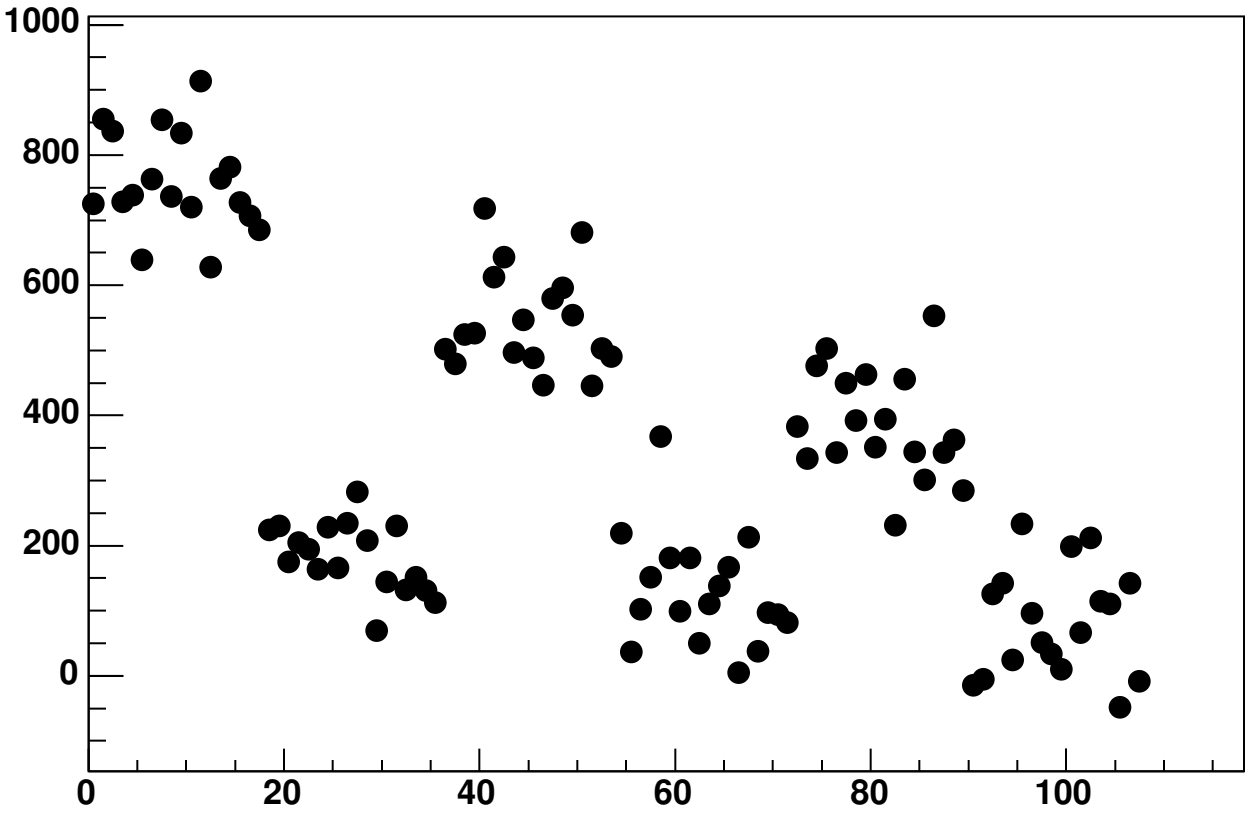
Enable 1, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



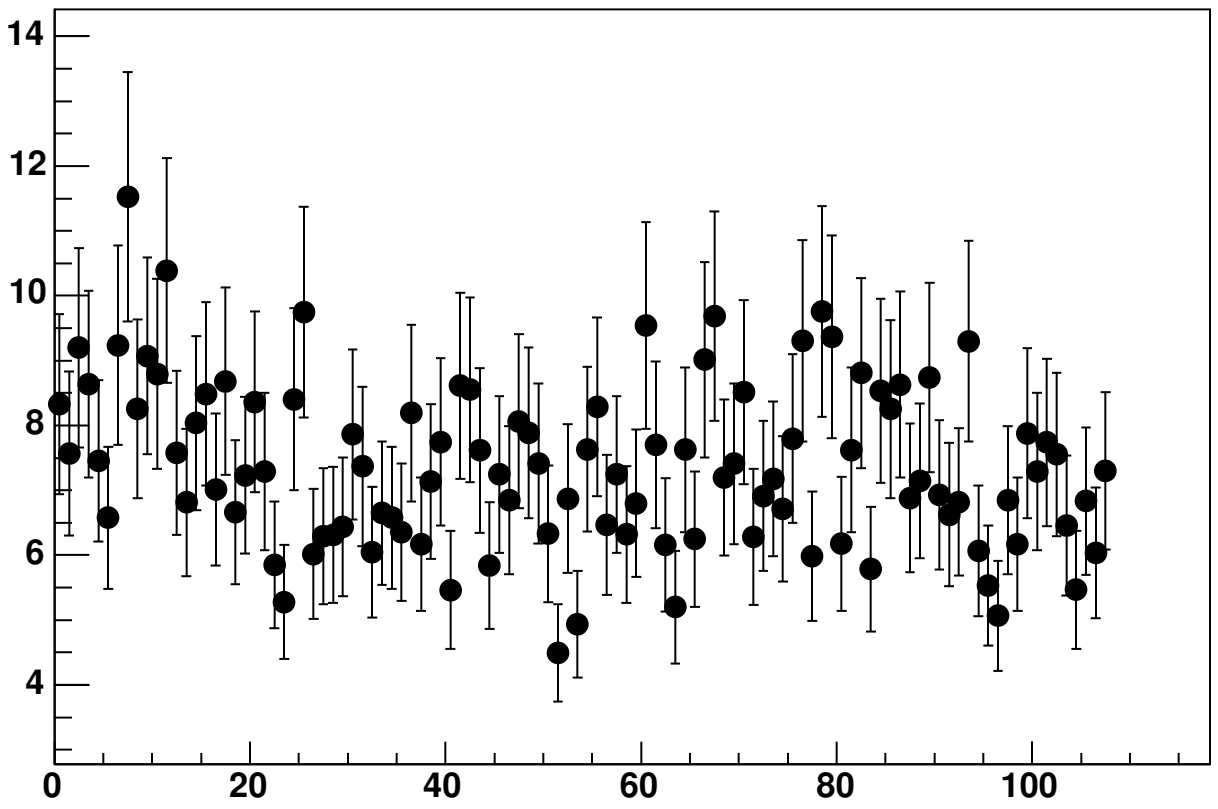
Enable 1, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



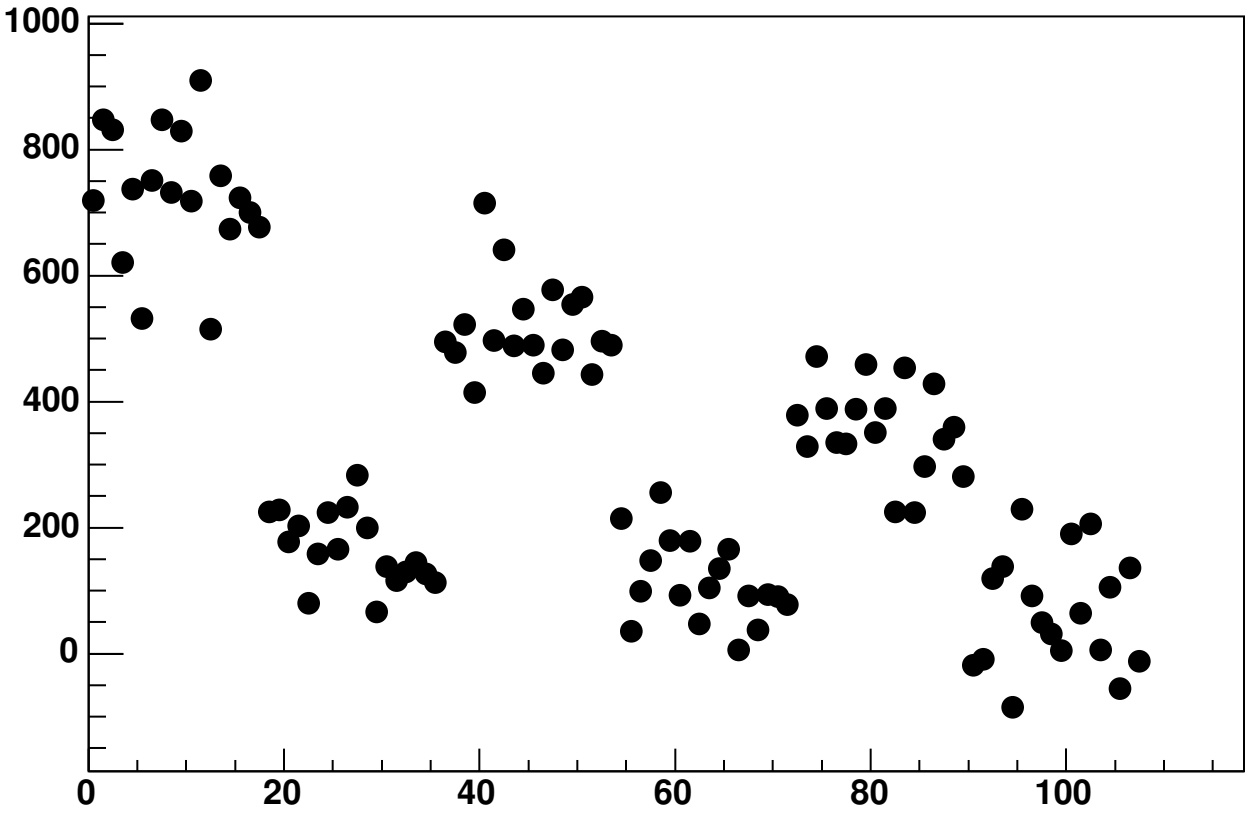
Enable 1, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



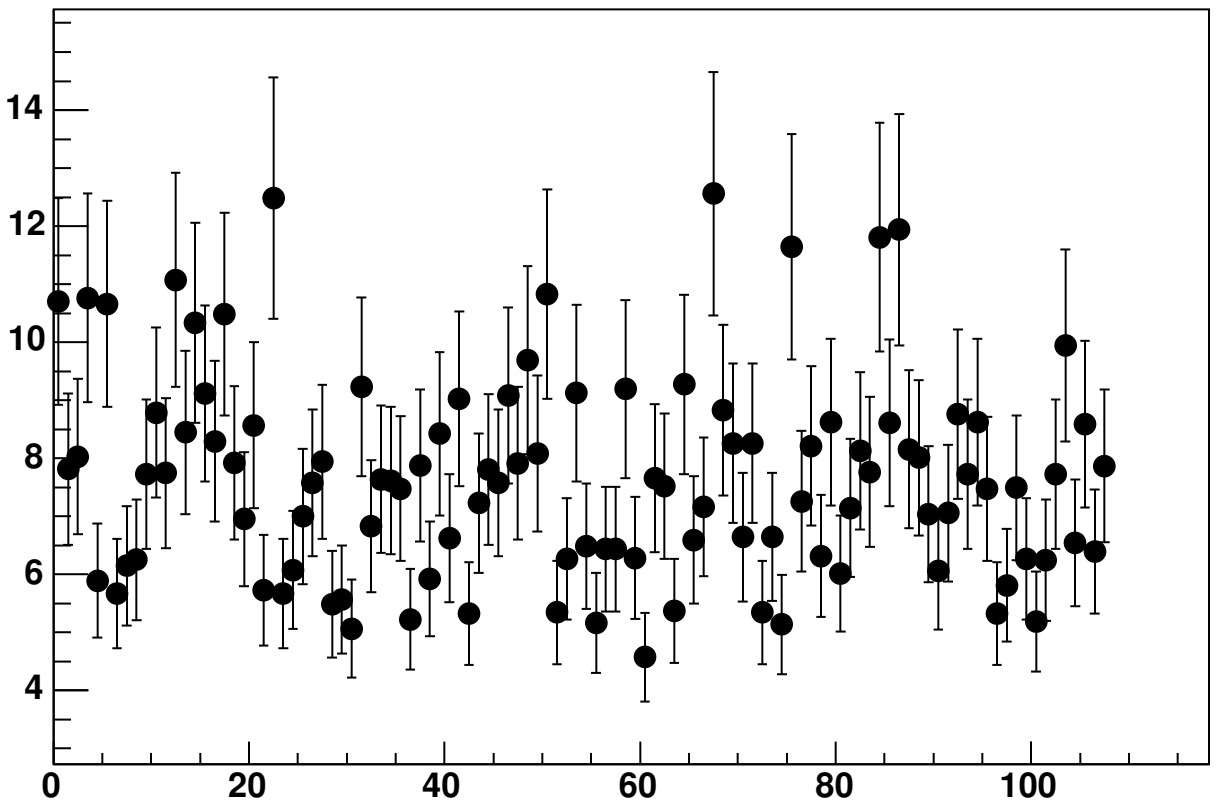
Enable 1, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



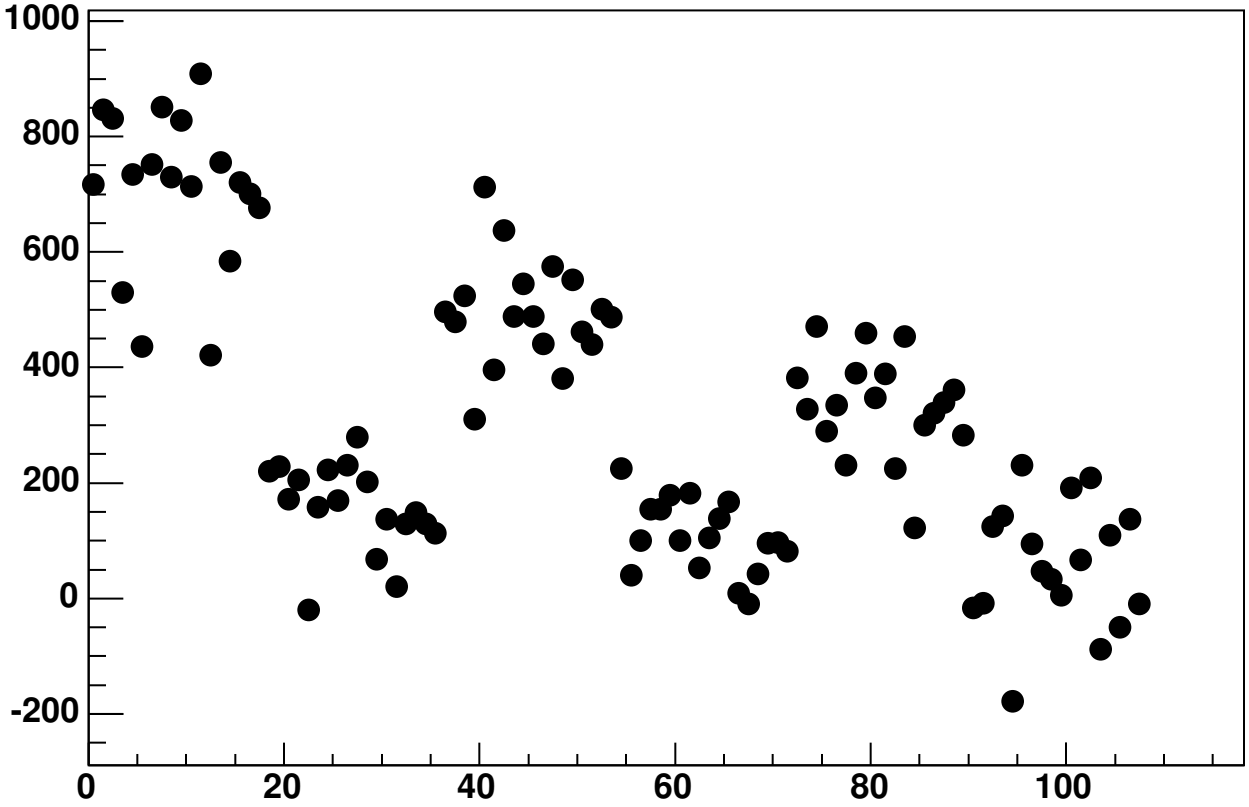
Enable 1, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



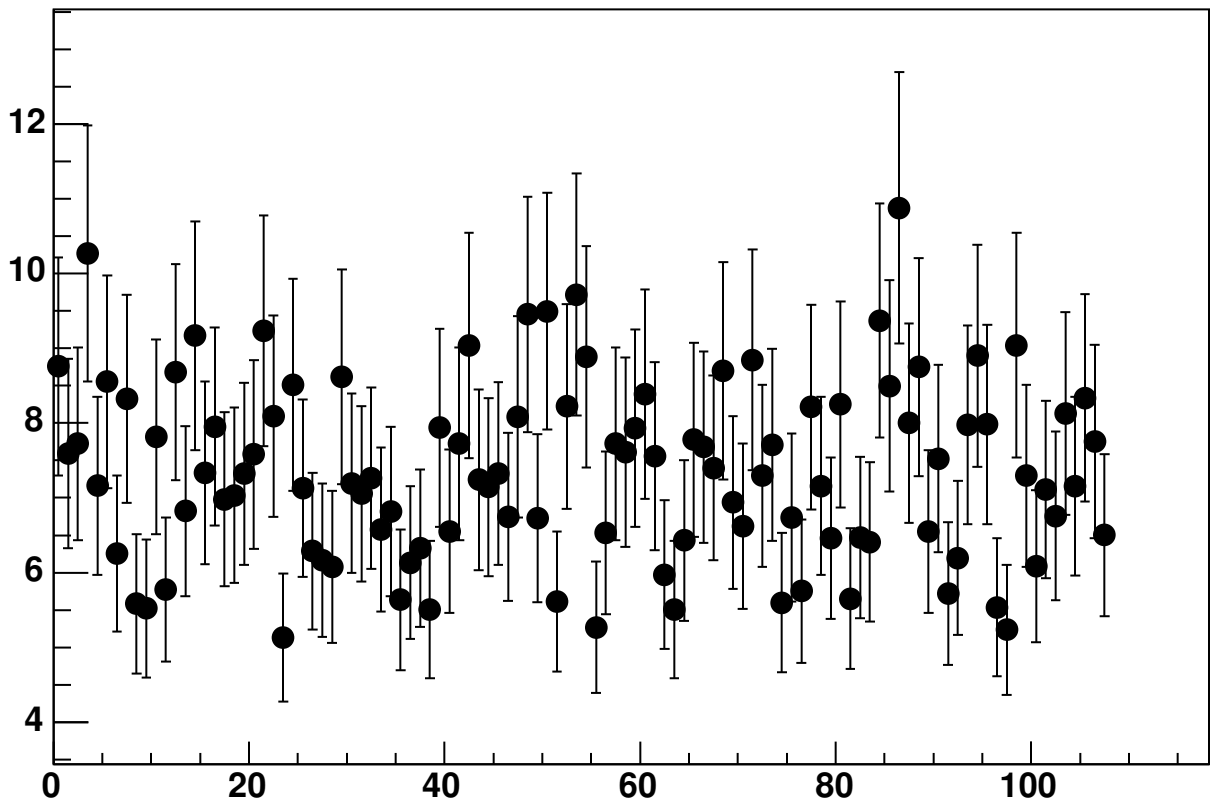
Enable 1, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



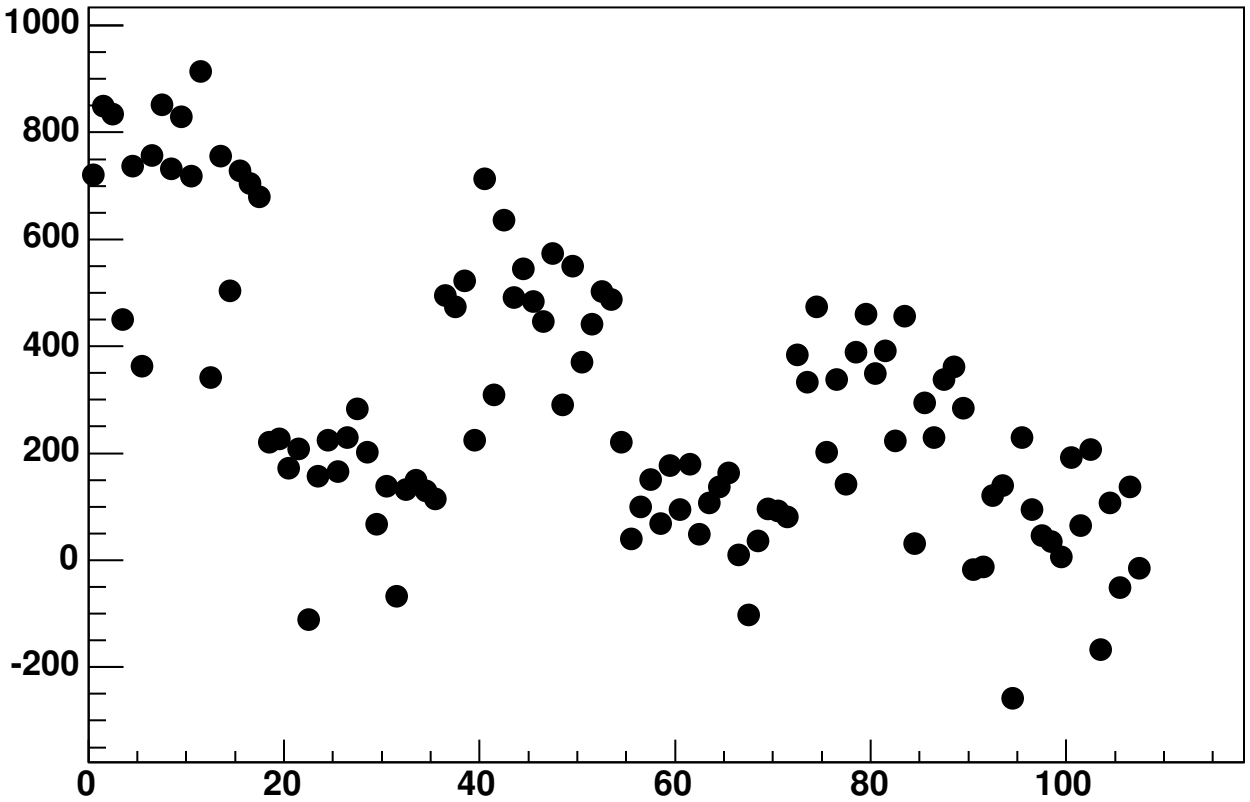
Enable 1, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



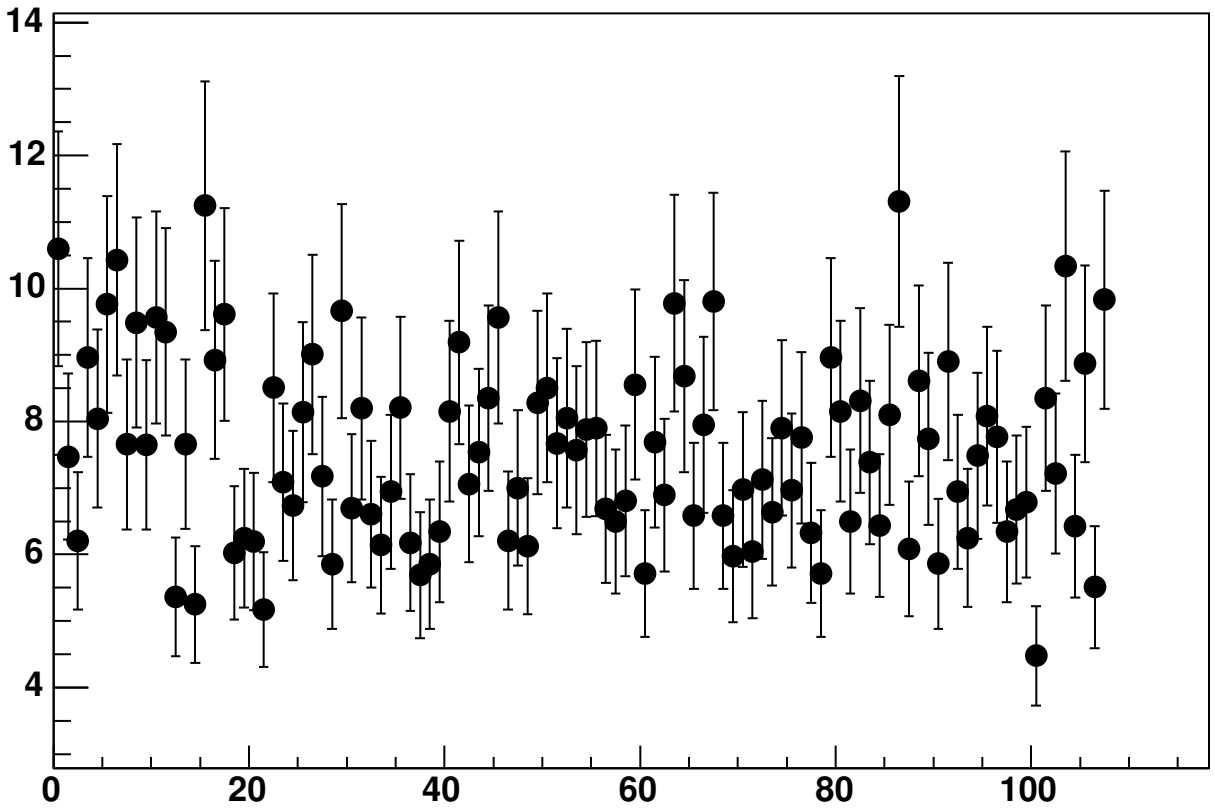
Enable 1, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



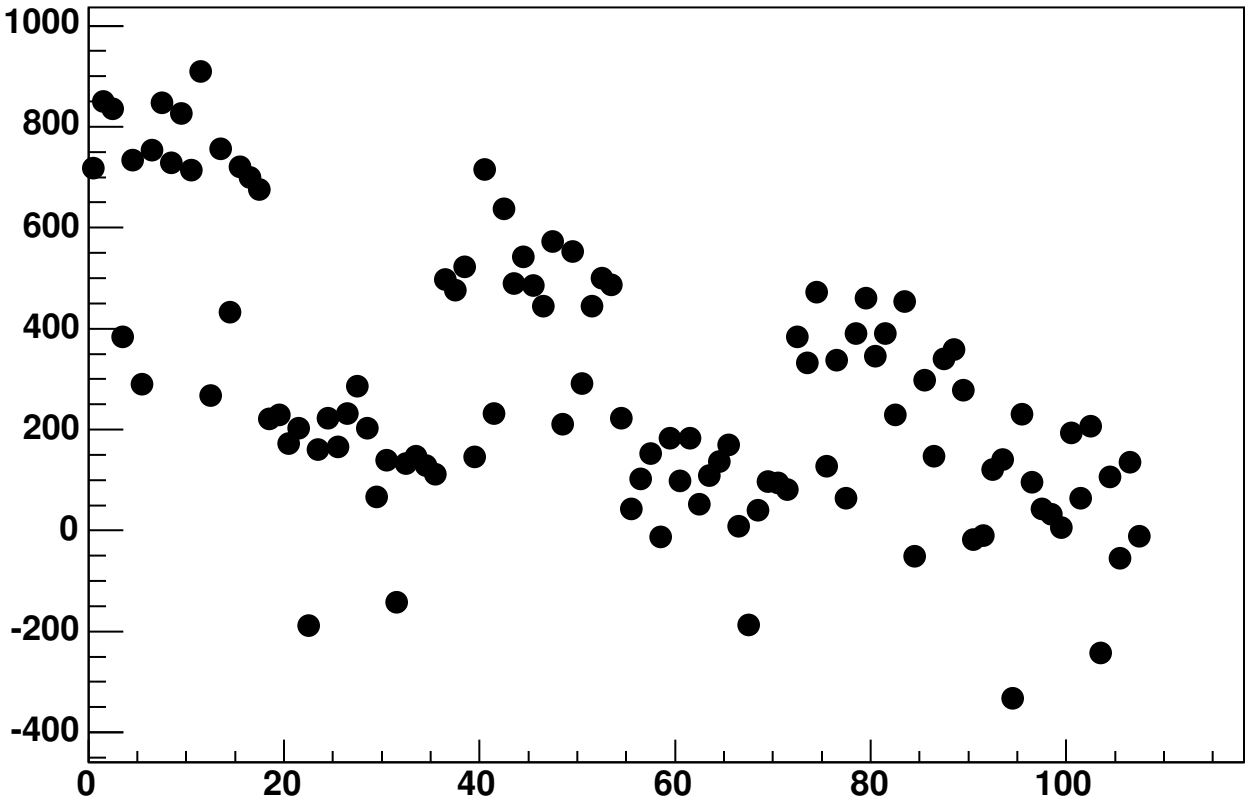
Enable 1, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



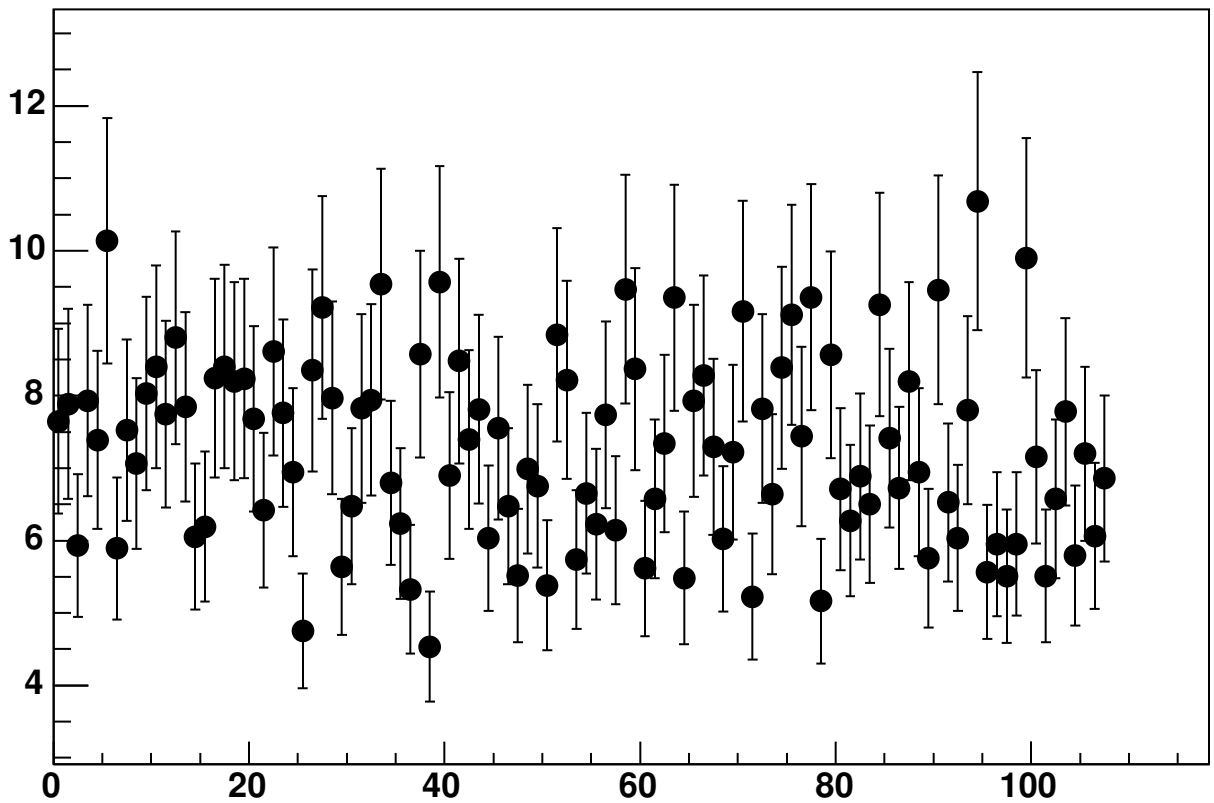
Enable 1, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



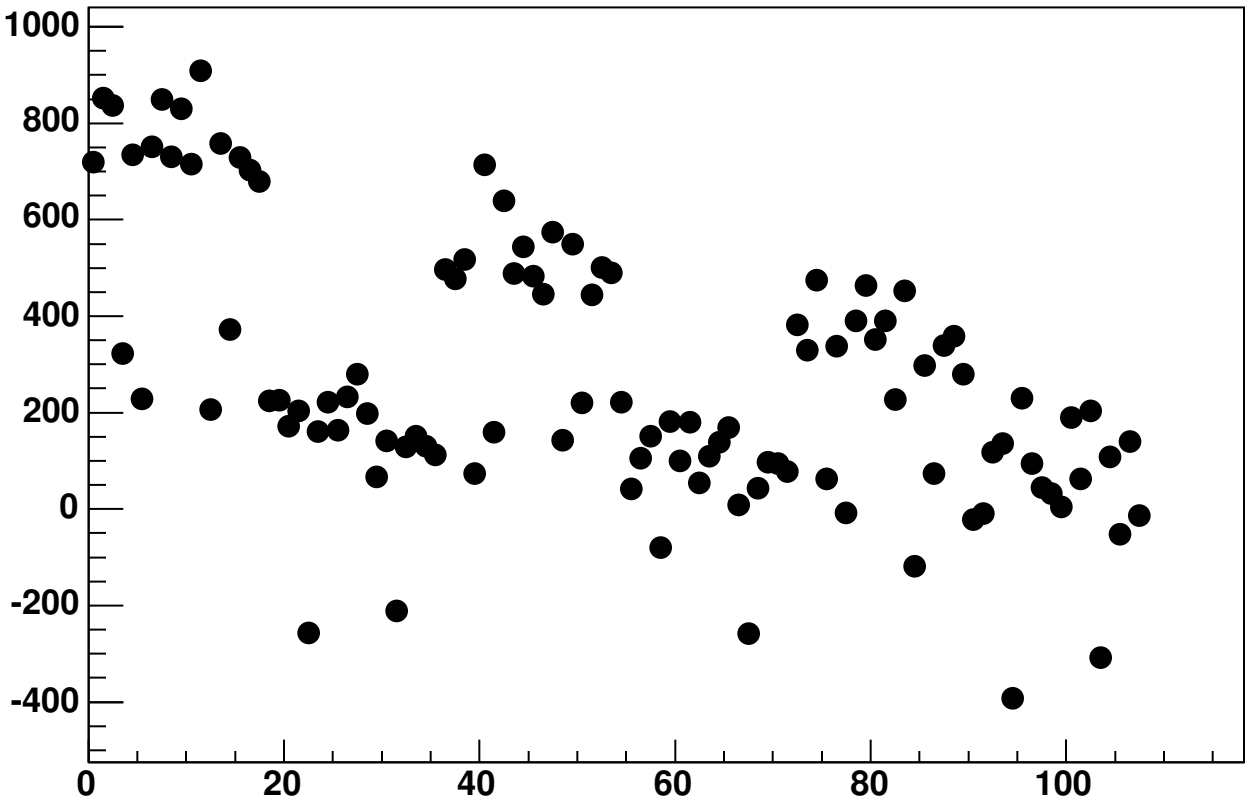
Enable 1, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



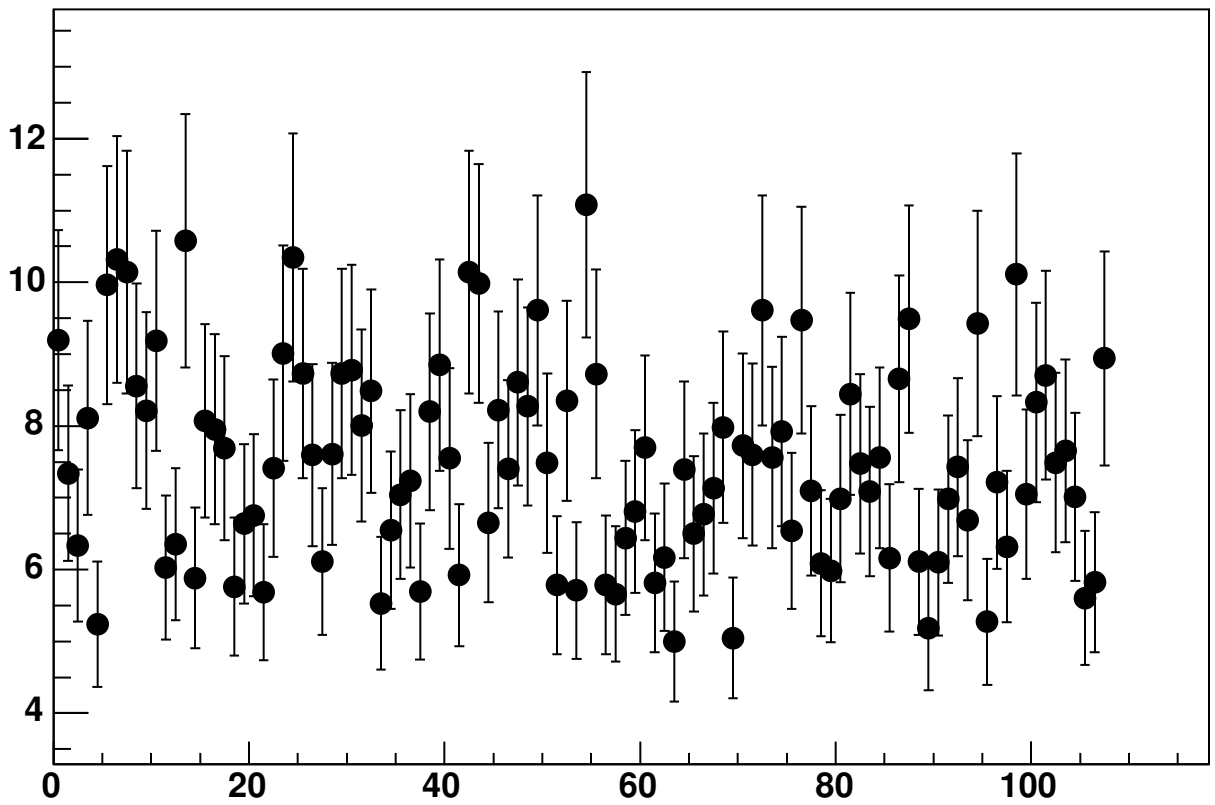
Enable 1, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



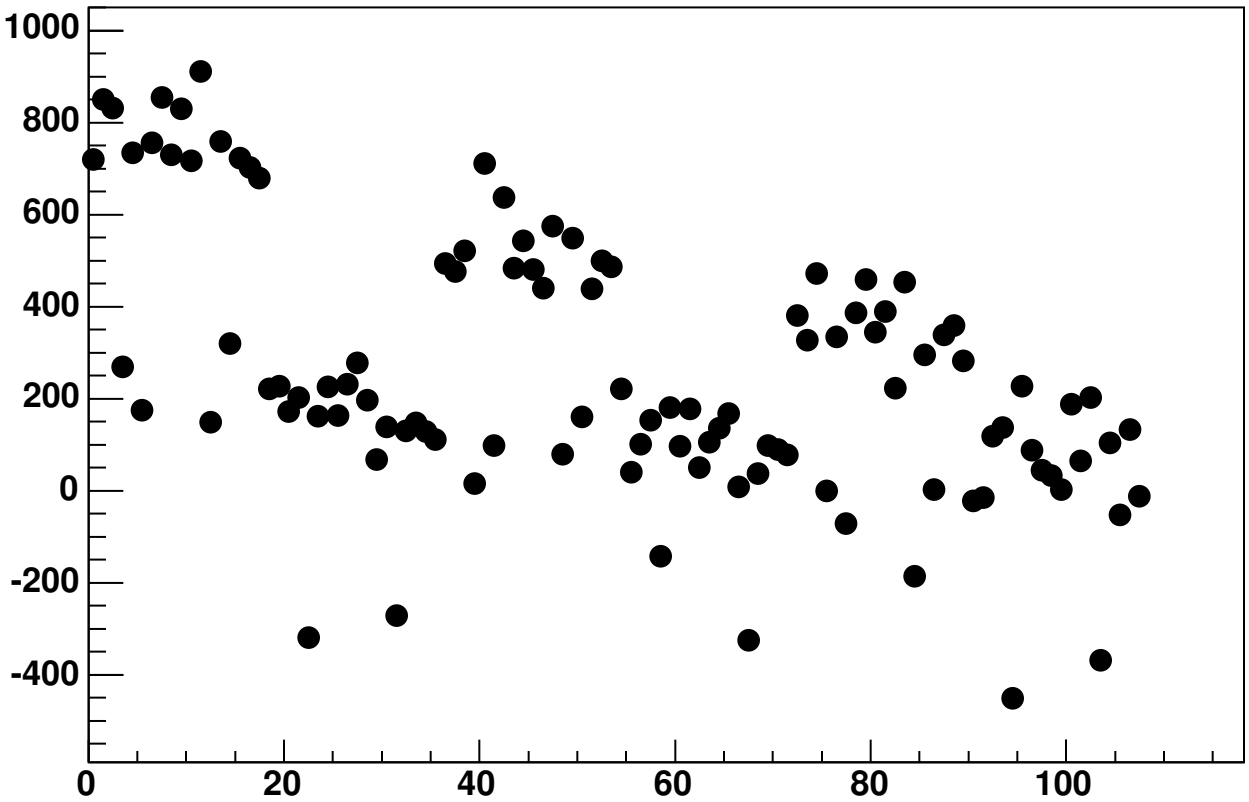
Enable 1, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



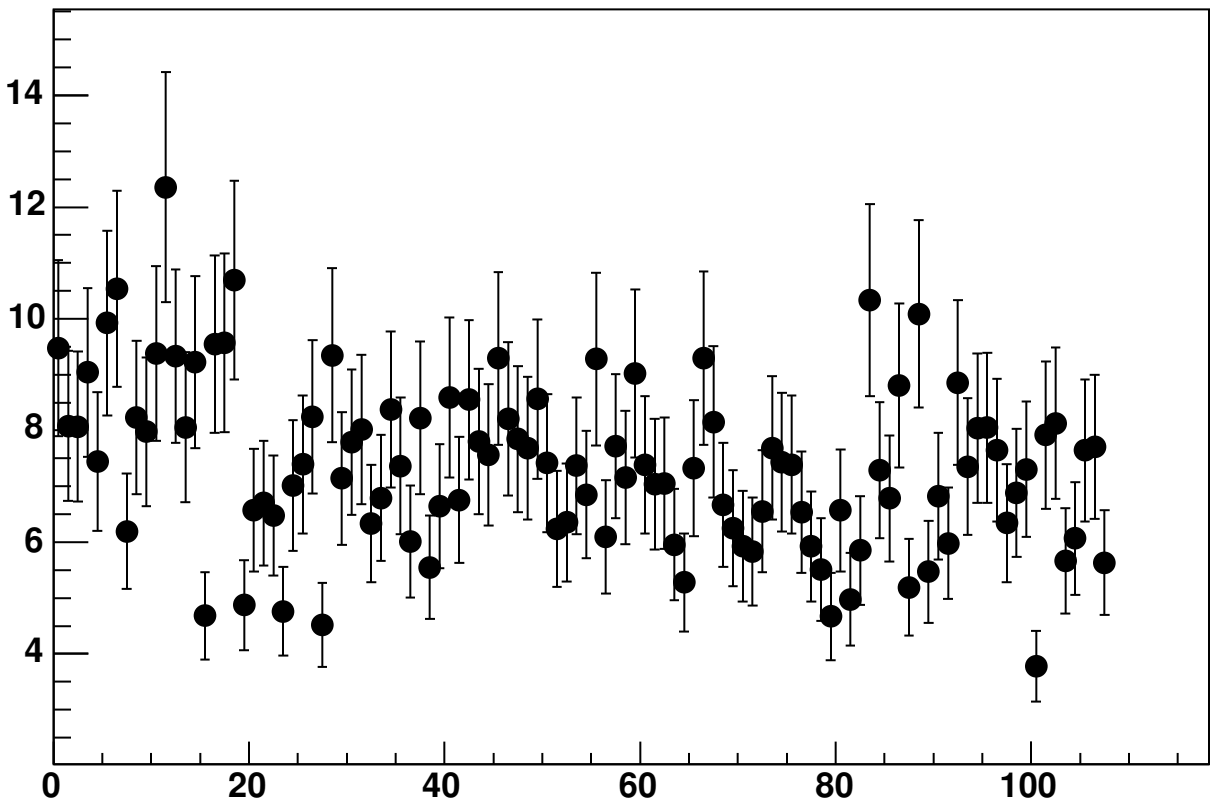
Enable 1, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



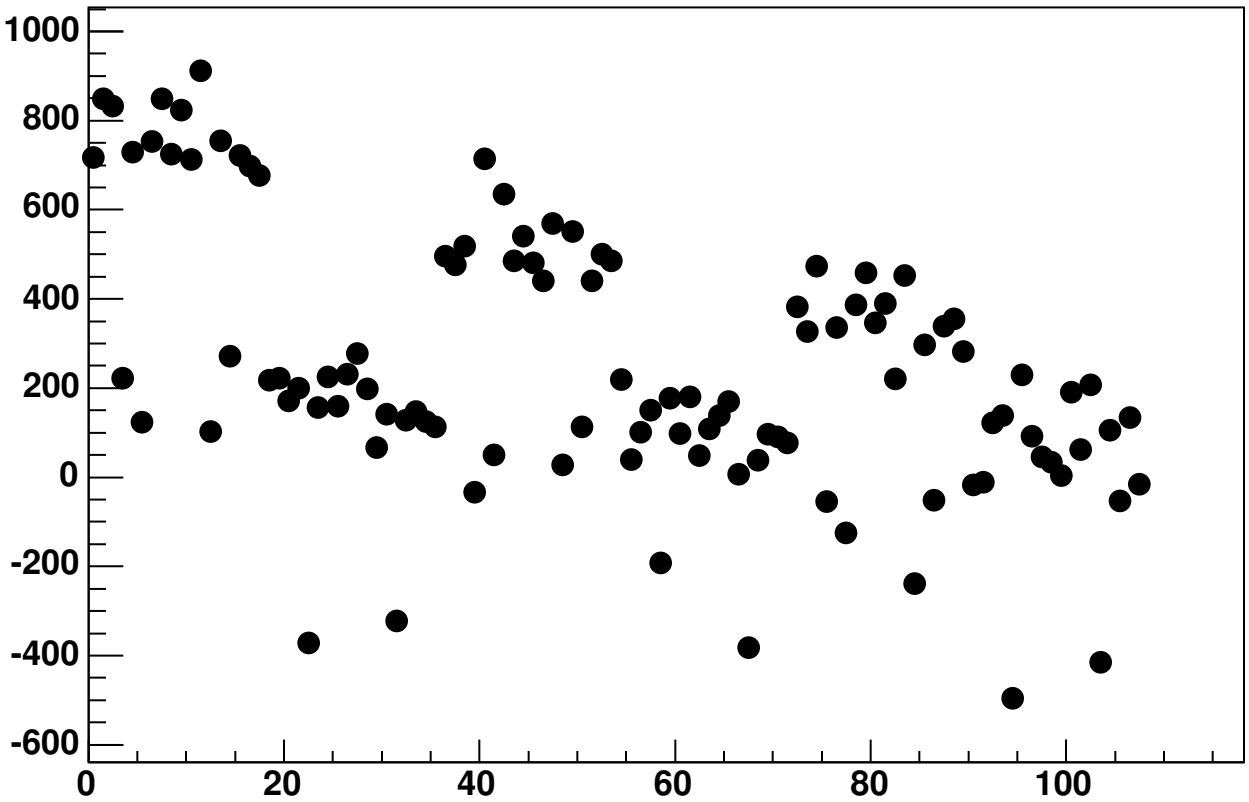
Enable 1, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



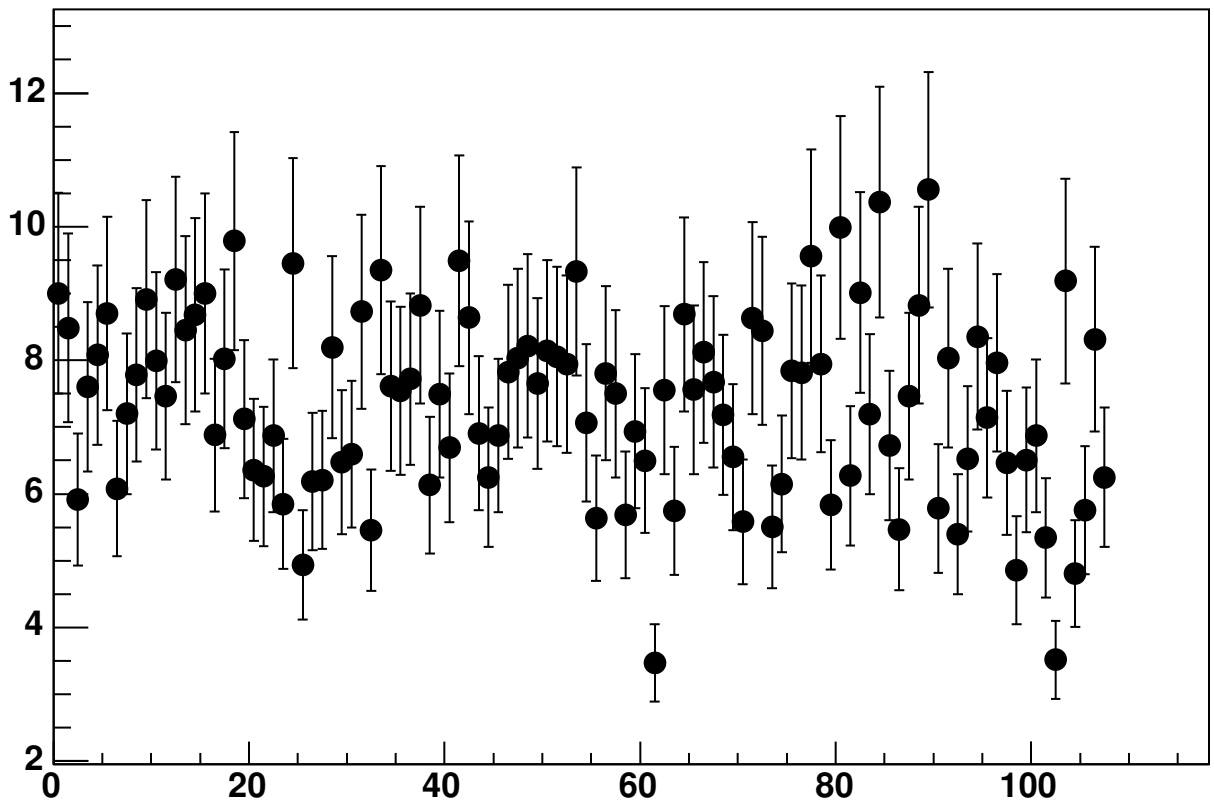
Enable 1, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



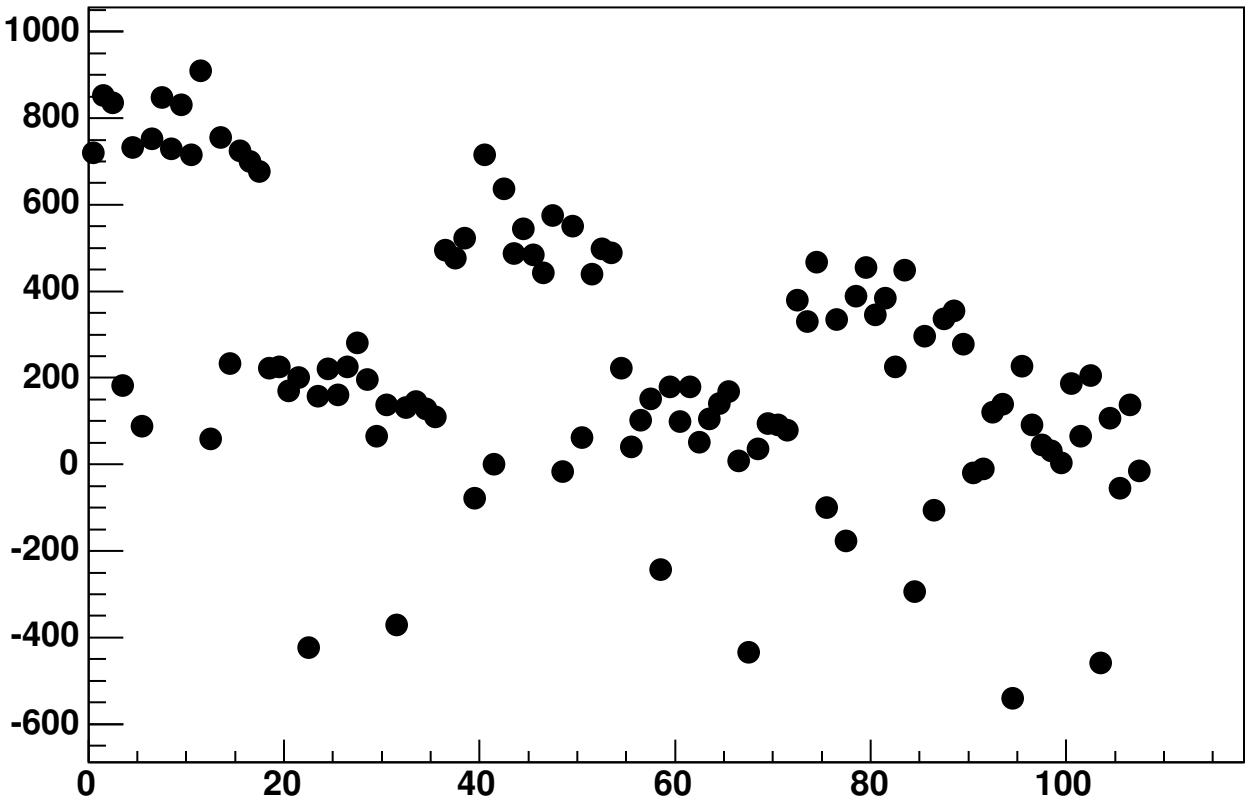
Enable 1, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



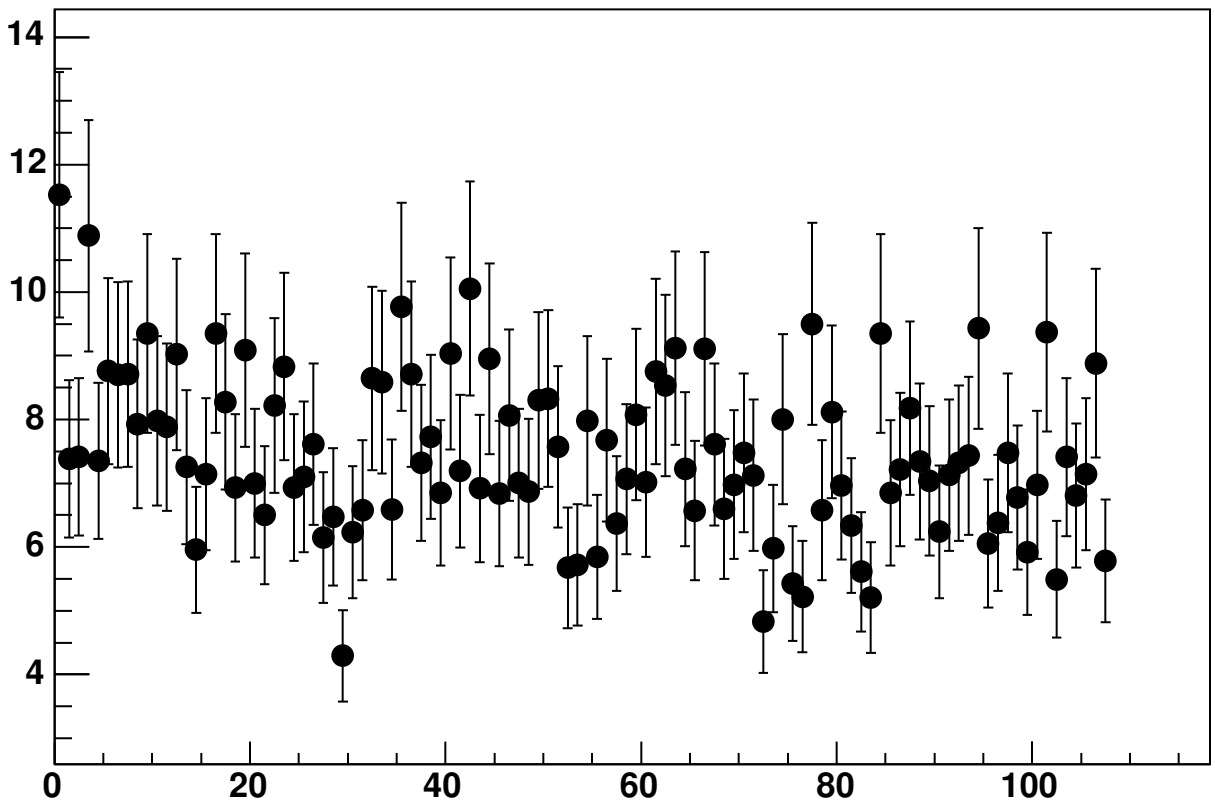
Enable 1, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



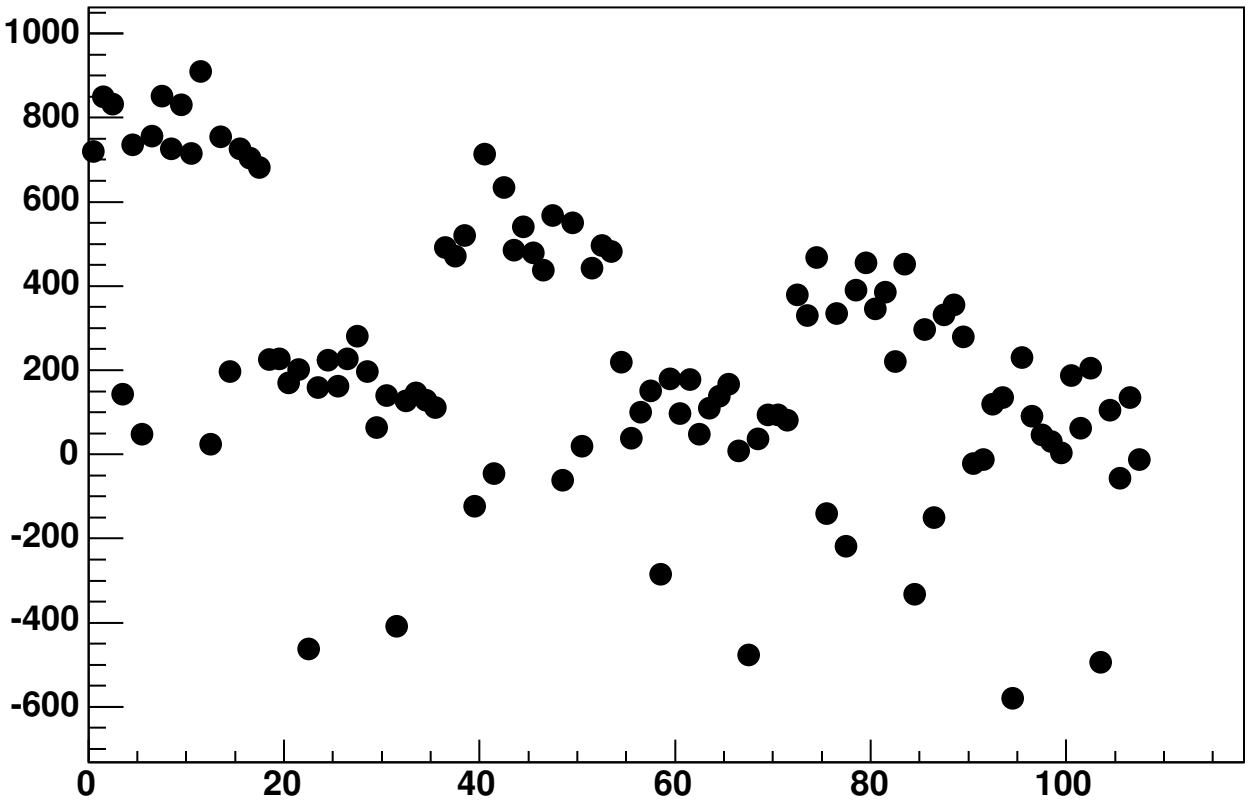
Enable 1, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



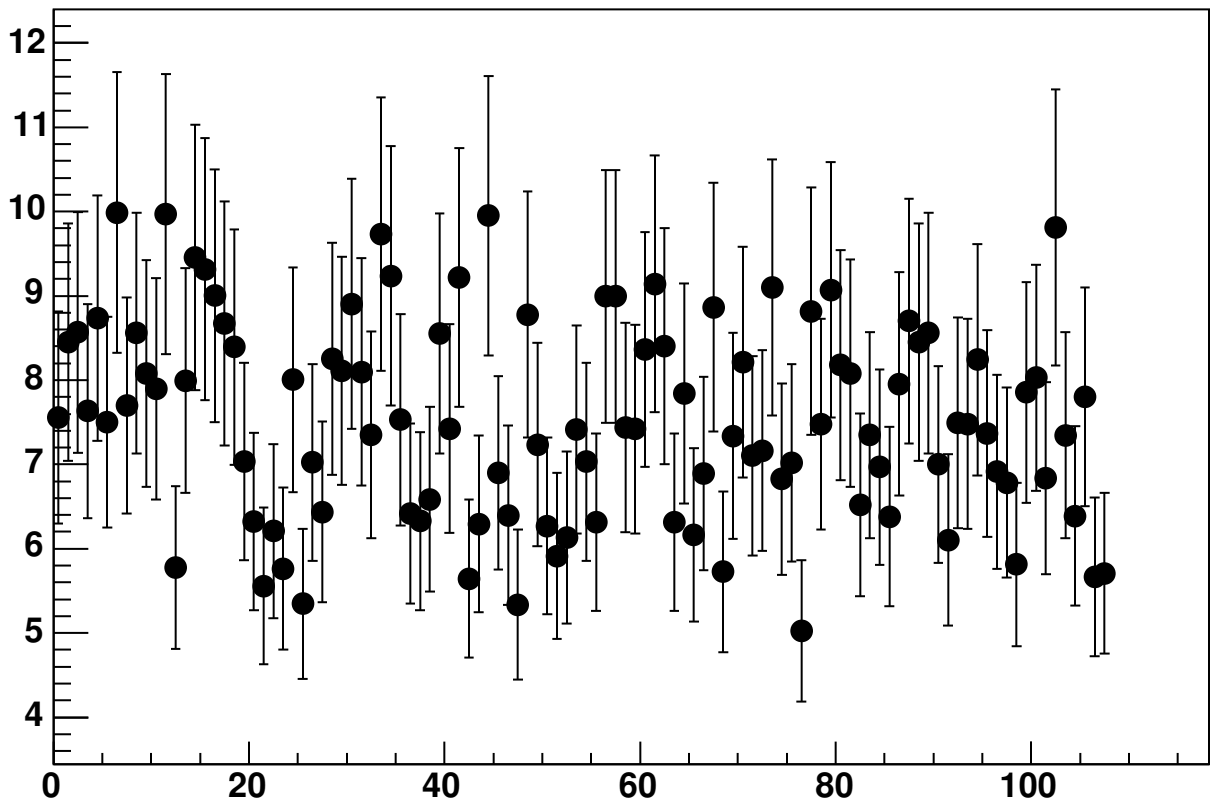
Enable 1, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



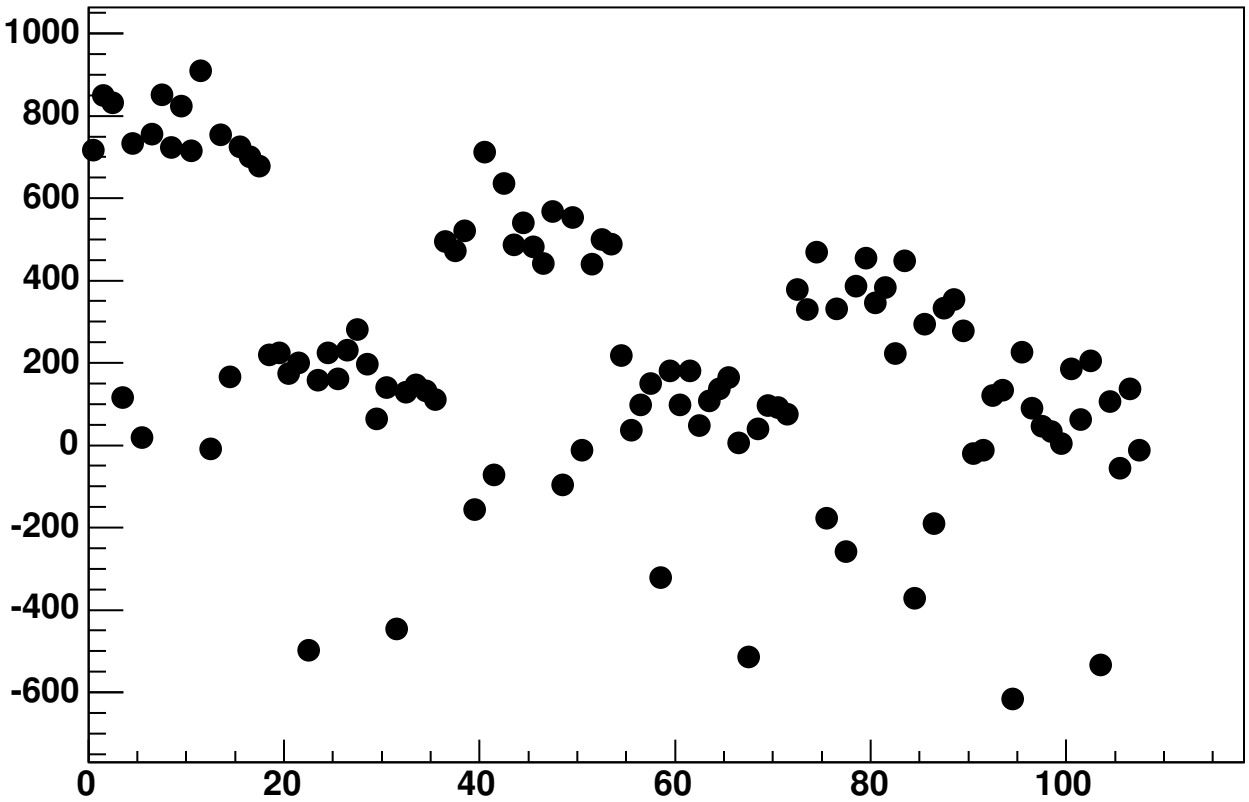
Enable 1, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



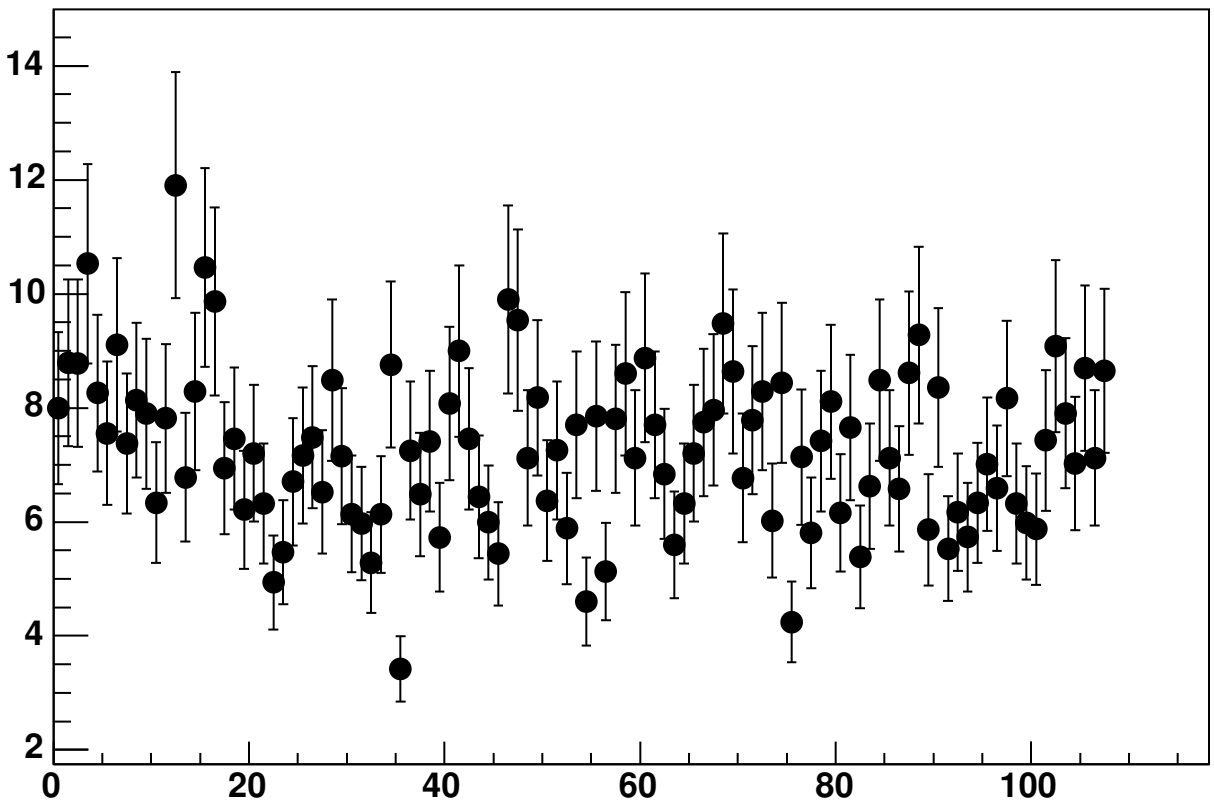
Enable 1, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



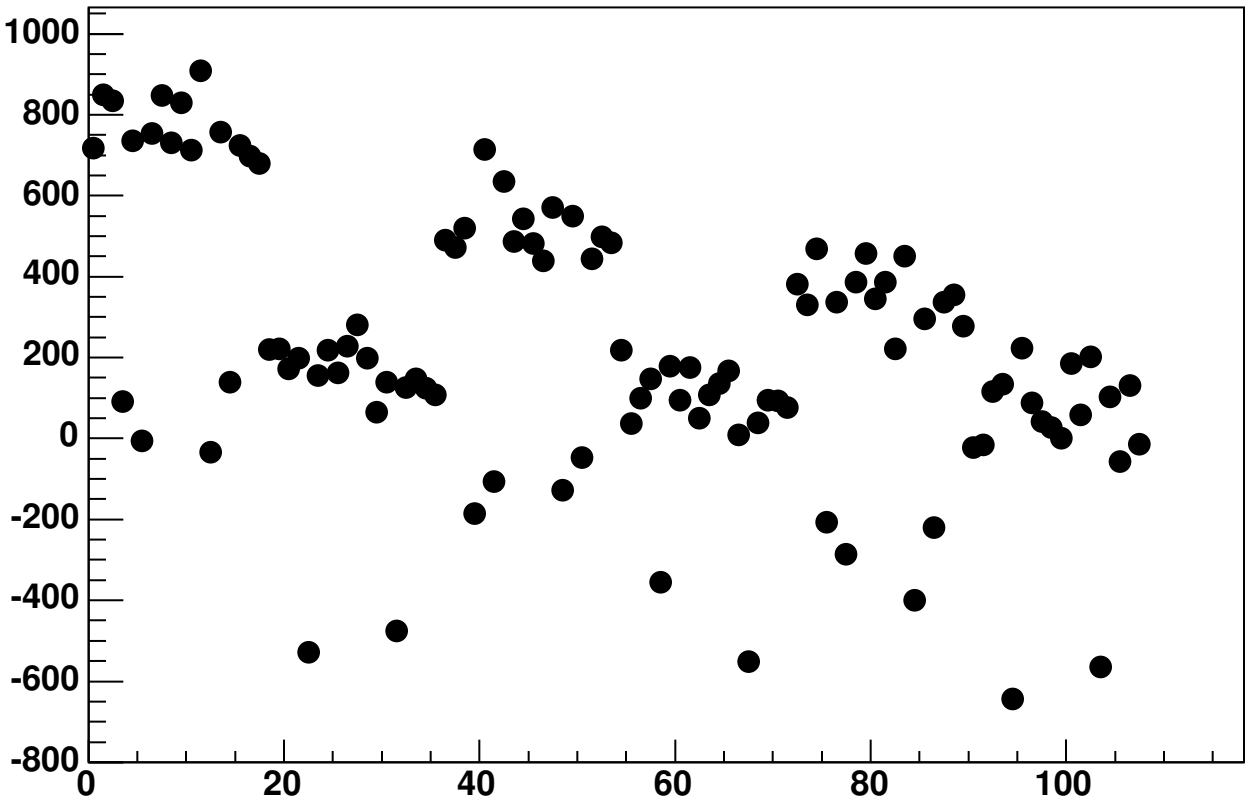
Enable 1, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



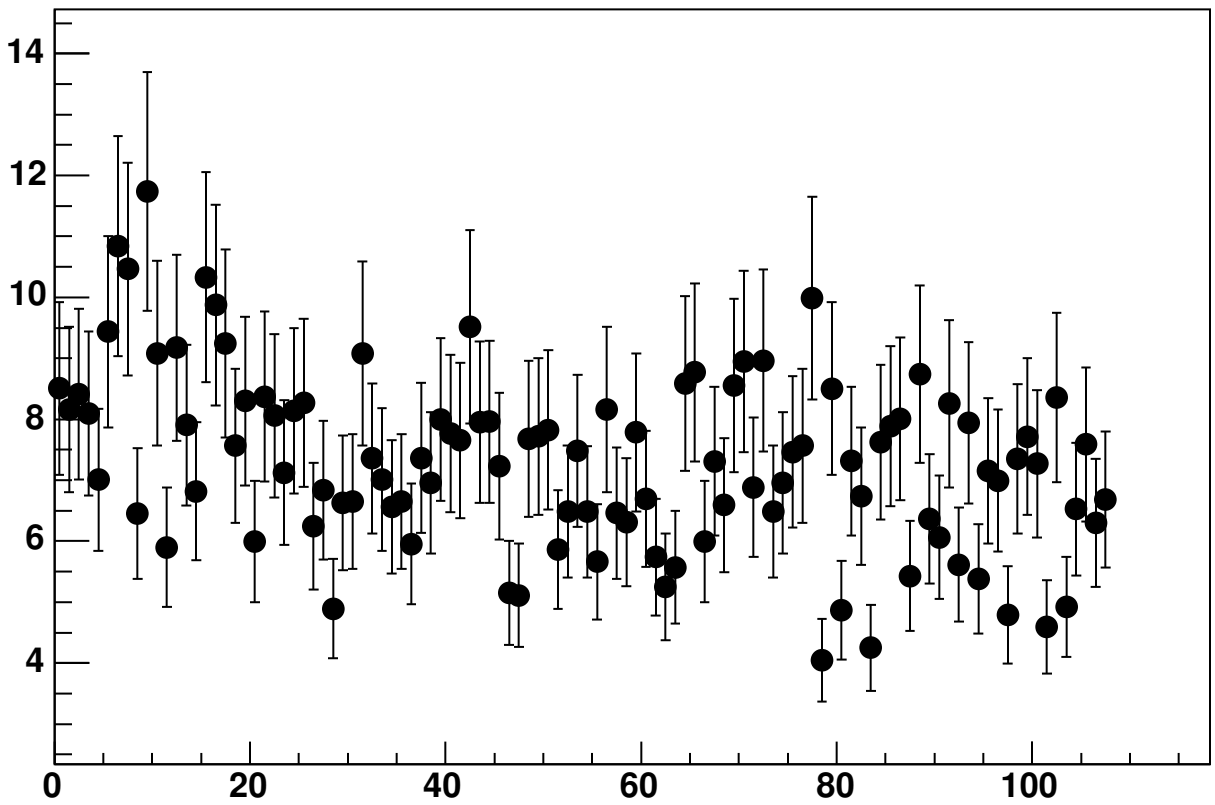
Enable 1, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



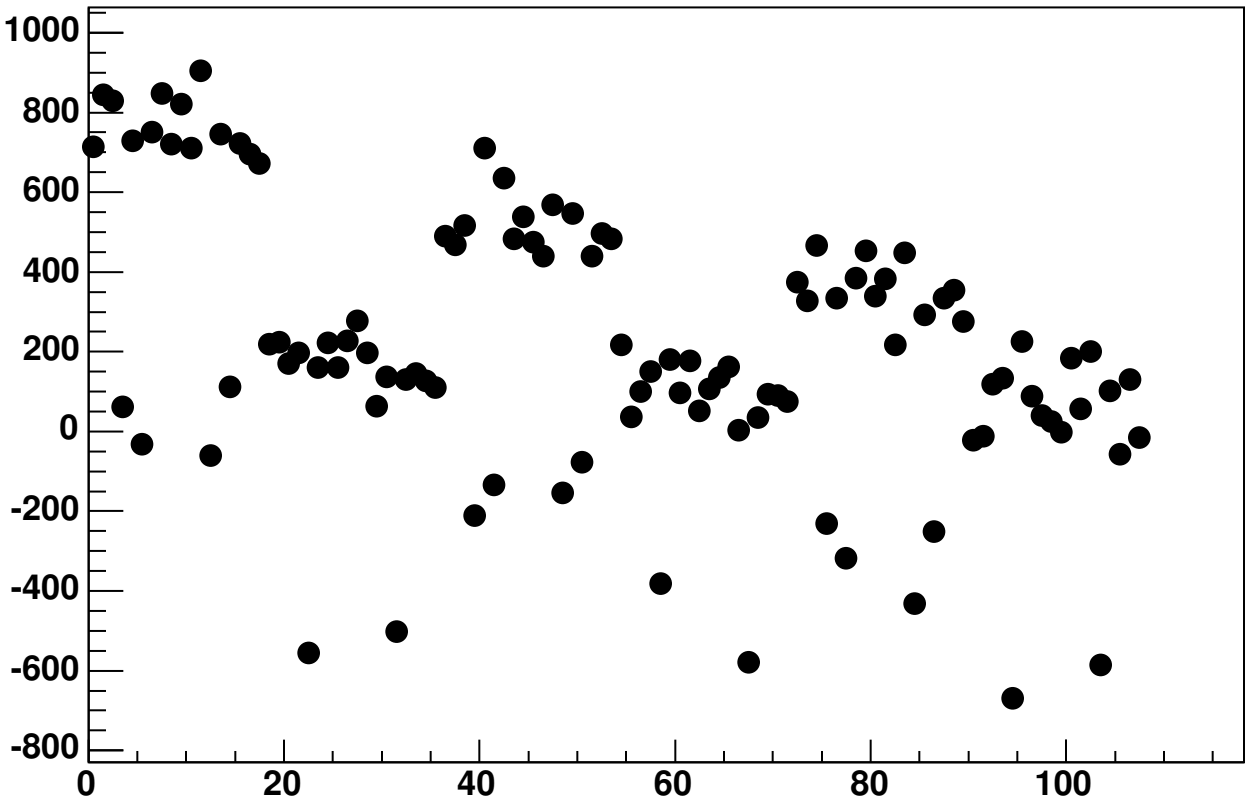
Enable 1, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



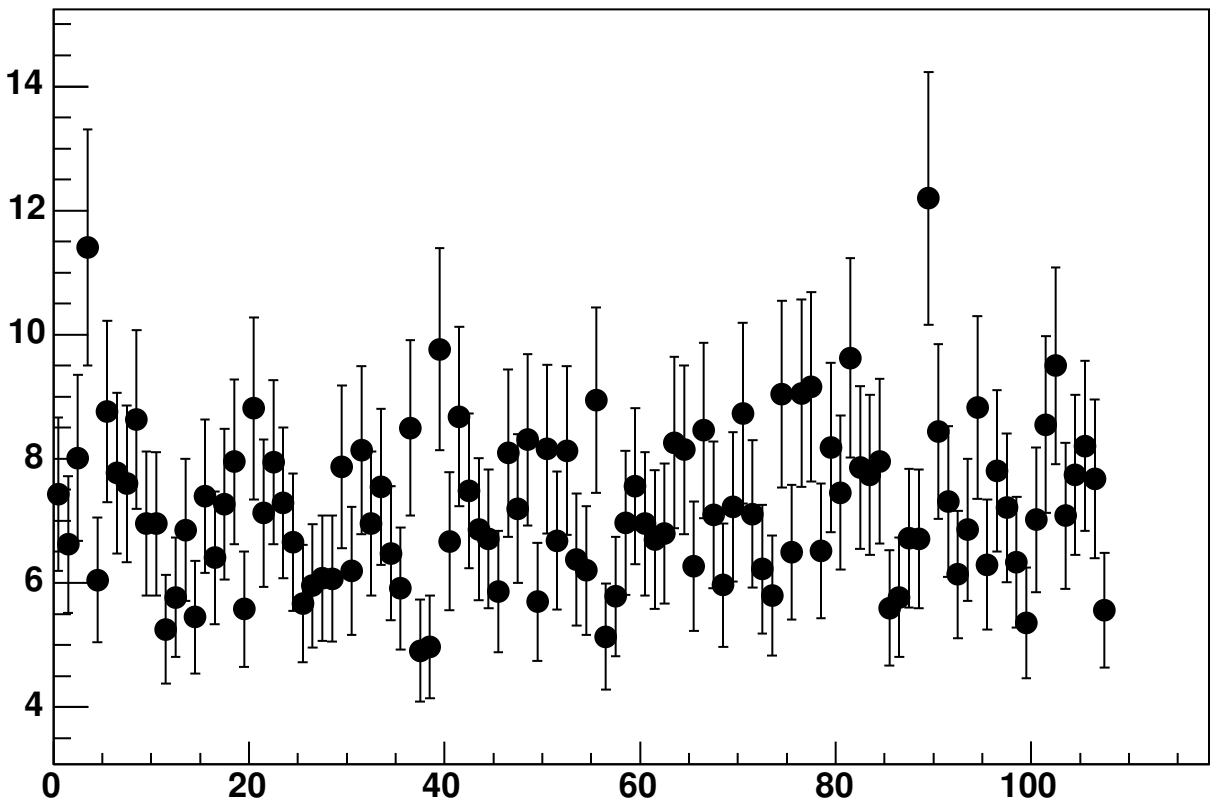
Enable 1, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



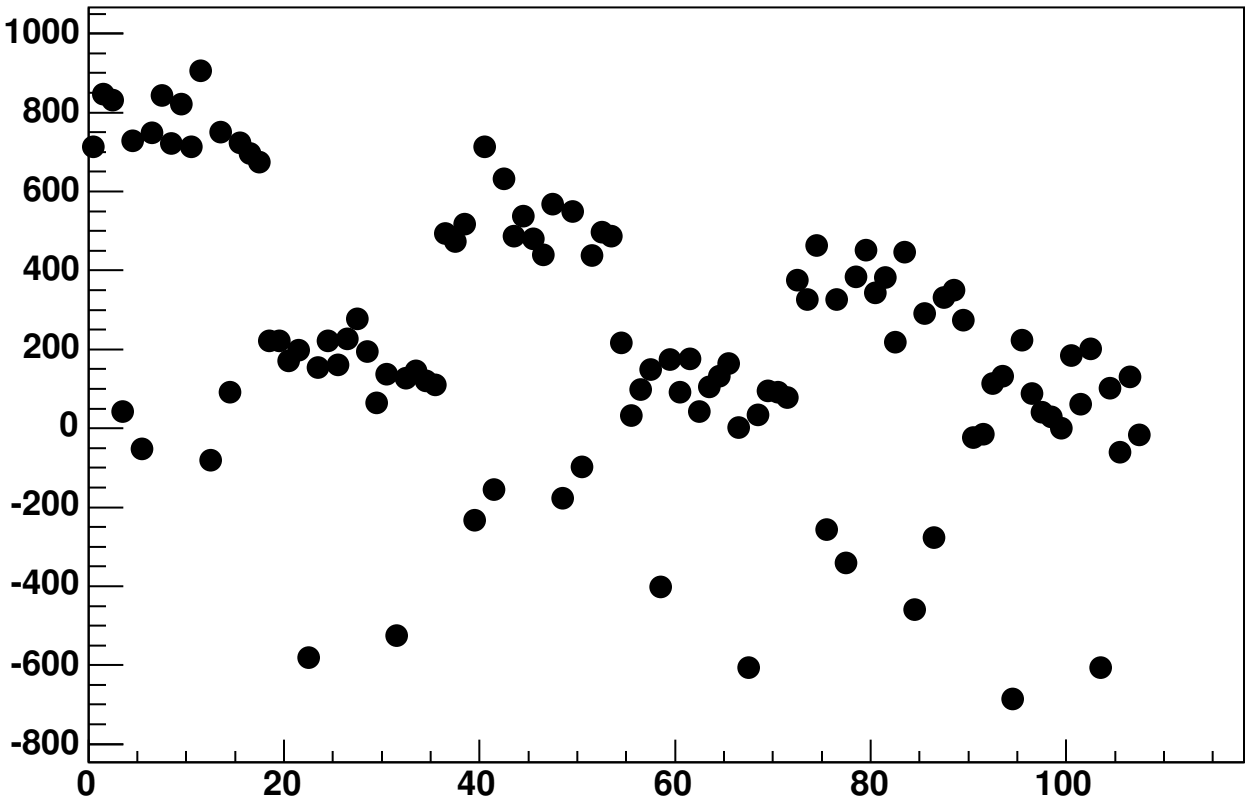
Enable 1, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



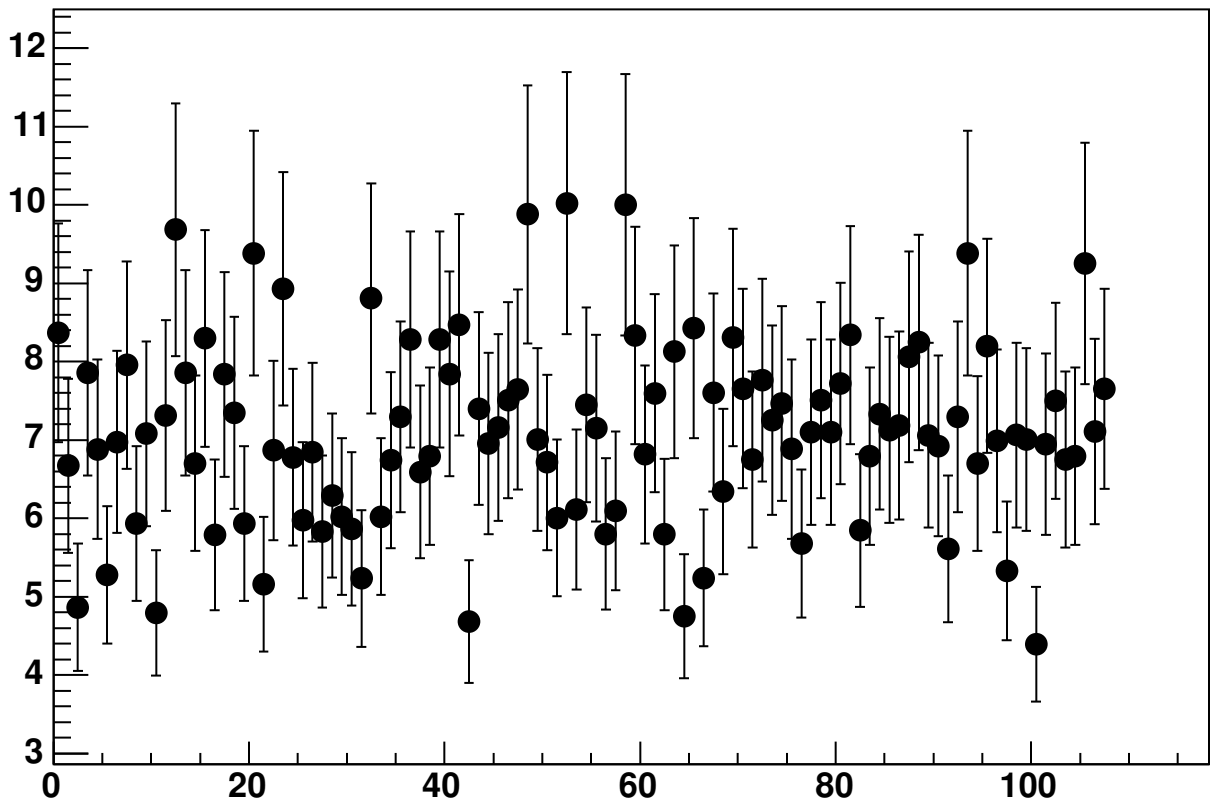
Enable 1, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



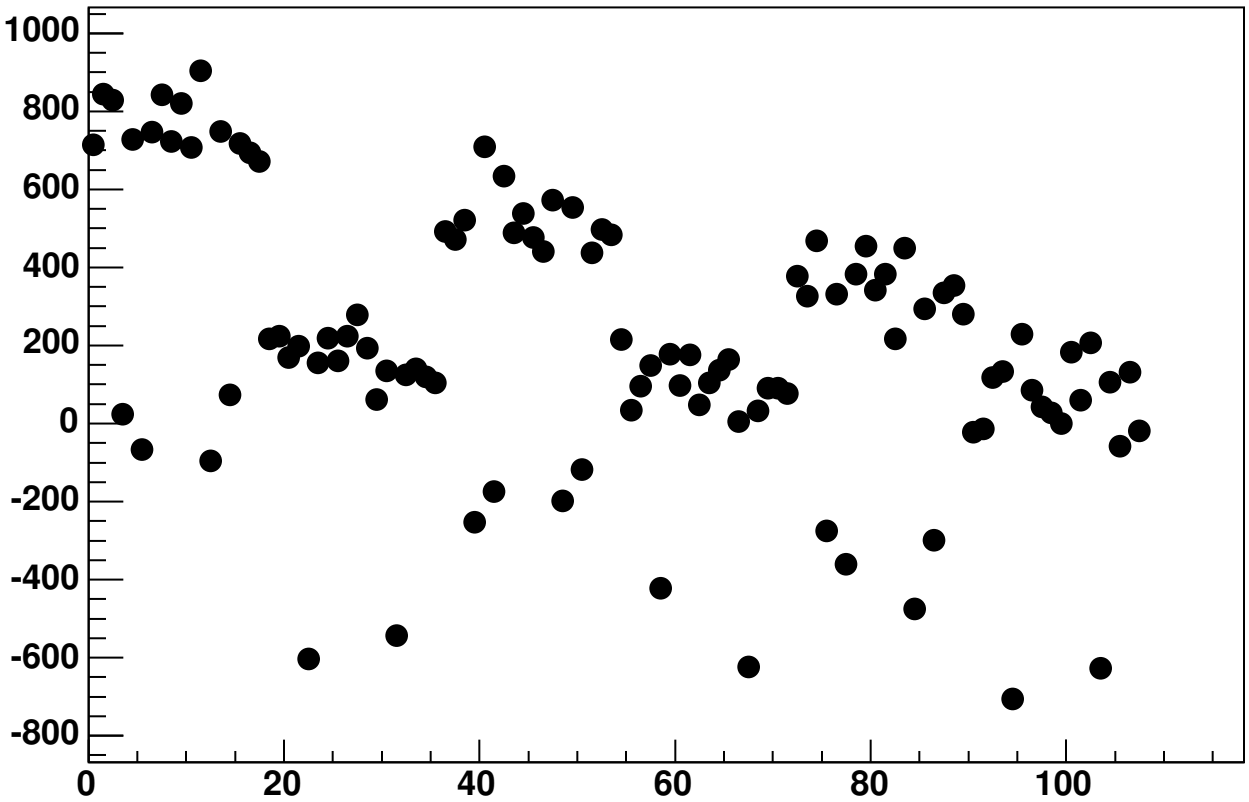
Enable 1, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



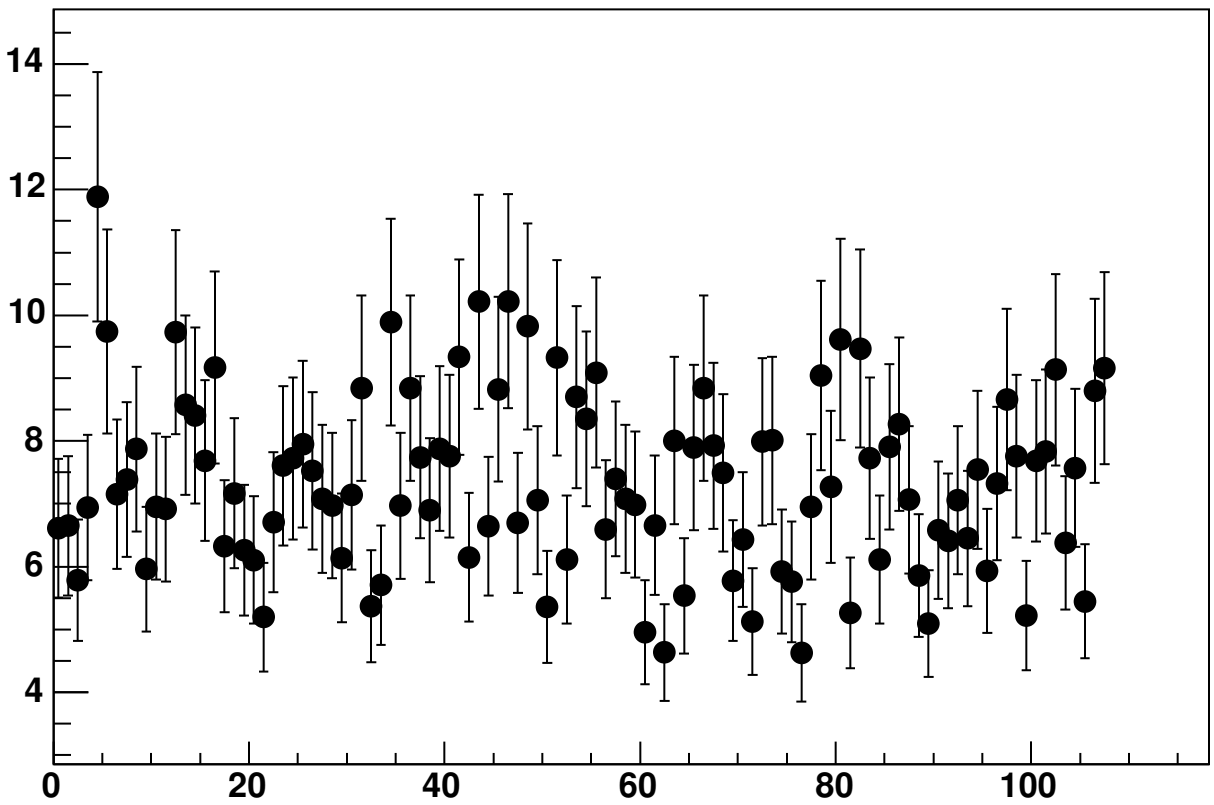
Enable 1, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



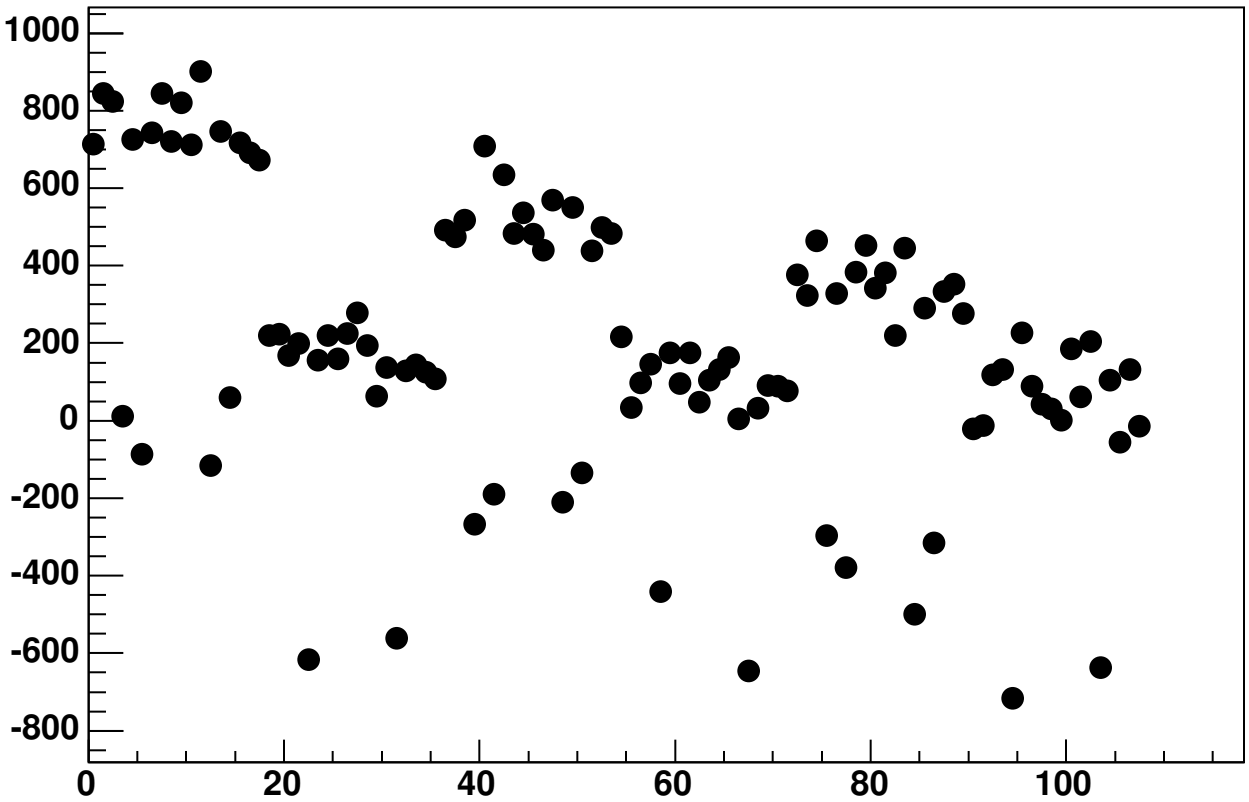
Enable 1, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



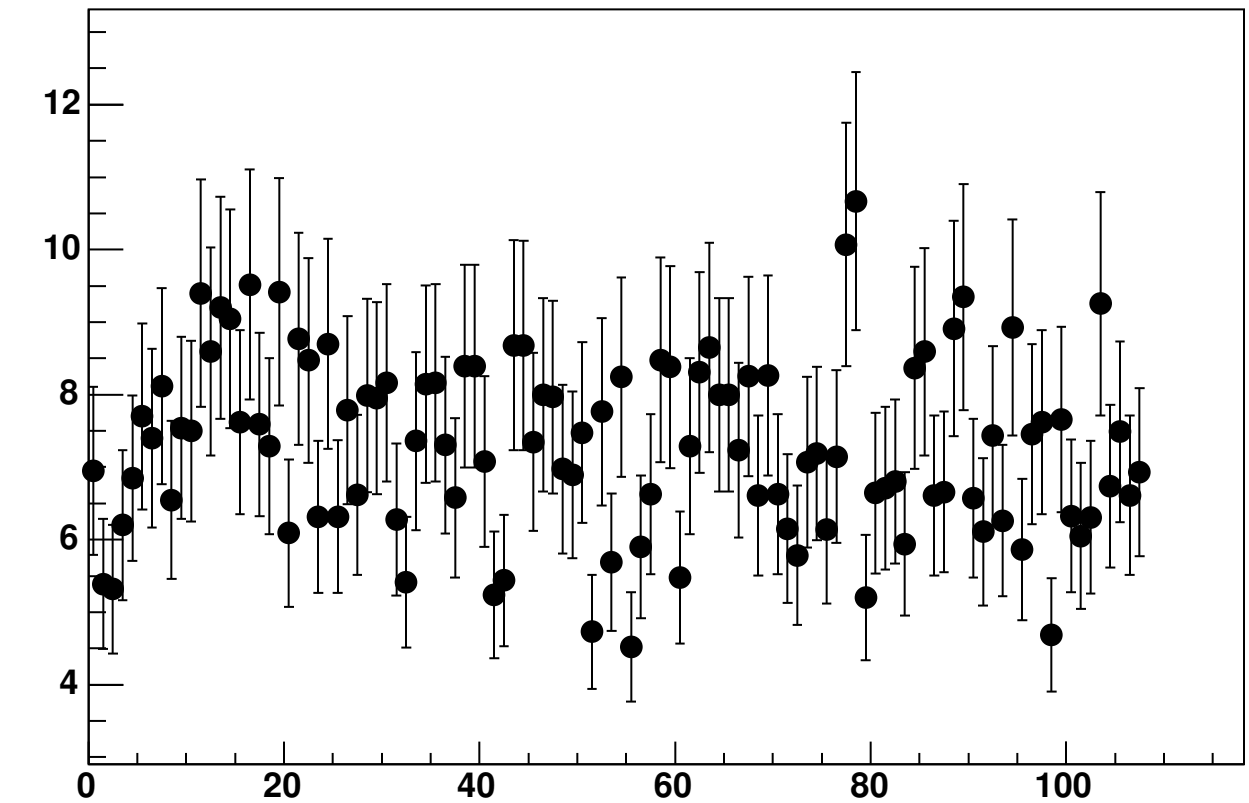
Enable 1, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



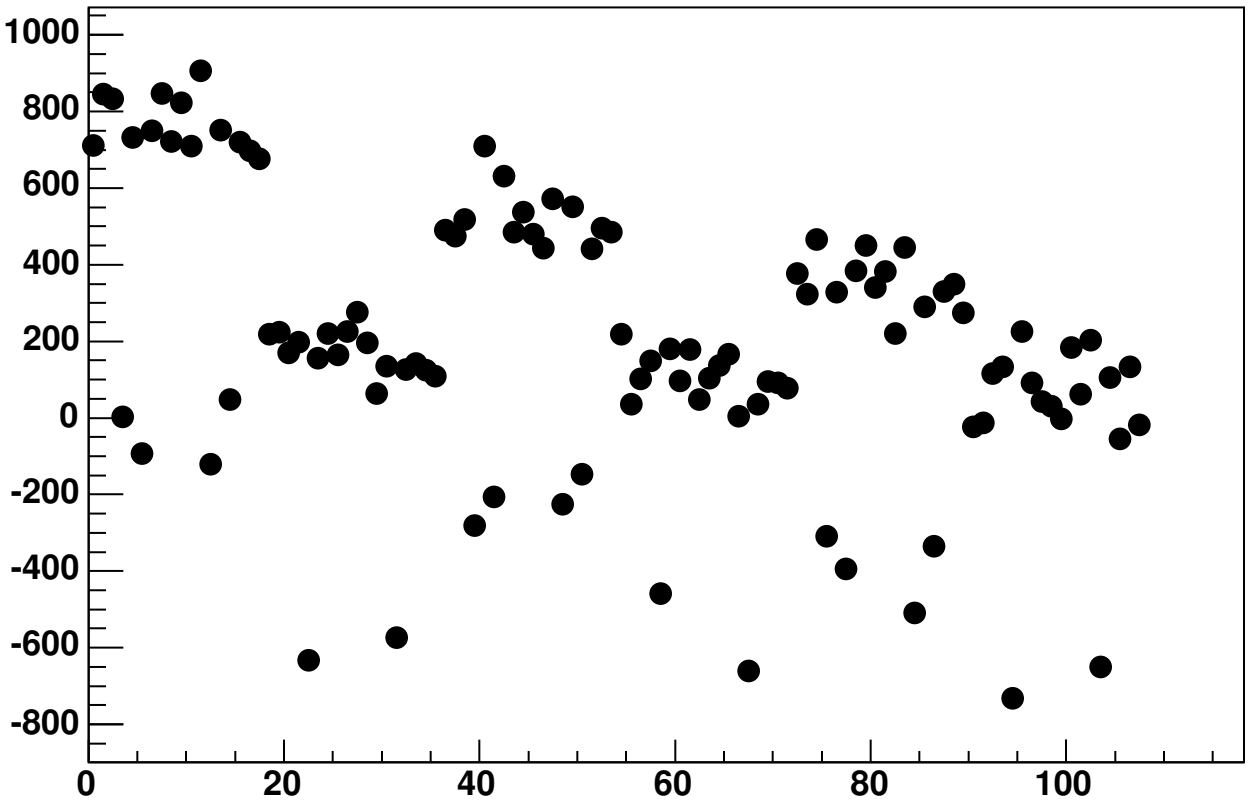
Enable 1, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



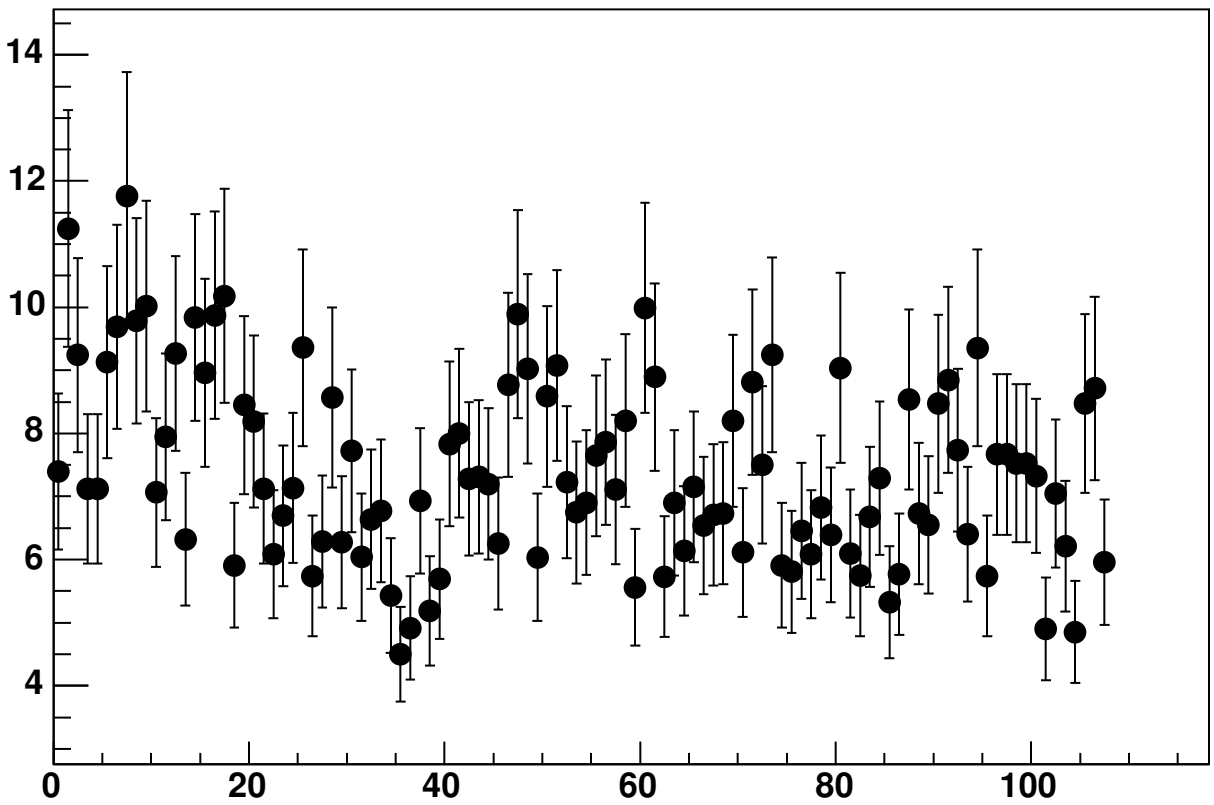
Enable 1, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan



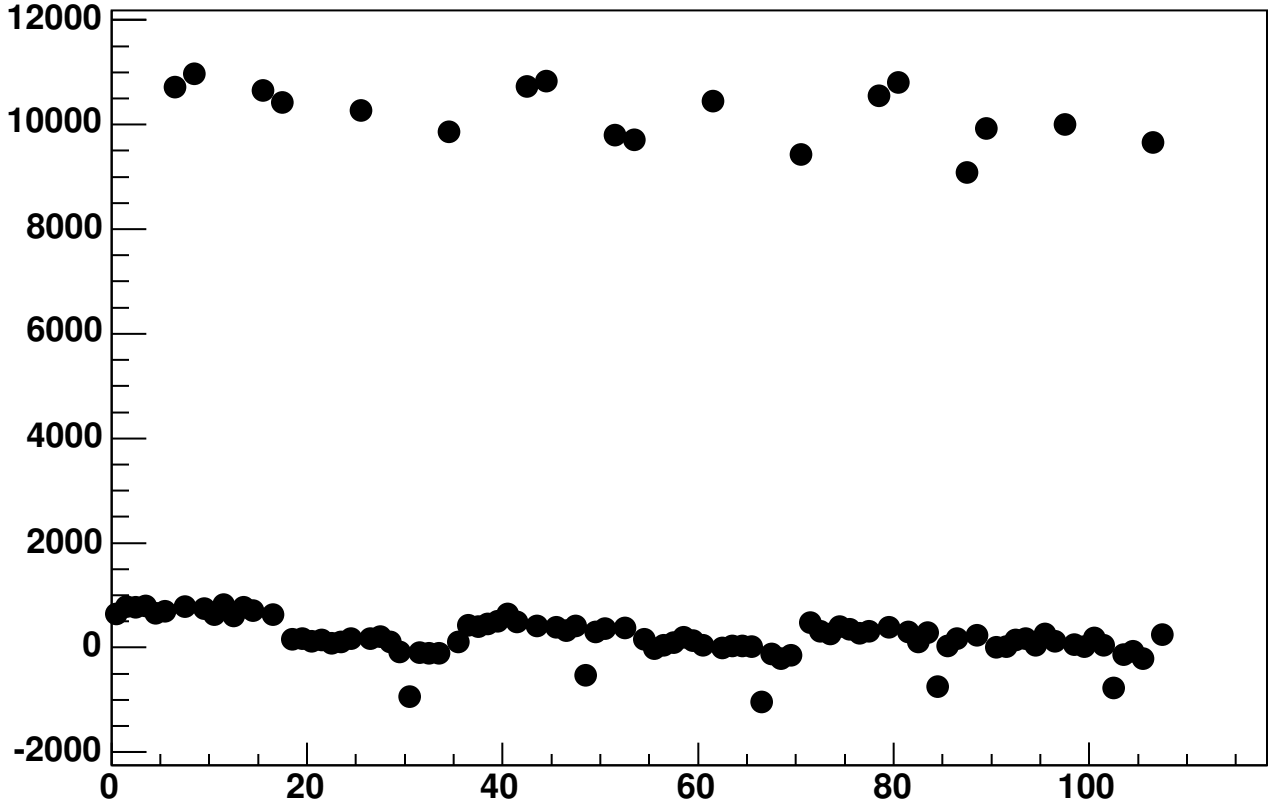
Enable 1, DAC=4000, Hold=245, ADC Mean vs 18*Chip+Chan



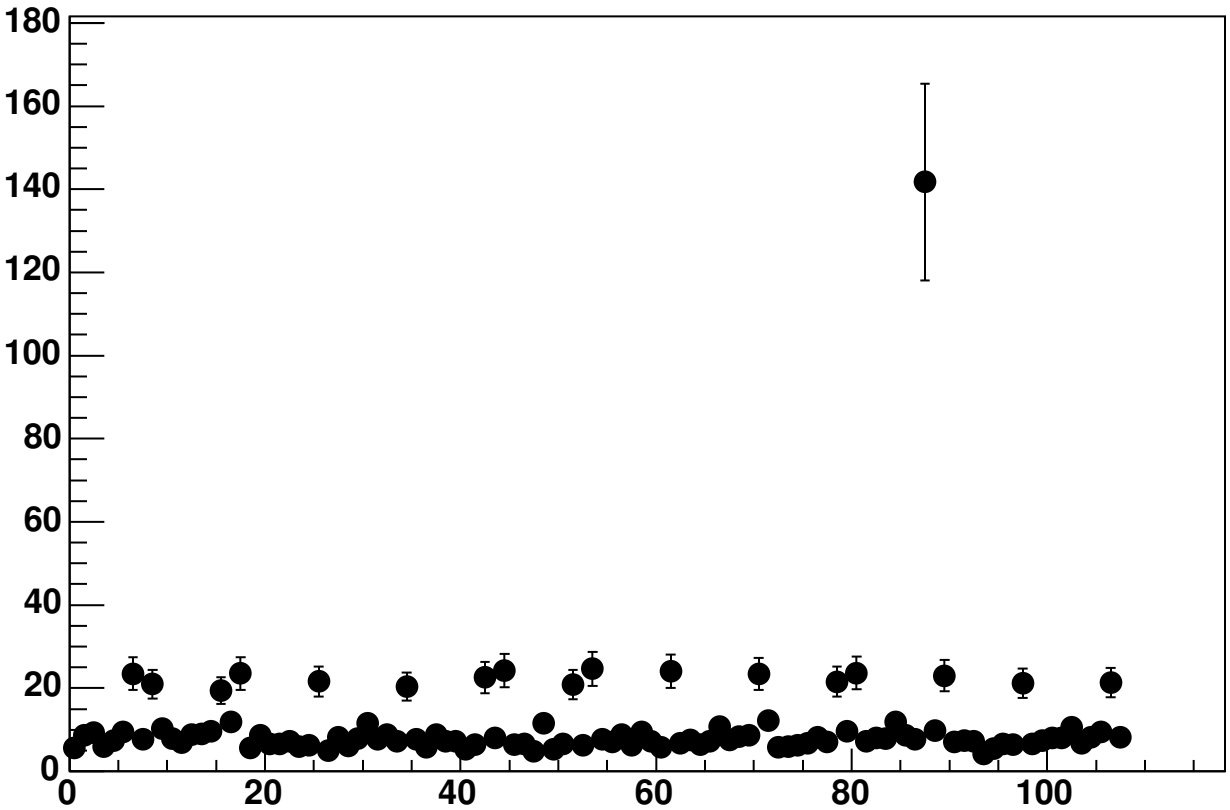
Enable 1, DAC=4000, Hold=245, ADC Noise vs 18*Chip+Chan



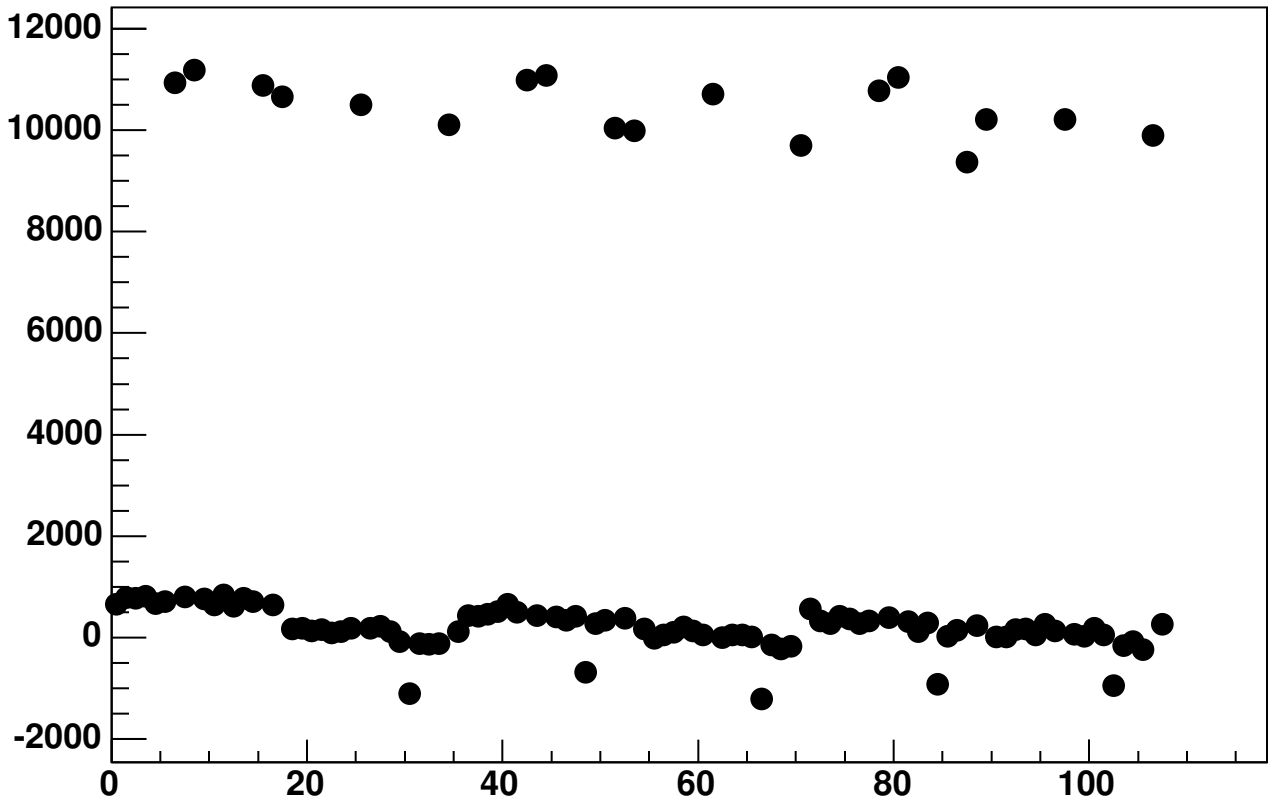
Enable 2, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



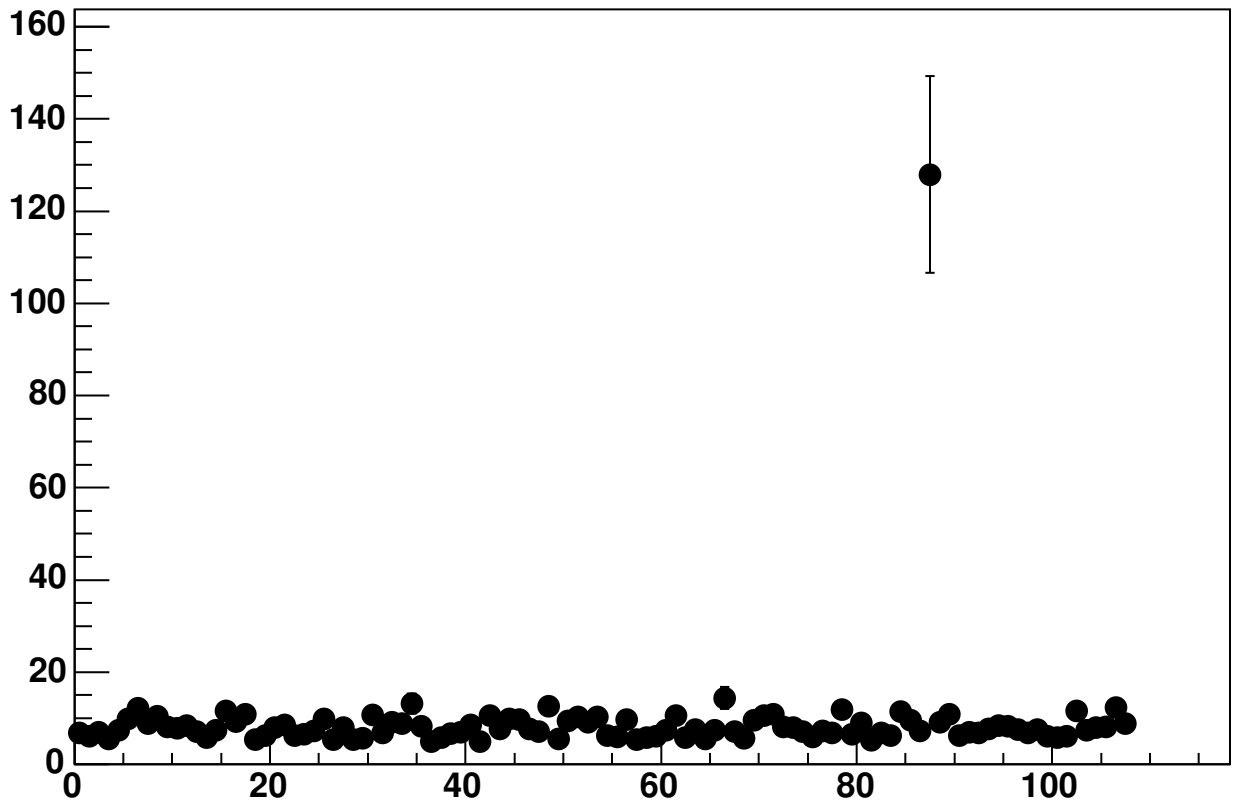
Enable 2, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



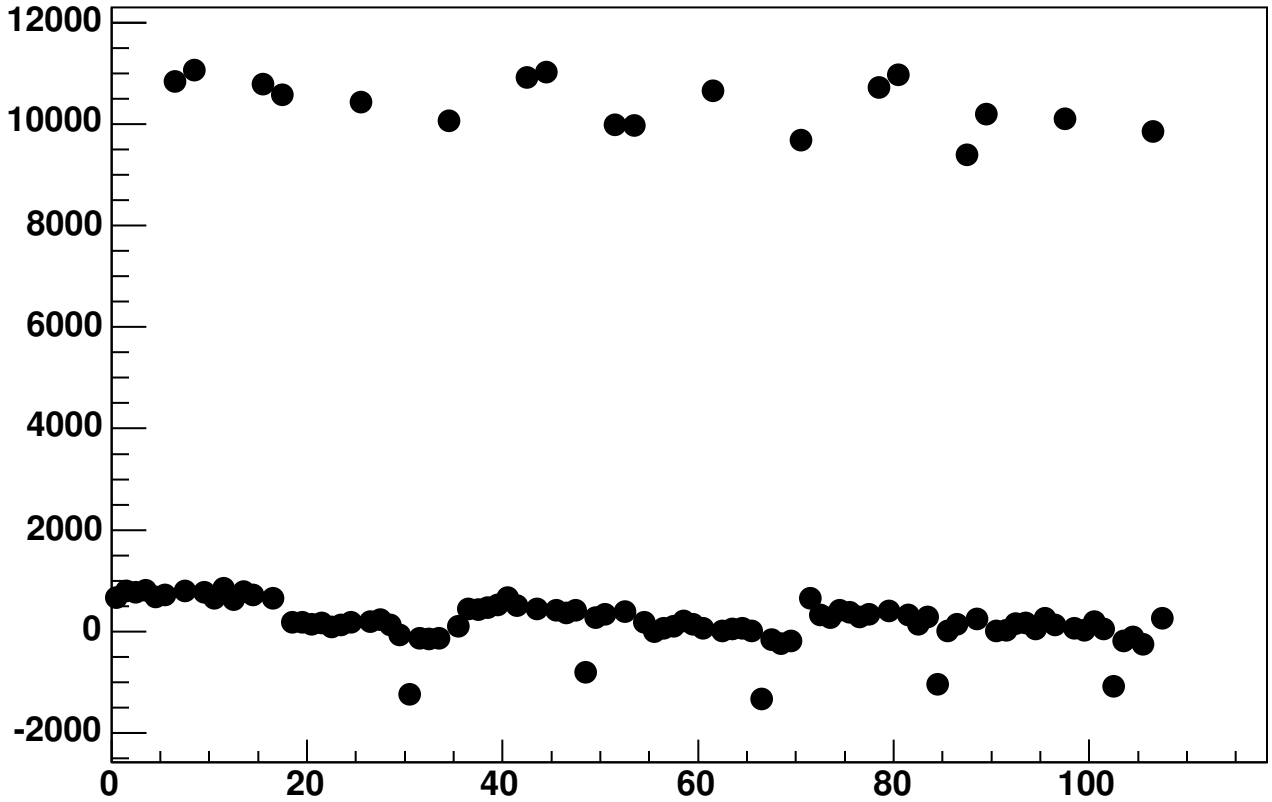
Enable 2, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



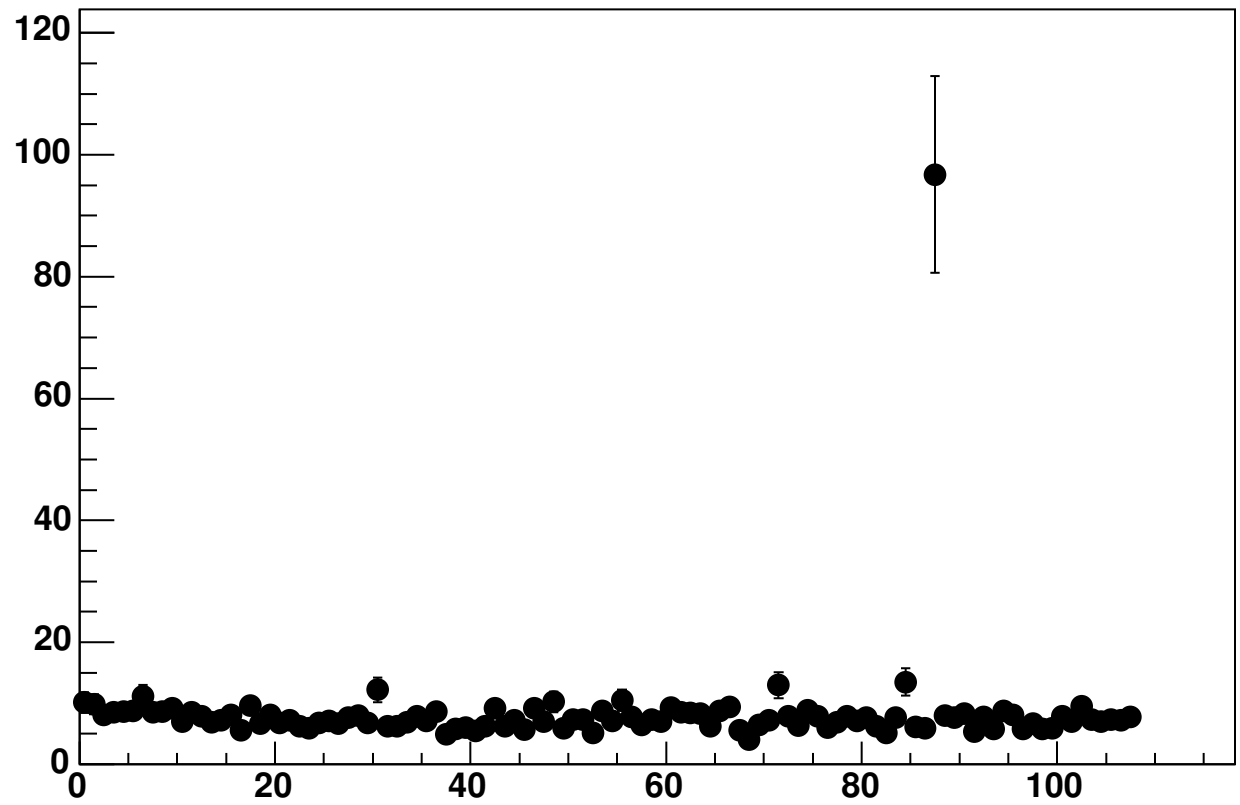
Enable 2, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



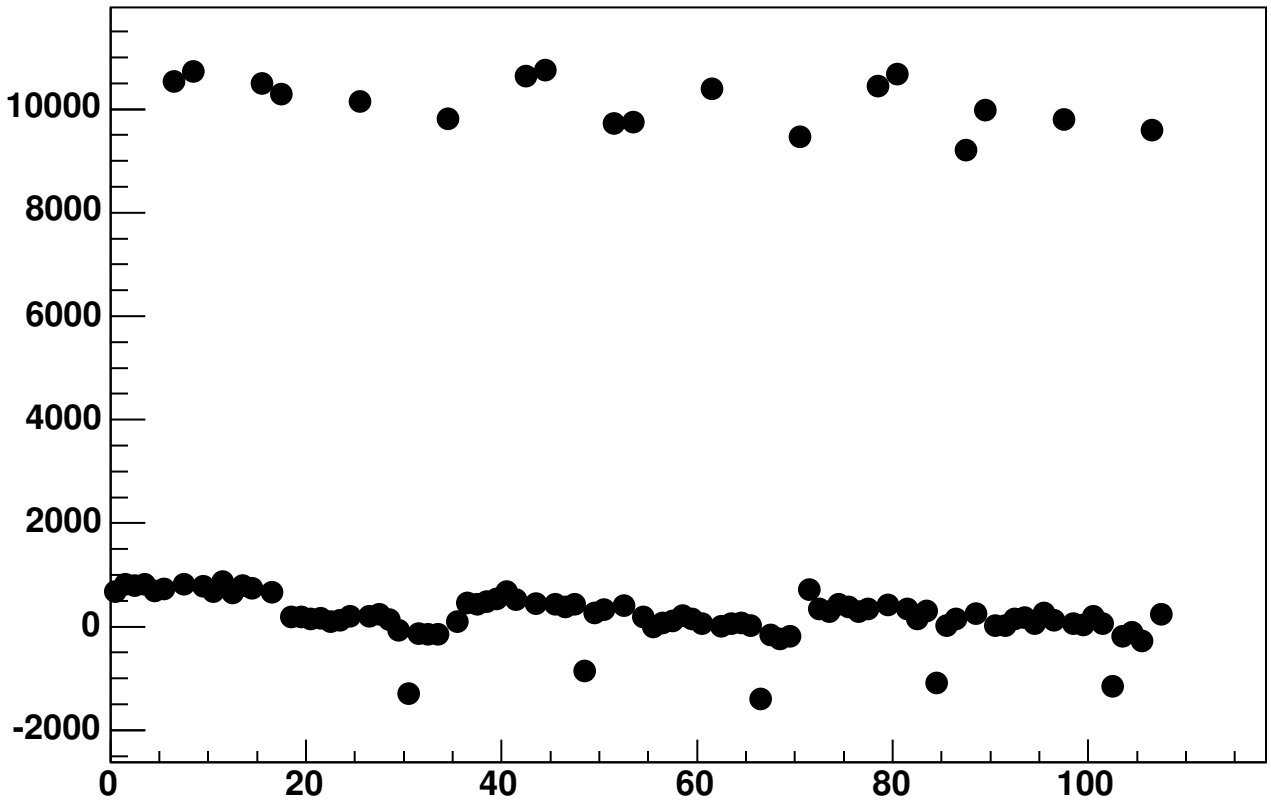
Enable 2, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



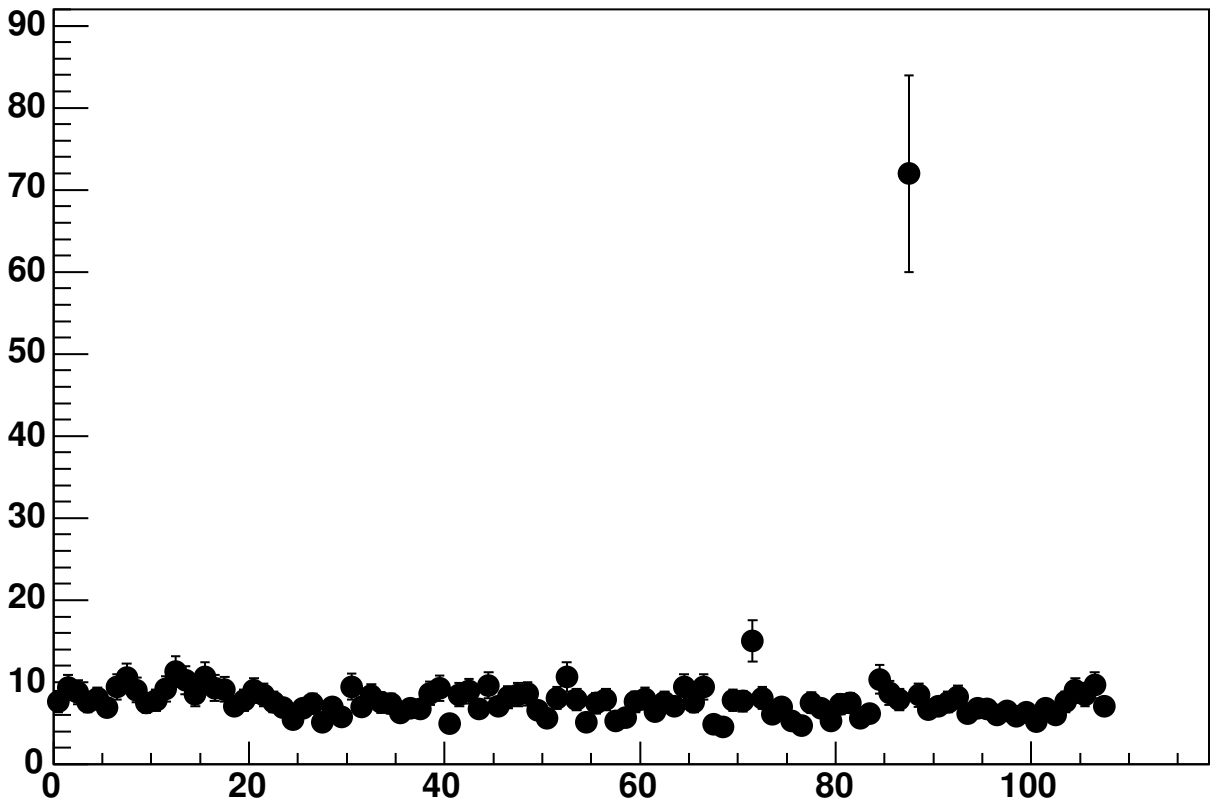
Enable 2, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



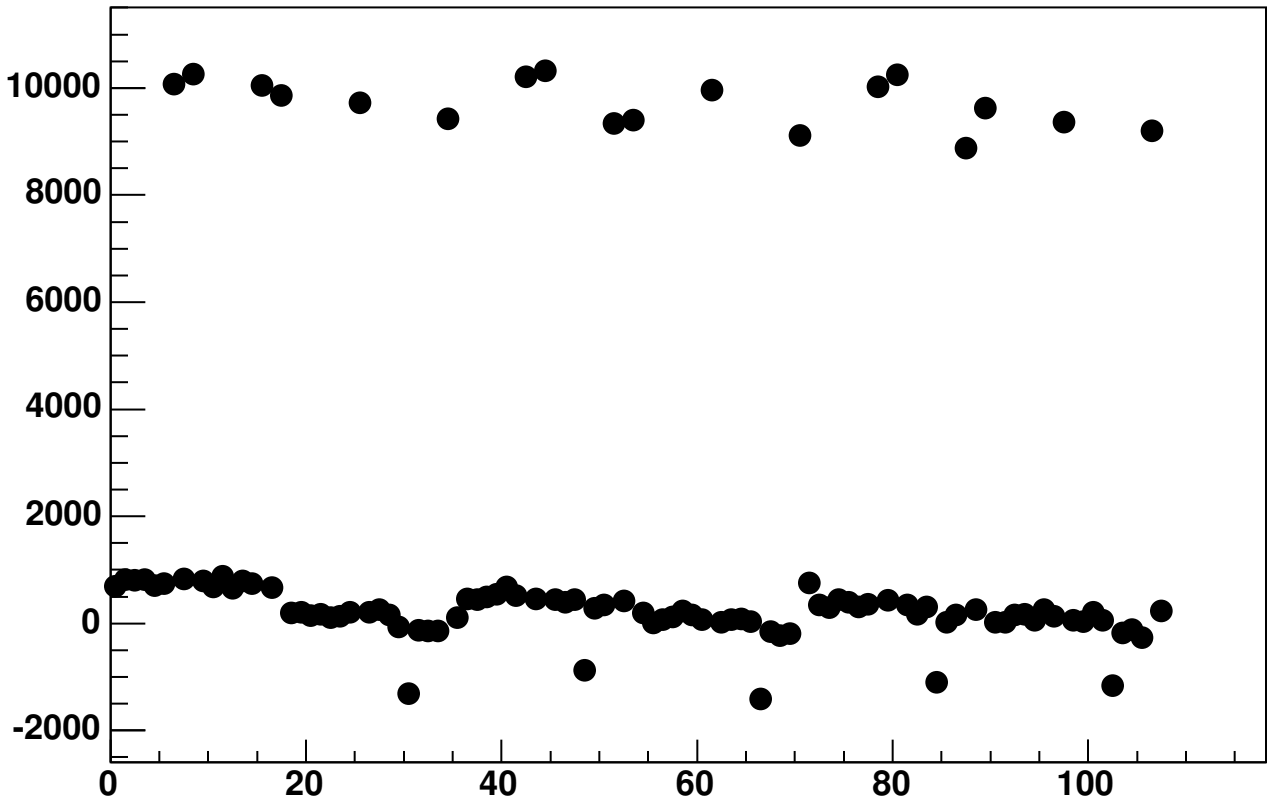
Enable 2, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



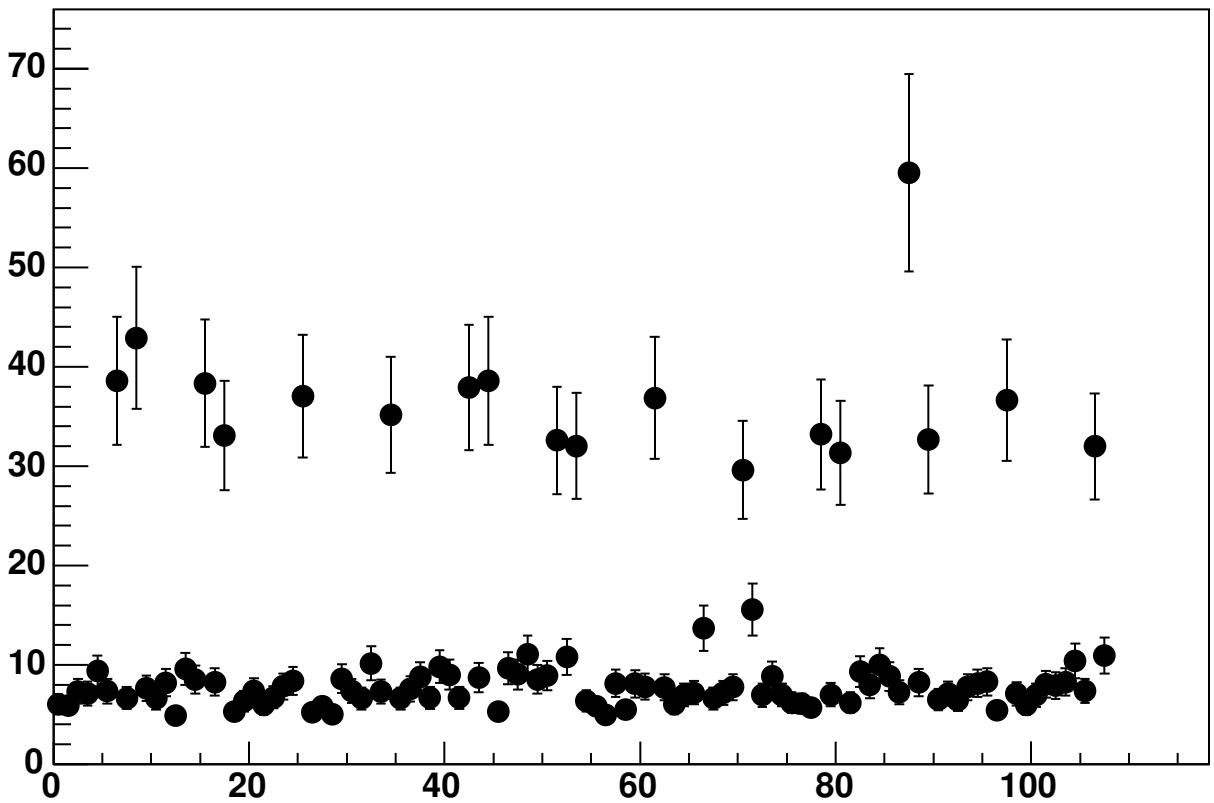
Enable 2, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



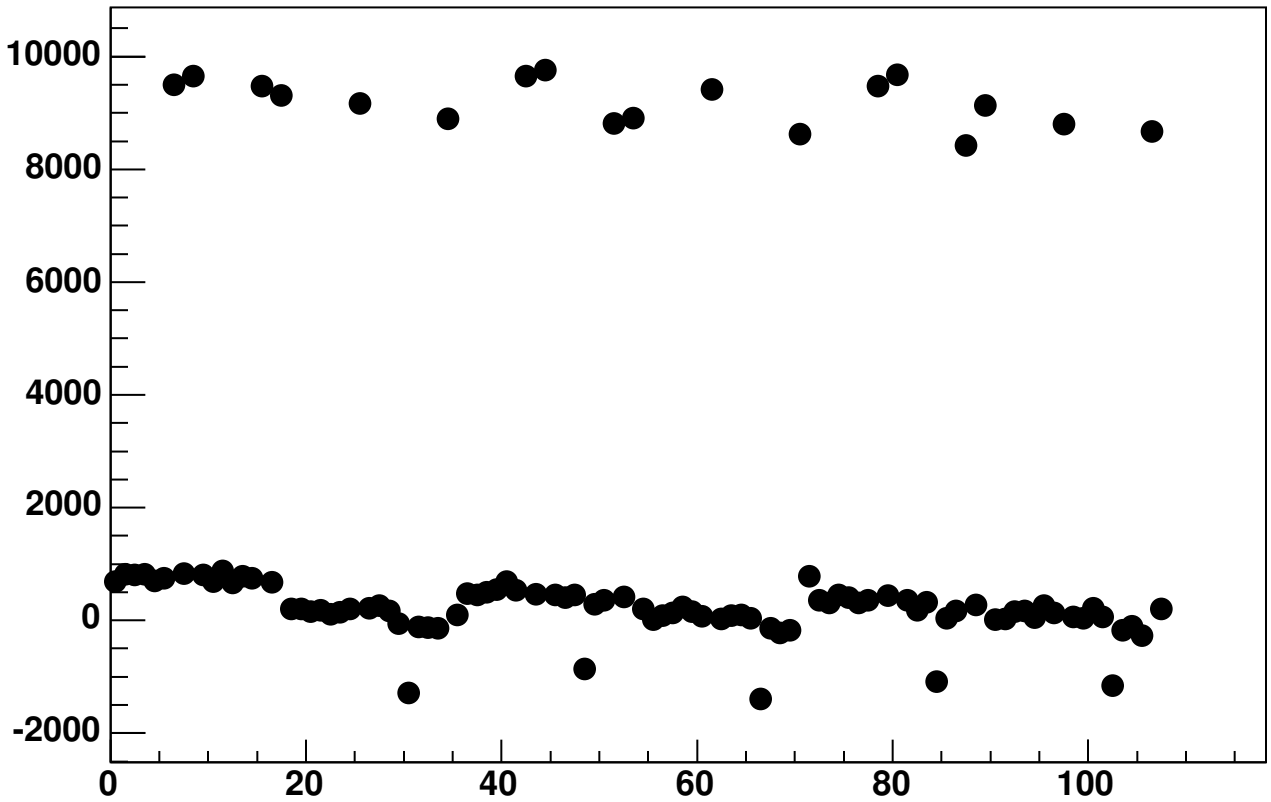
Enable 2, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



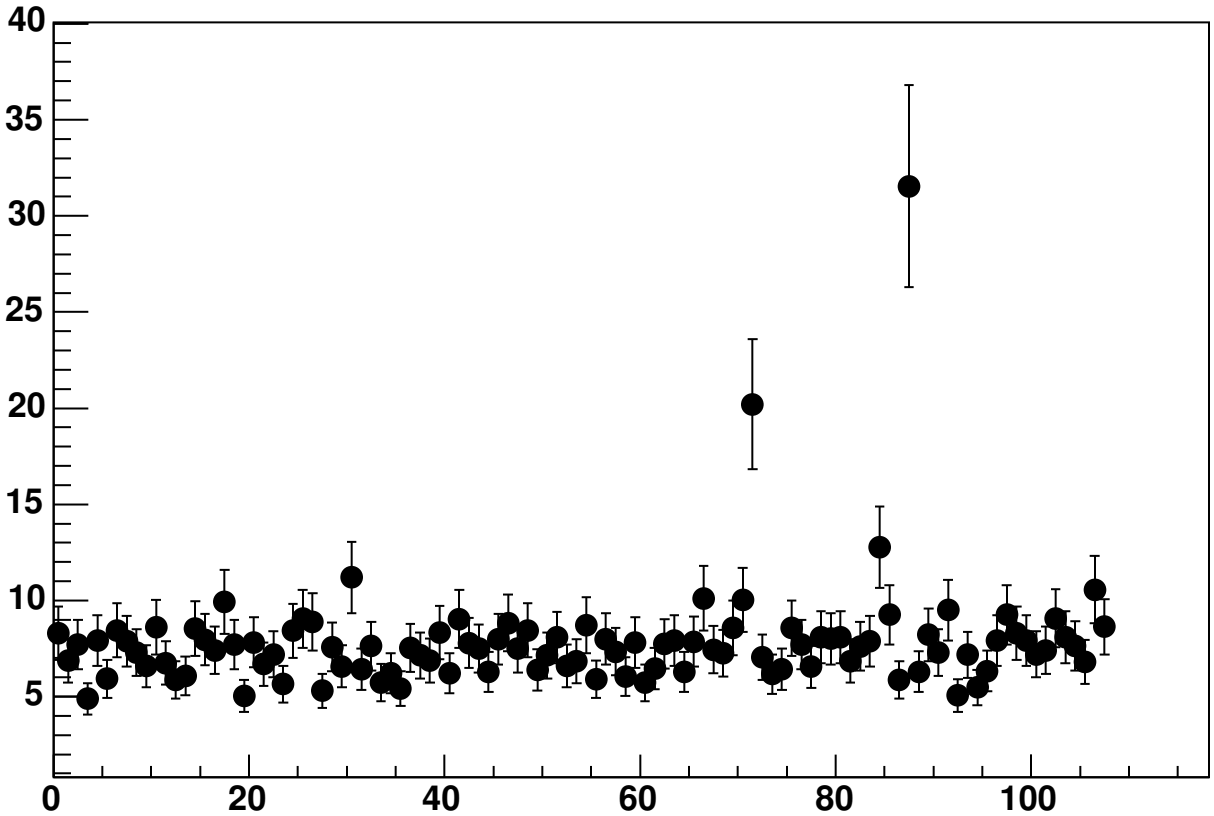
Enable 2, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



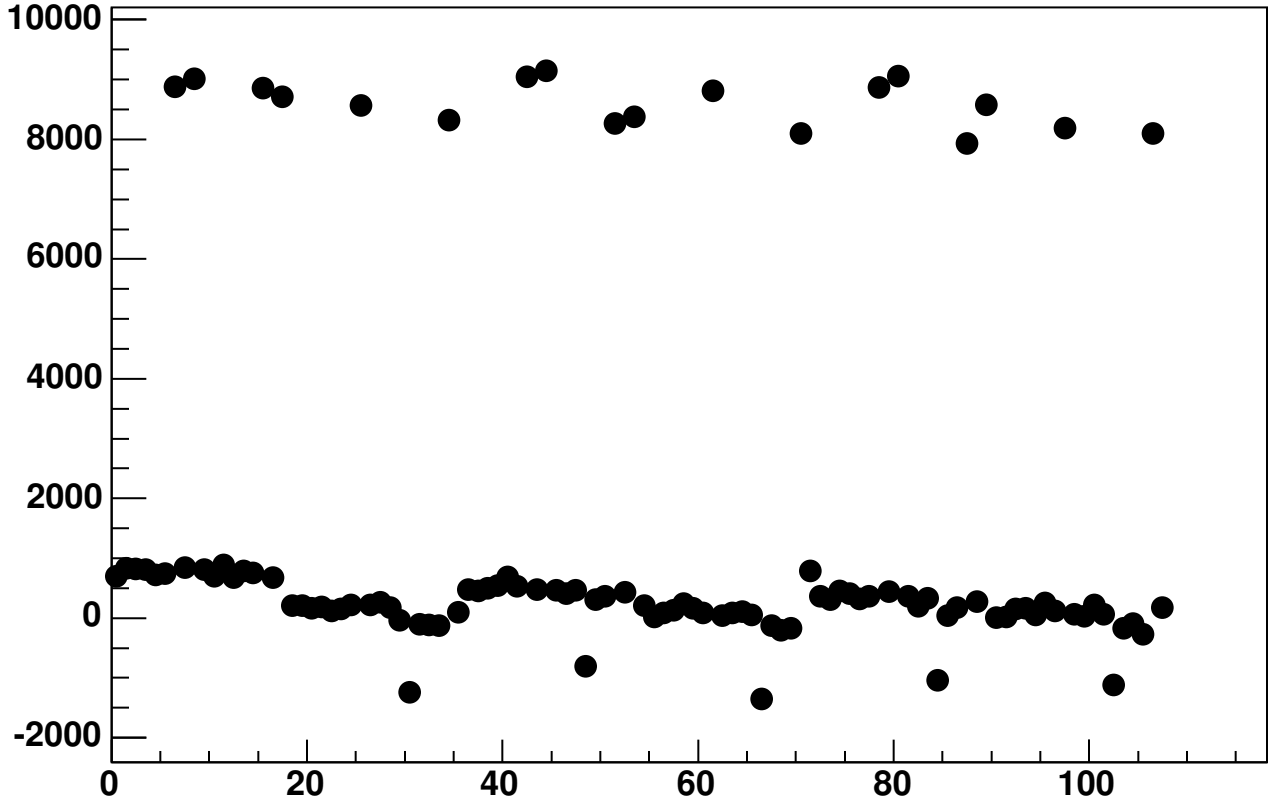
Enable 2, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



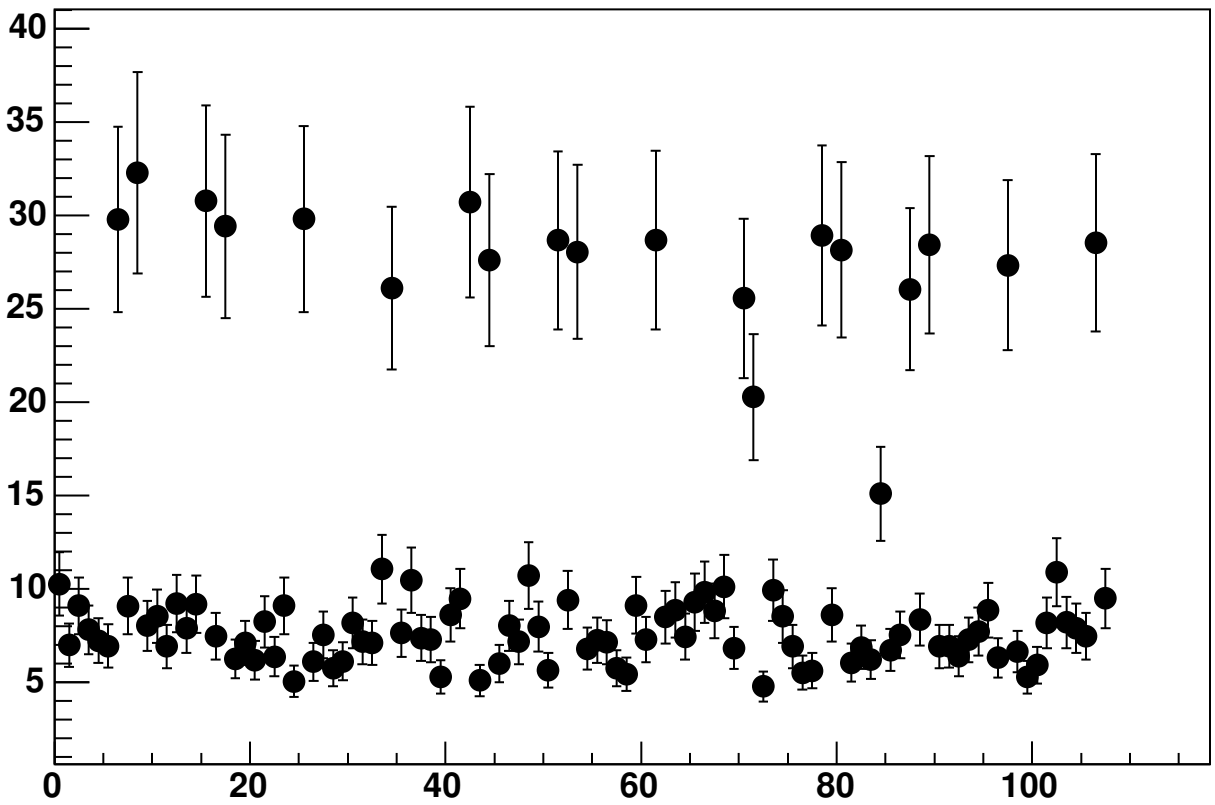
Enable 2, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



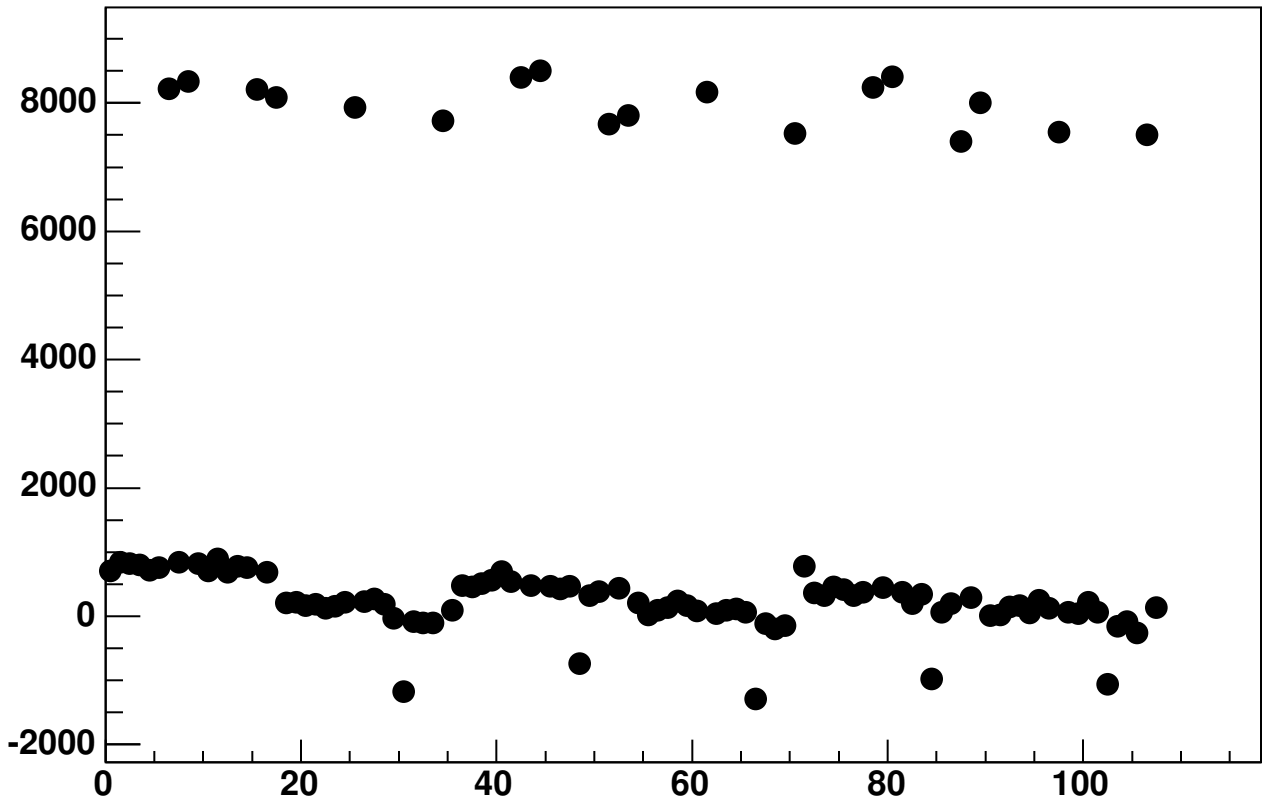
Enable 2, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



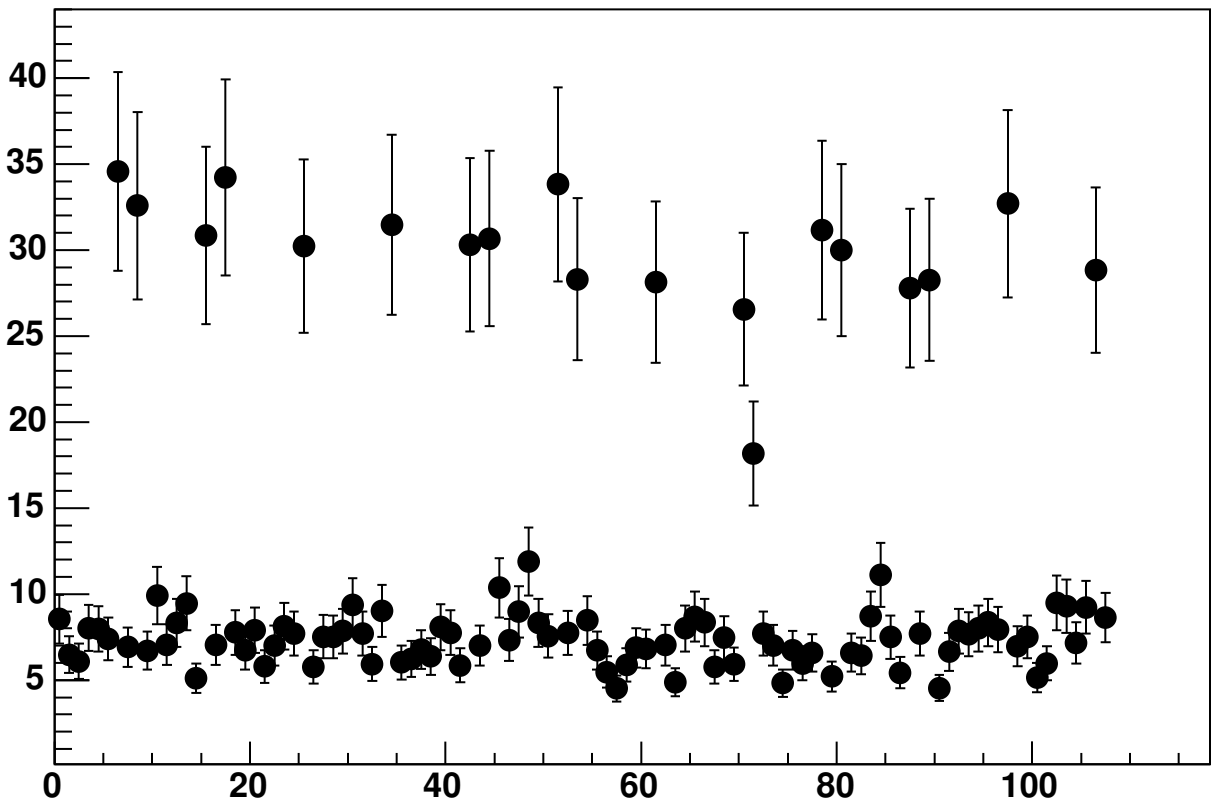
Enable 2, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



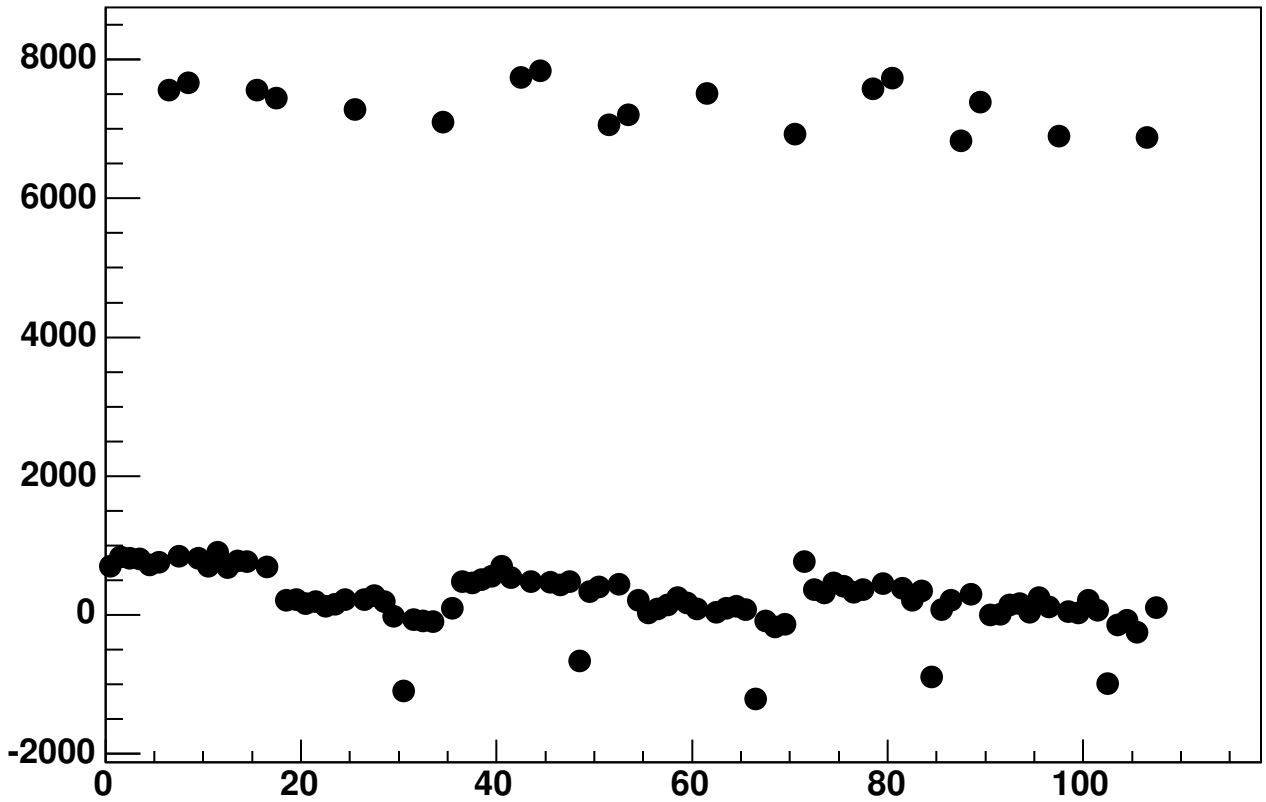
Enable 2, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



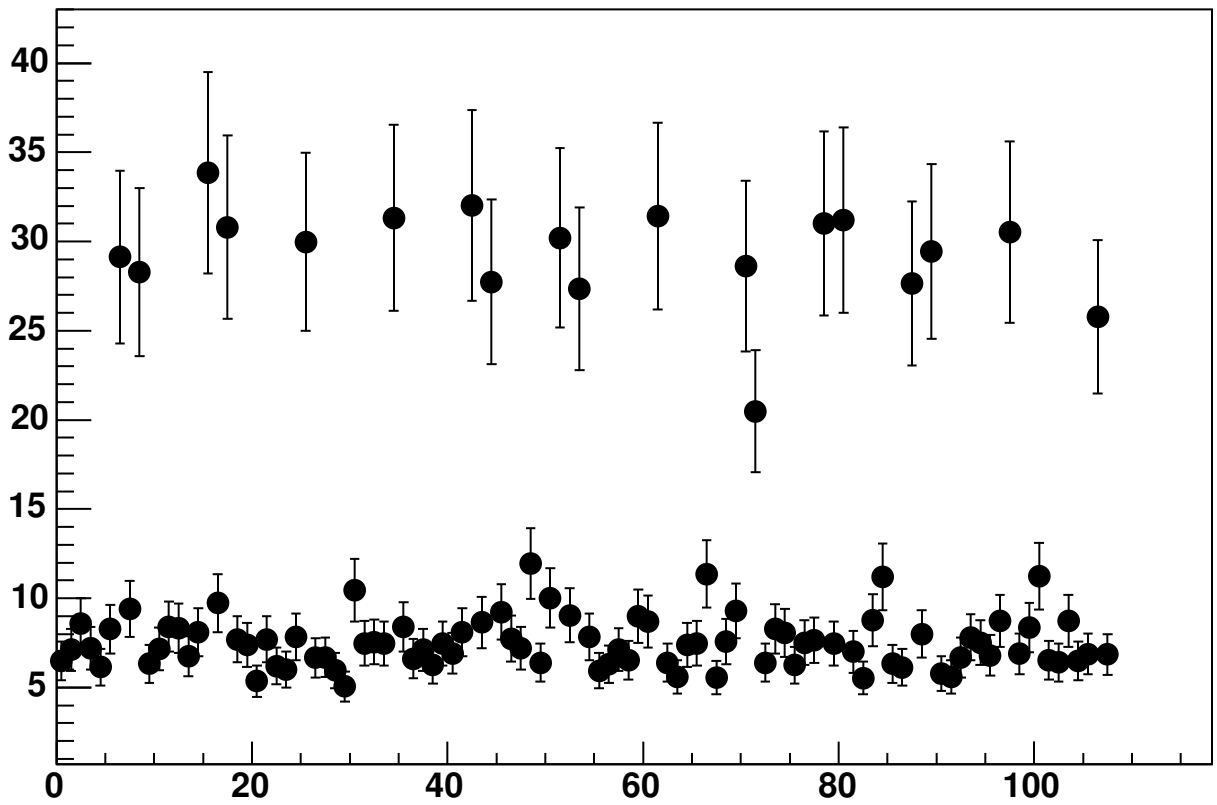
Enable 2, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



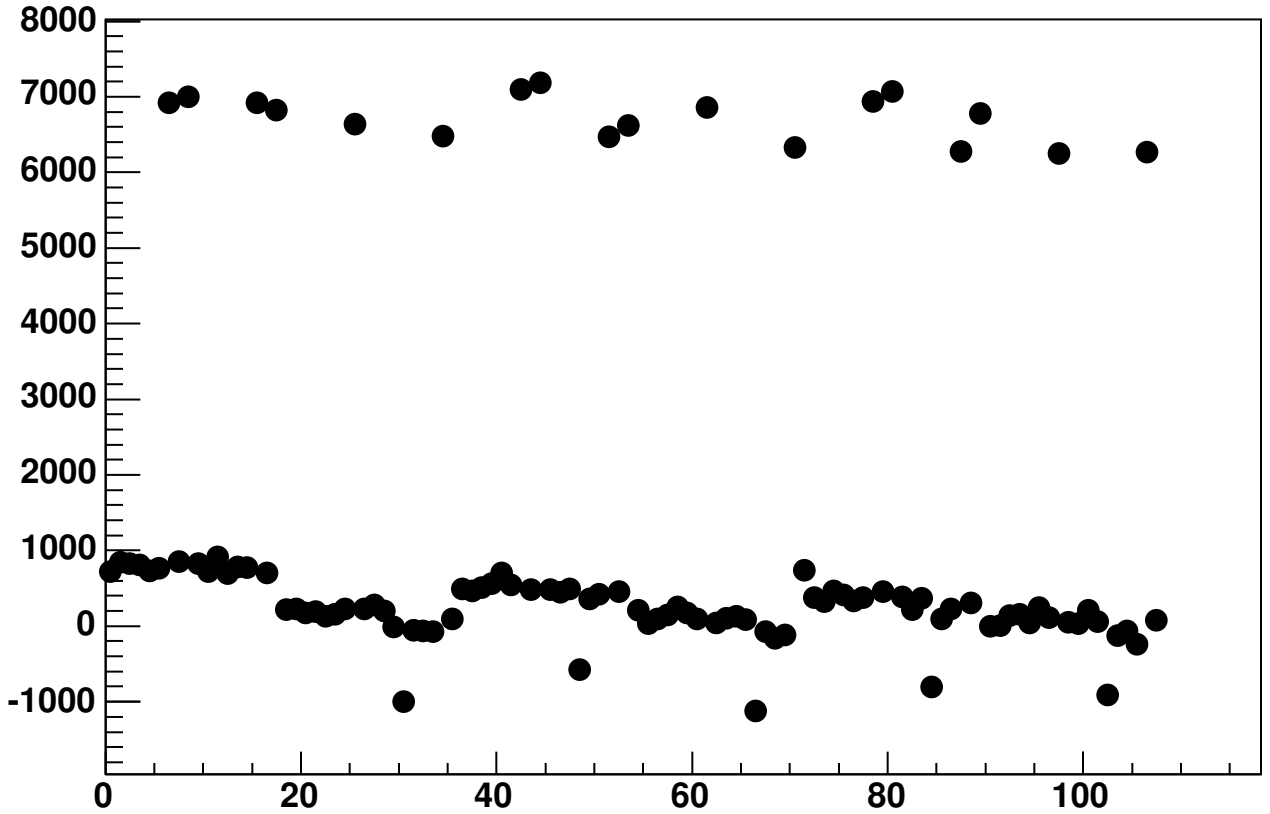
Enable 2, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



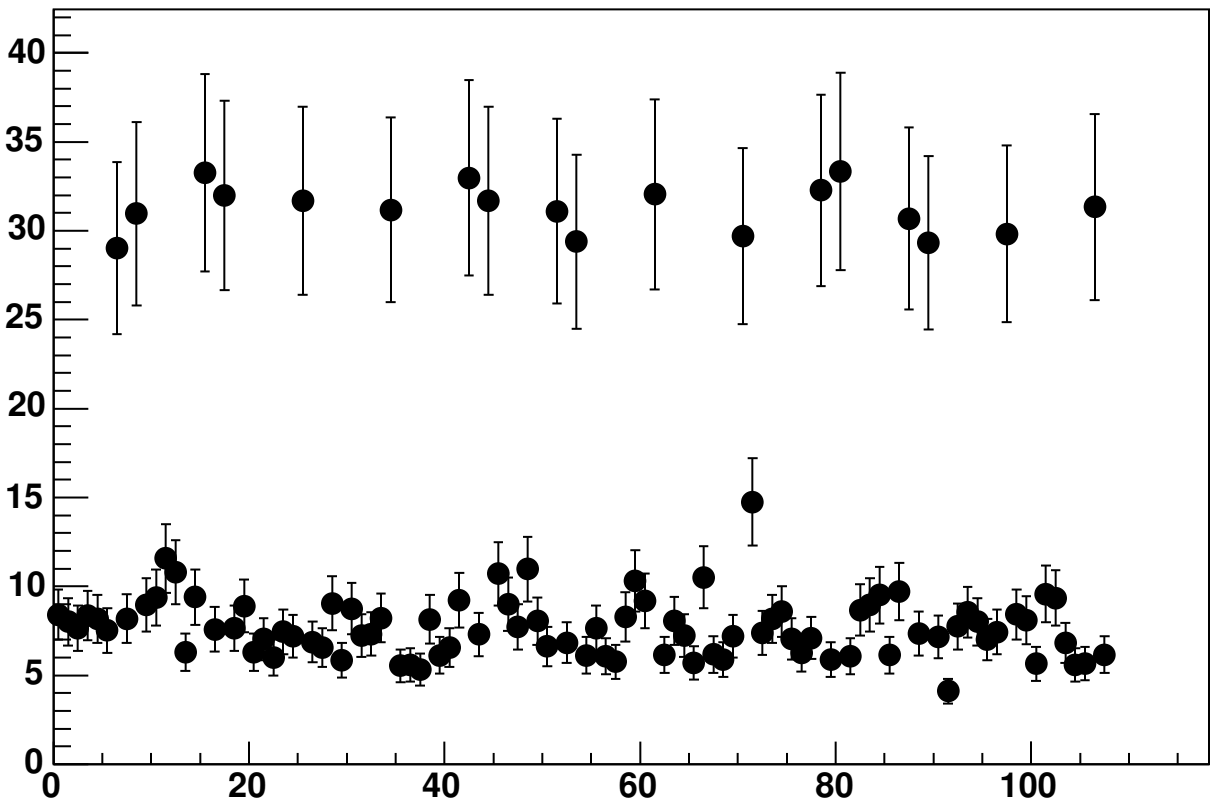
Enable 2, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



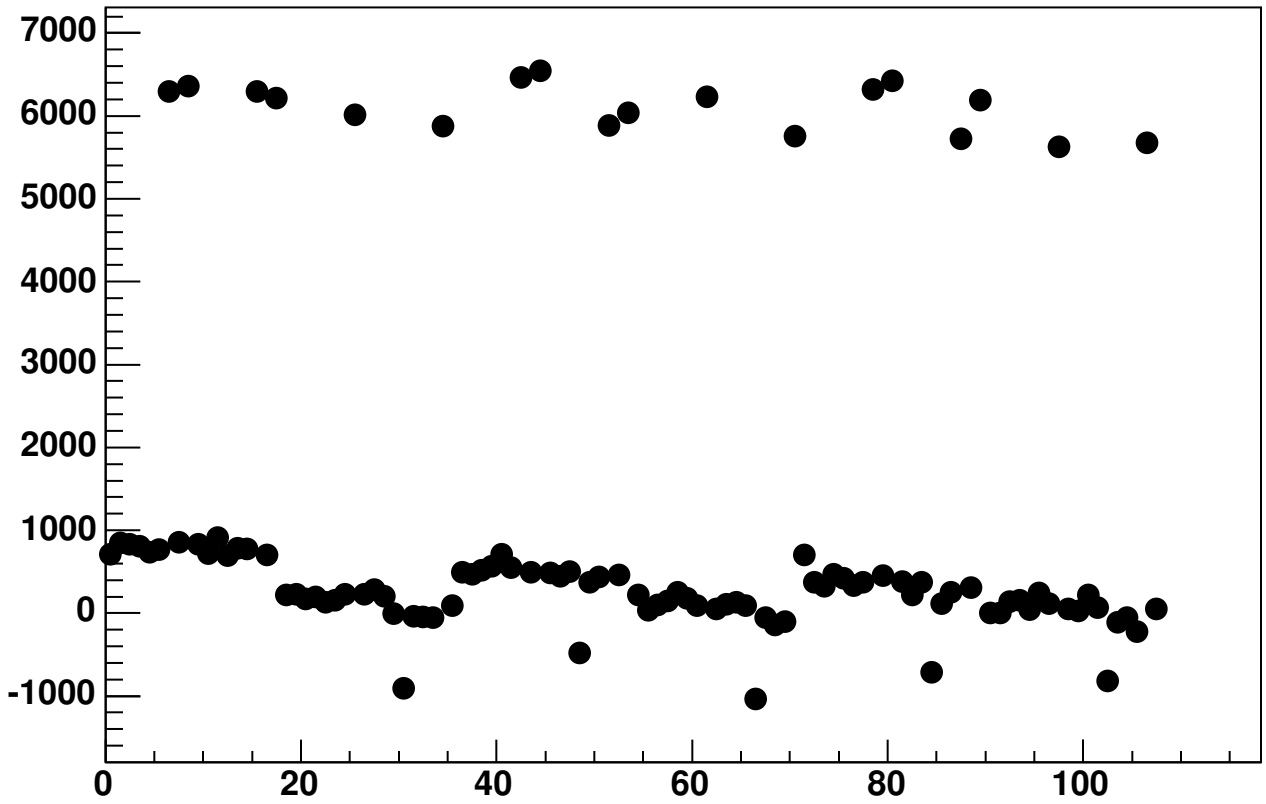
Enable 2, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



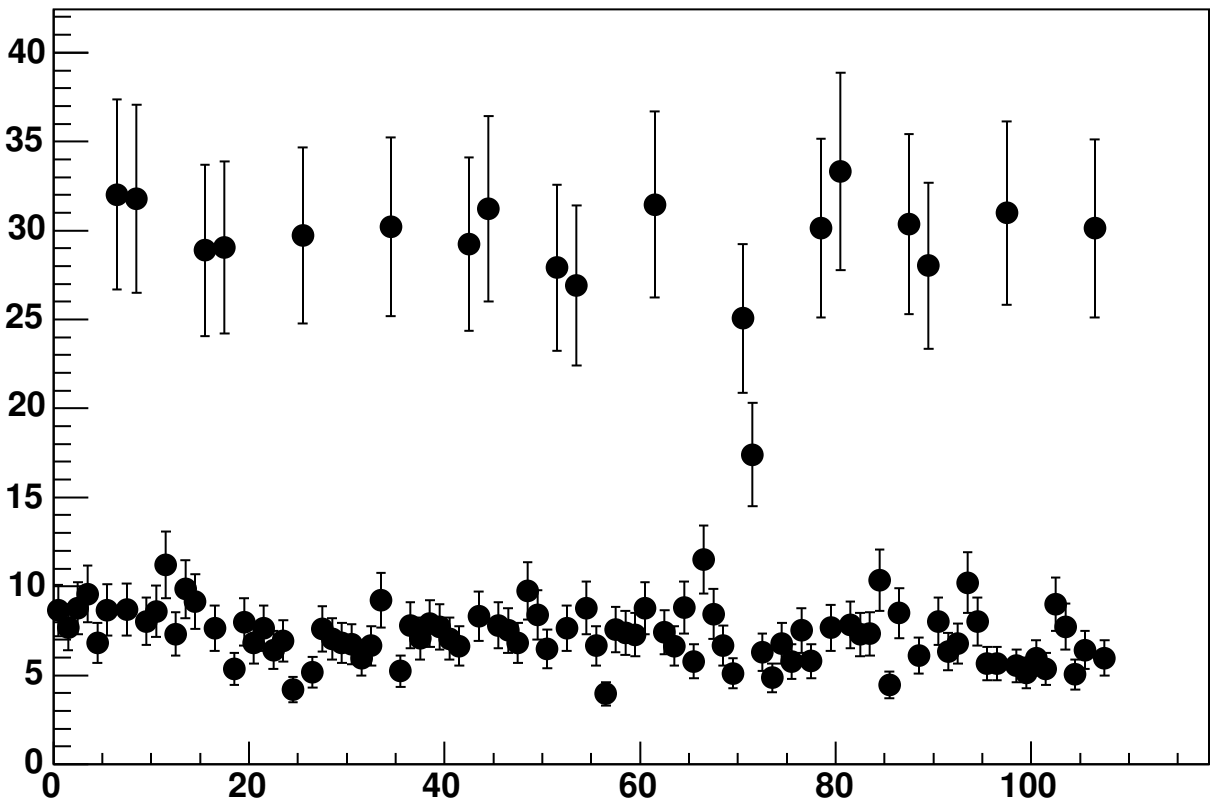
Enable 2, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



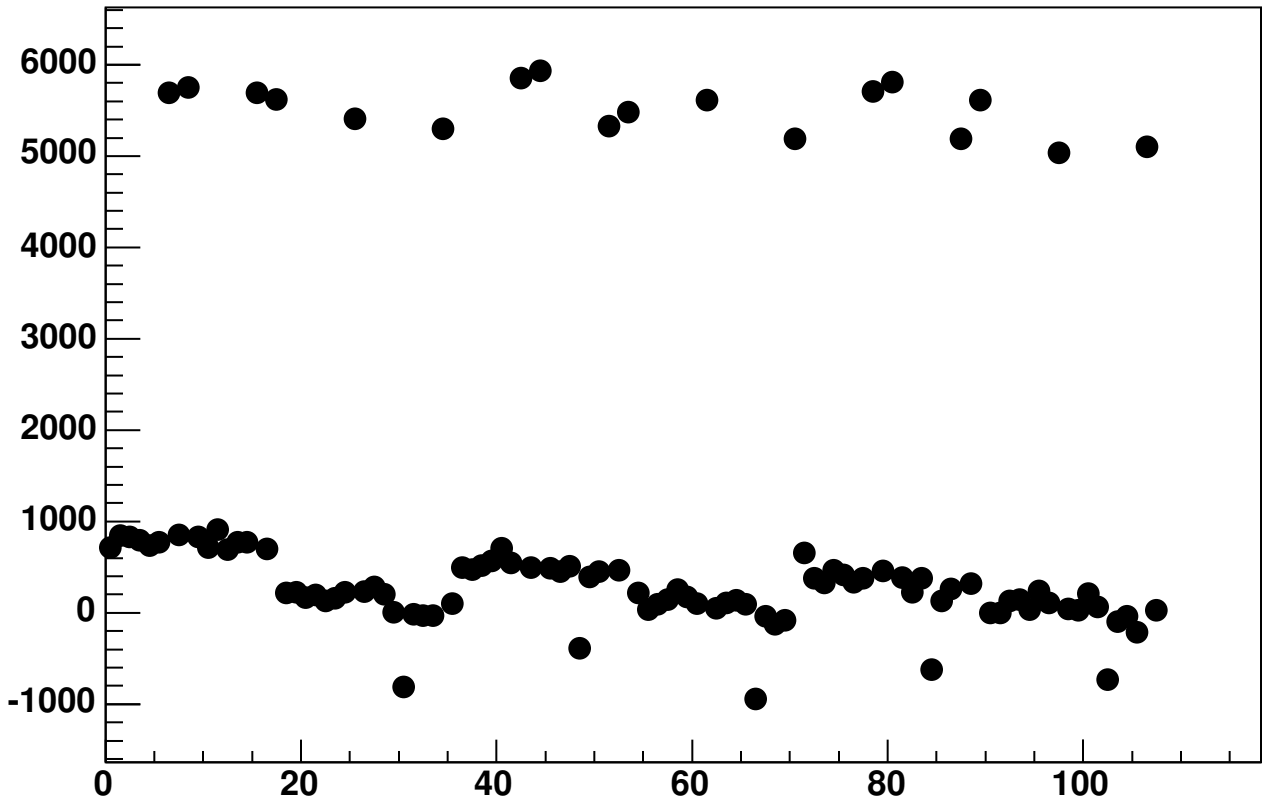
Enable 2, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



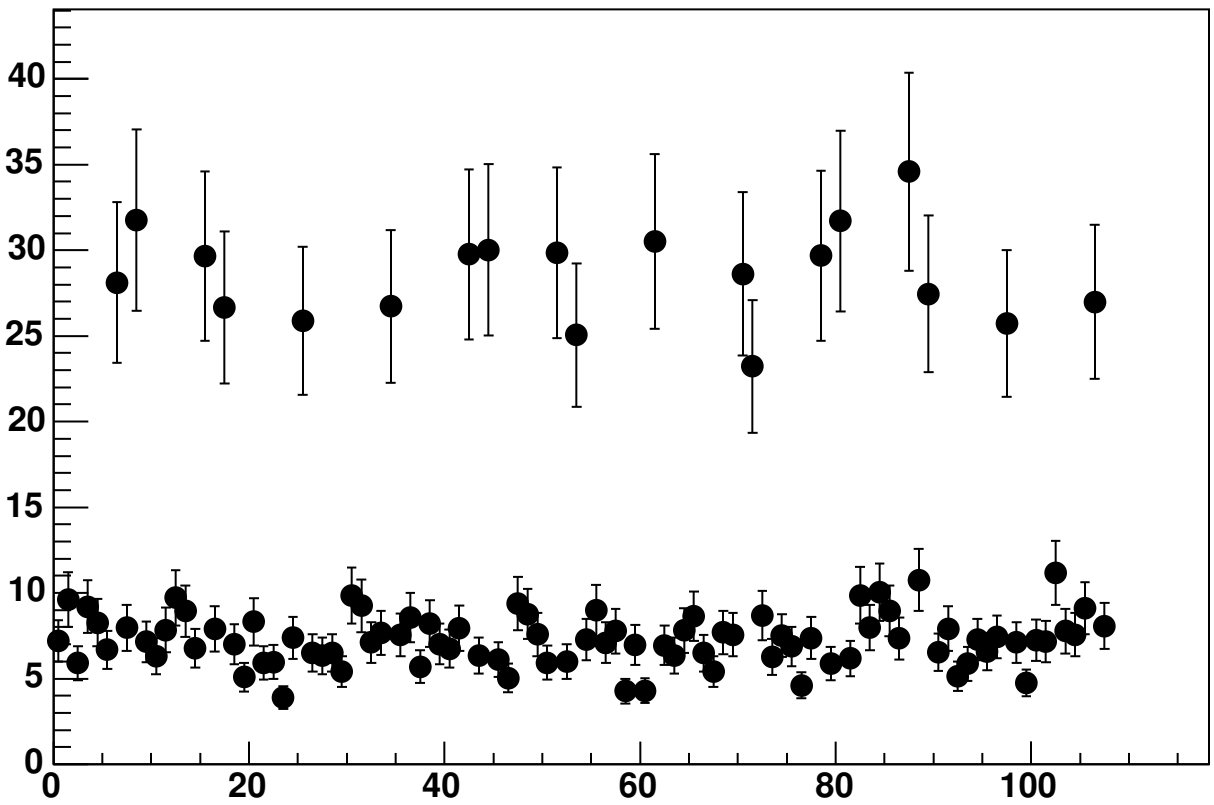
Enable 2, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



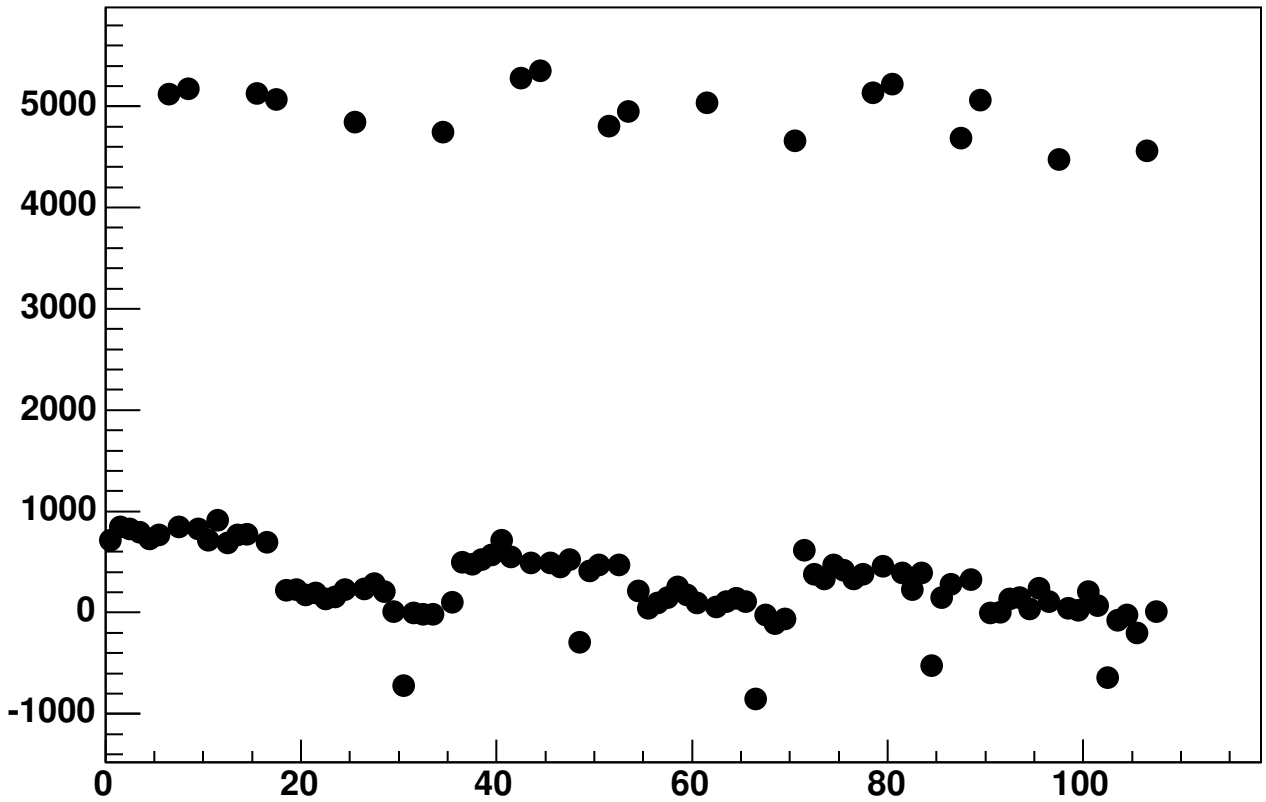
Enable 2, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



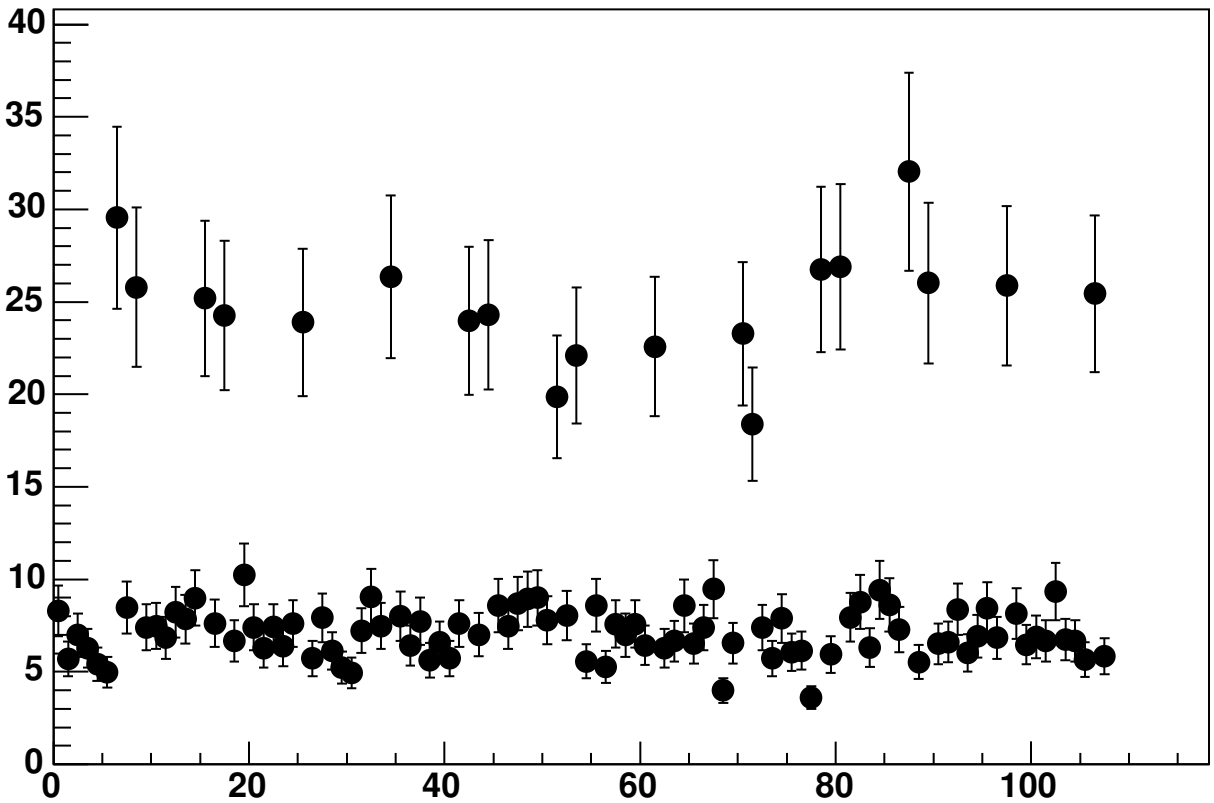
Enable 2, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



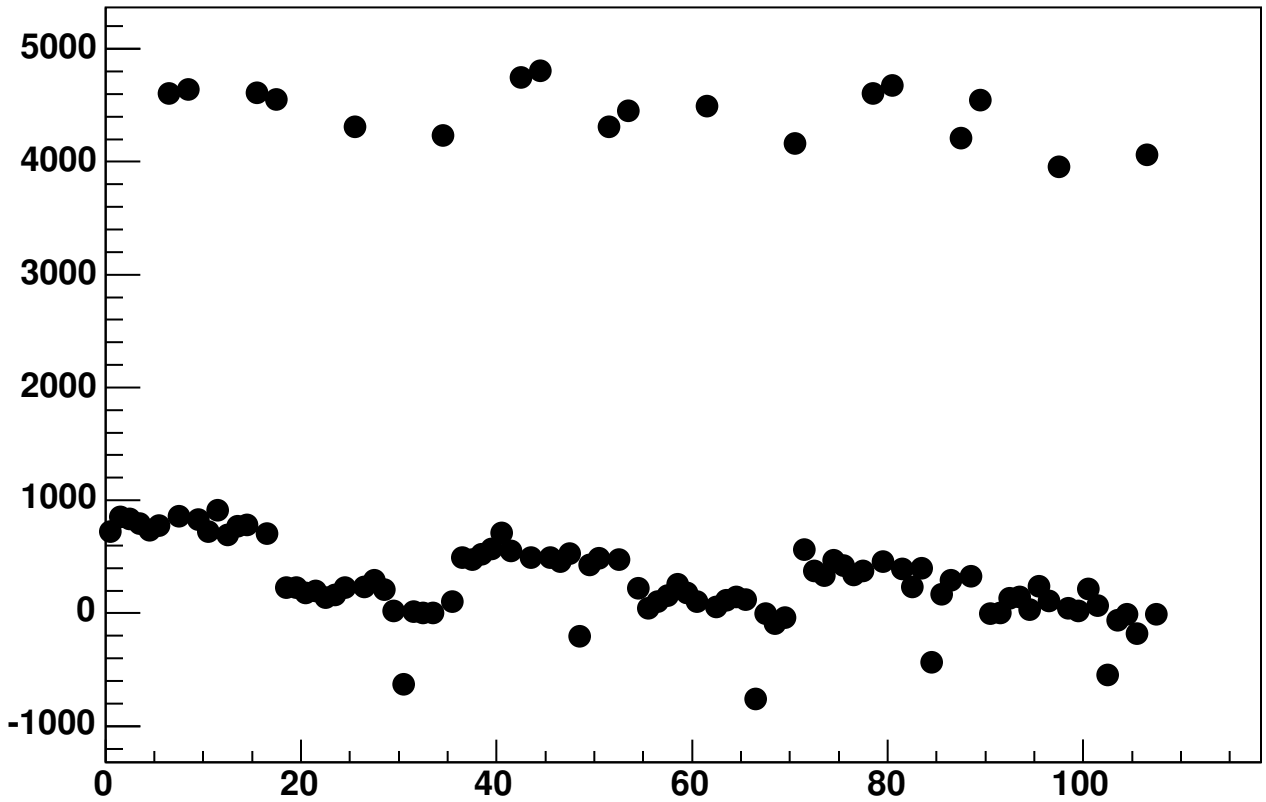
Enable 2, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



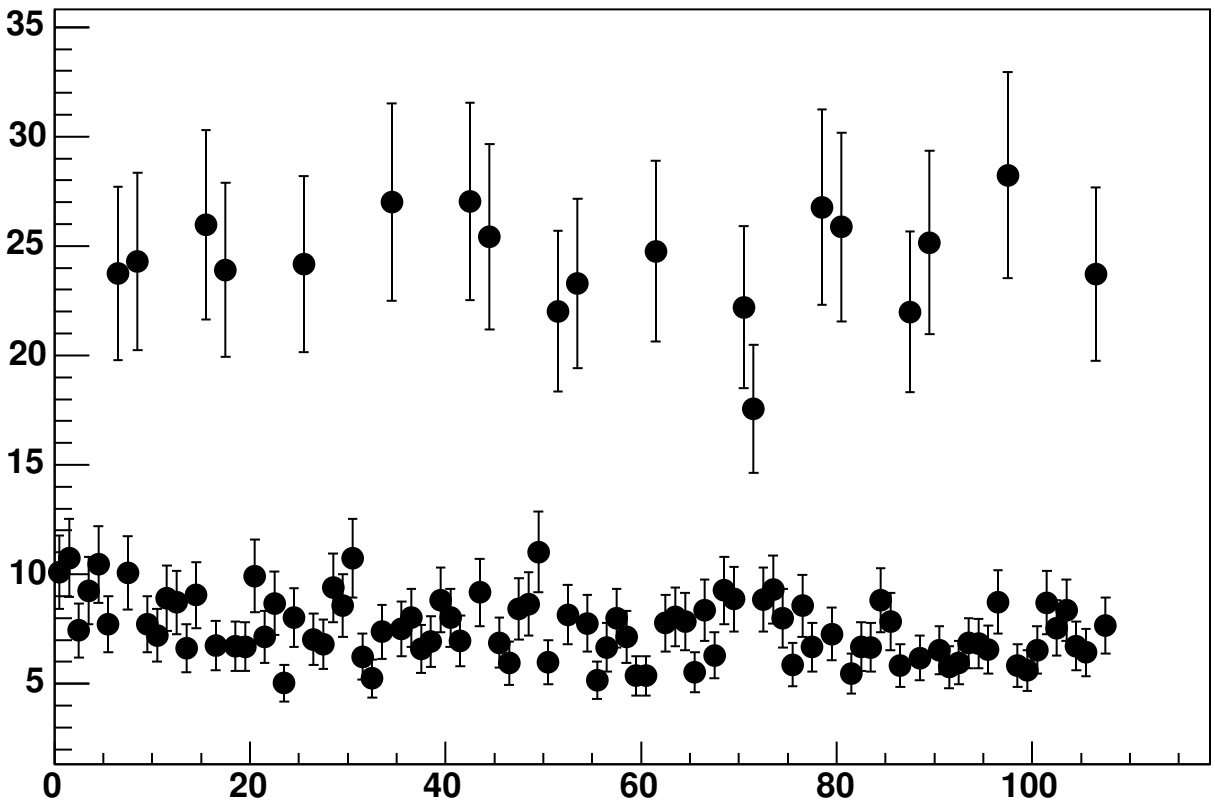
Enable 2, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



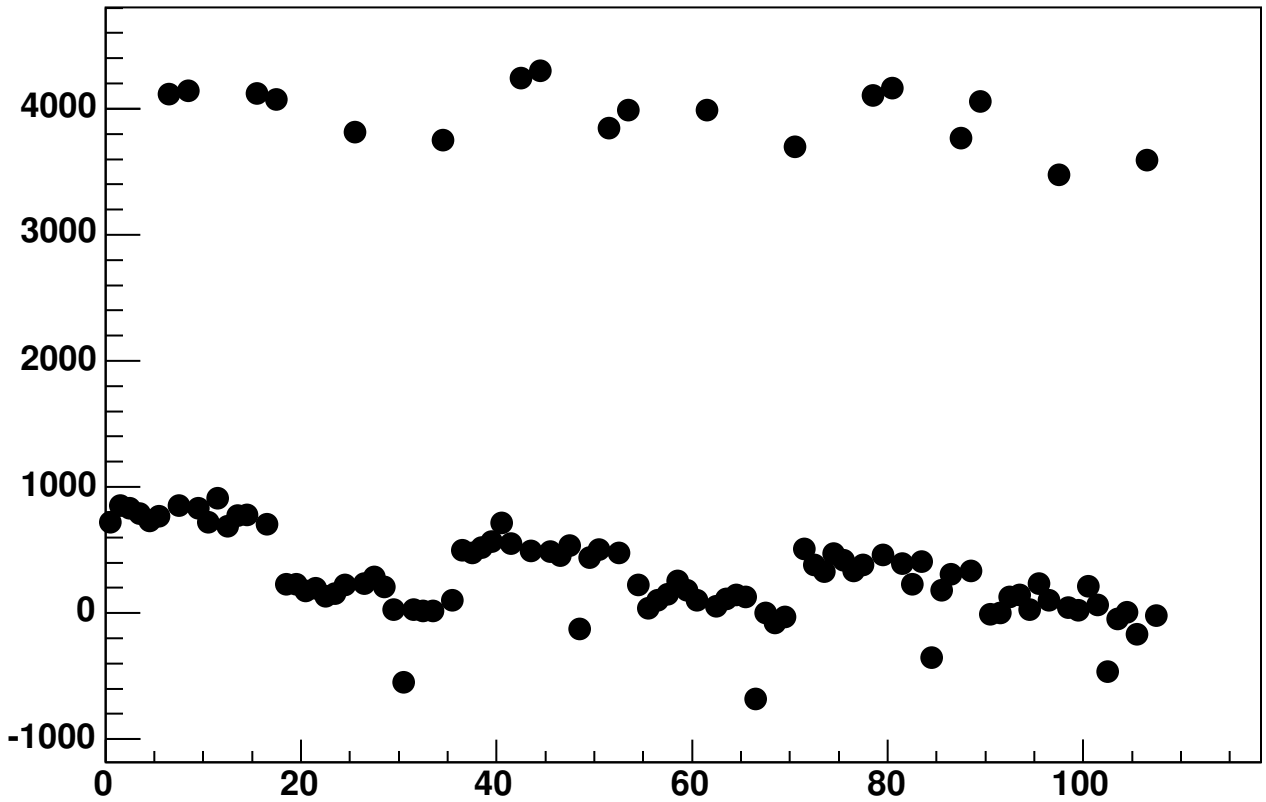
Enable 2, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



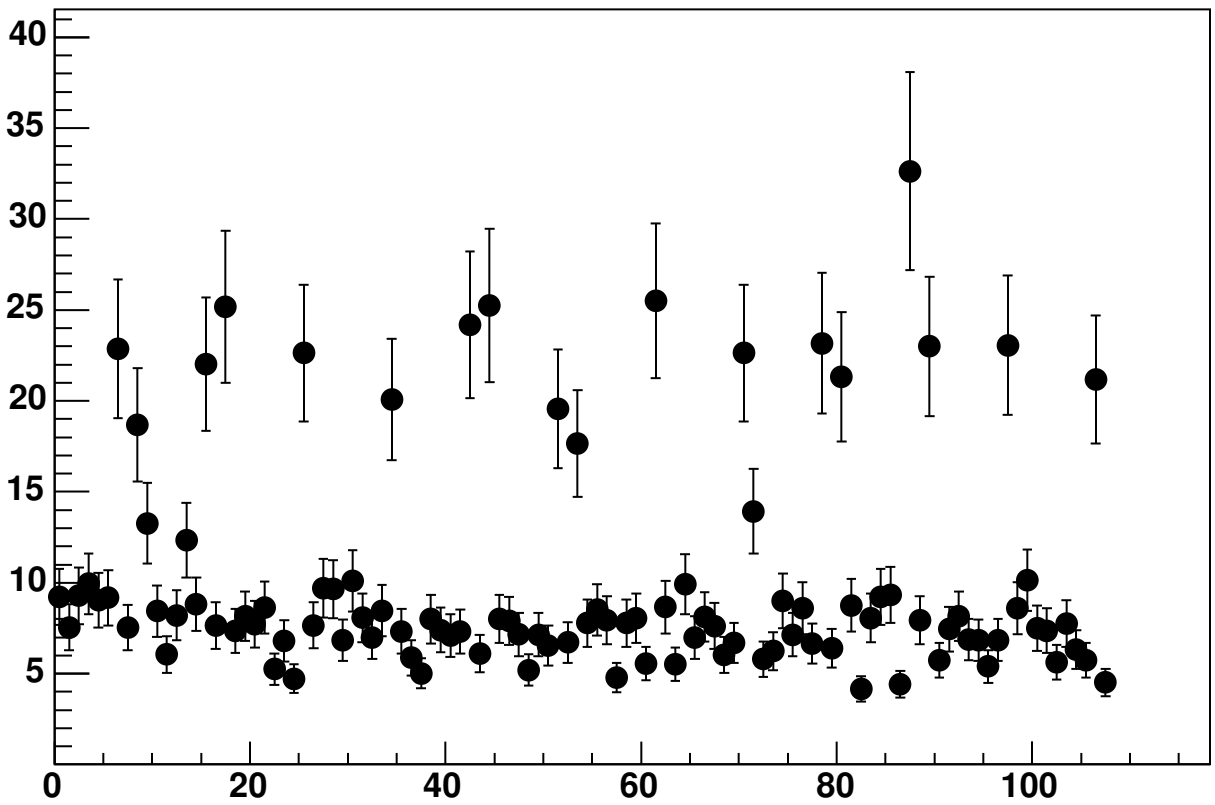
Enable 2, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



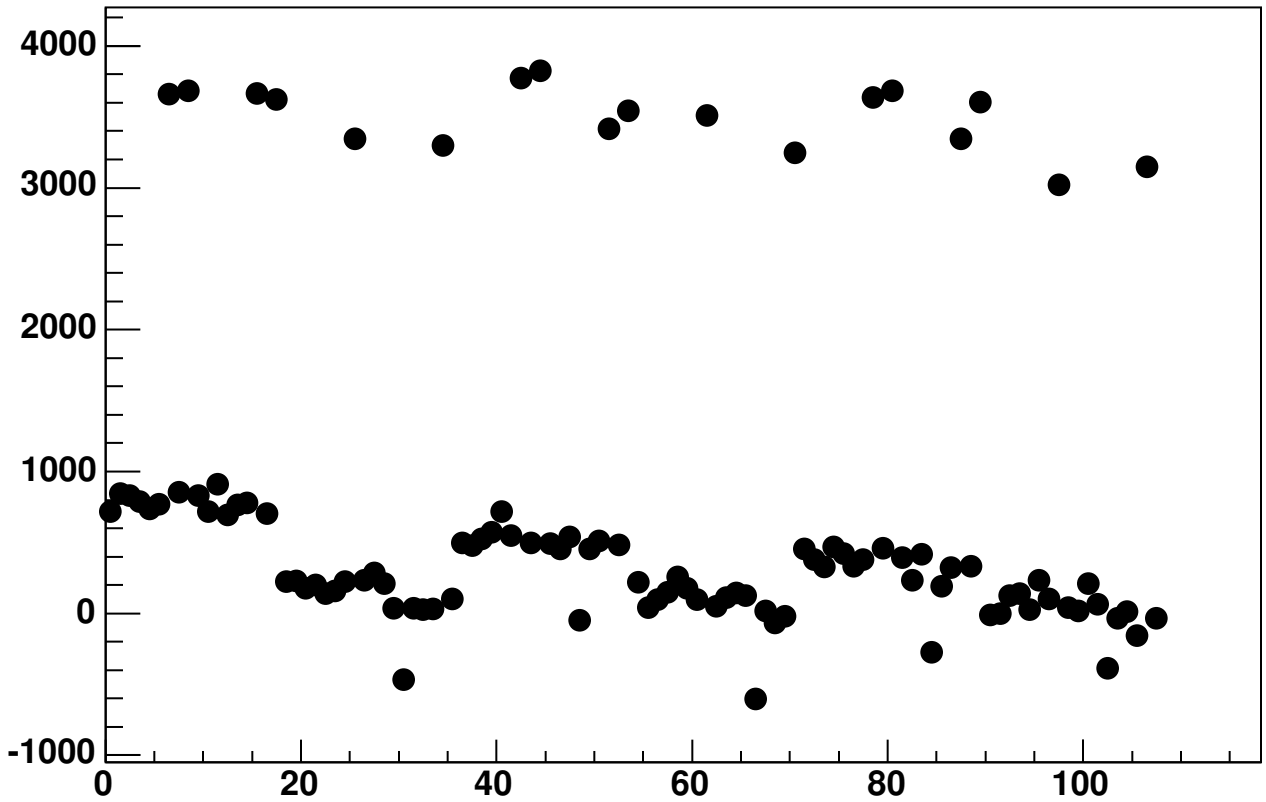
Enable 2, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



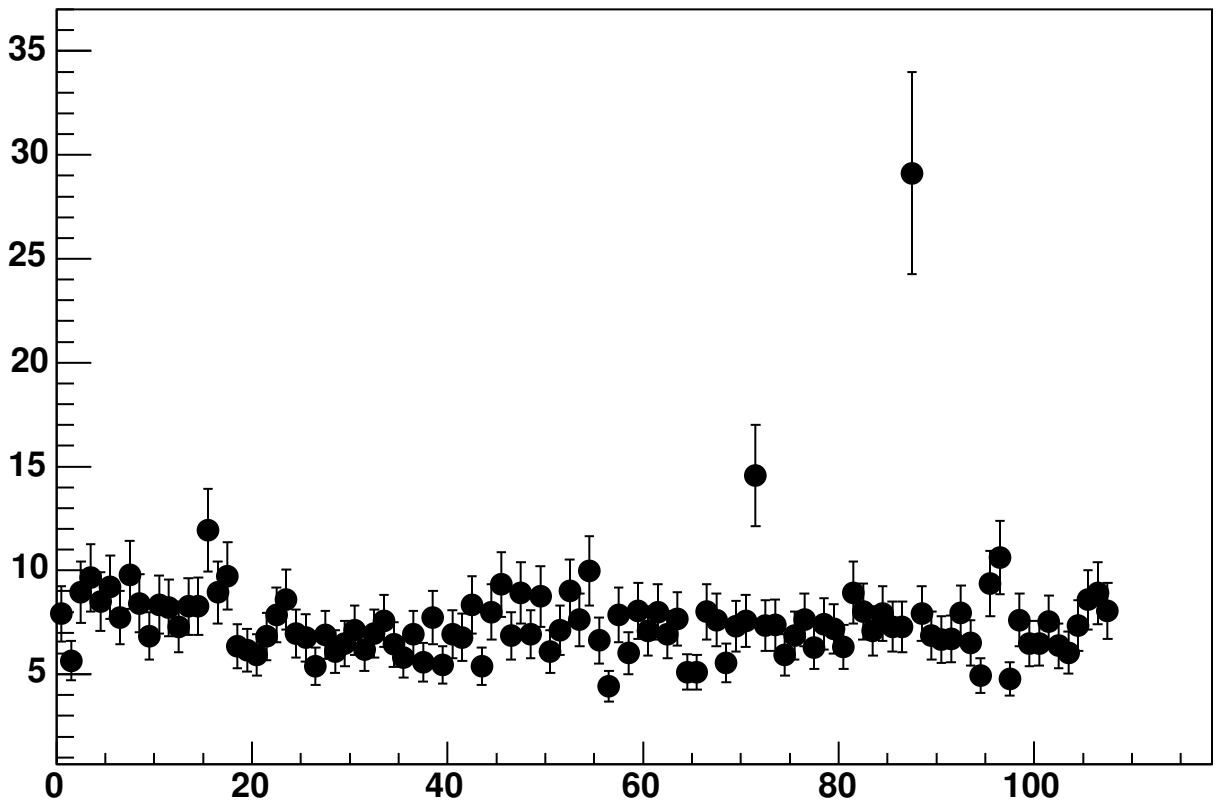
Enable 2, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



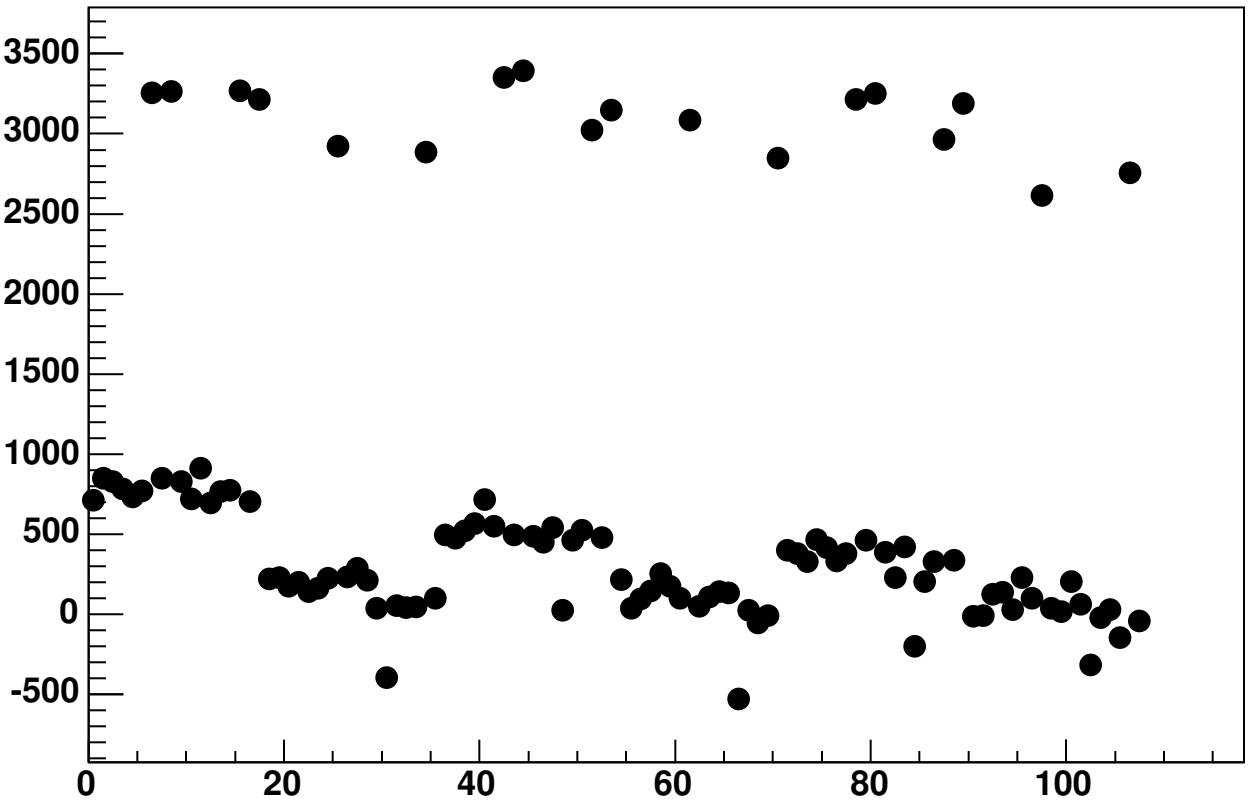
Enable 2, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



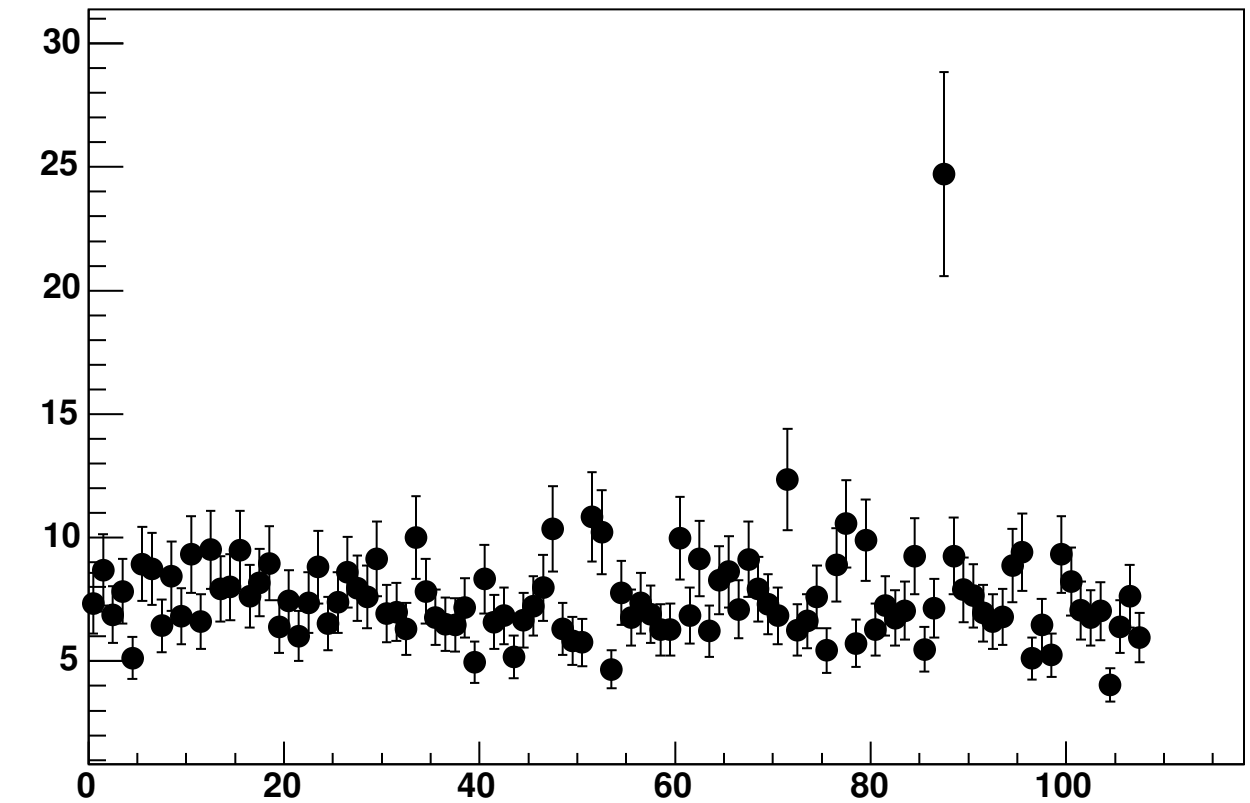
Enable 2, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



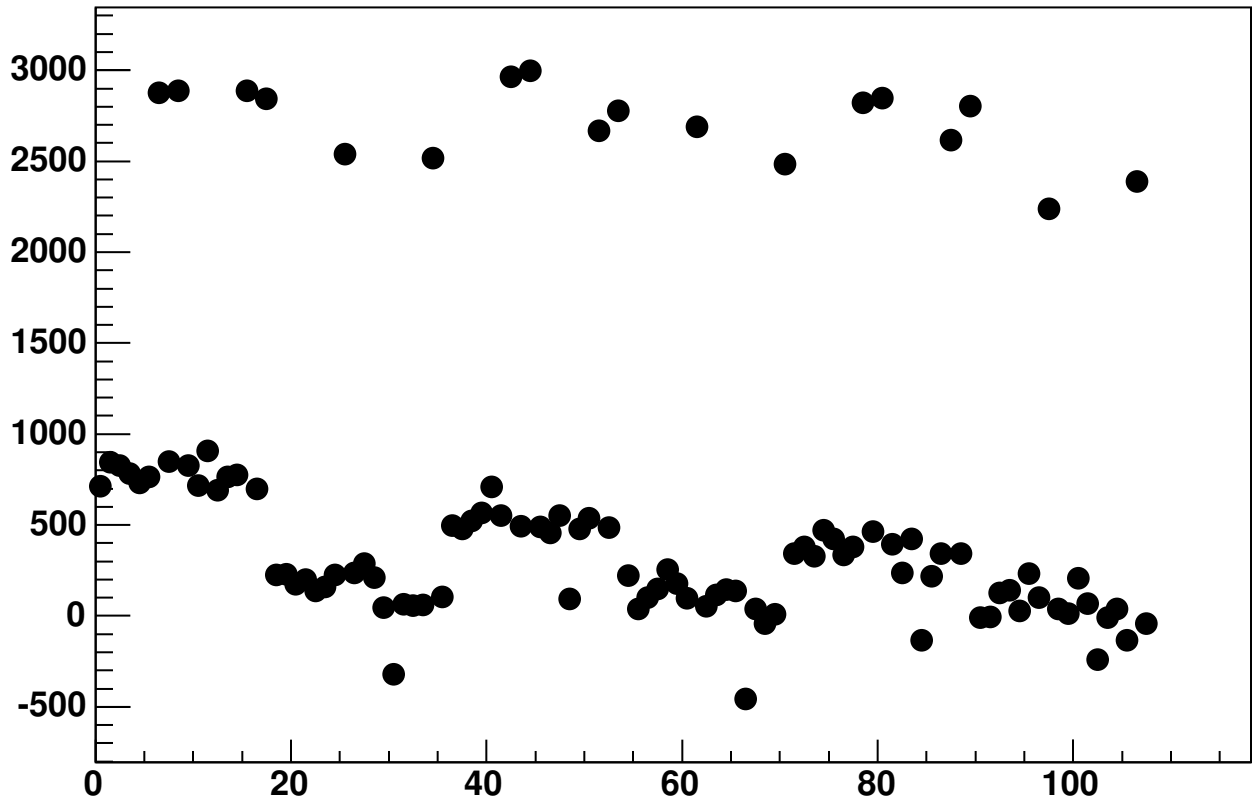
Enable 2, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



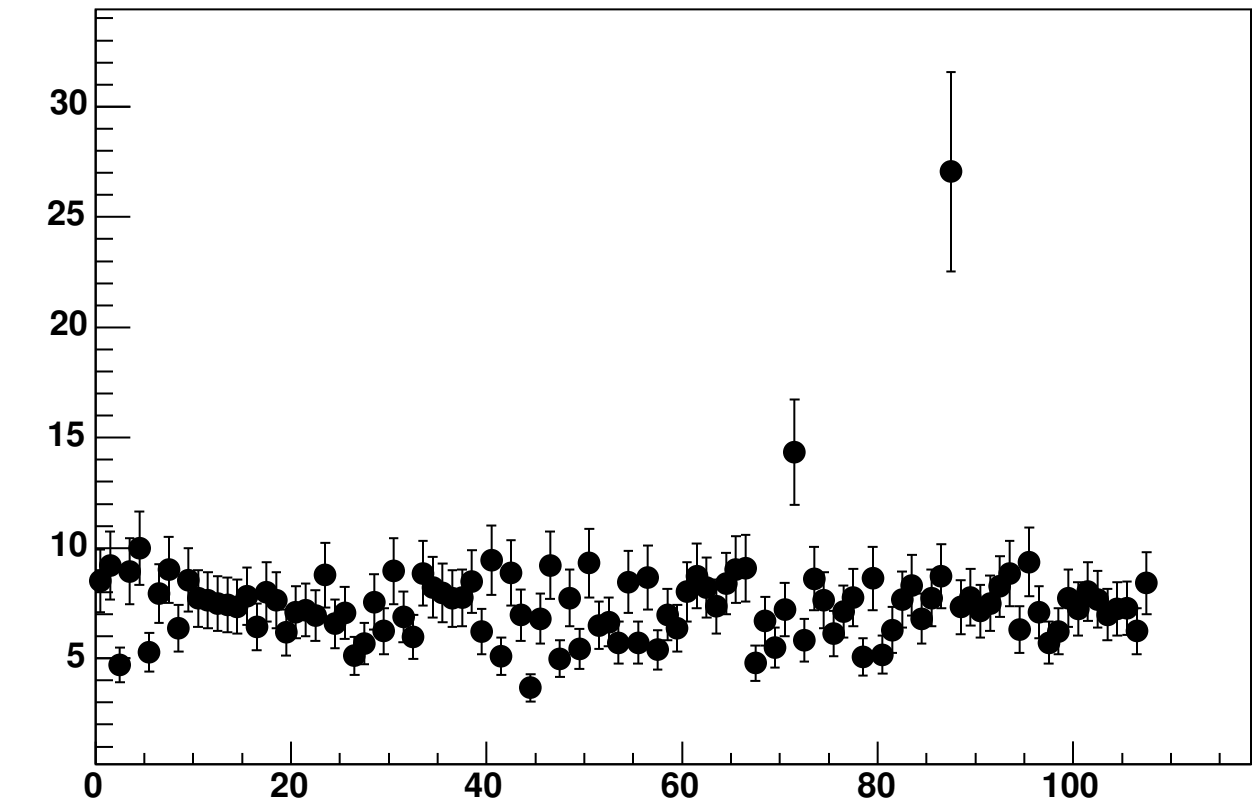
Enable 2, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



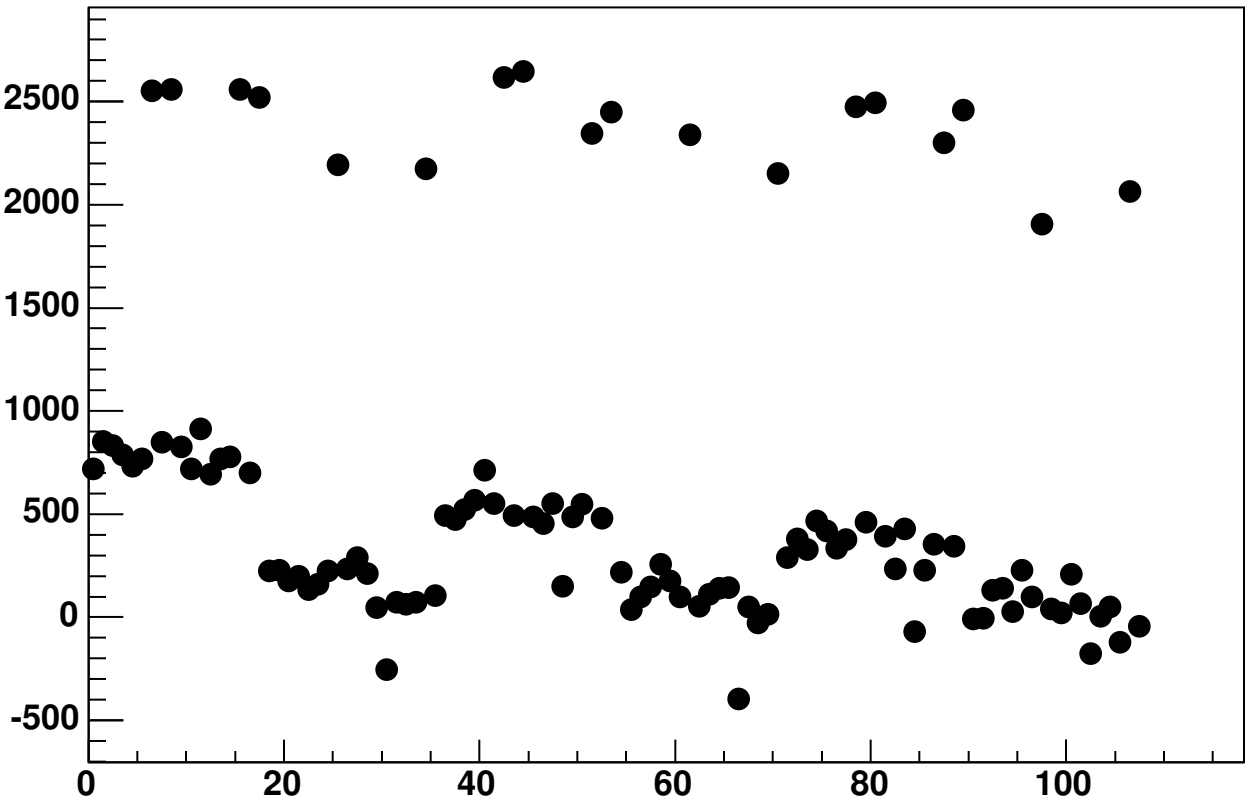
Enable 2, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



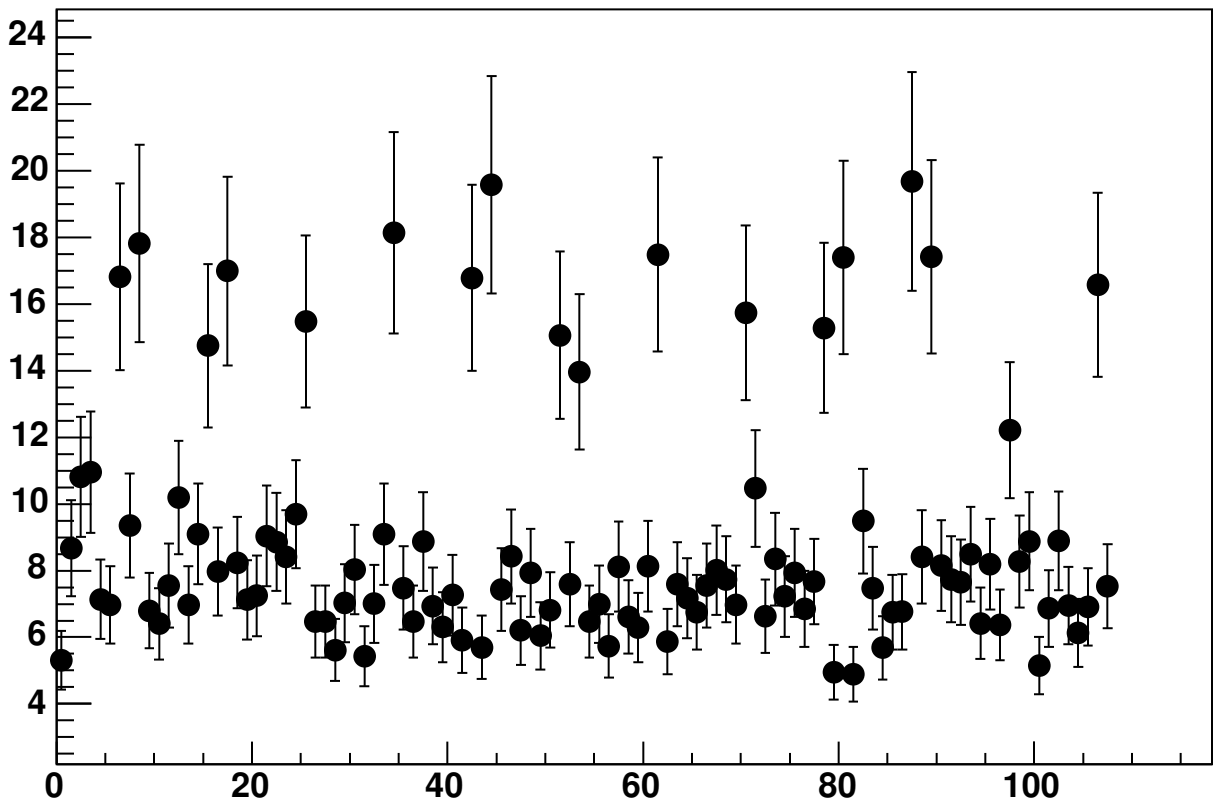
Enable 2, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



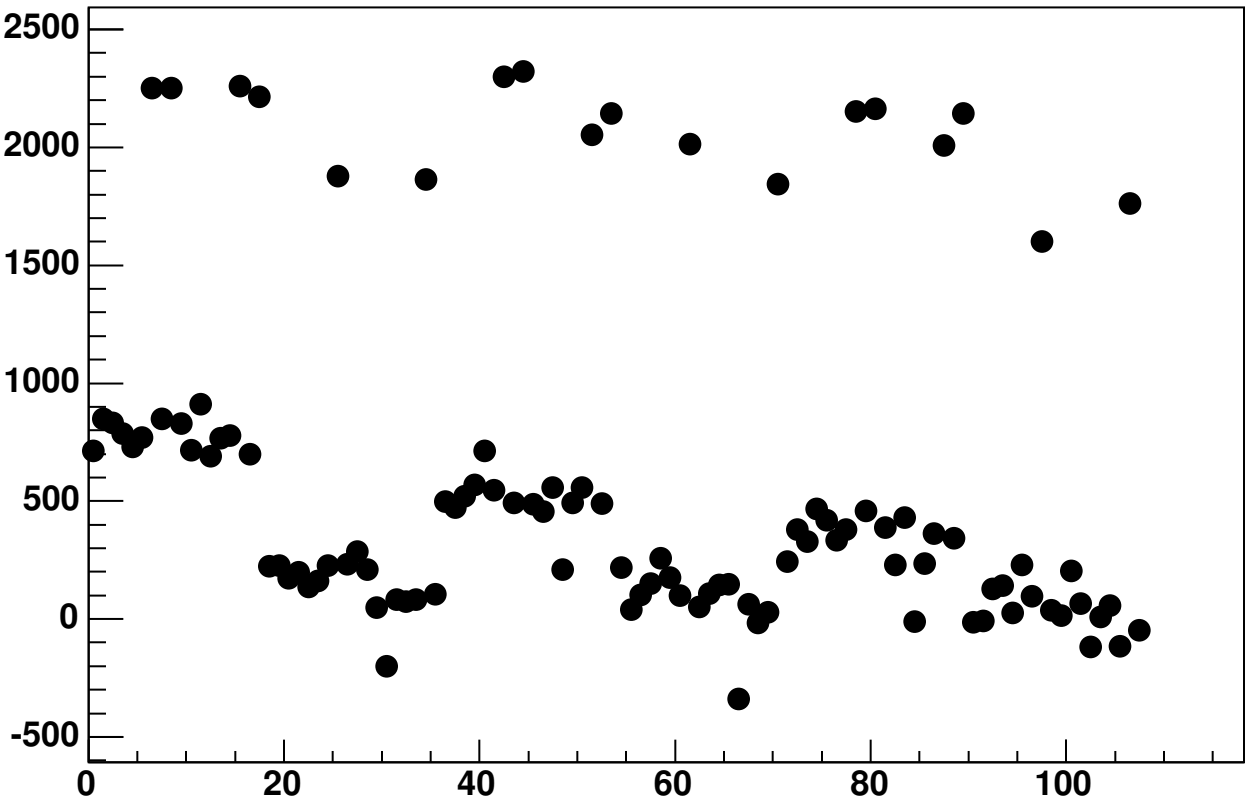
Enable 2, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



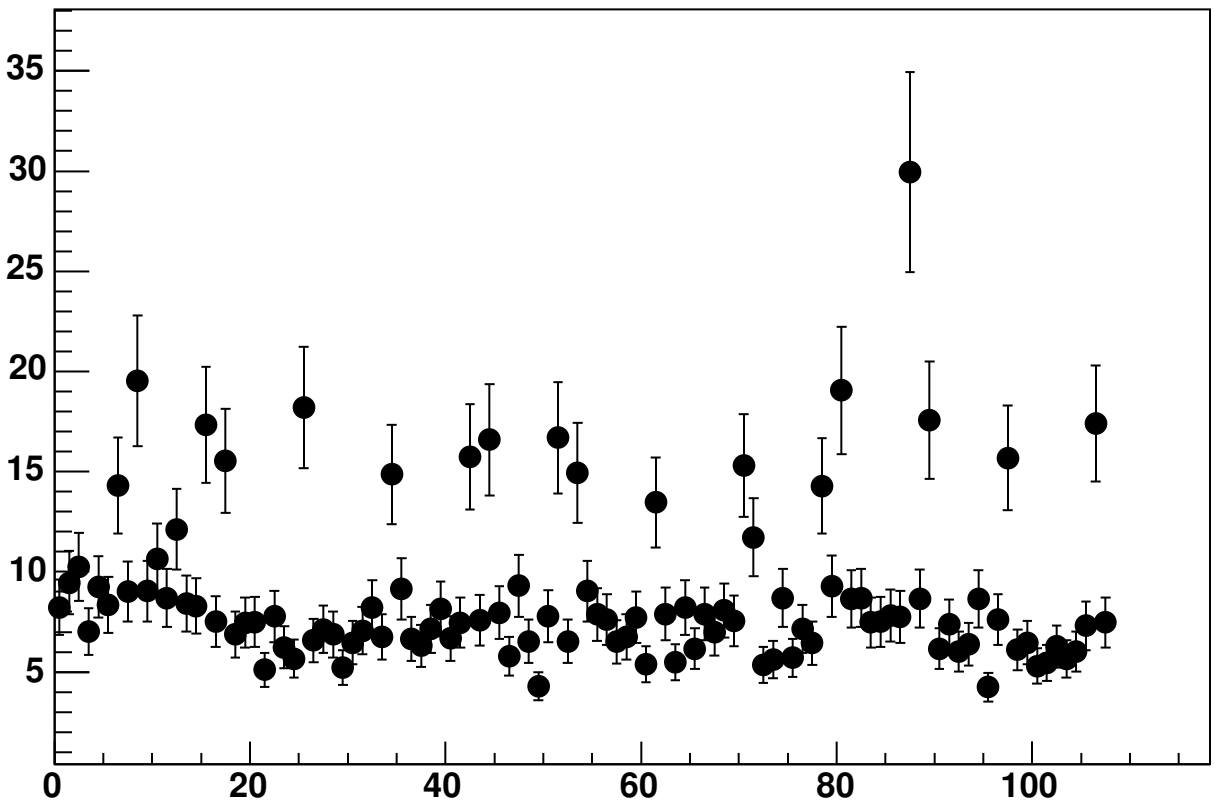
Enable 2, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



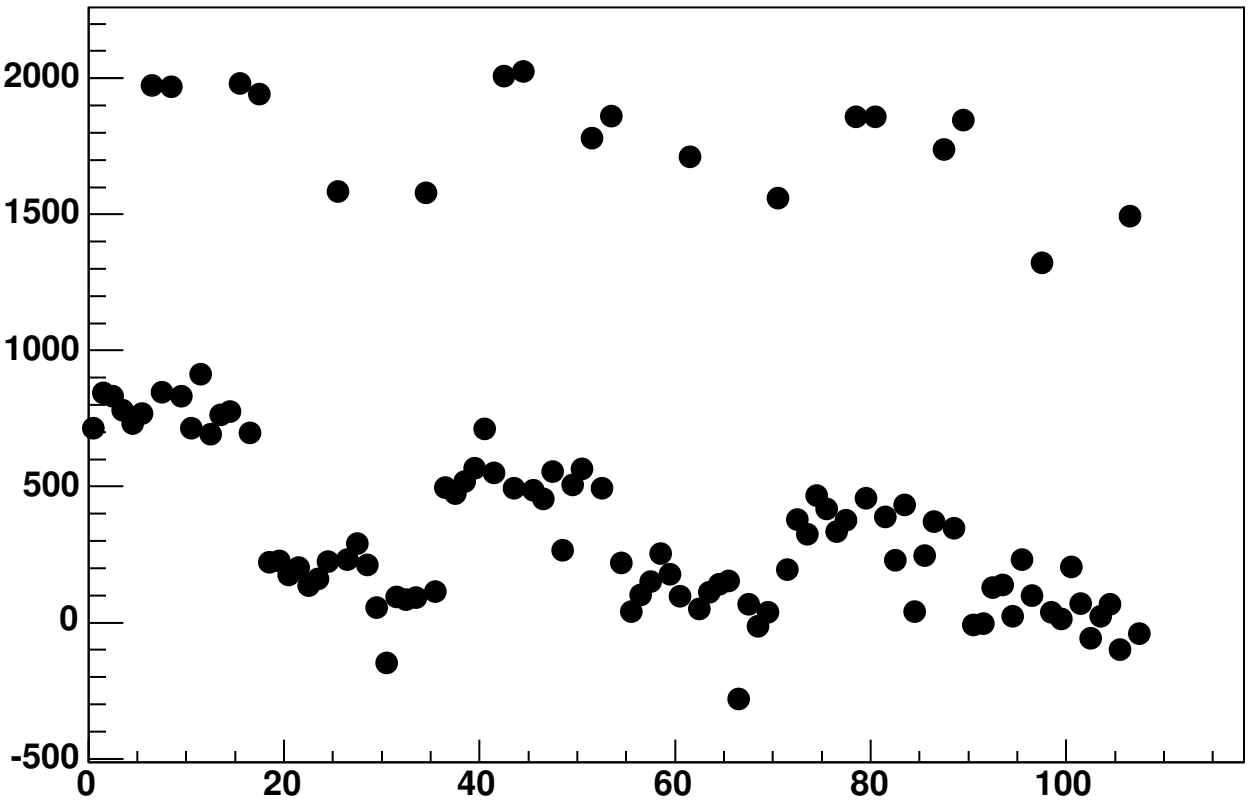
Enable 2, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



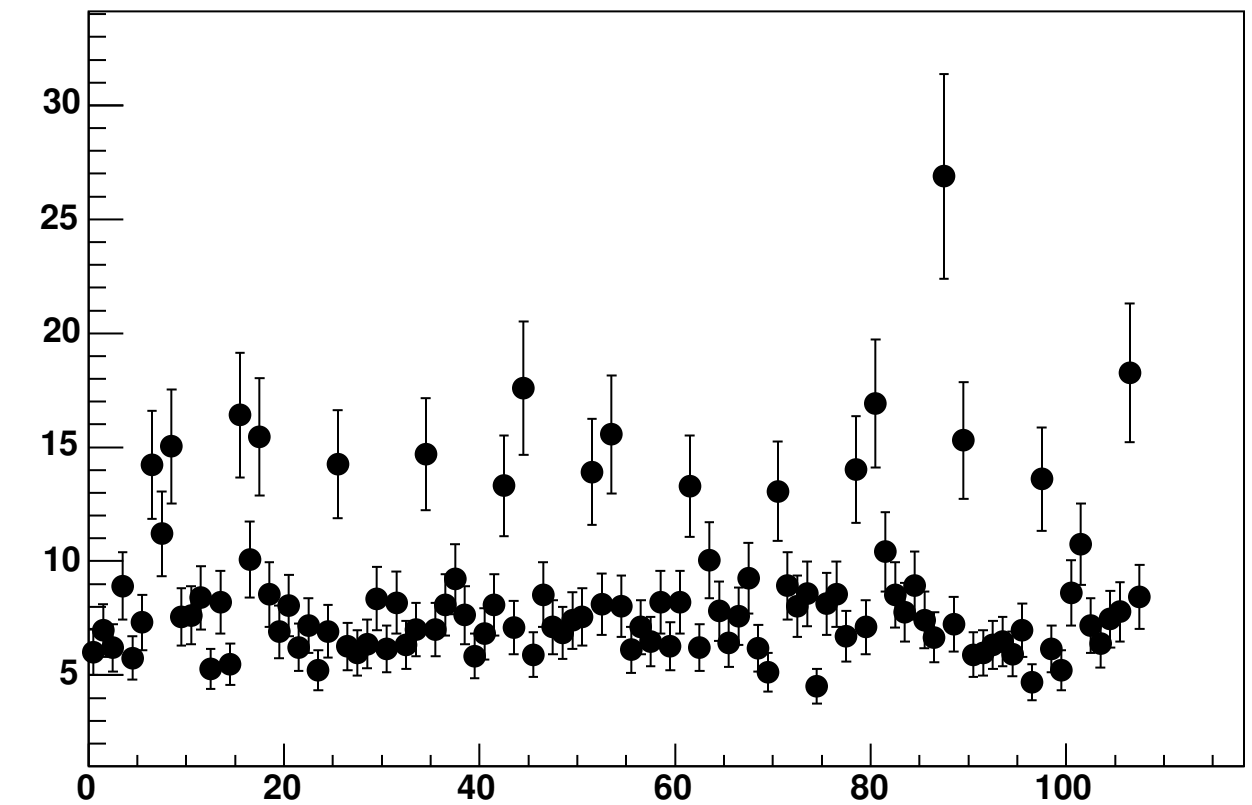
Enable 2, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



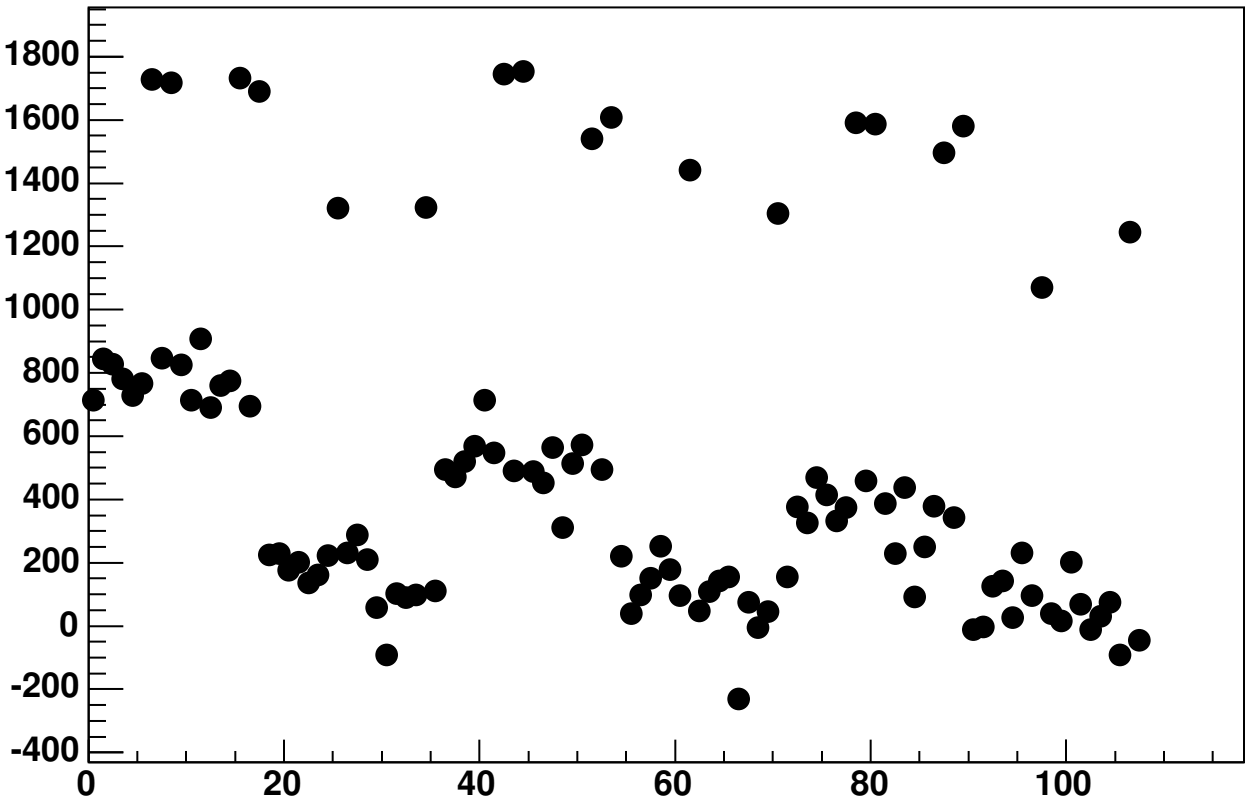
Enable 2, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



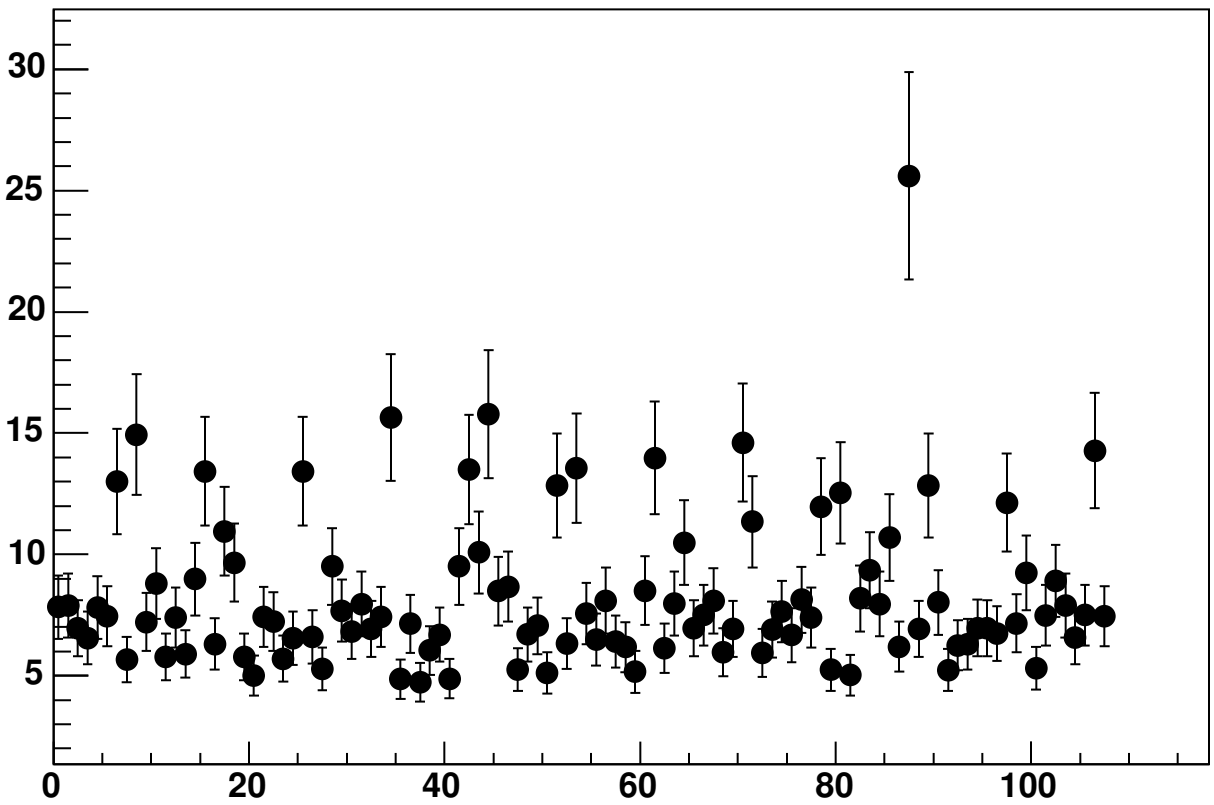
Enable 2, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



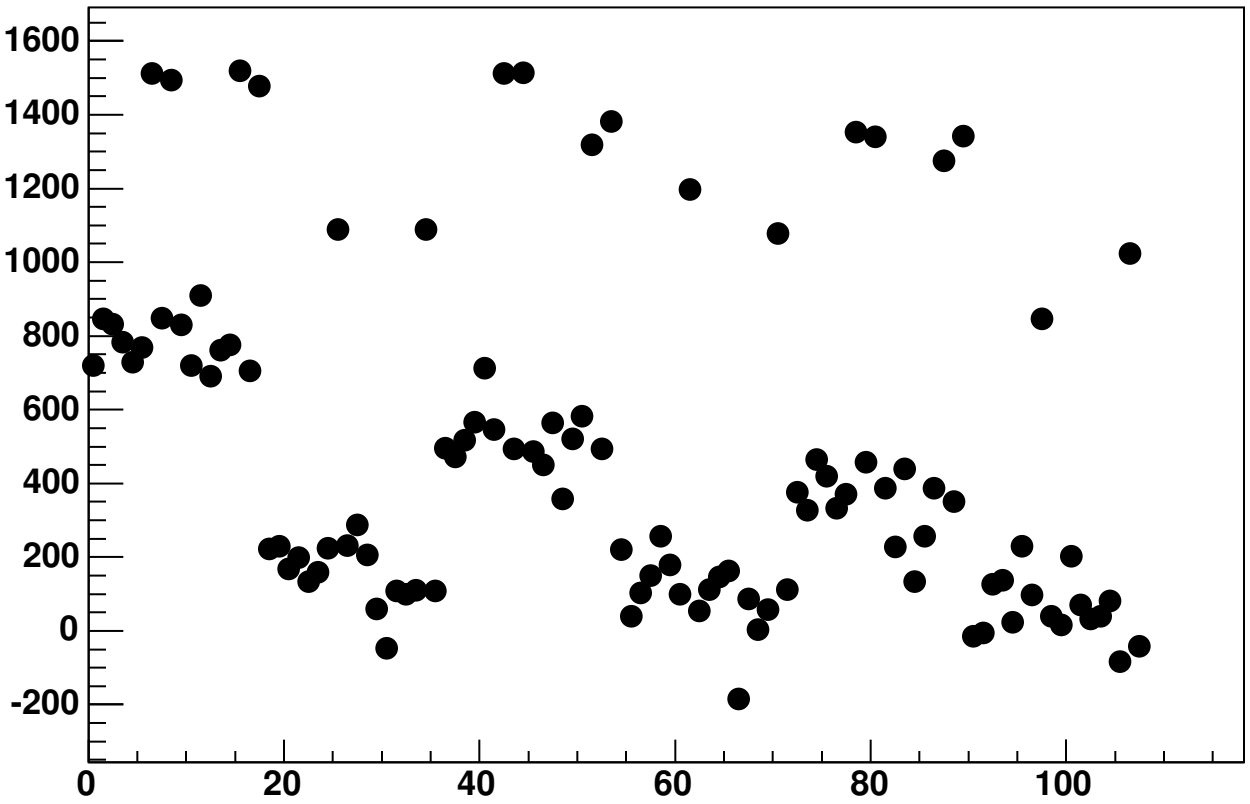
Enable 2, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



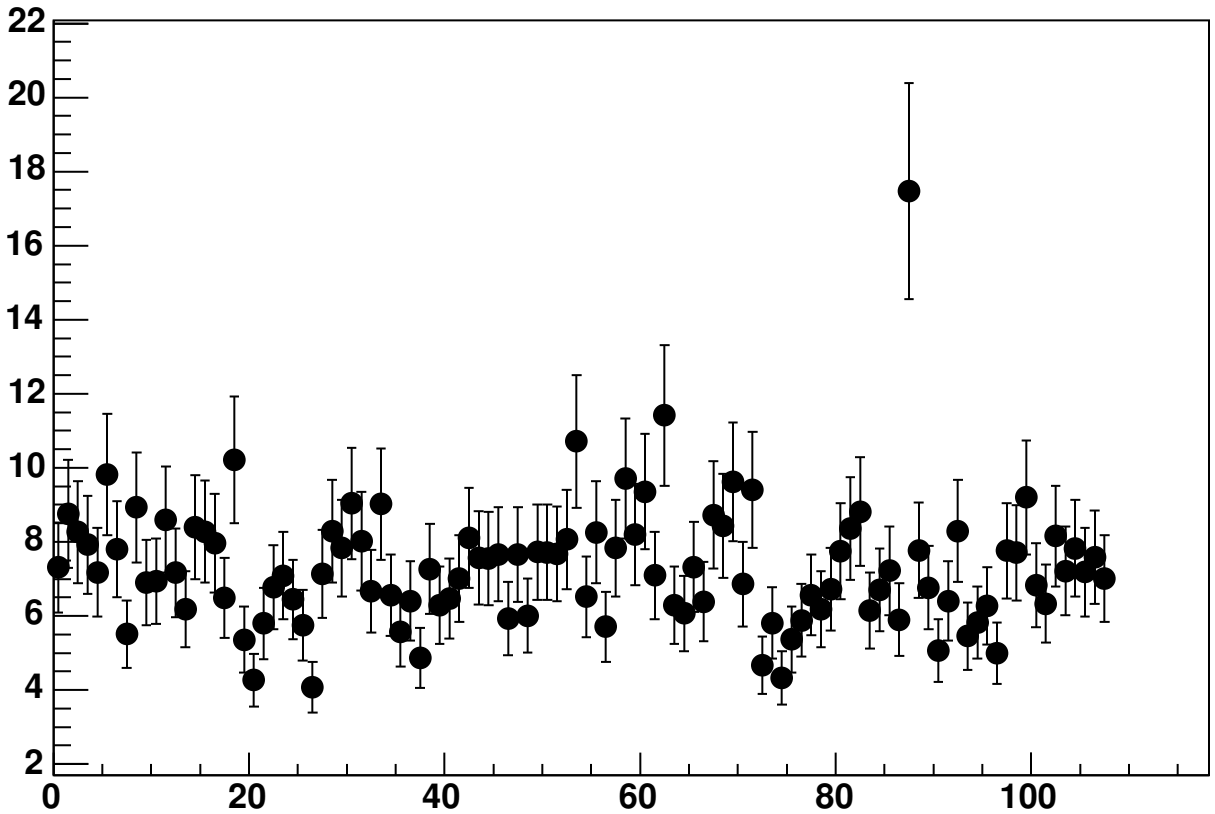
Enable 2, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



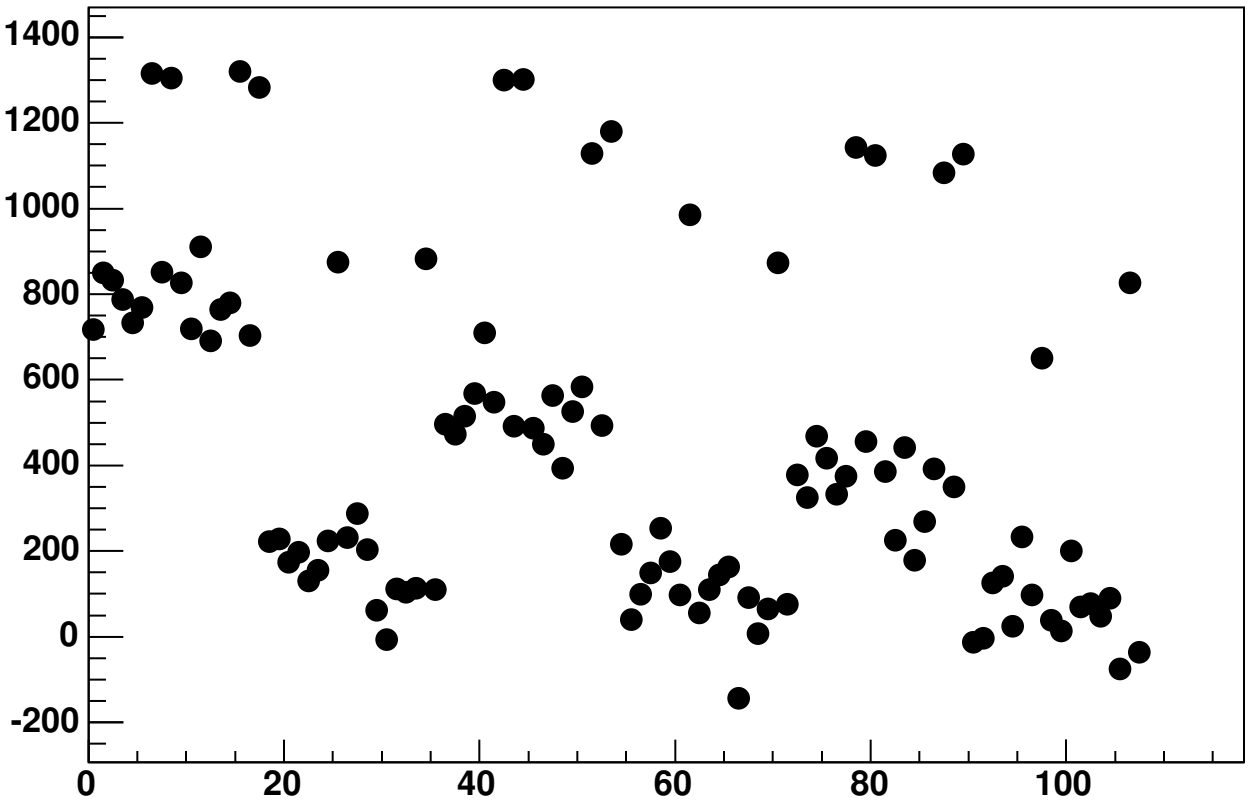
Enable 2, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



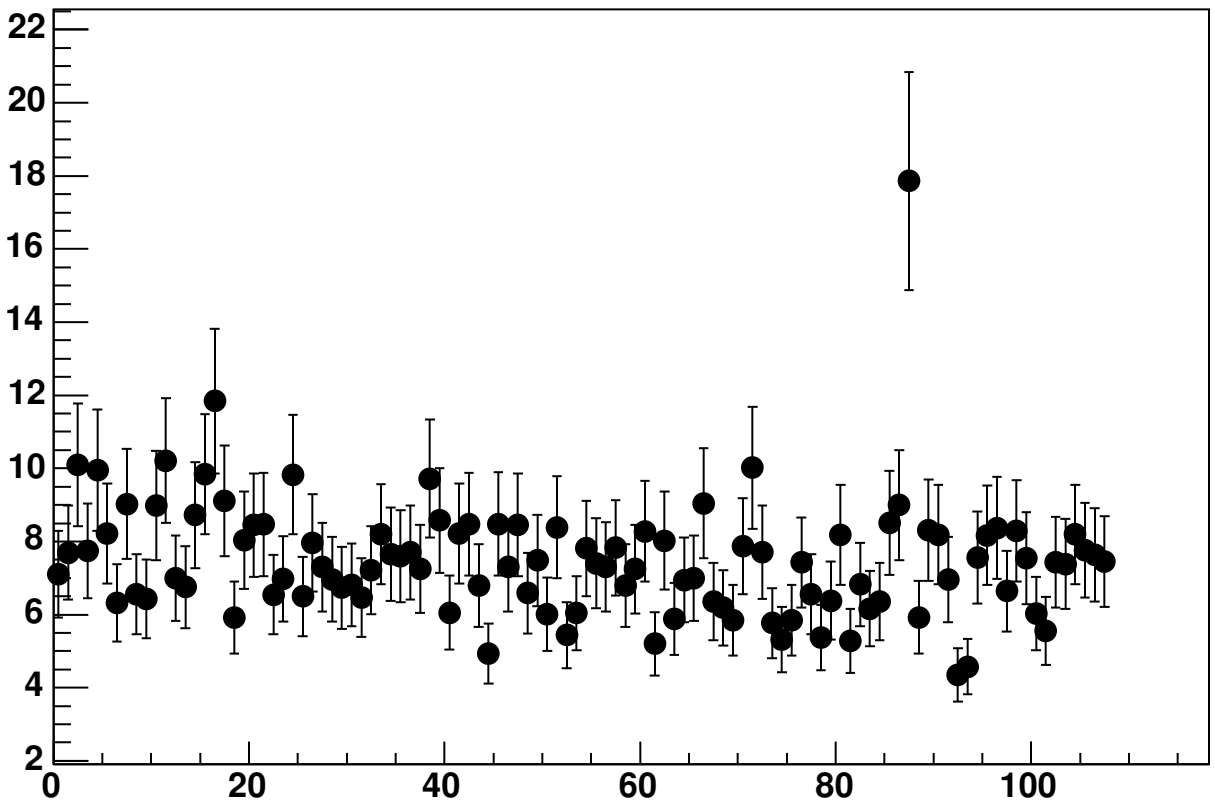
Enable 2, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



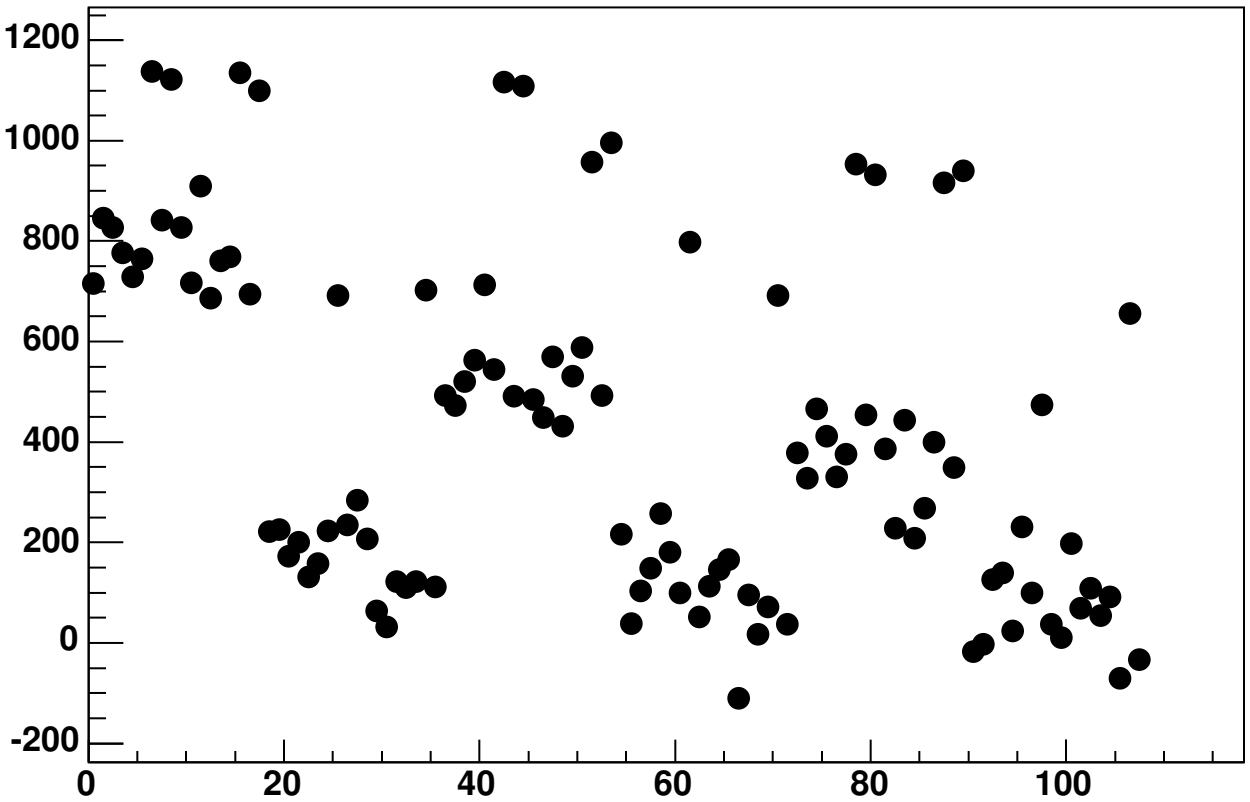
Enable 2, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



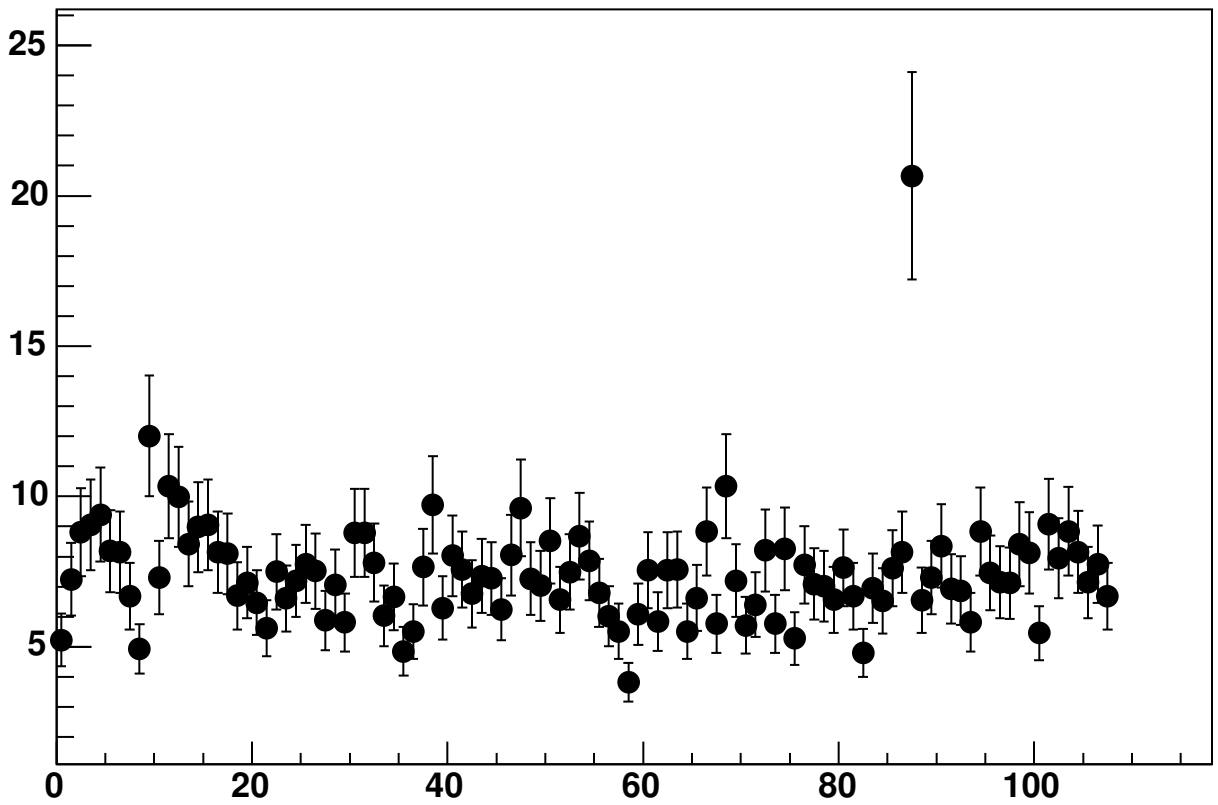
Enable 2, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



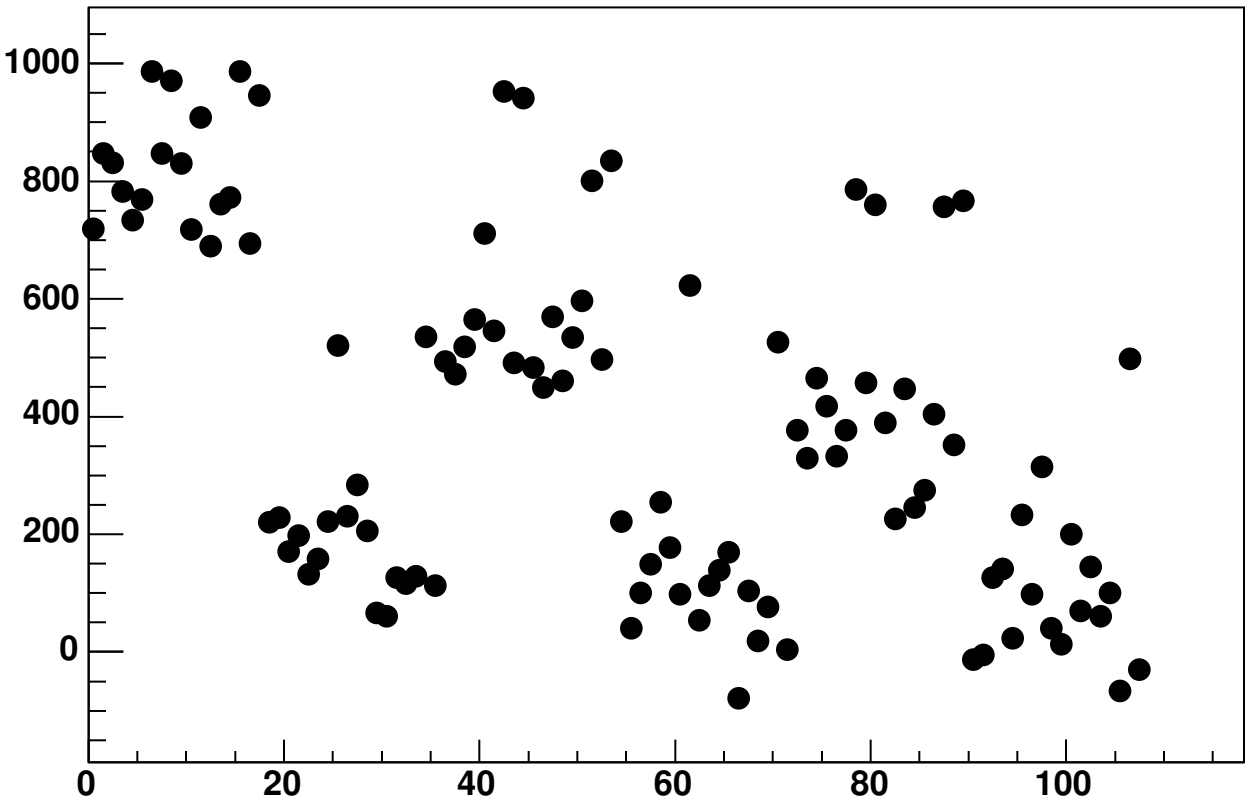
Enable 2, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



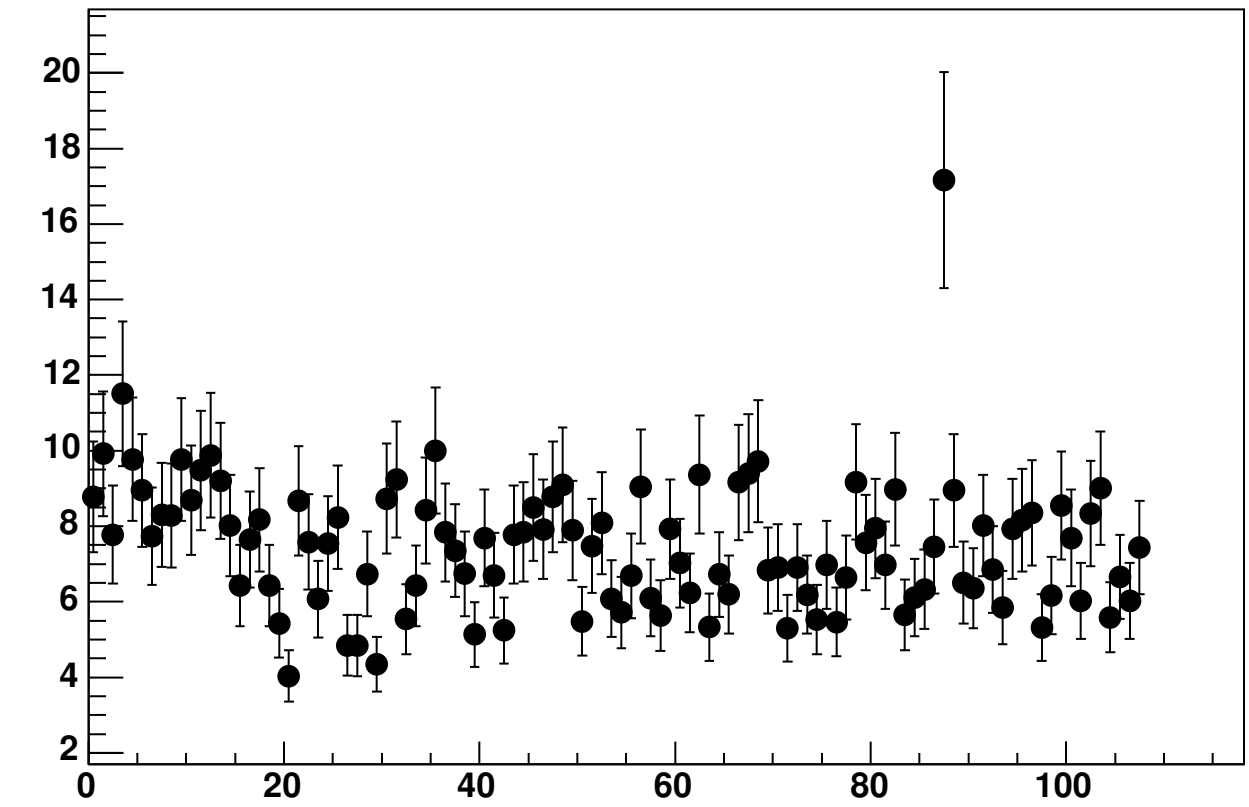
Enable 2, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



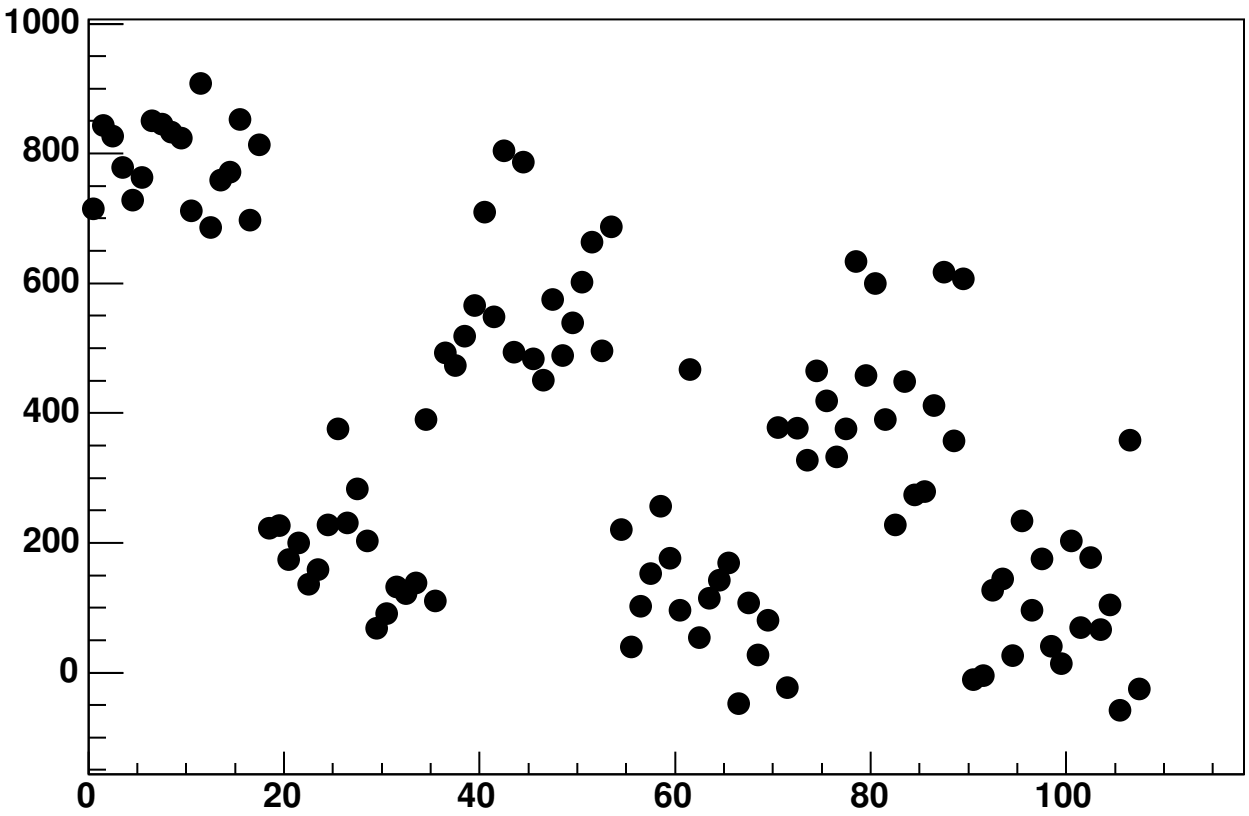
Enable 2, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



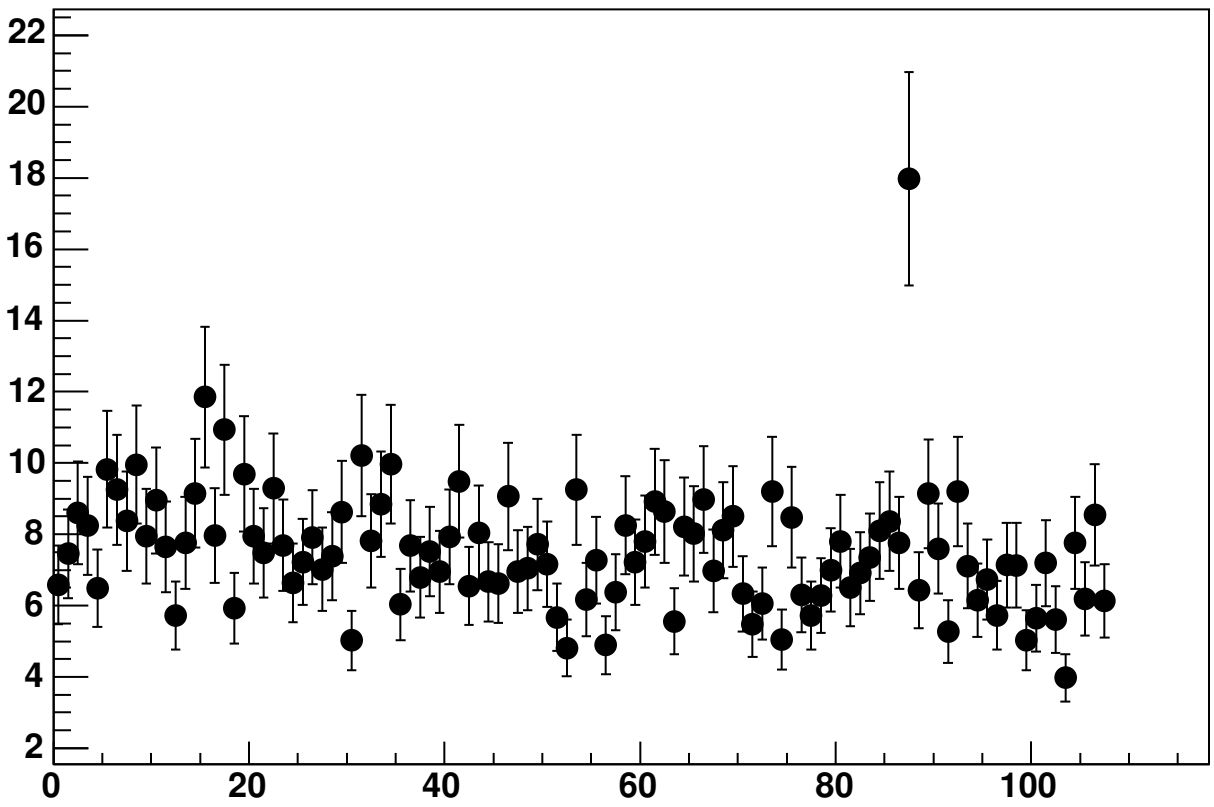
Enable 2, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



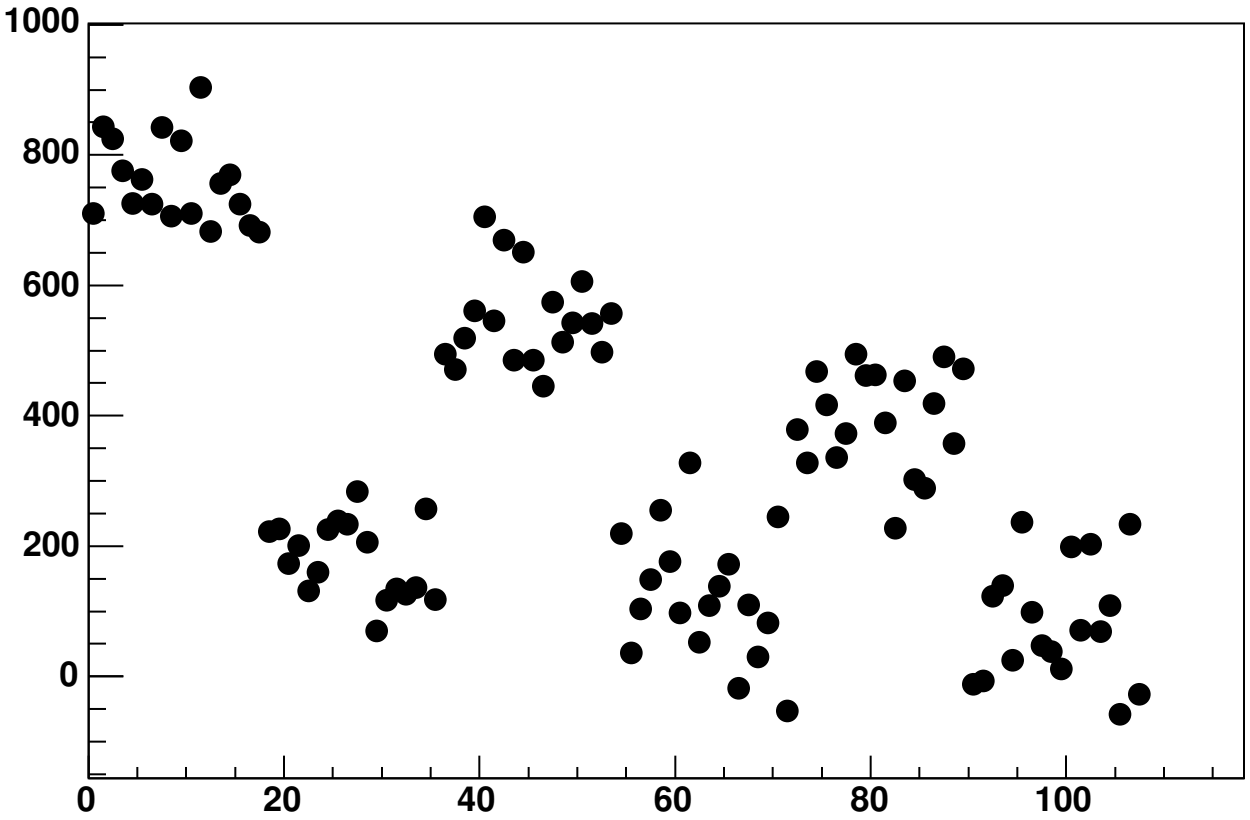
Enable 2, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



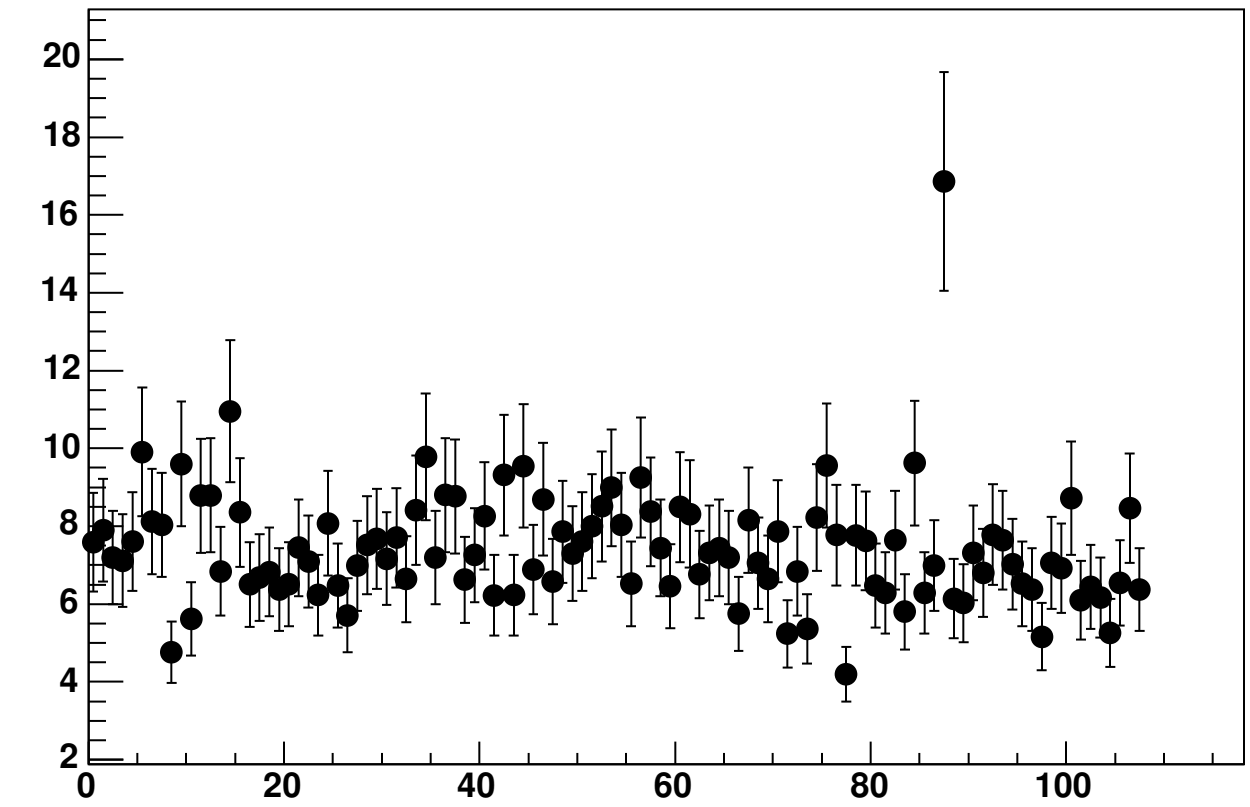
Enable 2, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



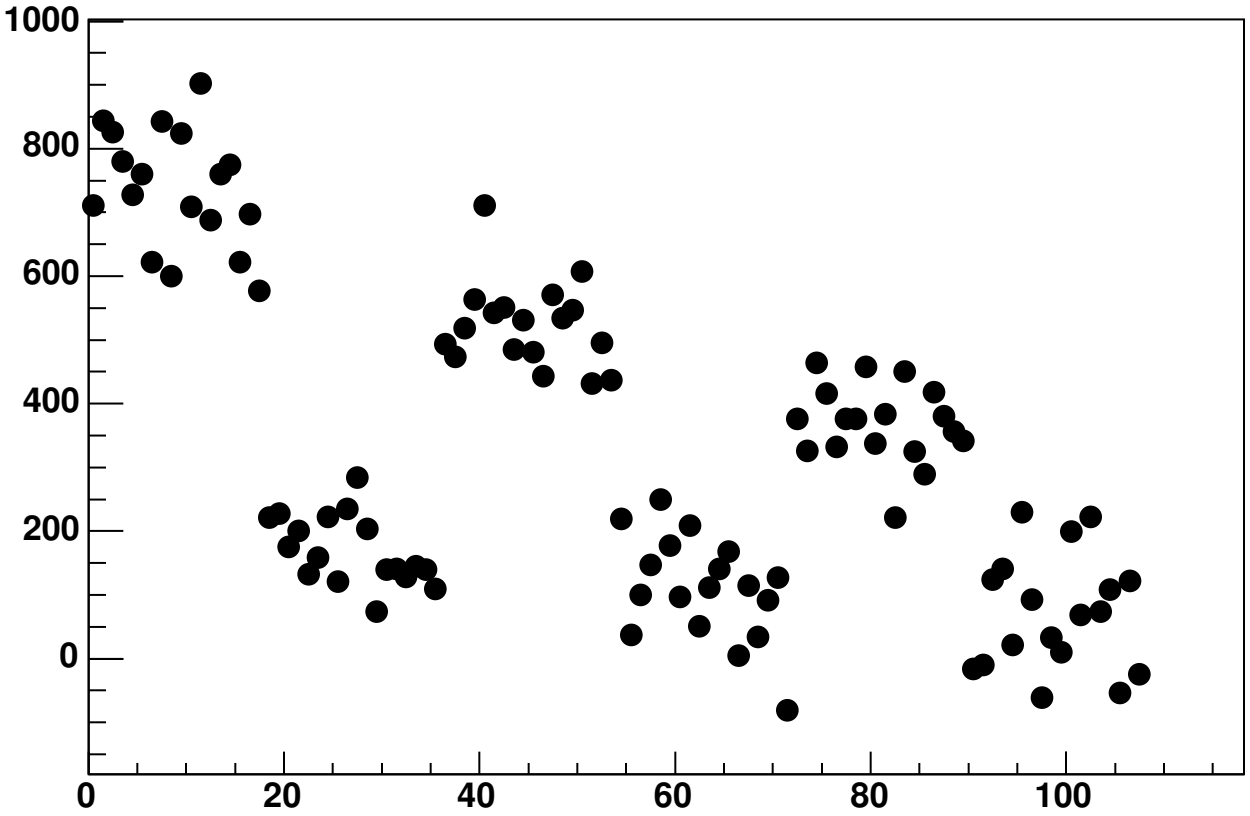
Enable 2, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



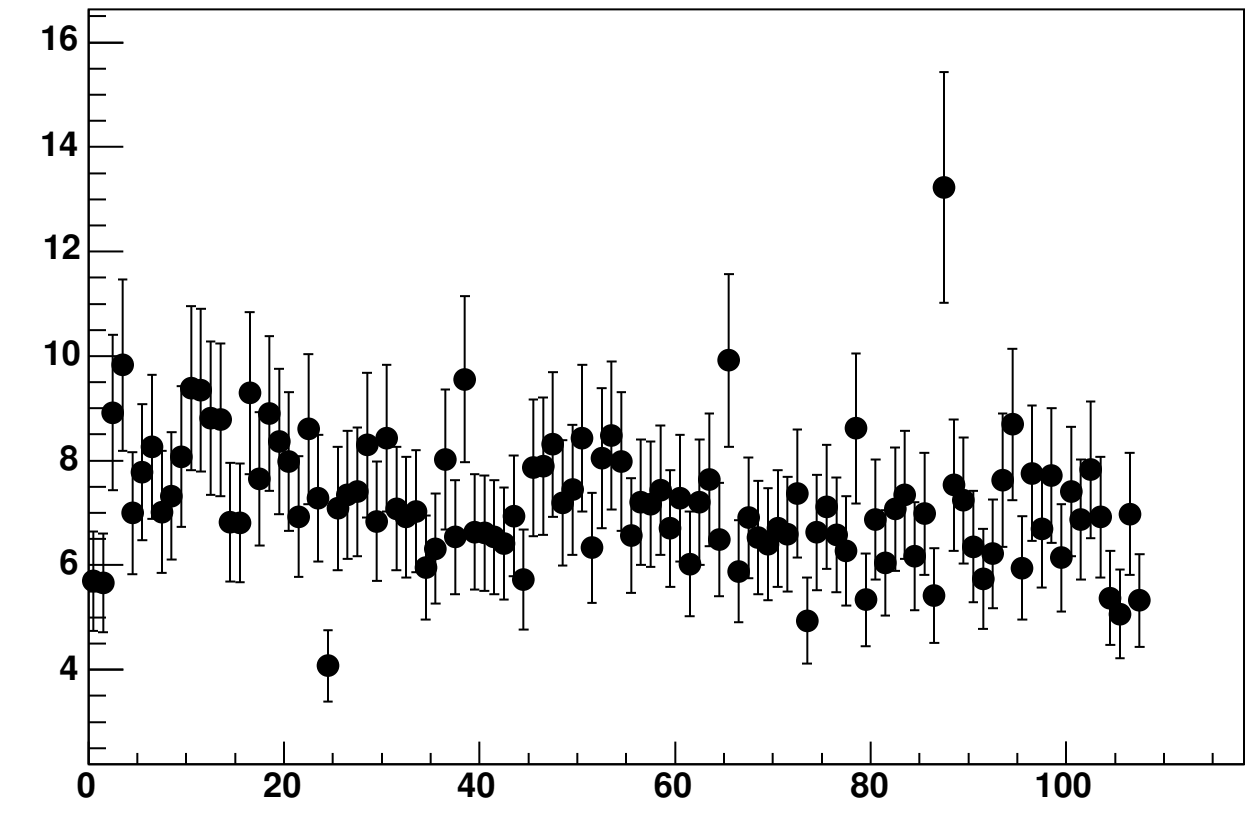
Enable 2, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



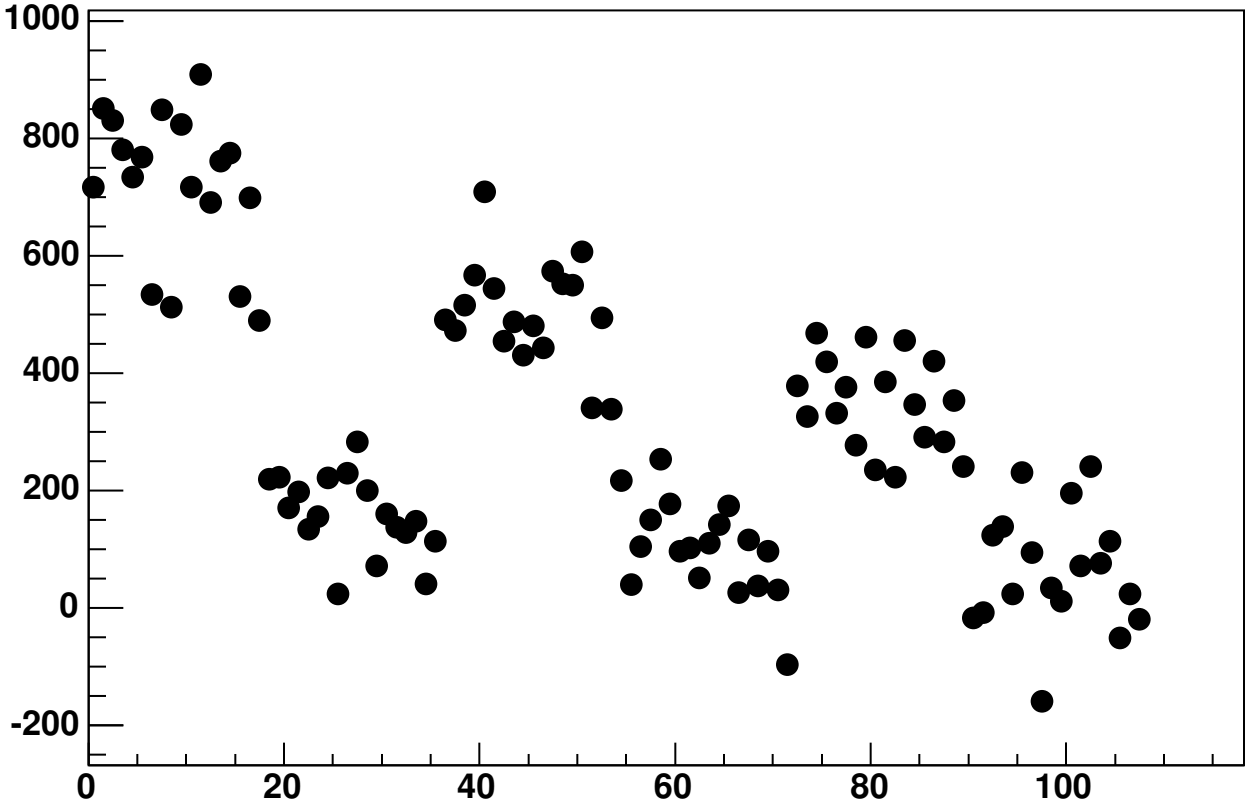
Enable 2, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



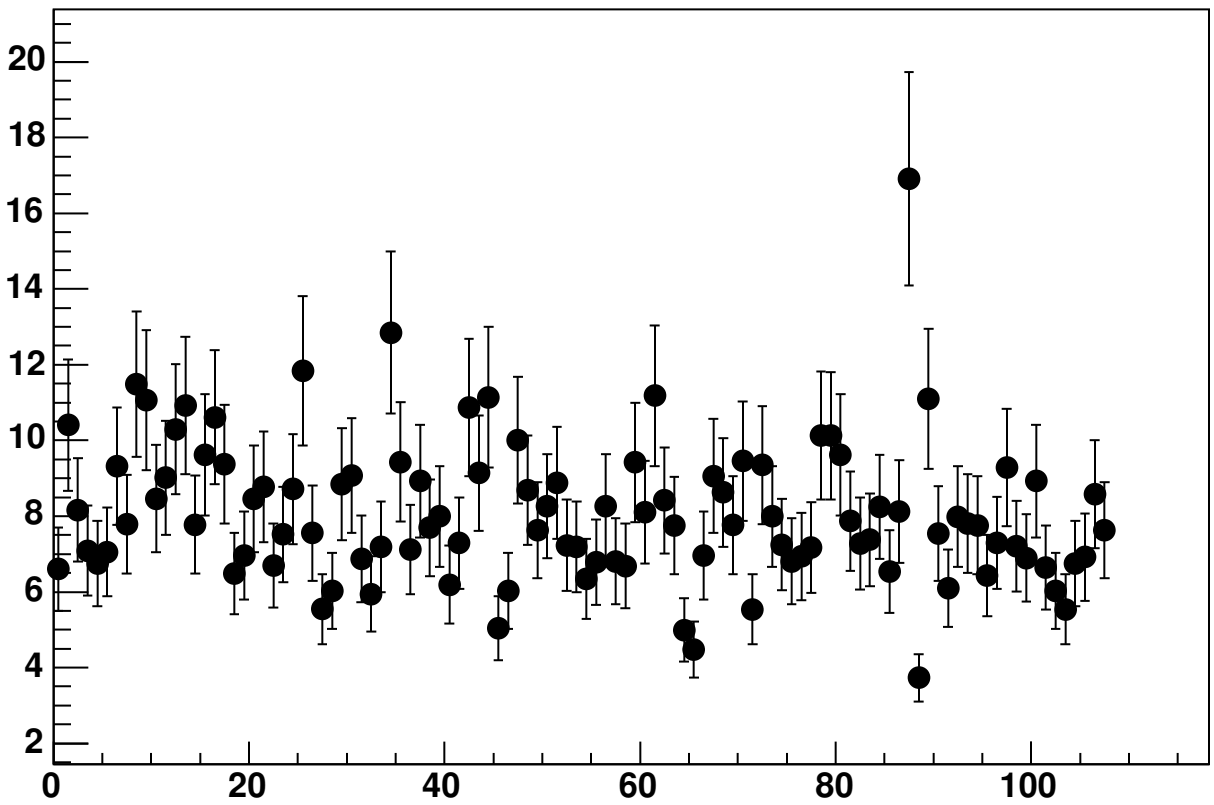
Enable 2, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



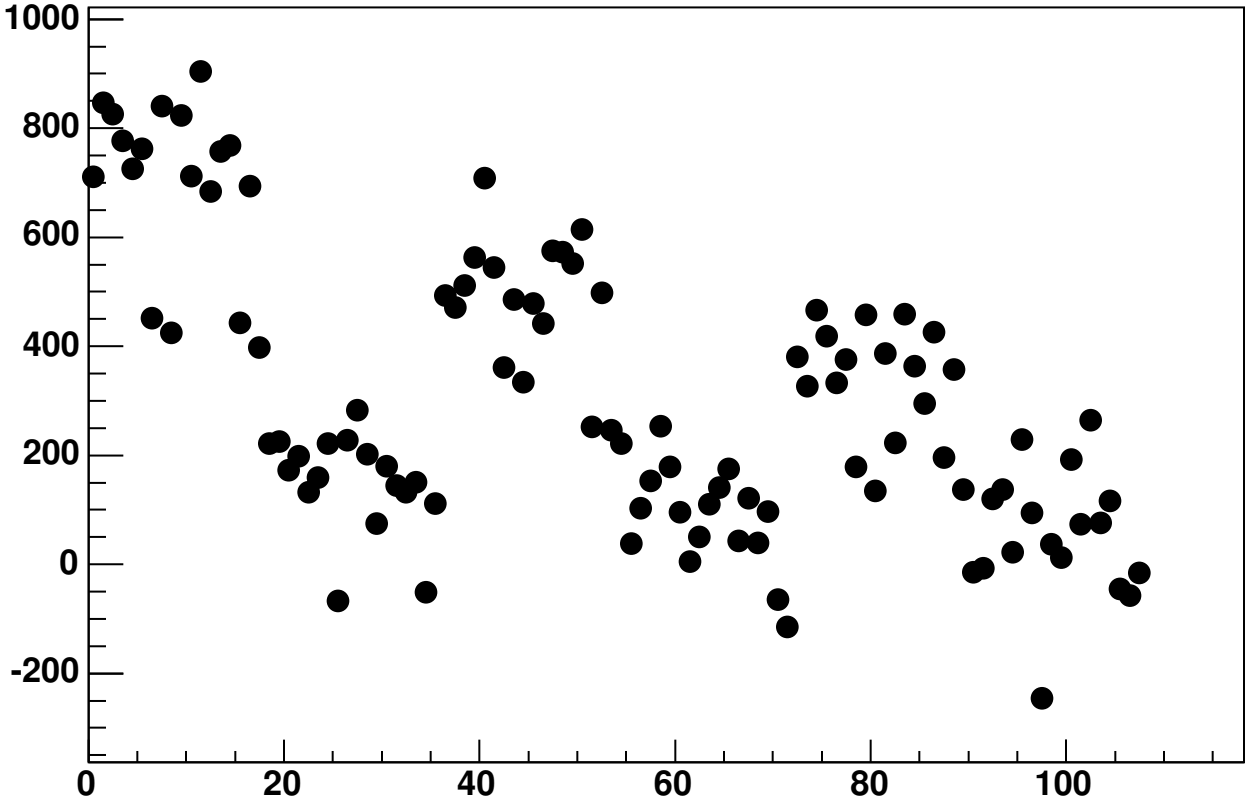
Enable 2, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



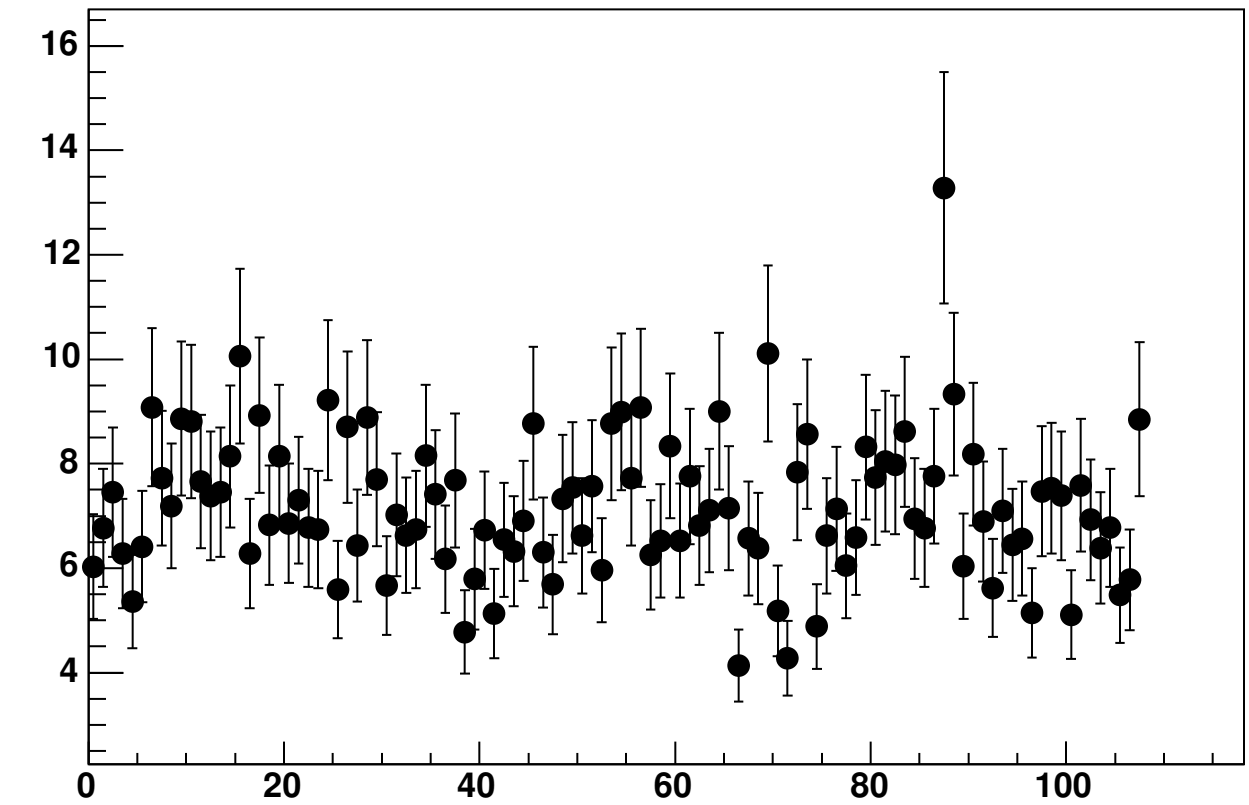
Enable 2, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



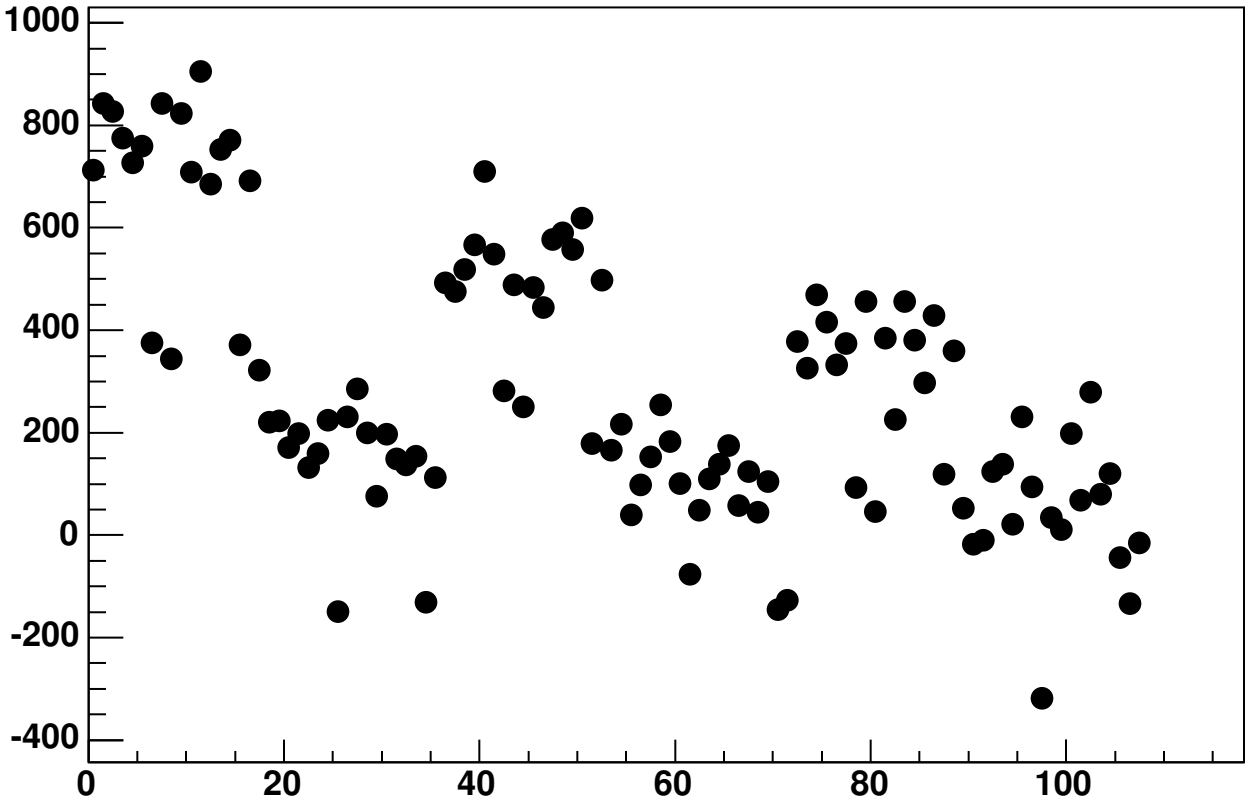
Enable 2, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



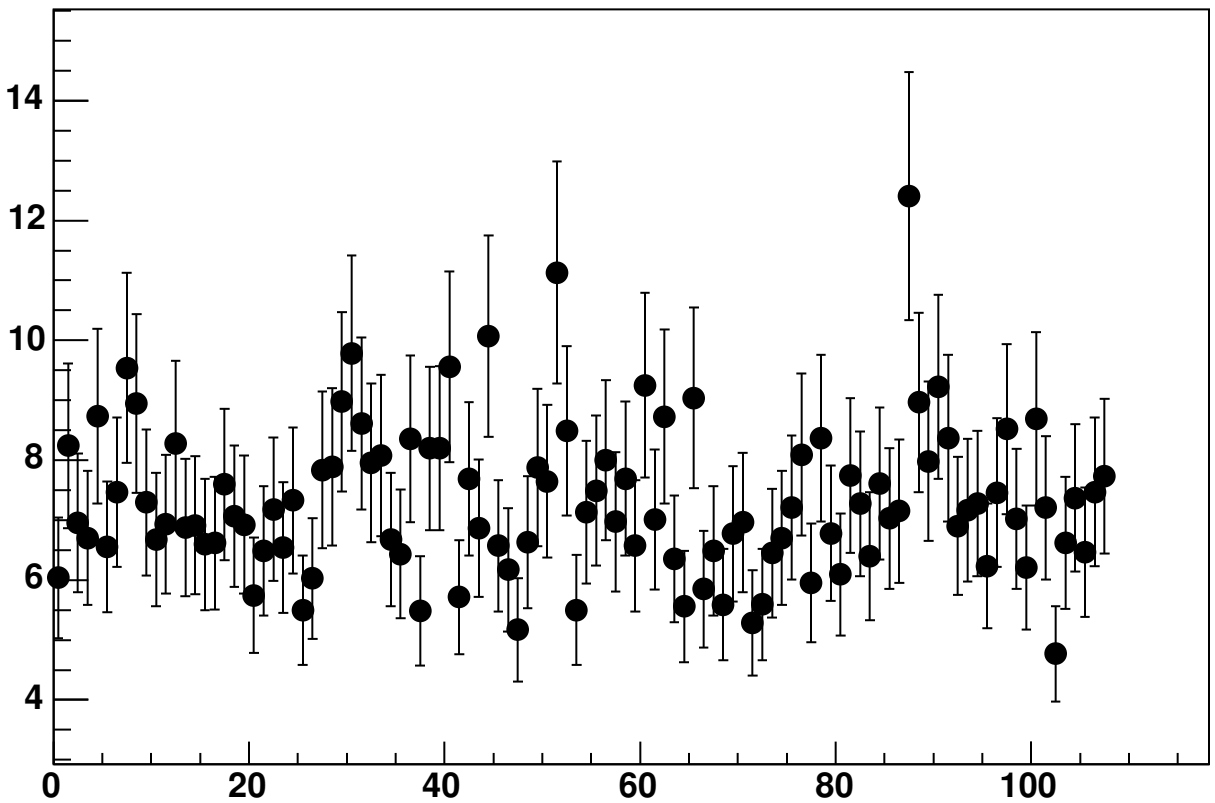
Enable 2, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



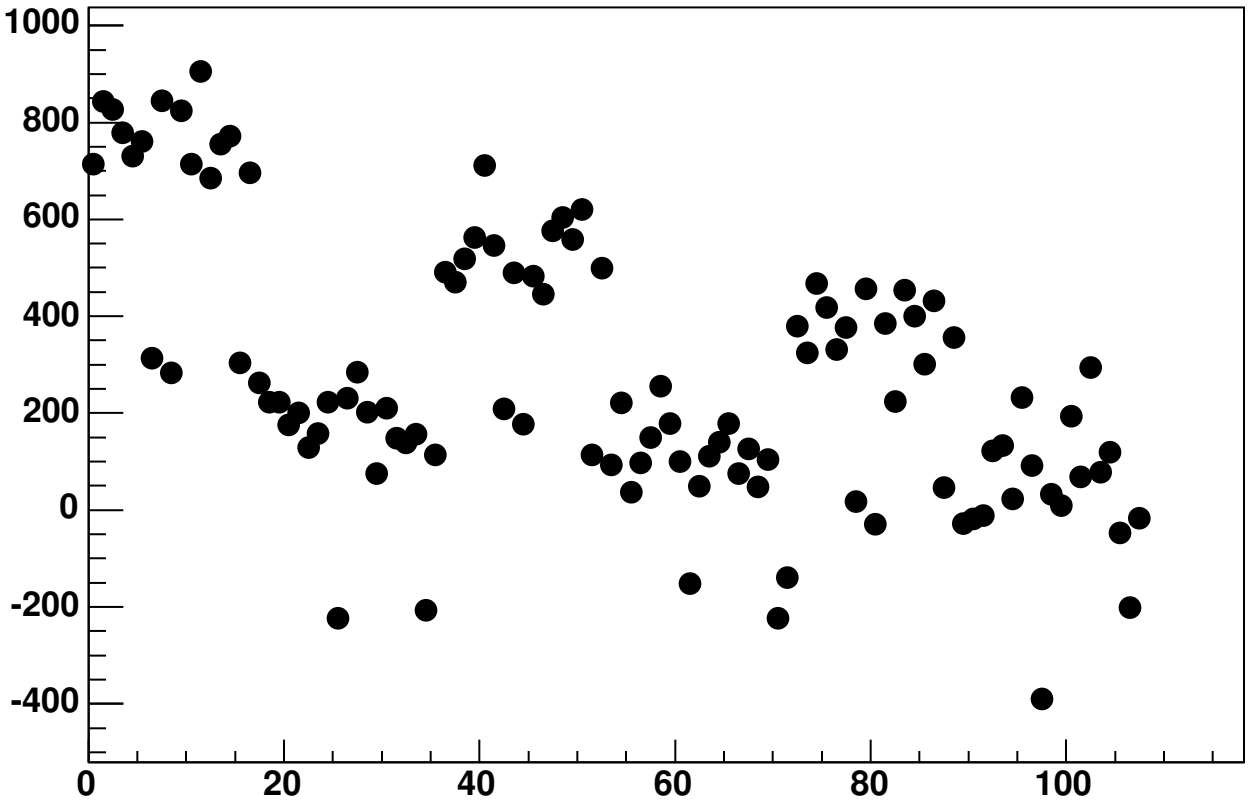
Enable 2, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



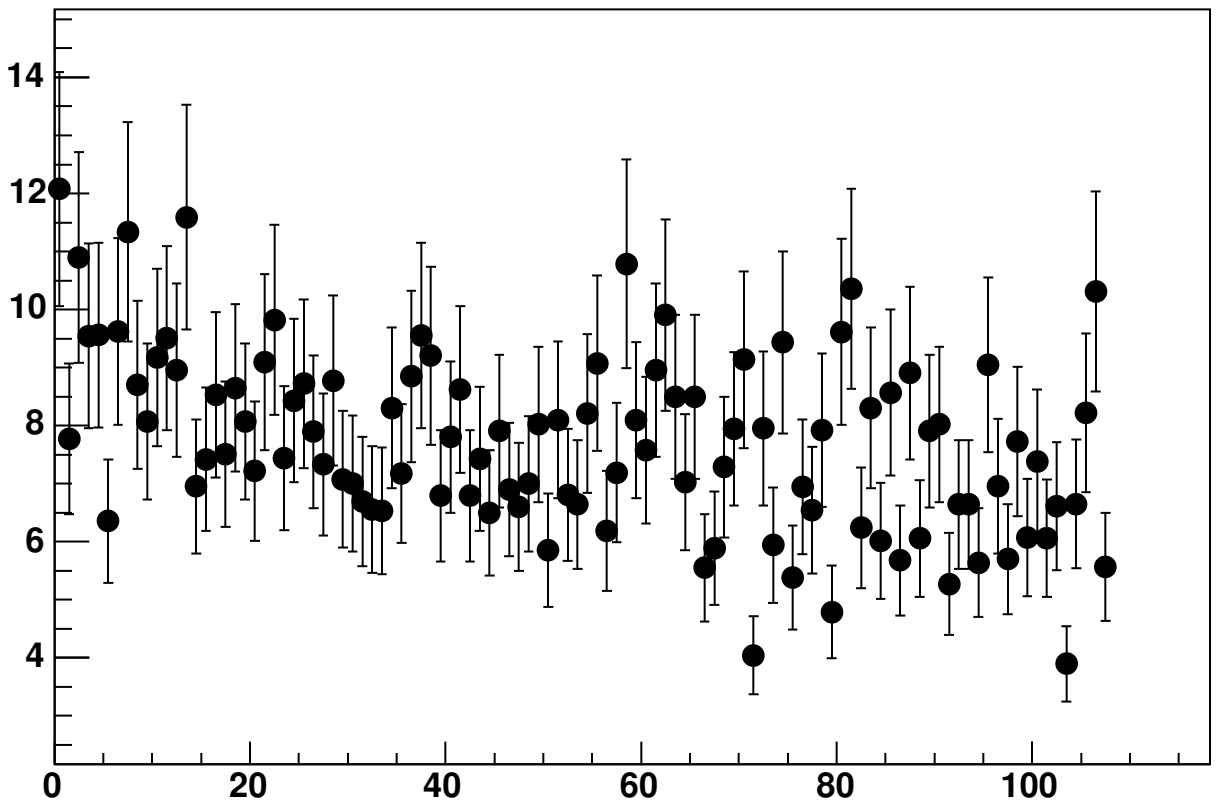
Enable 2, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



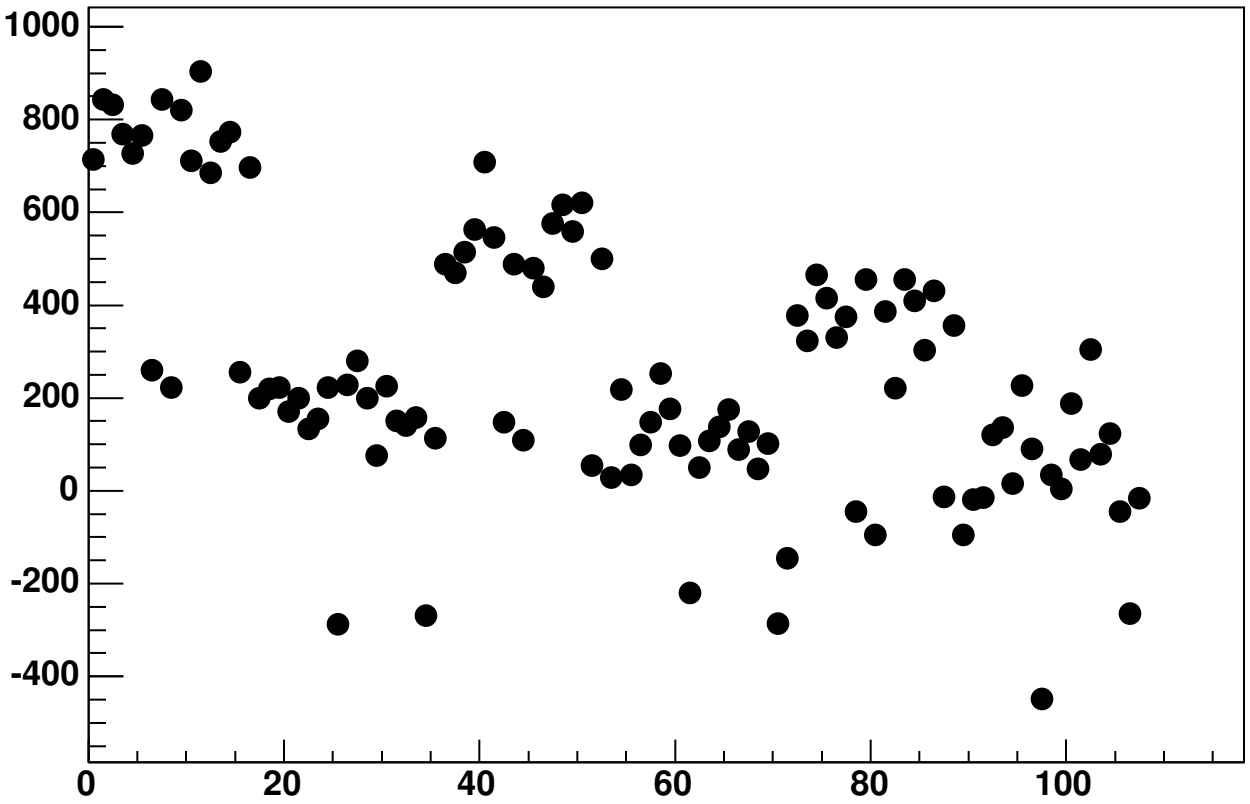
Enable 2, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



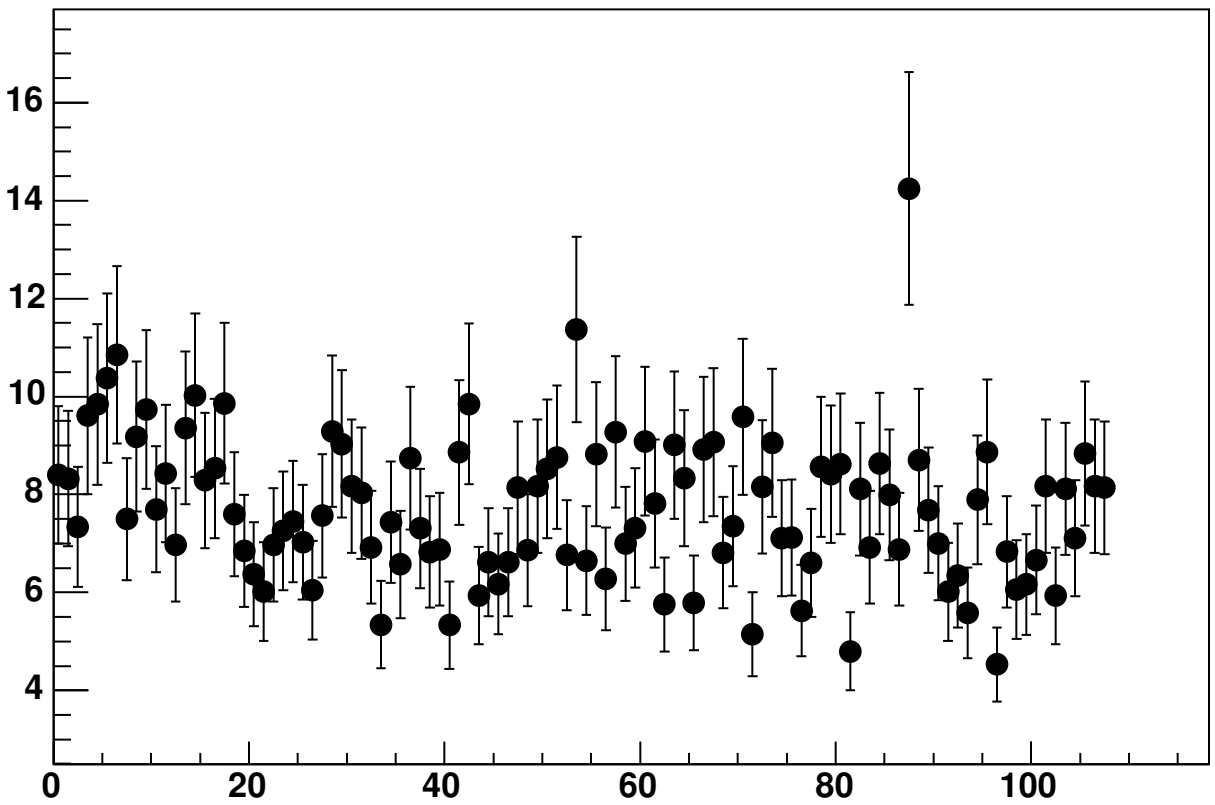
Enable 2, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



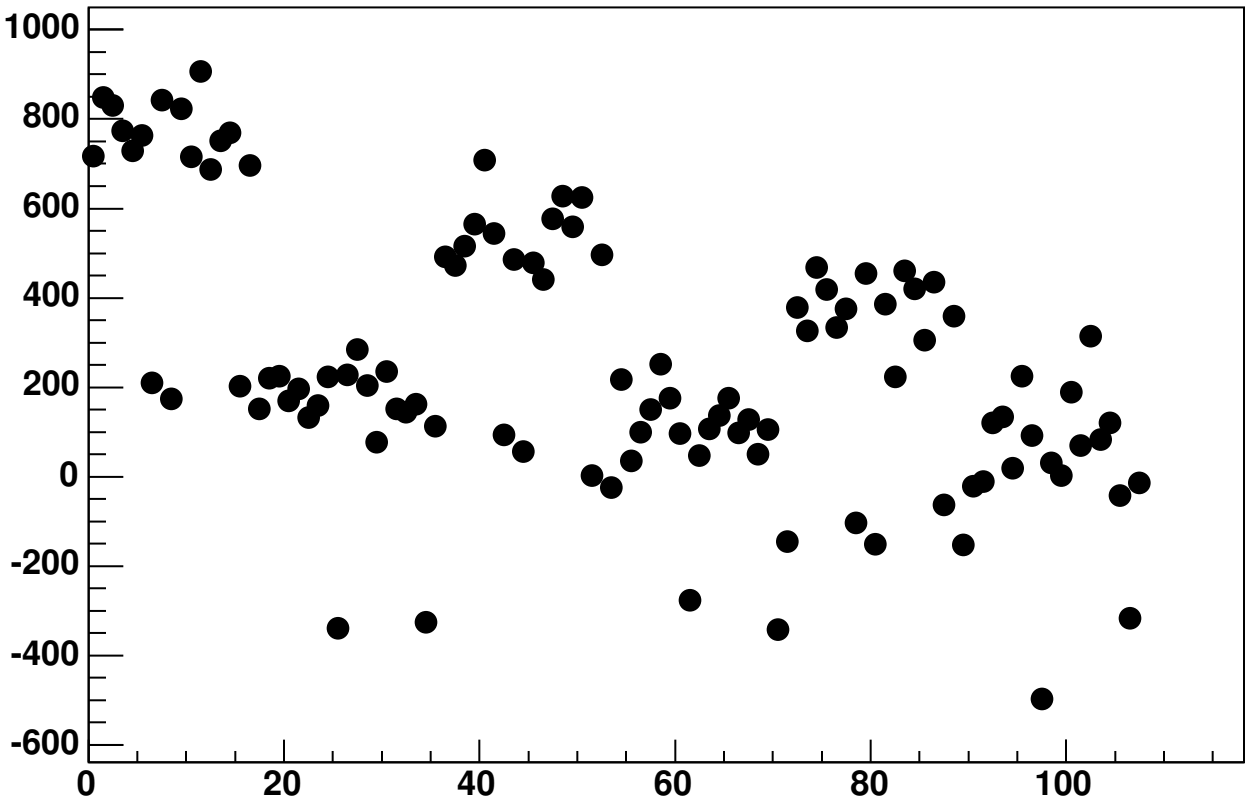
Enable 2, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



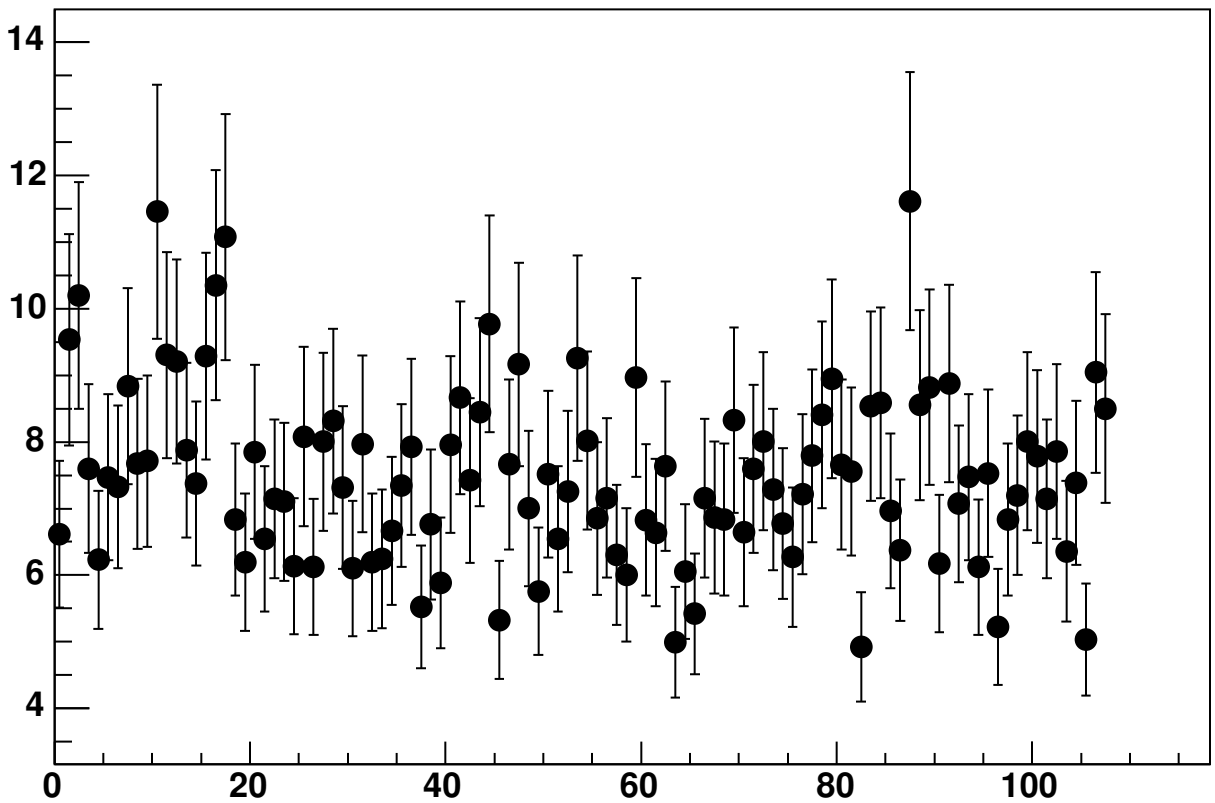
Enable 2, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



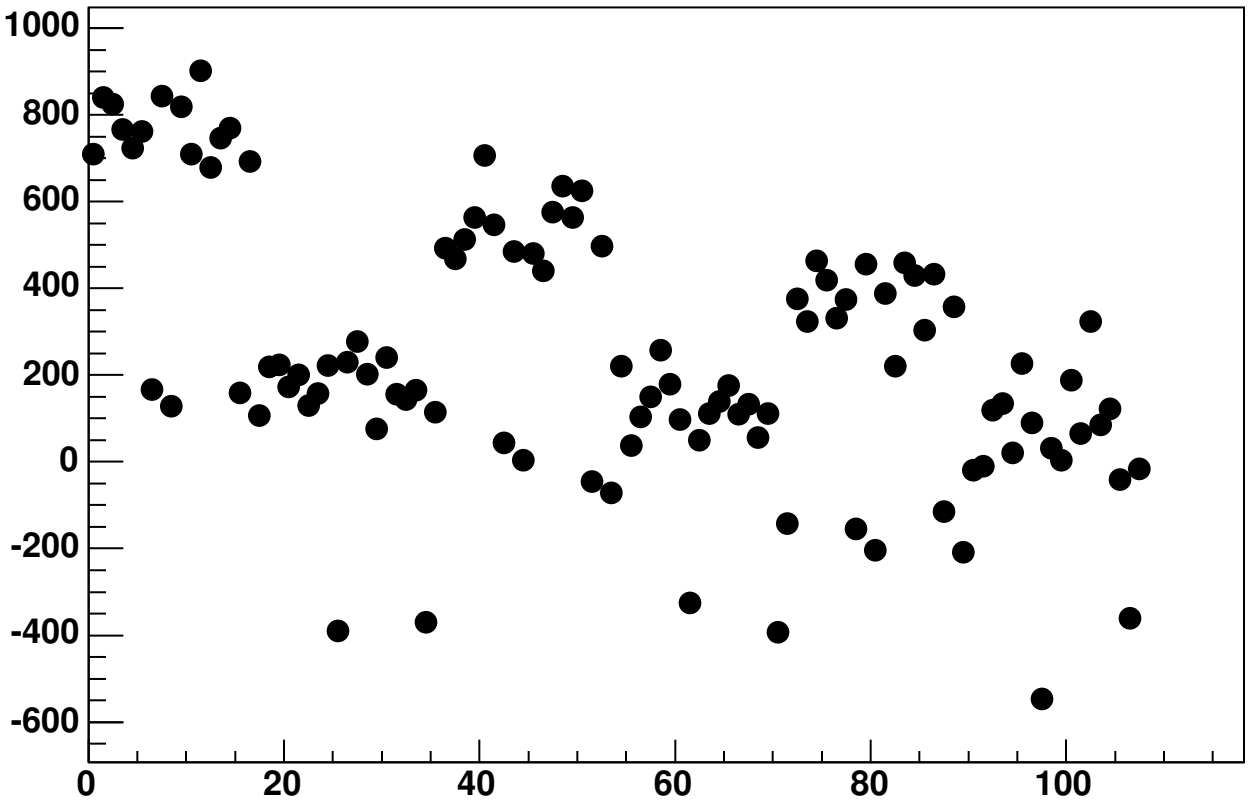
Enable 2, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



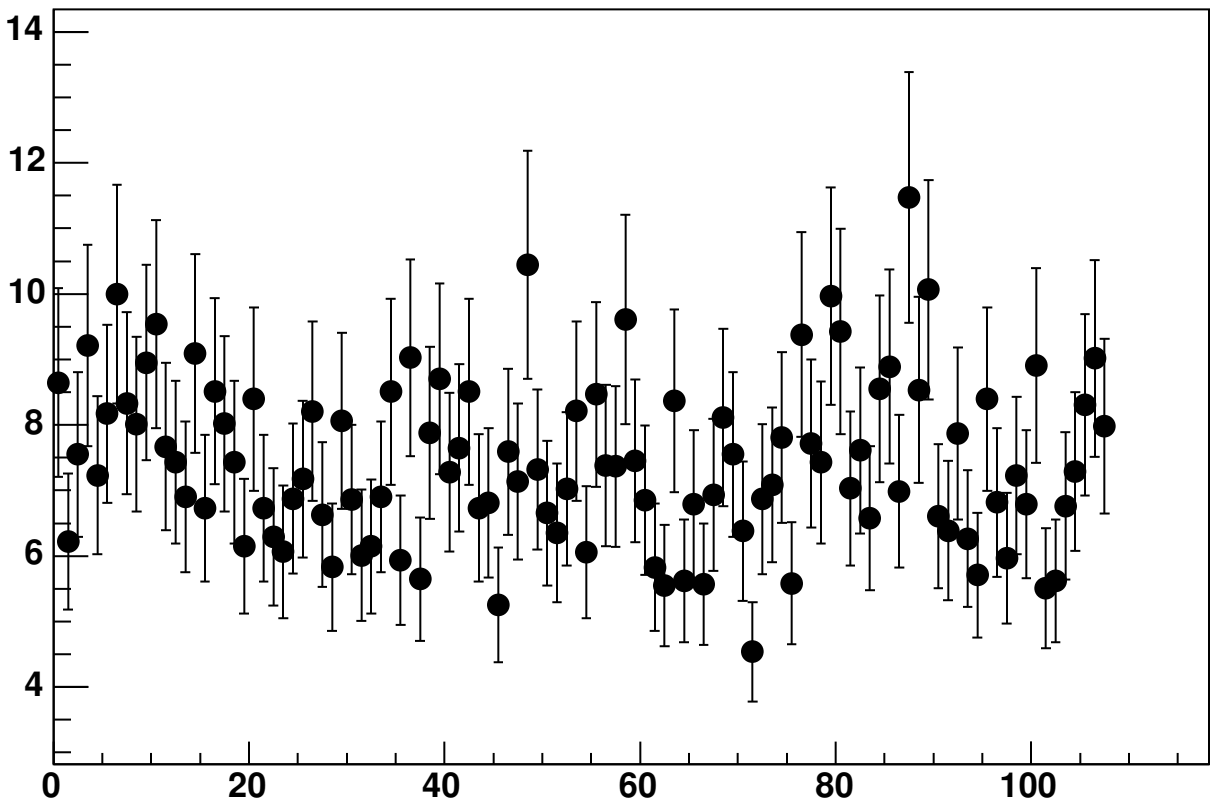
Enable 2, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



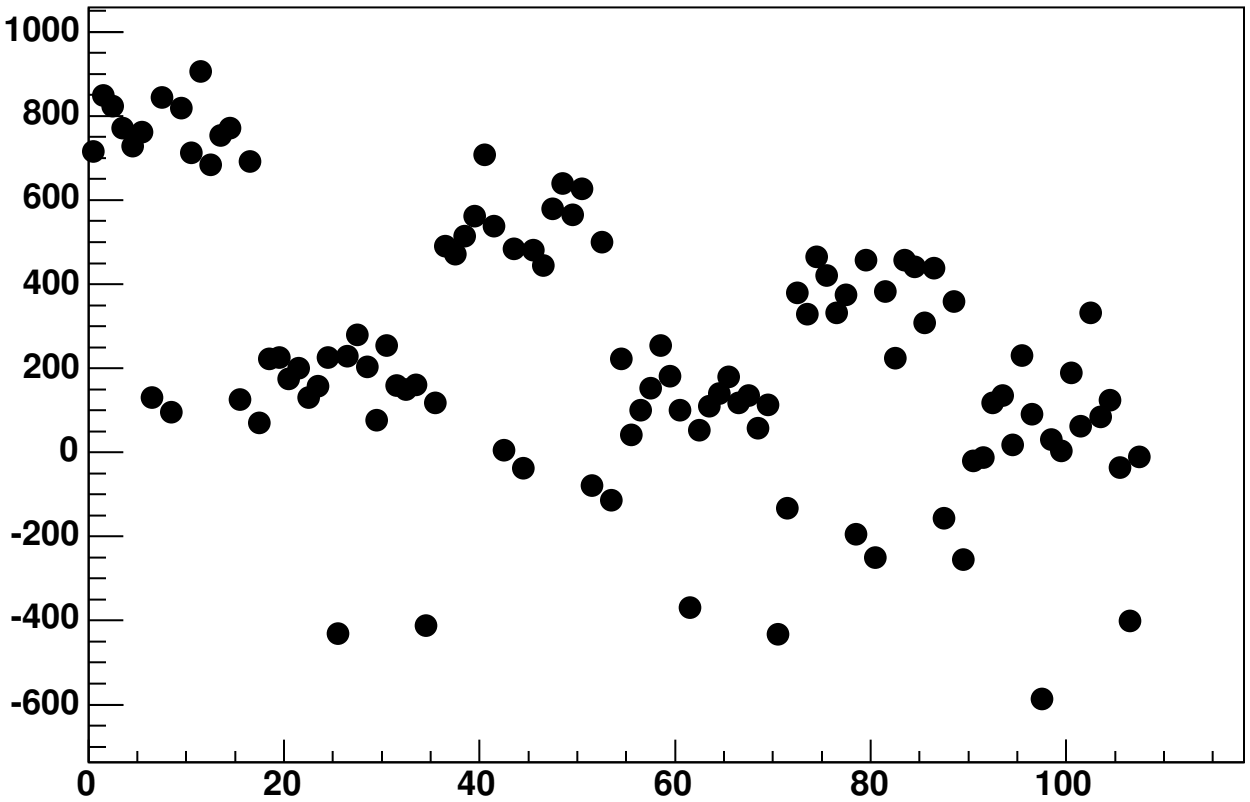
Enable 2, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



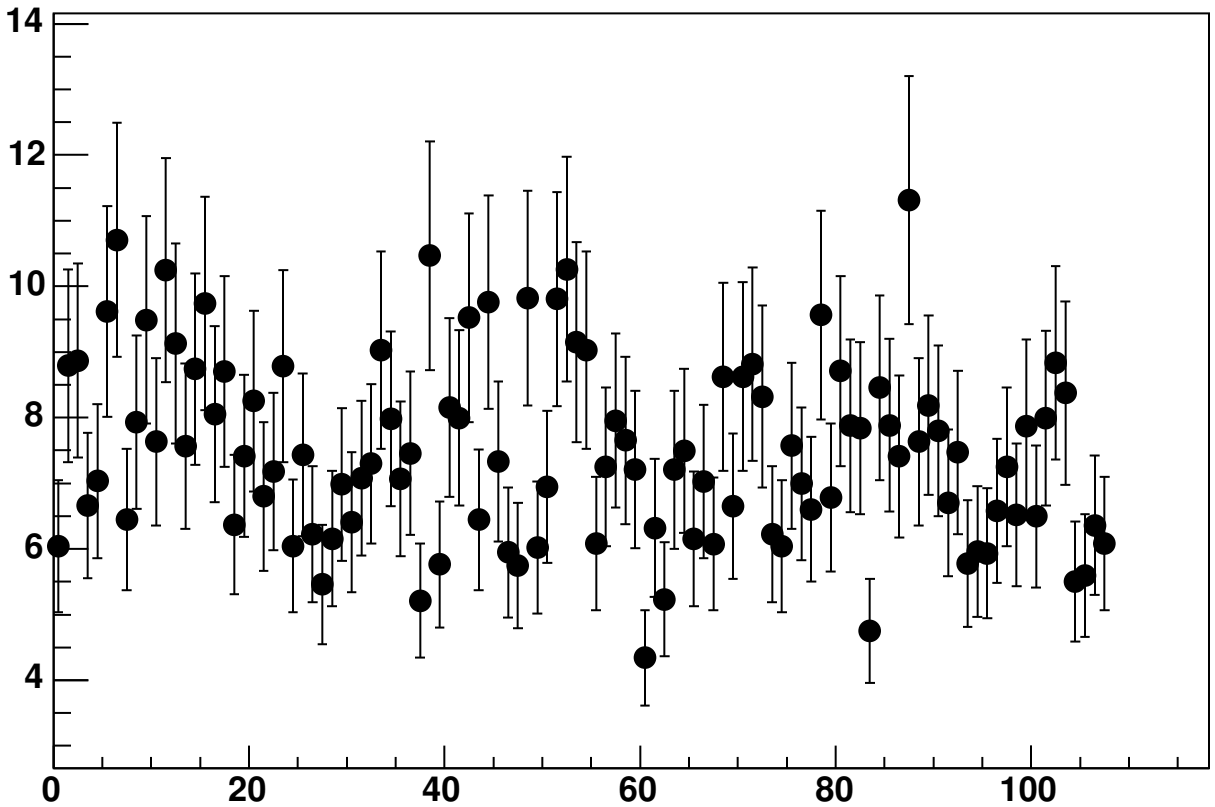
Enable 2, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



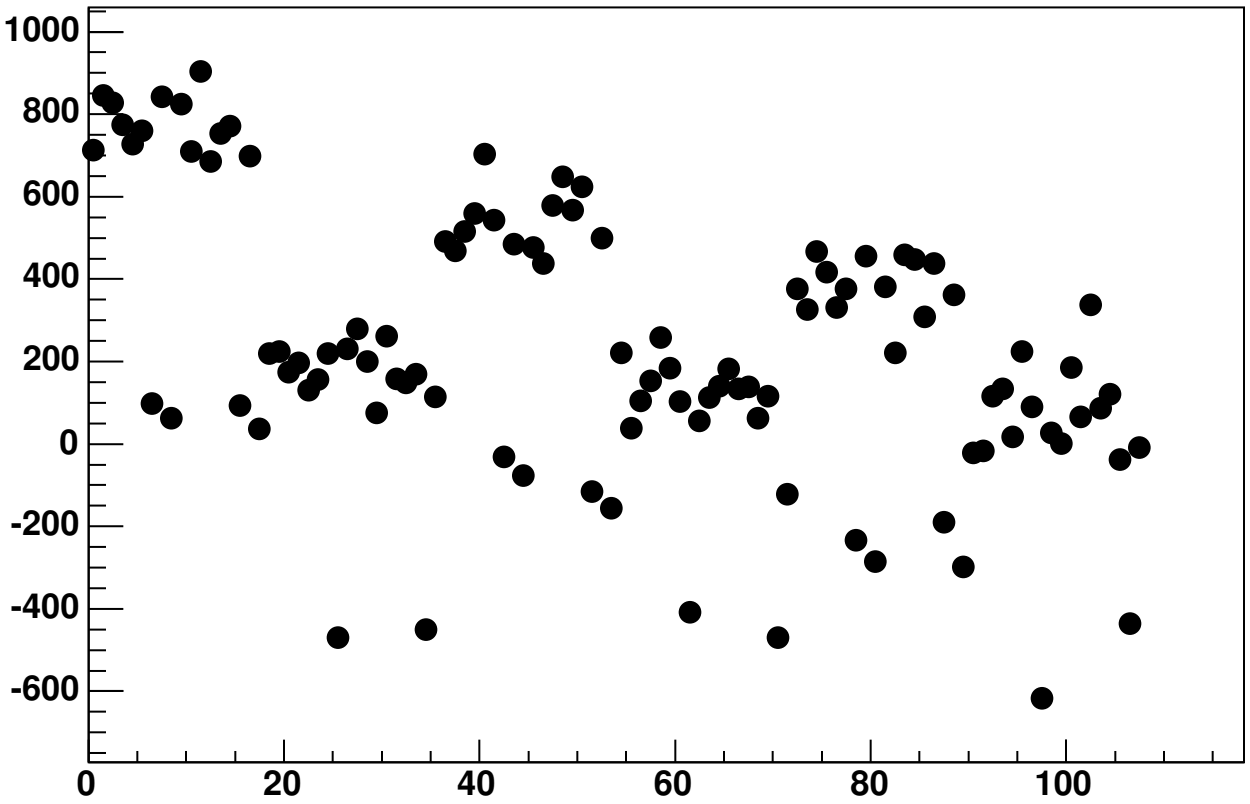
Enable 2, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



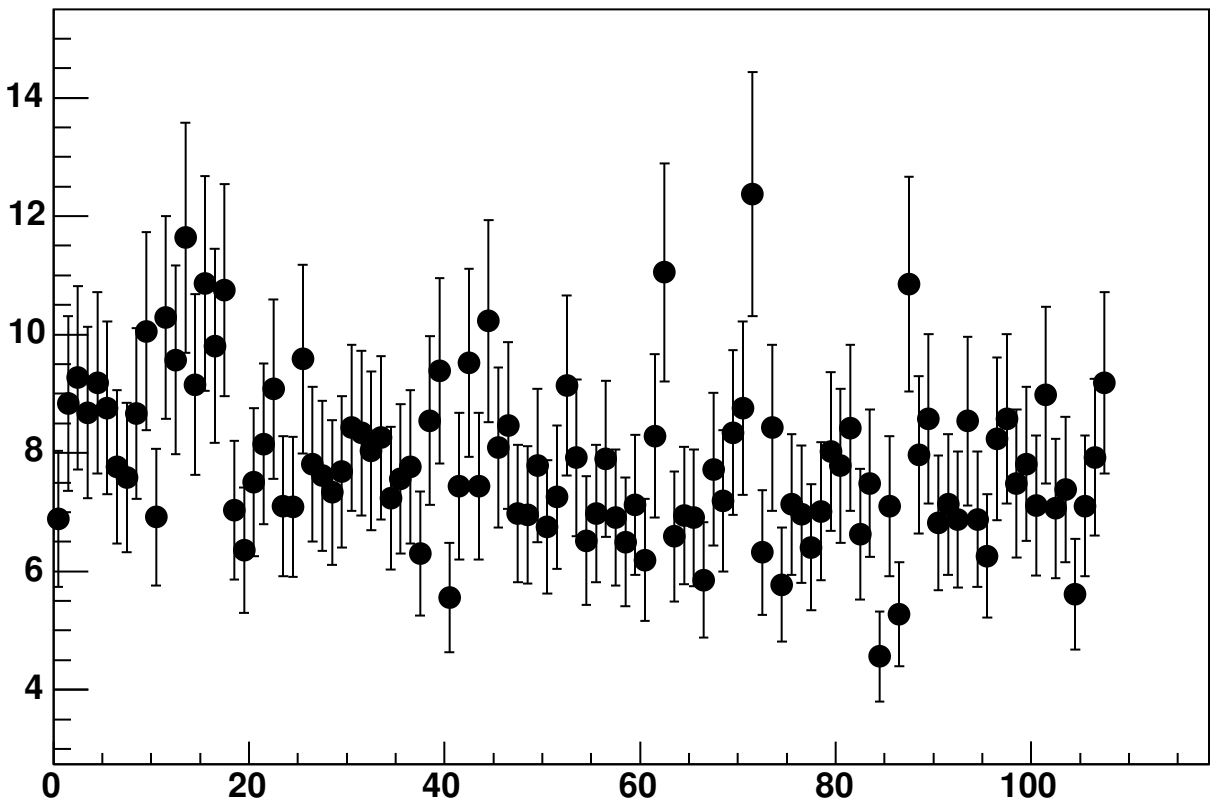
Enable 2, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



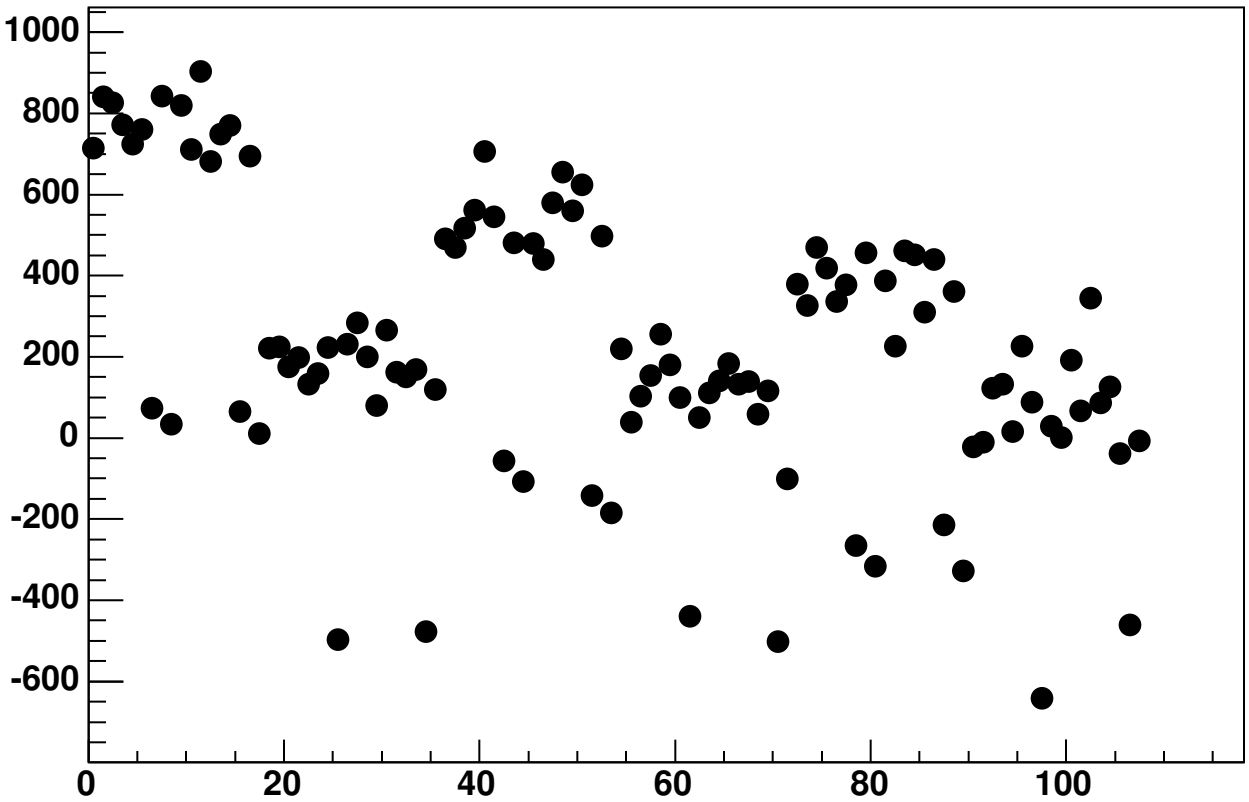
Enable 2, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



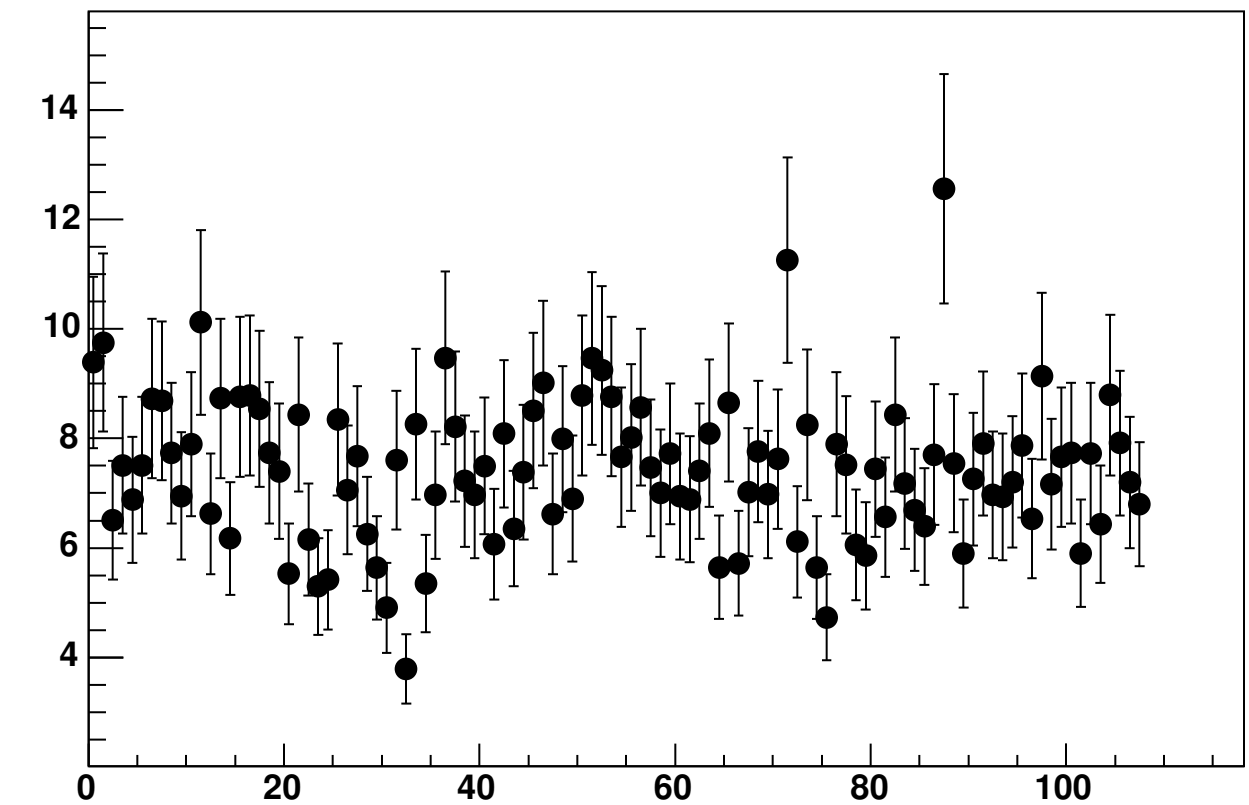
Enable 2, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



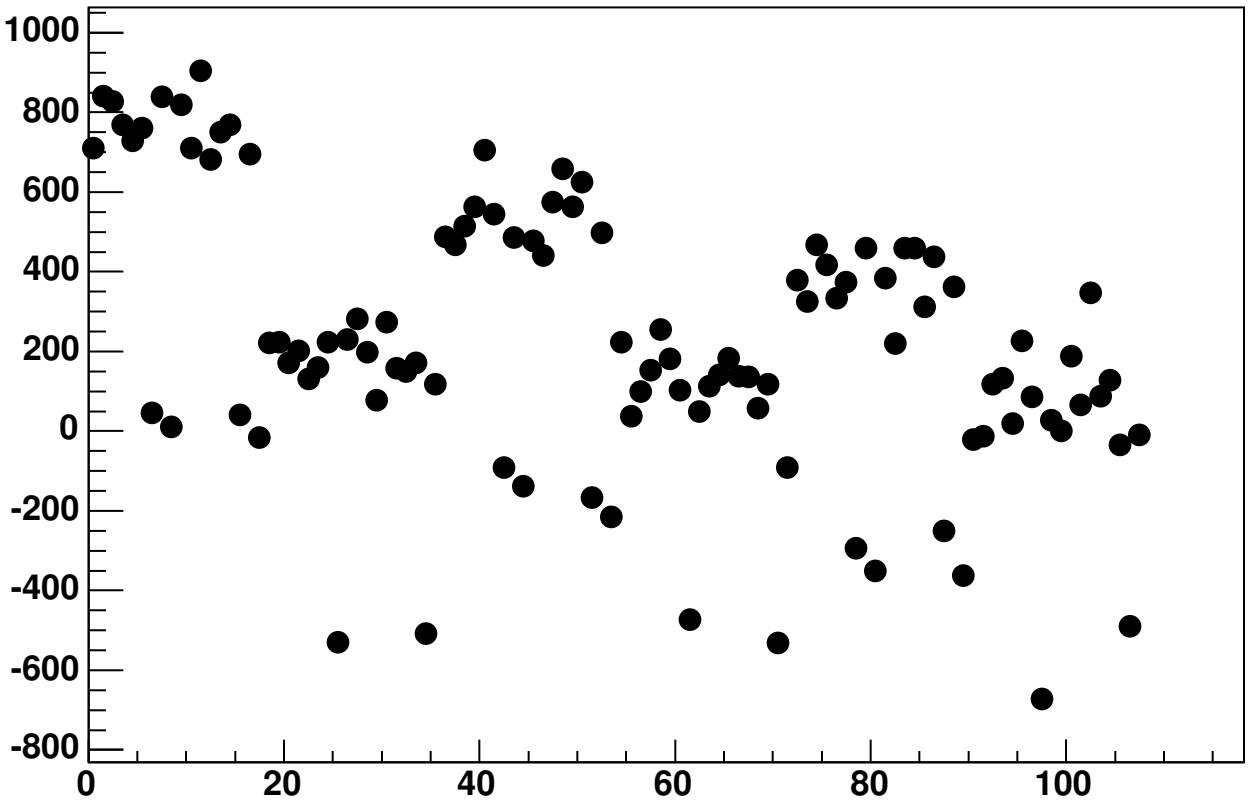
Enable 2, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



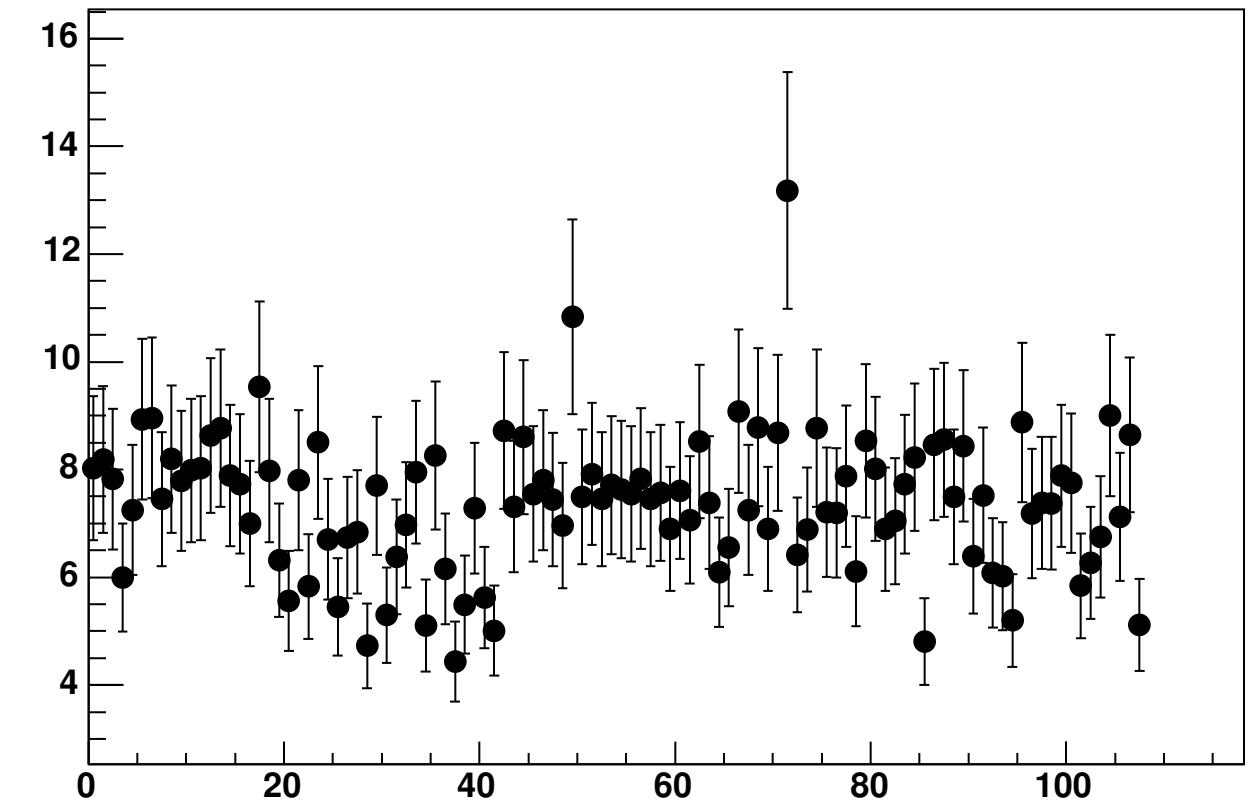
Enable 2, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



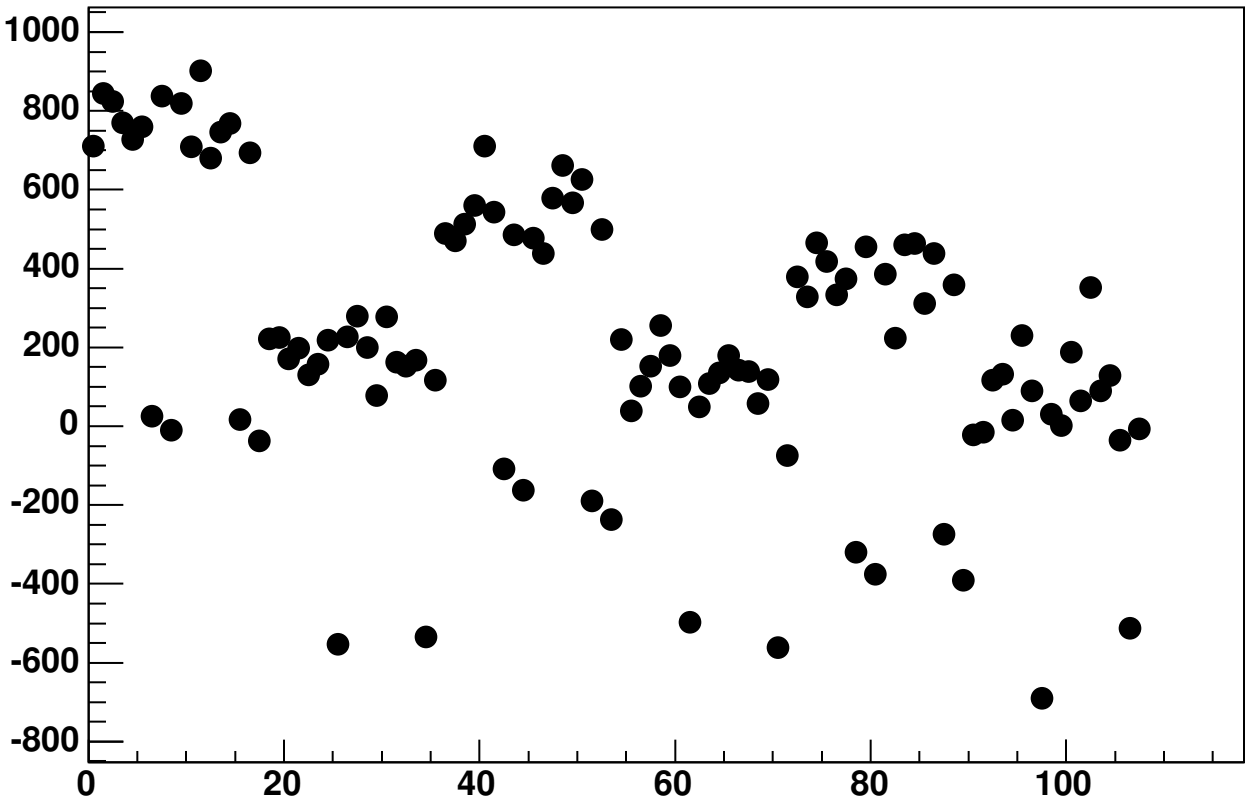
Enable 2, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



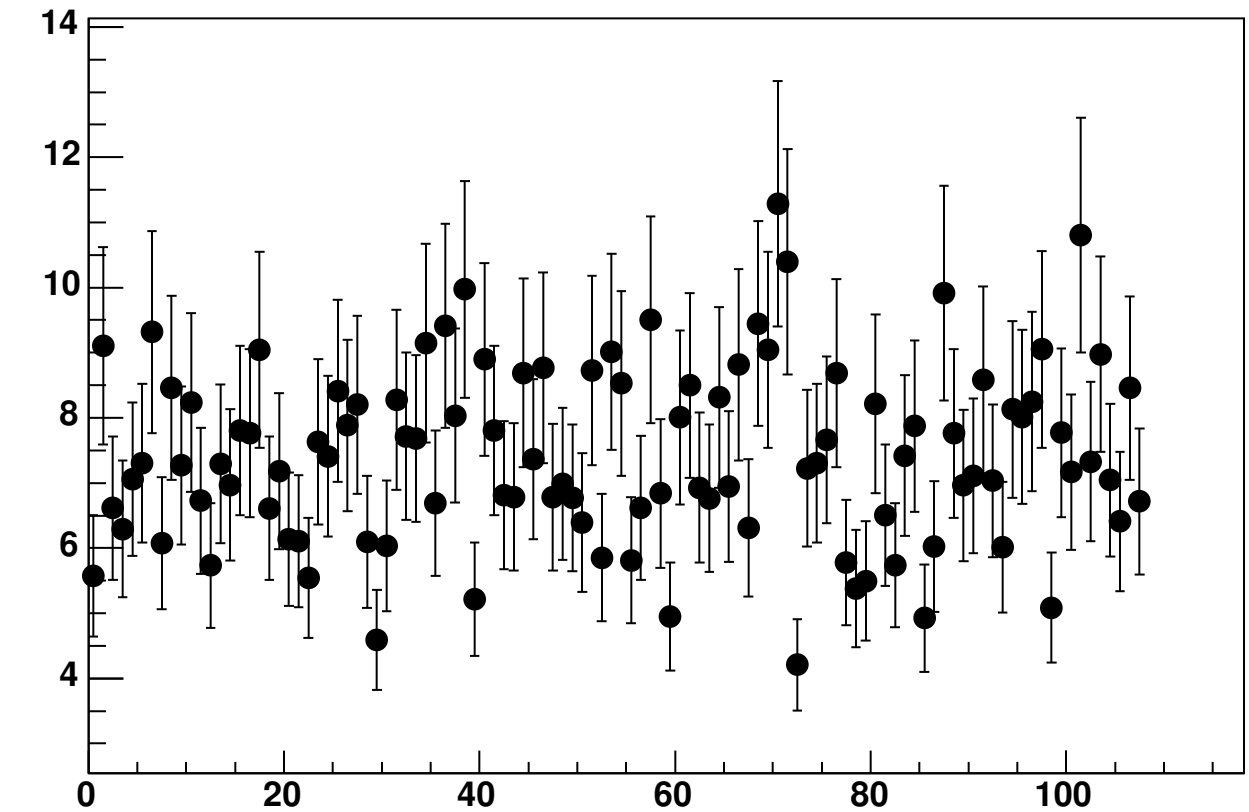
Enable 2, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



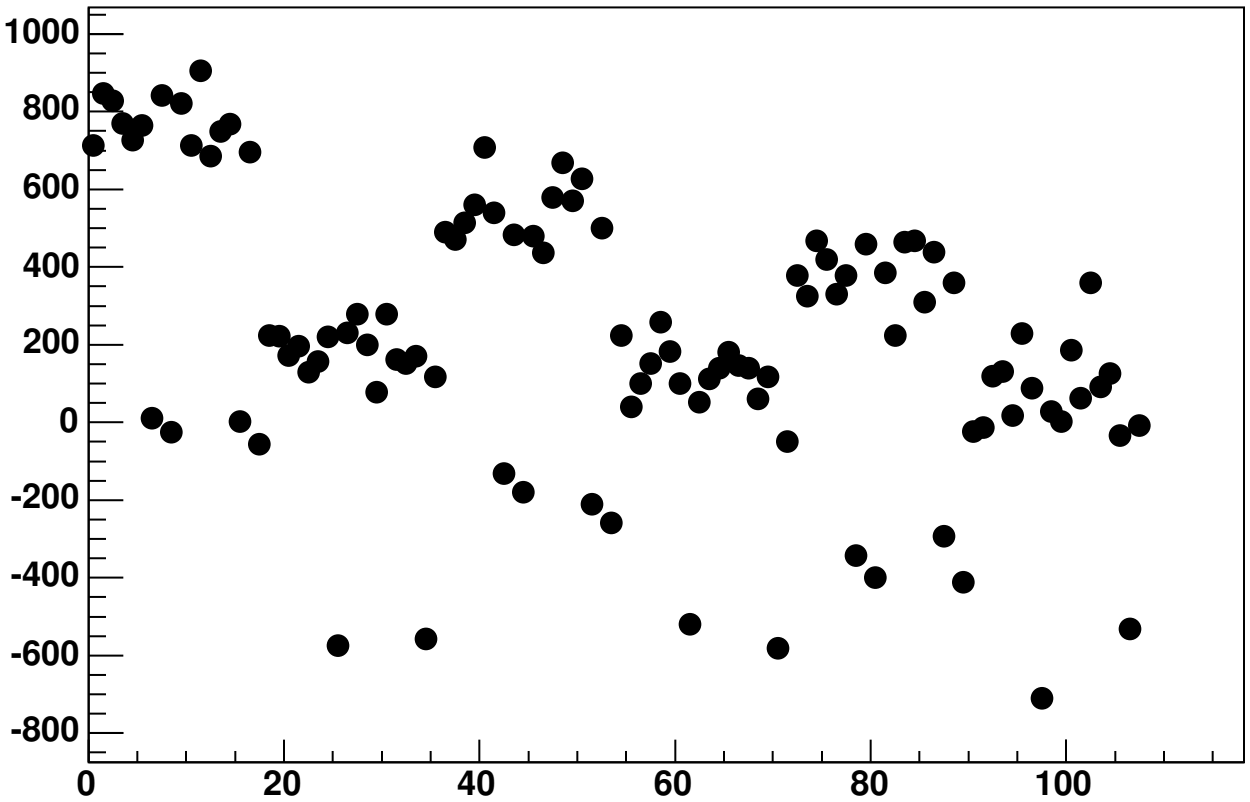
Enable 2, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



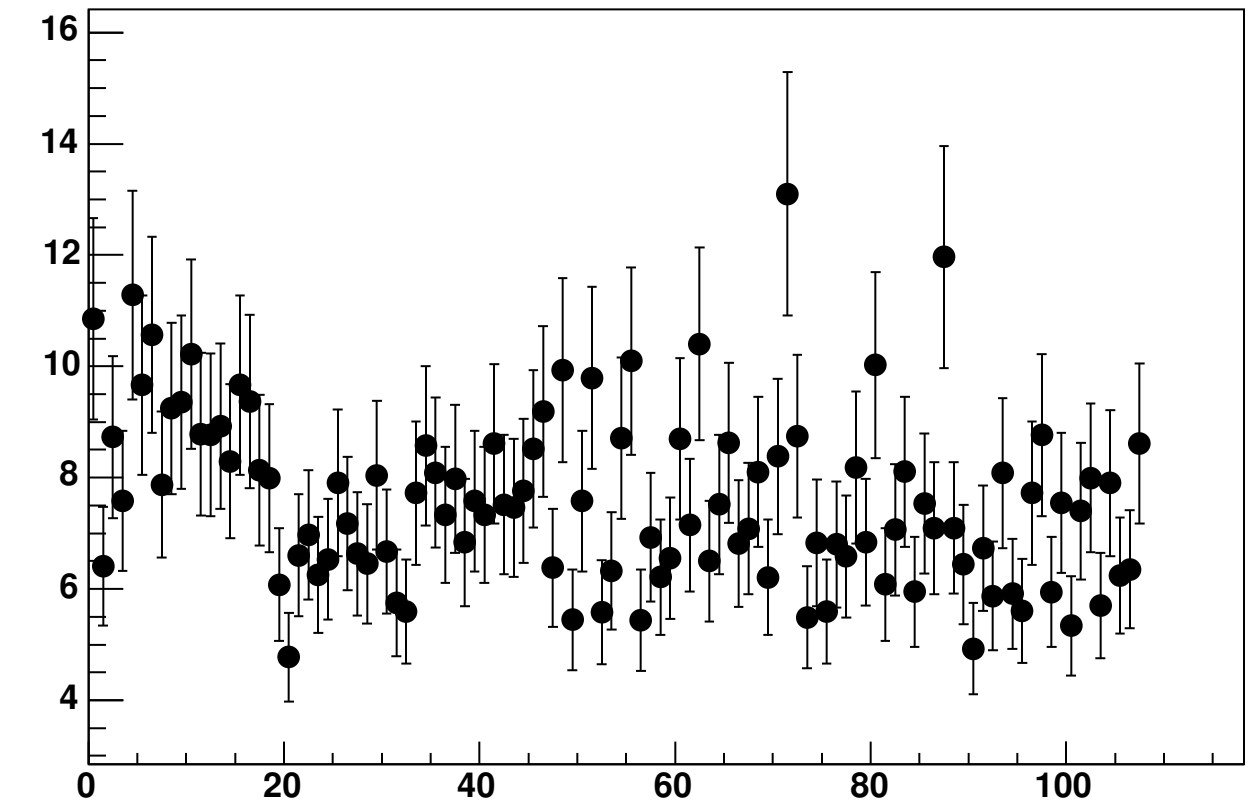
Enable 2, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



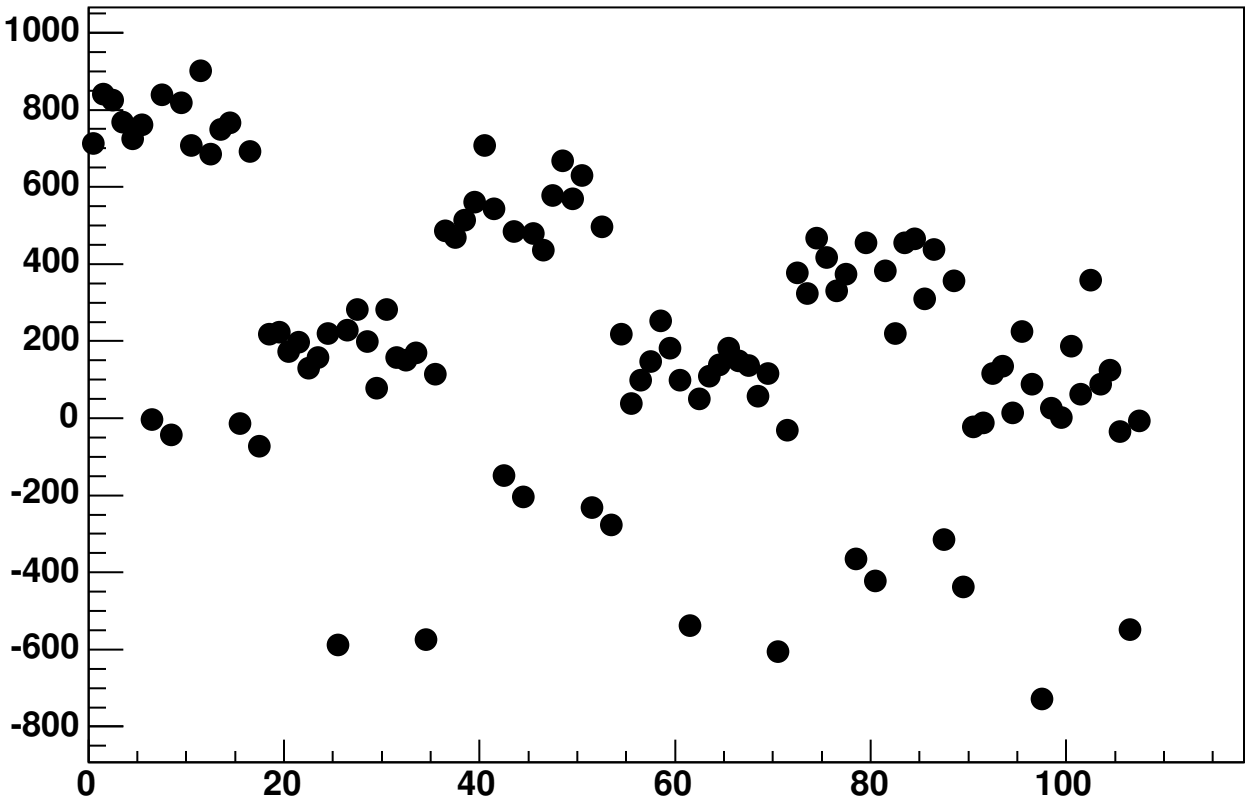
Enable 2, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



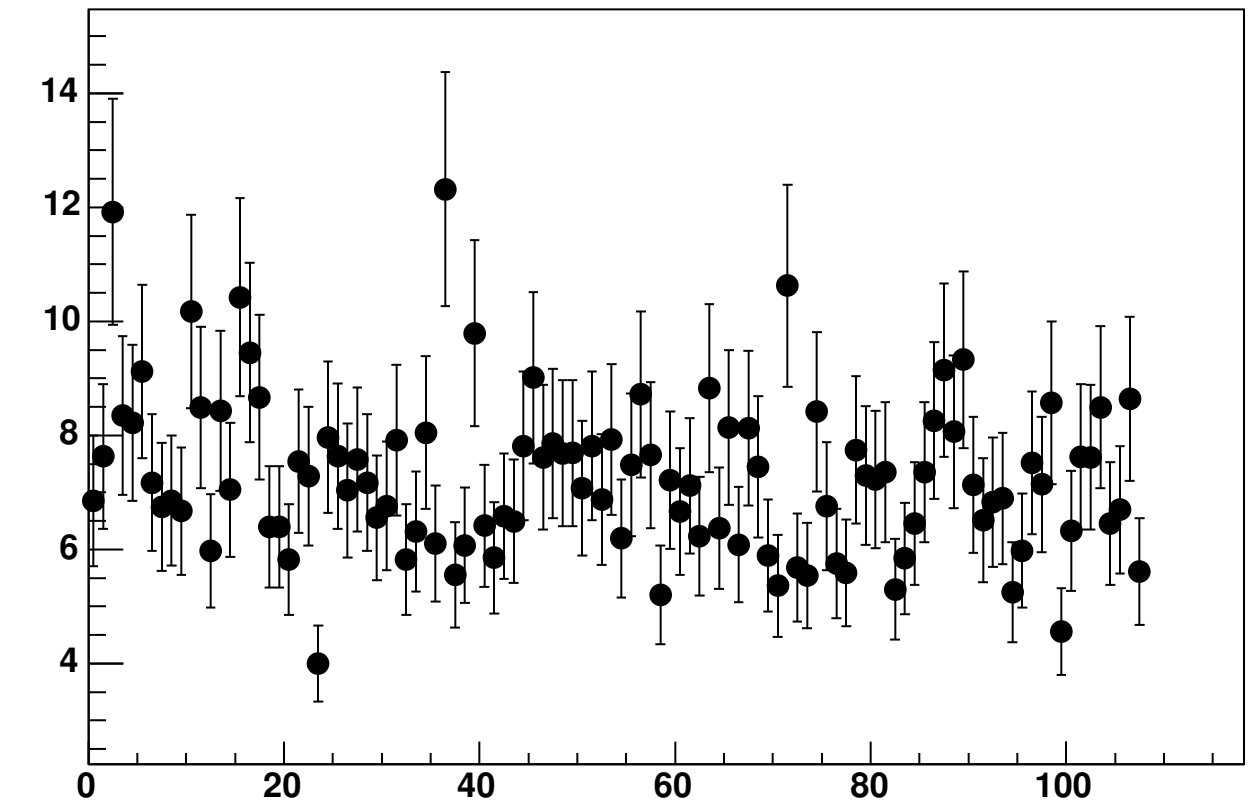
Enable 2, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



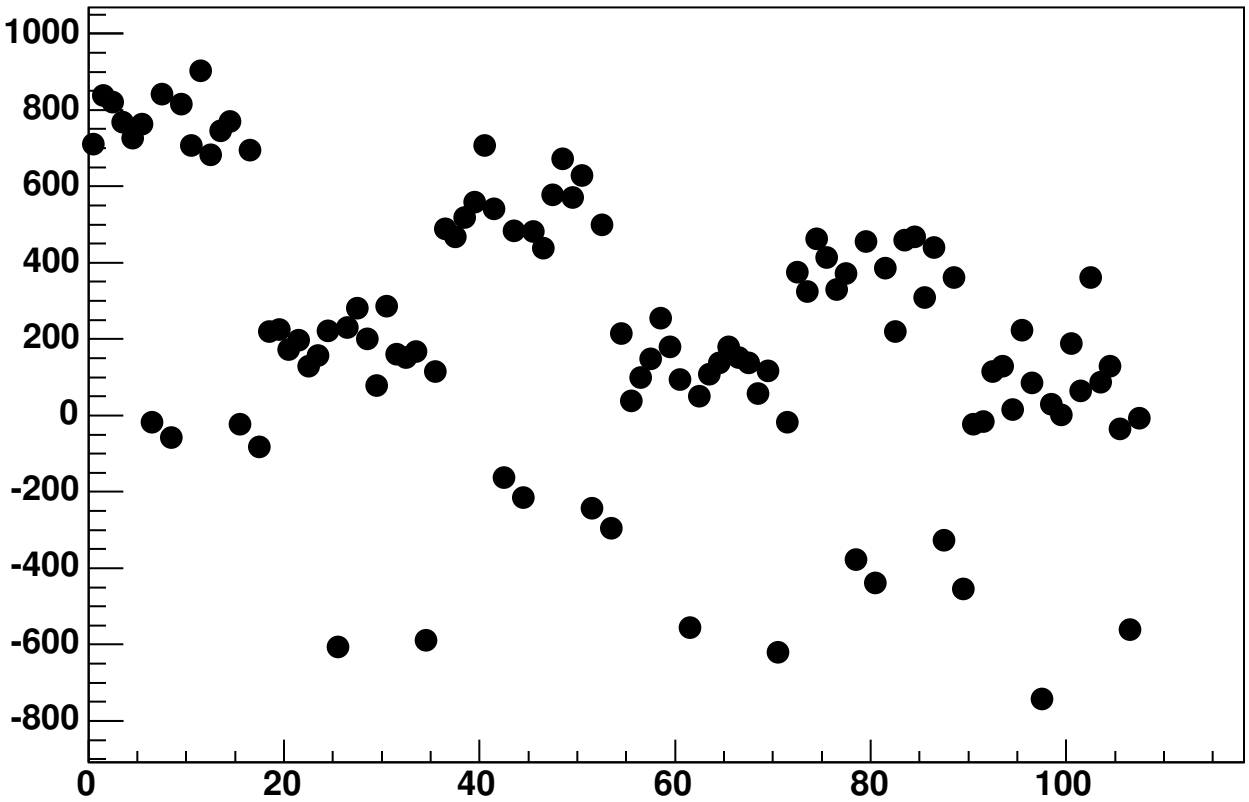
Enable 2, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



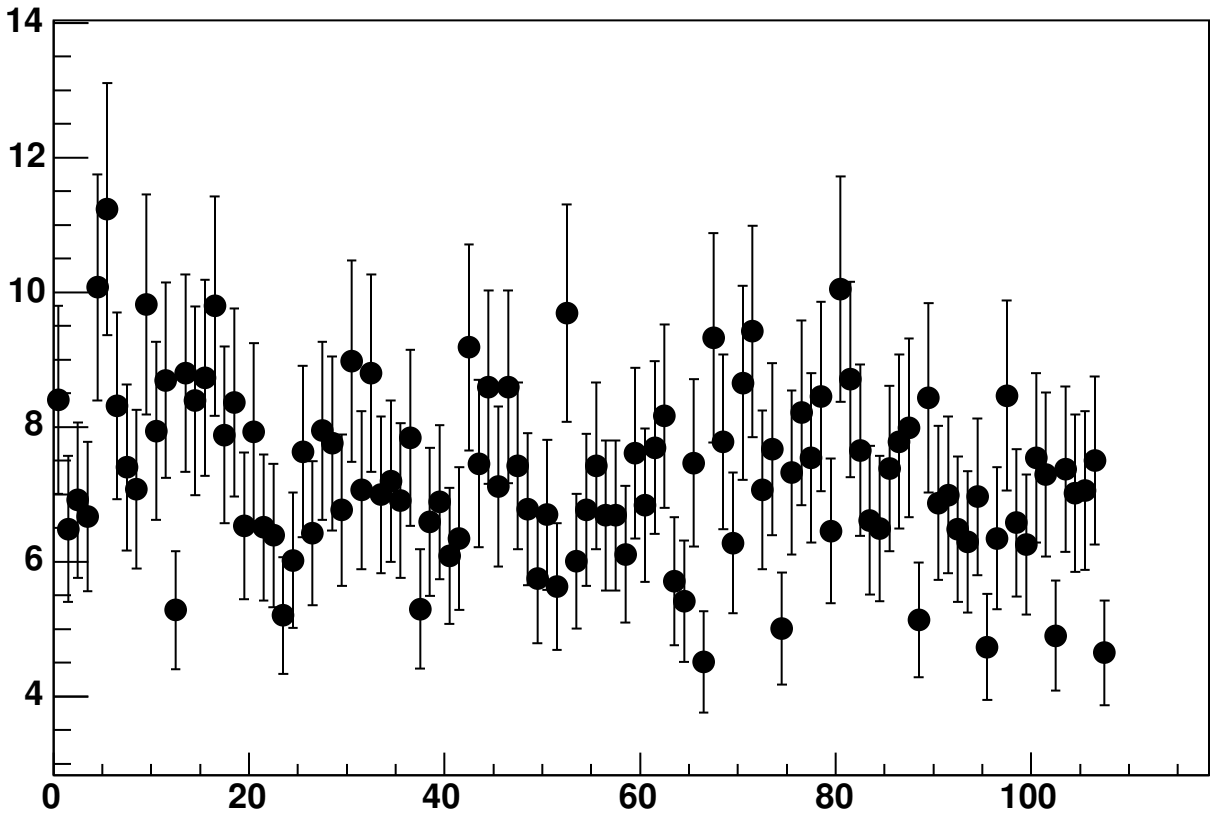
Enable 2, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan



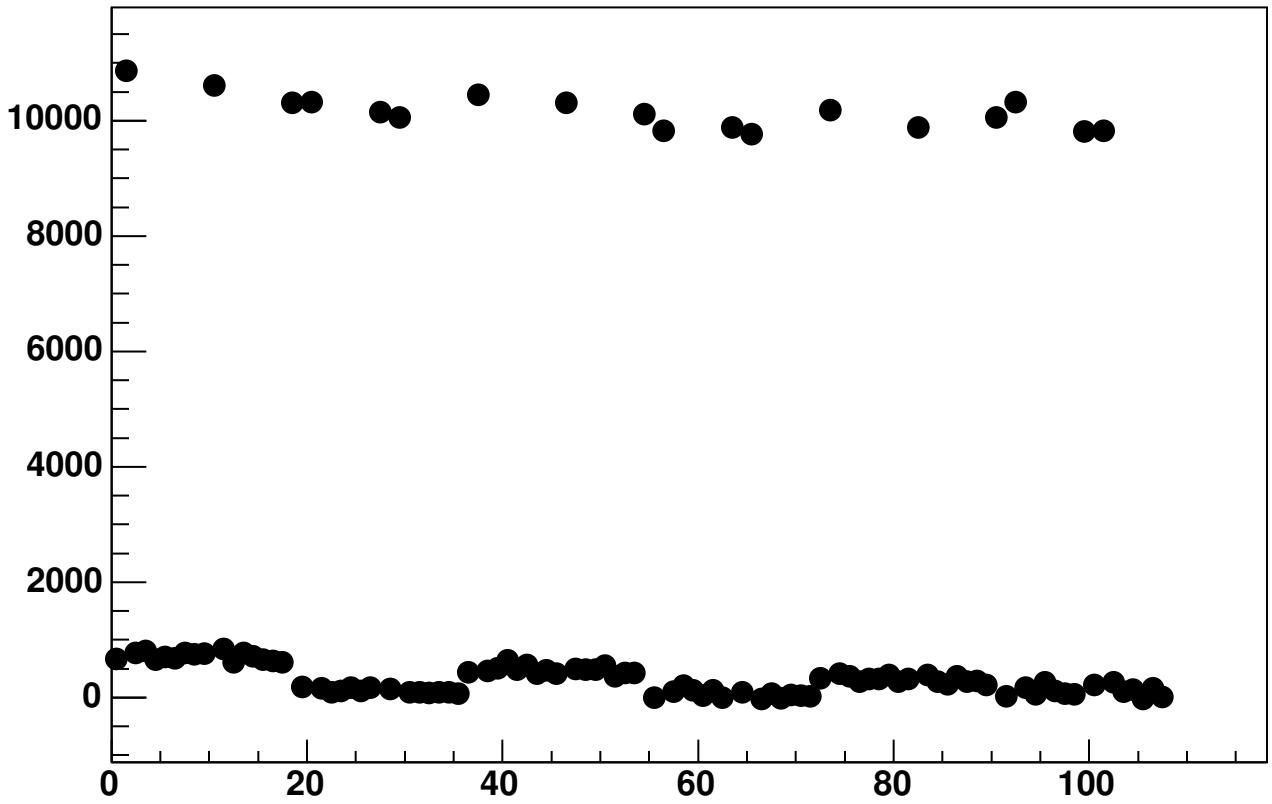
Enable 2, DAC=4000, Hold=245, ADC Mean vs 18*Chip+Chan



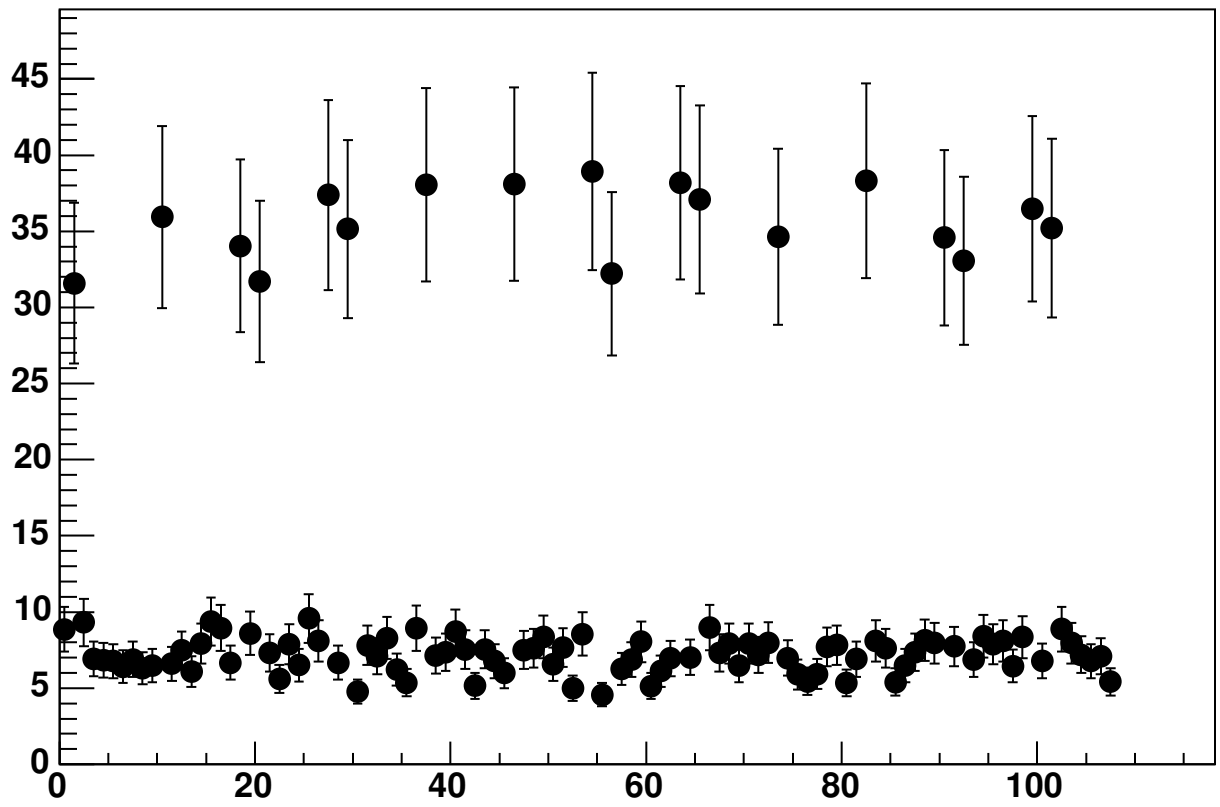
Enable 2, DAC=4000, Hold=245, ADC Noise vs 18*Chip+Chan



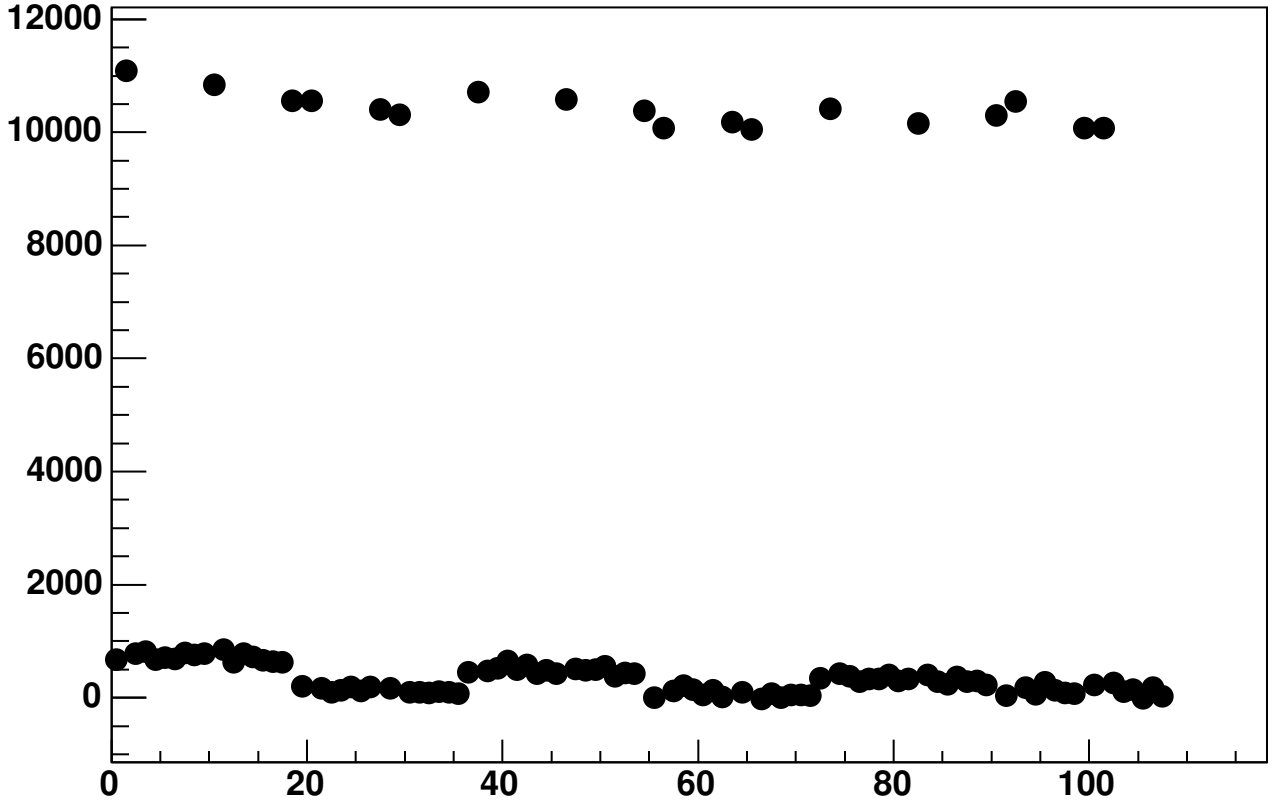
Enable 3, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



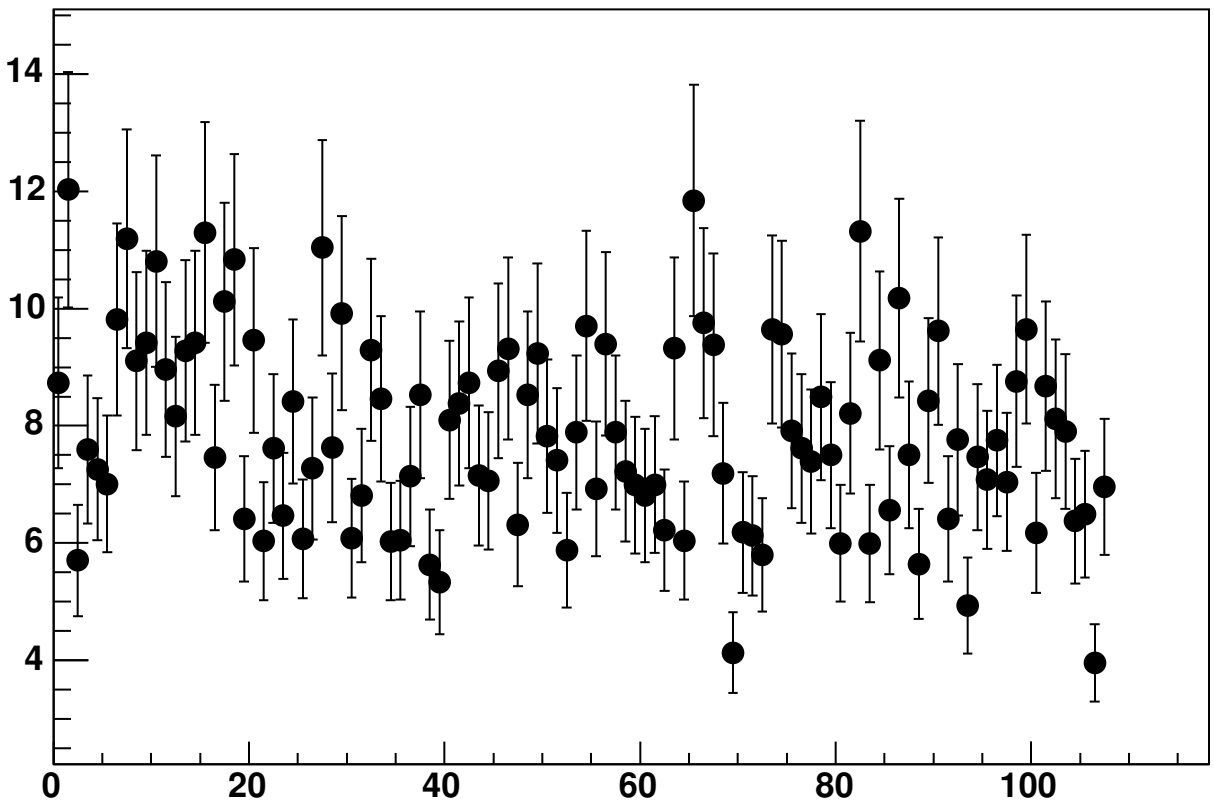
Enable 3, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



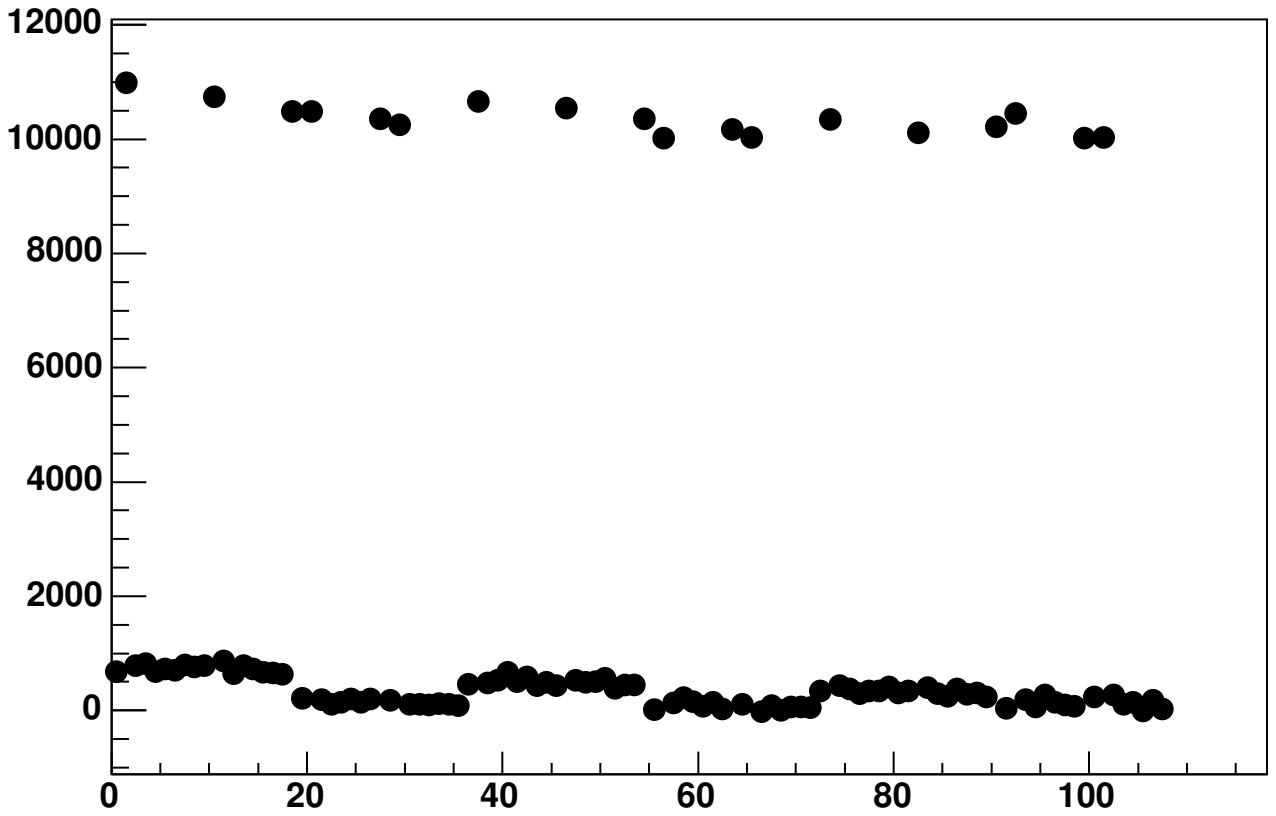
Enable 3, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



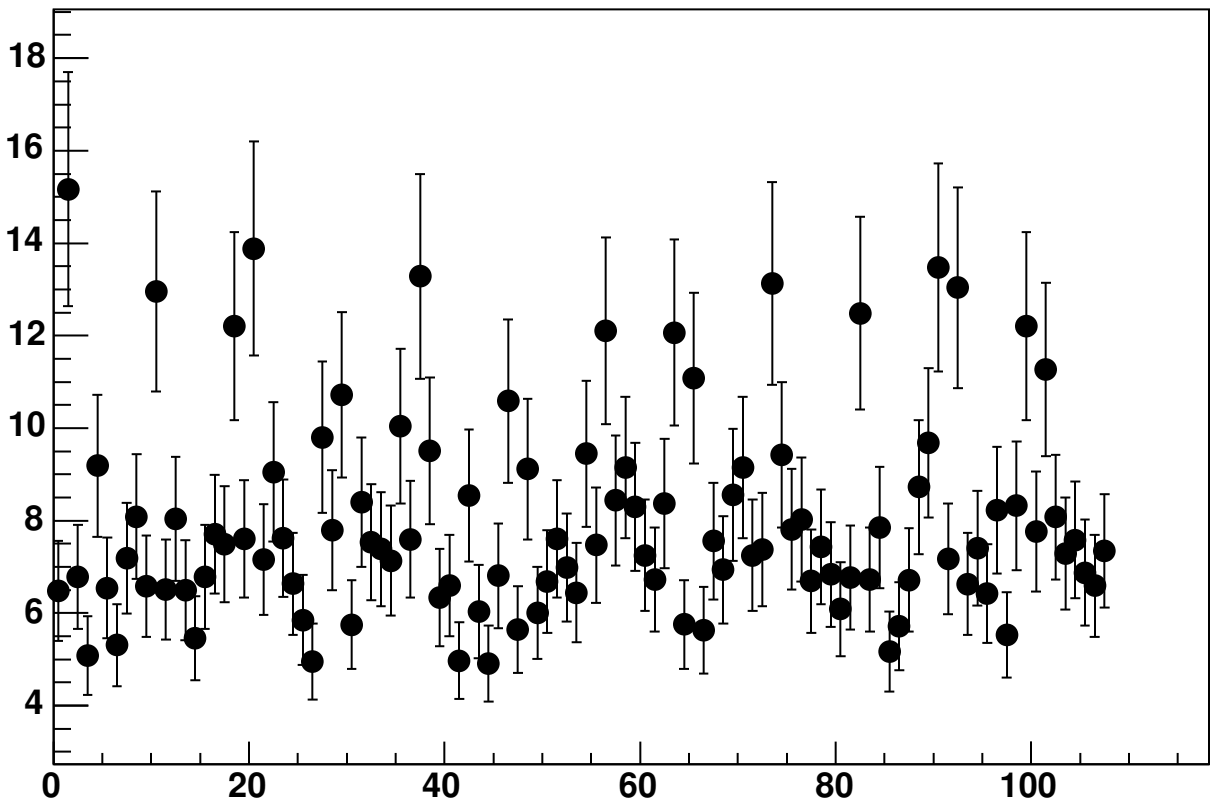
Enable 3, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



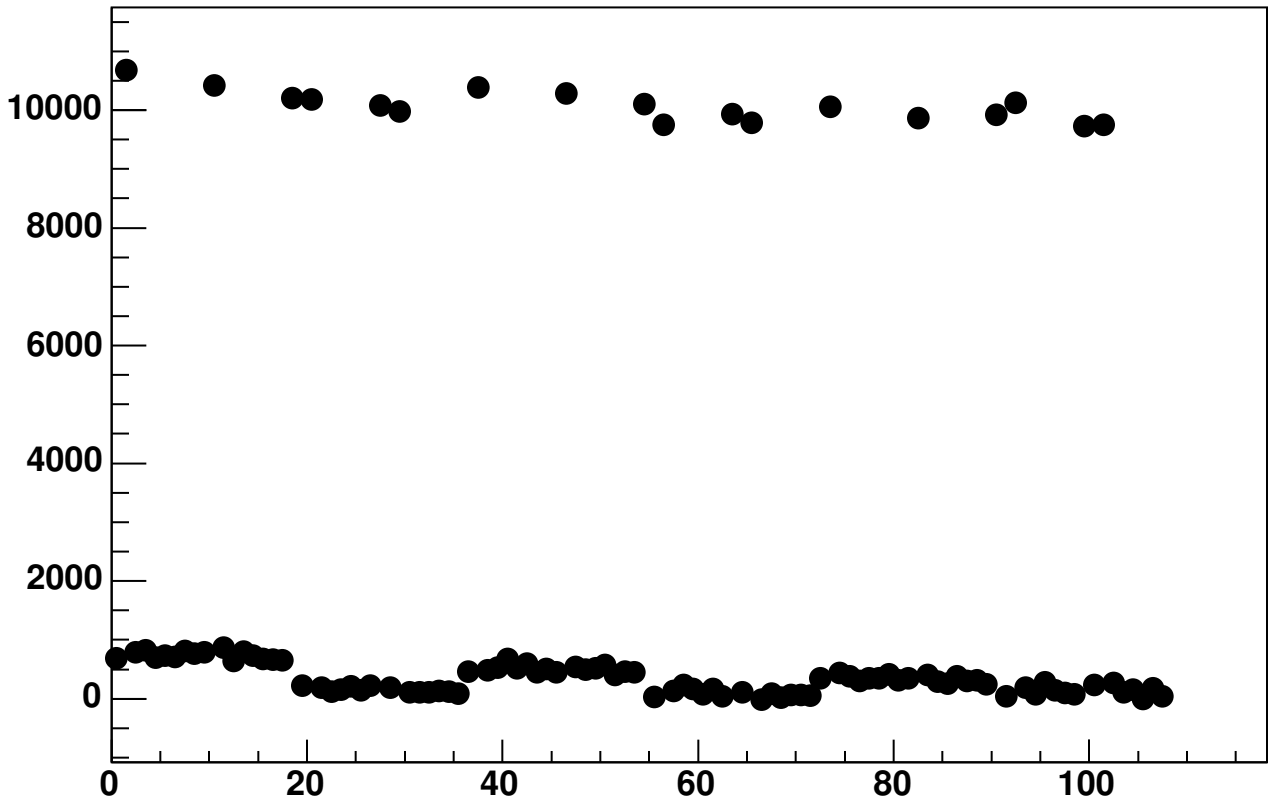
Enable 3, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



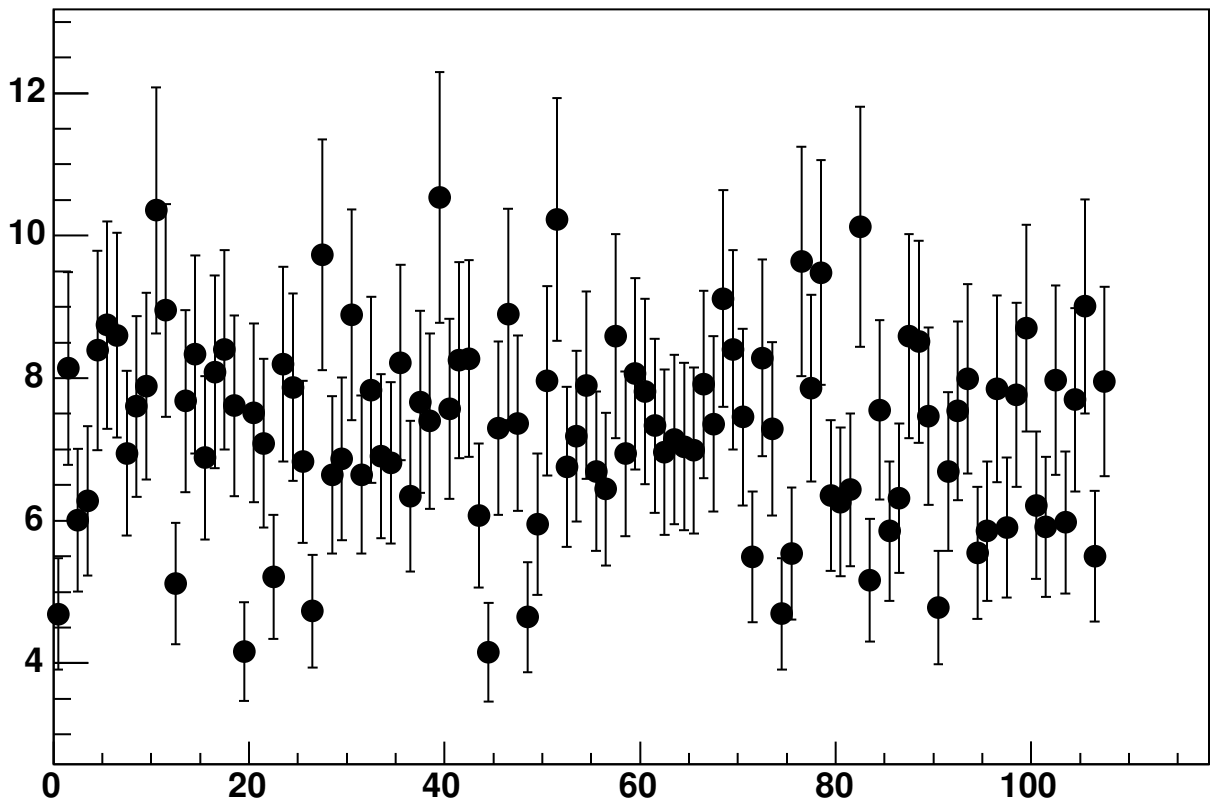
Enable 3, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



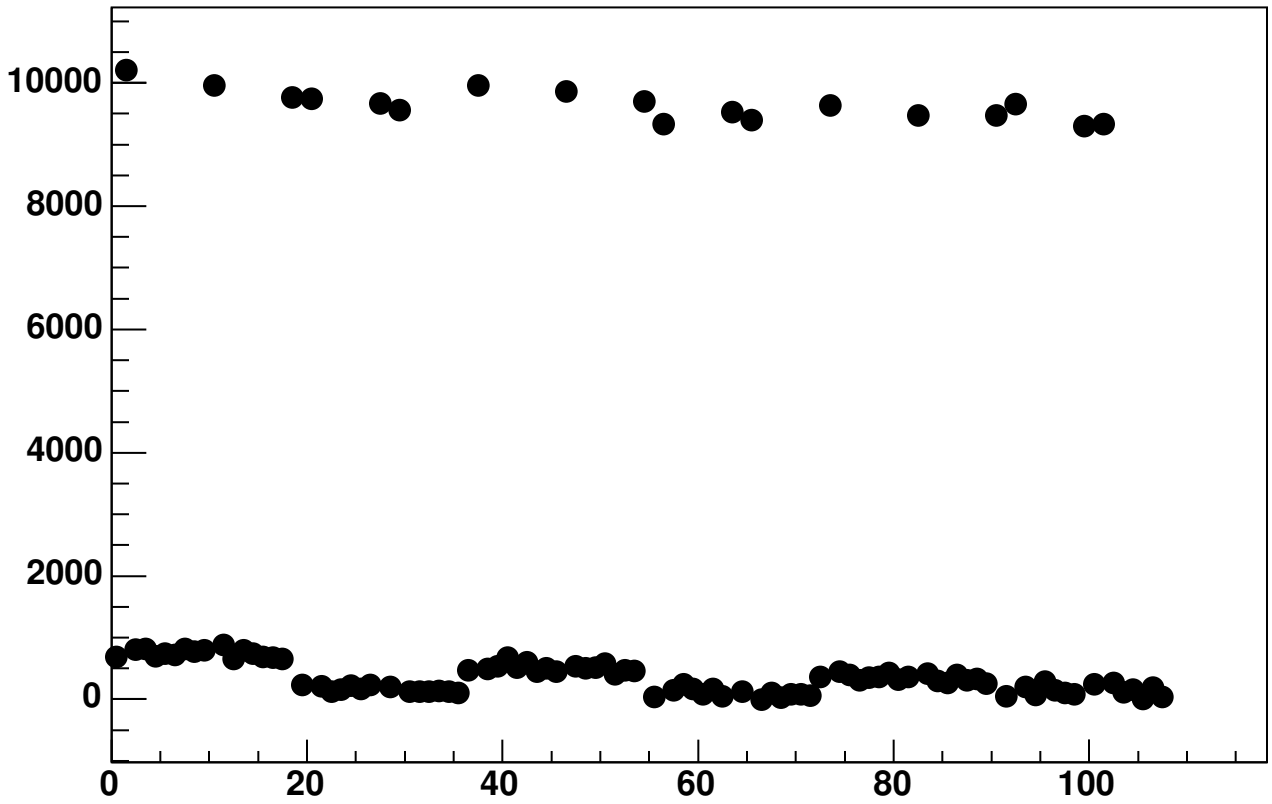
Enable 3, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



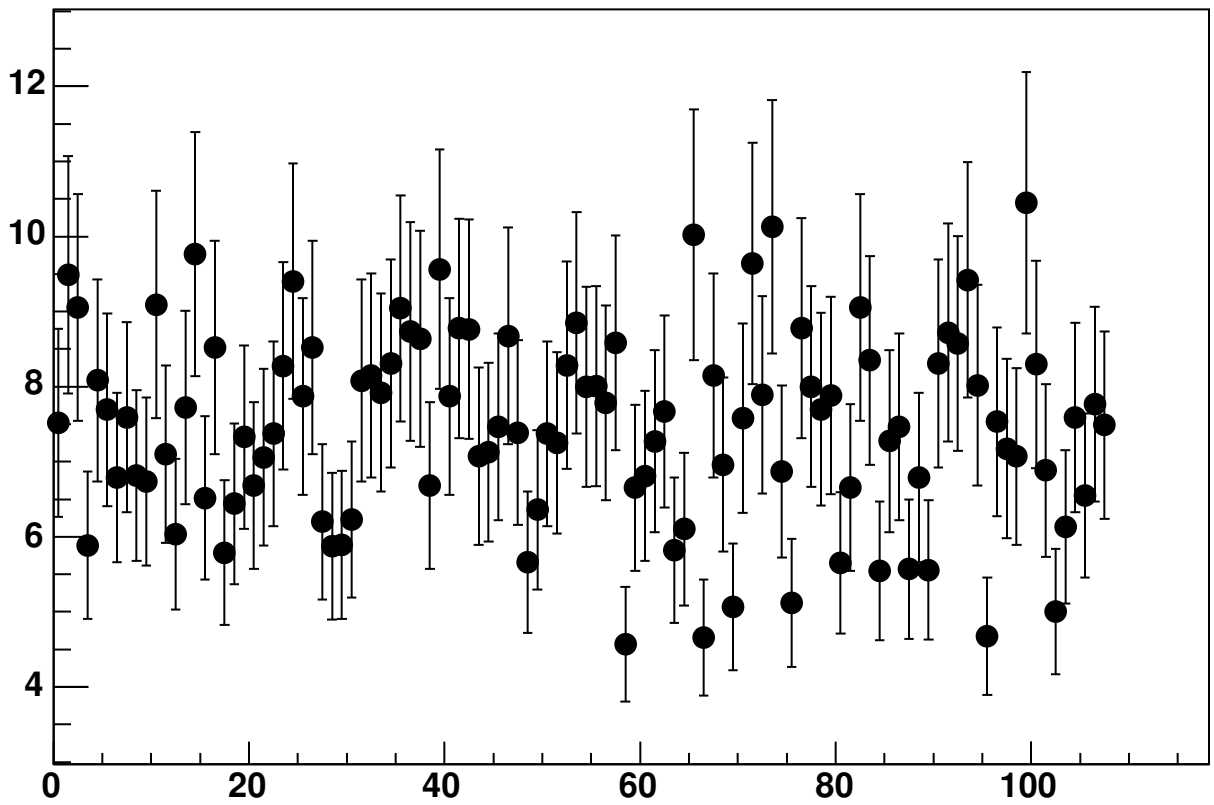
Enable 3, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



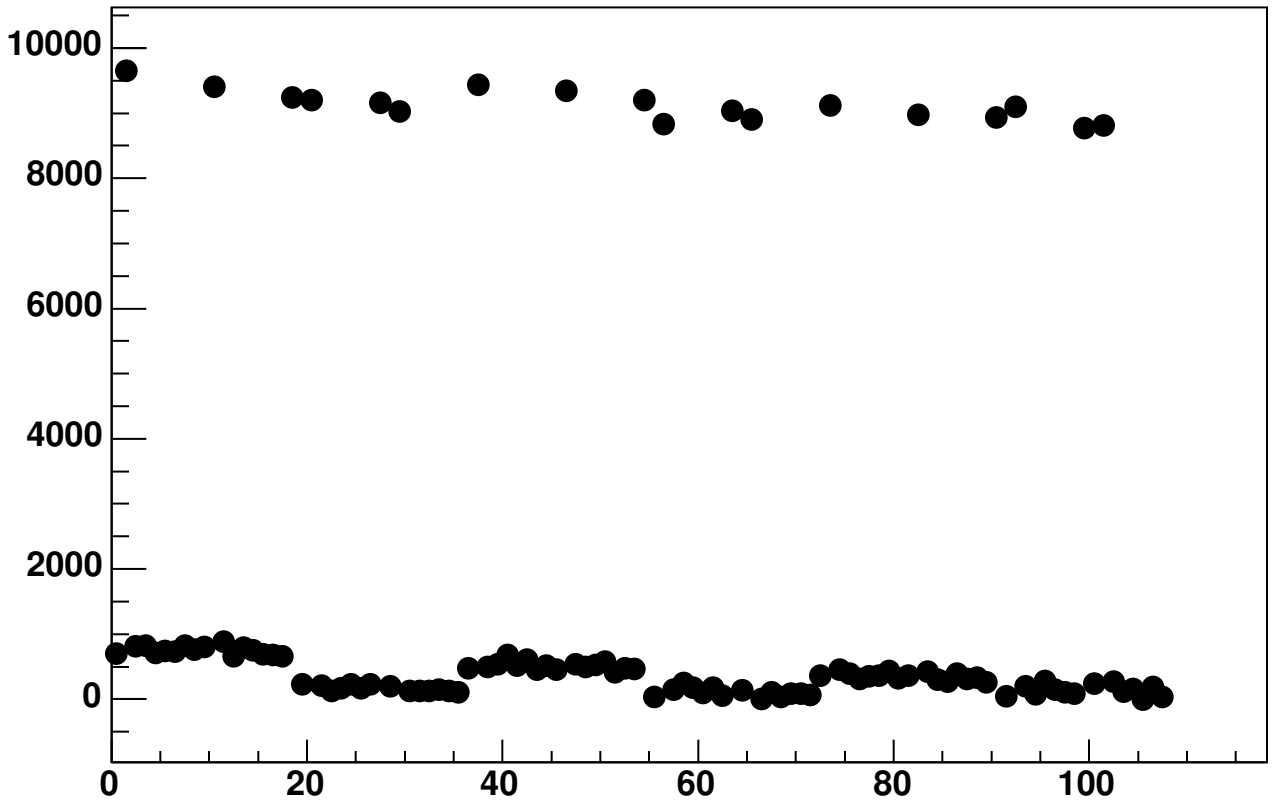
Enable 3, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



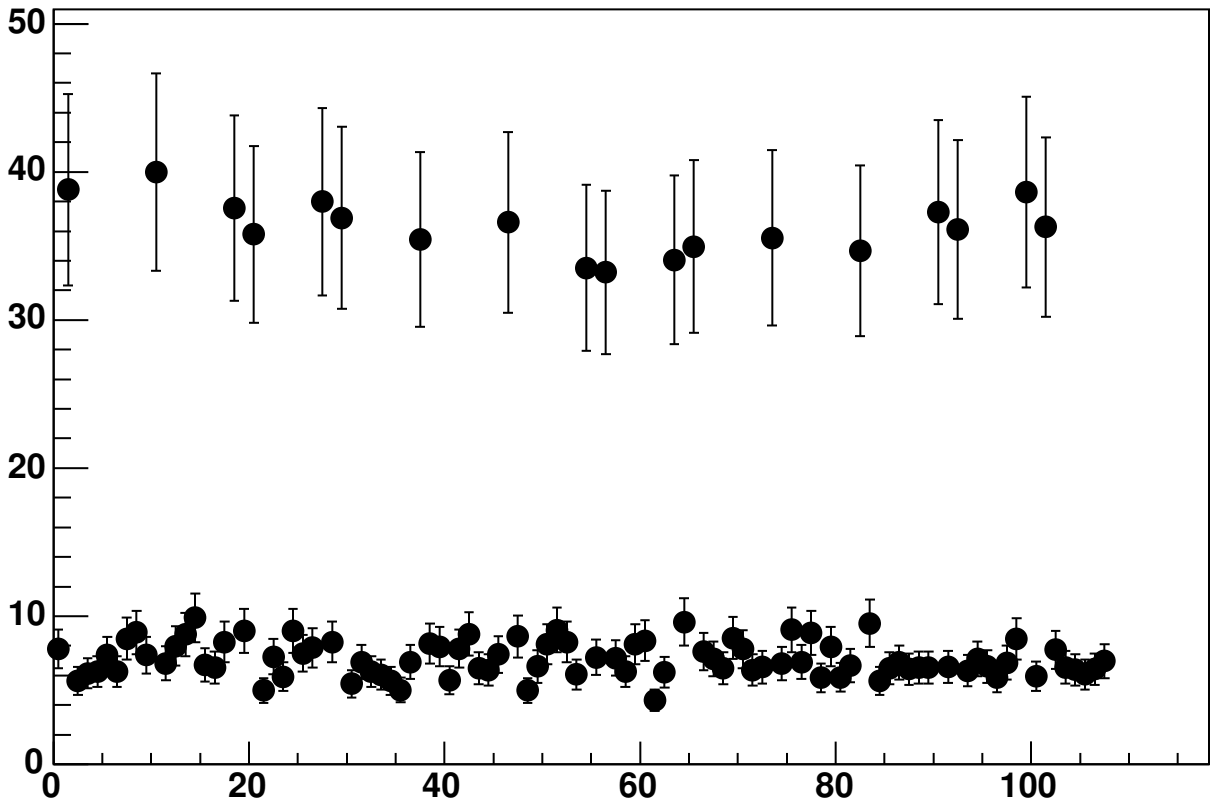
Enable 3, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



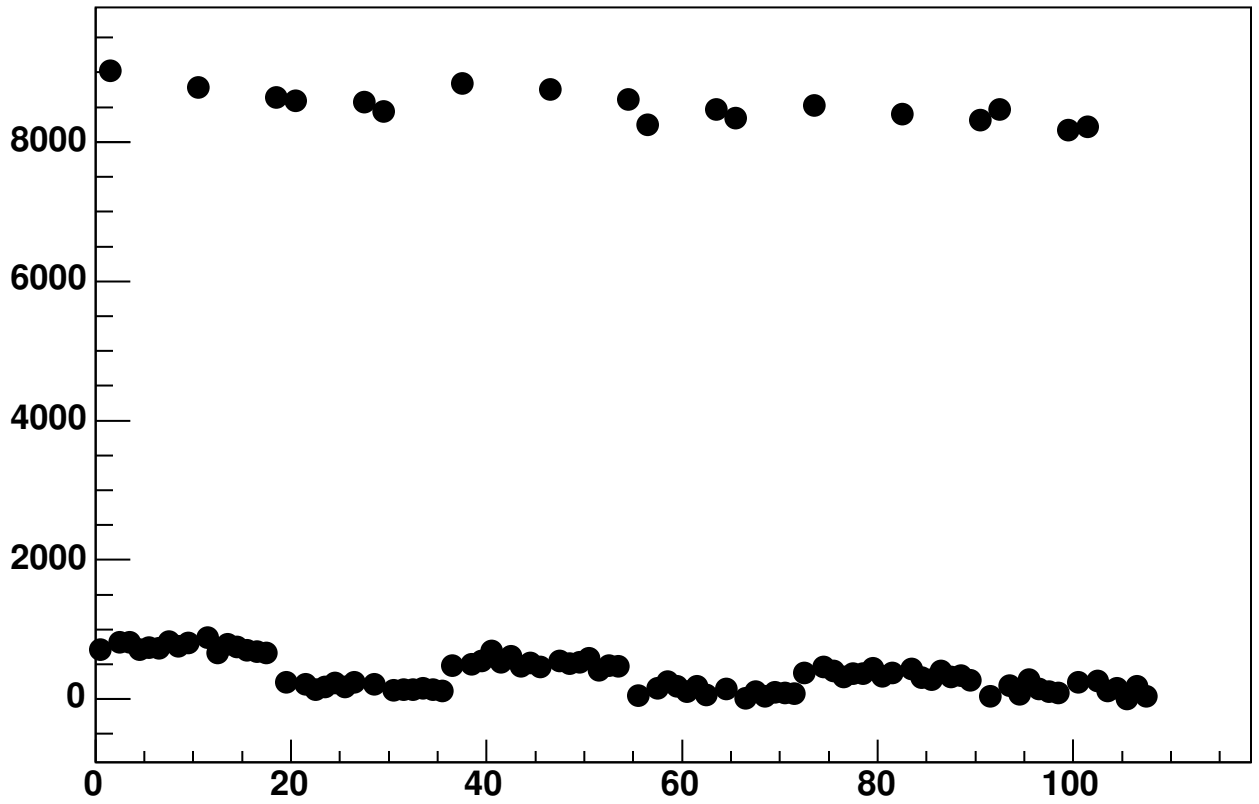
Enable 3, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



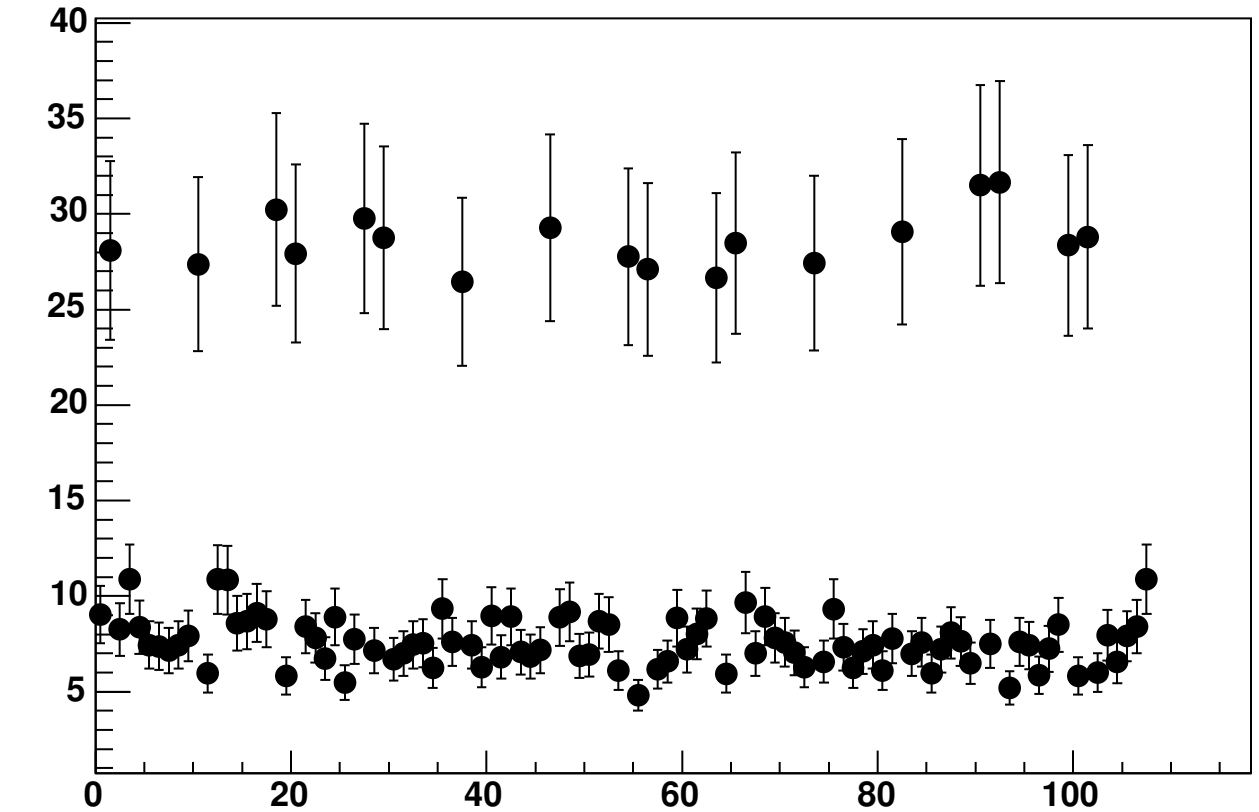
Enable 3, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



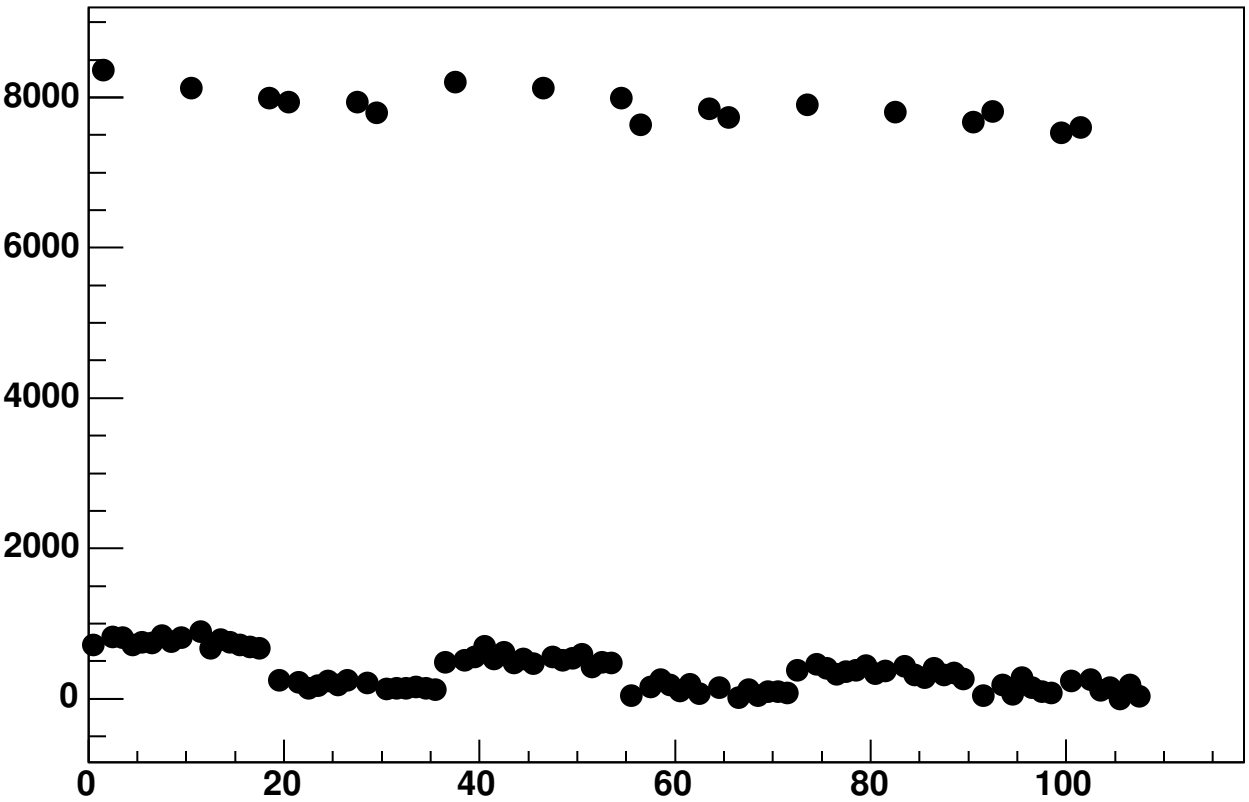
Enable 3, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



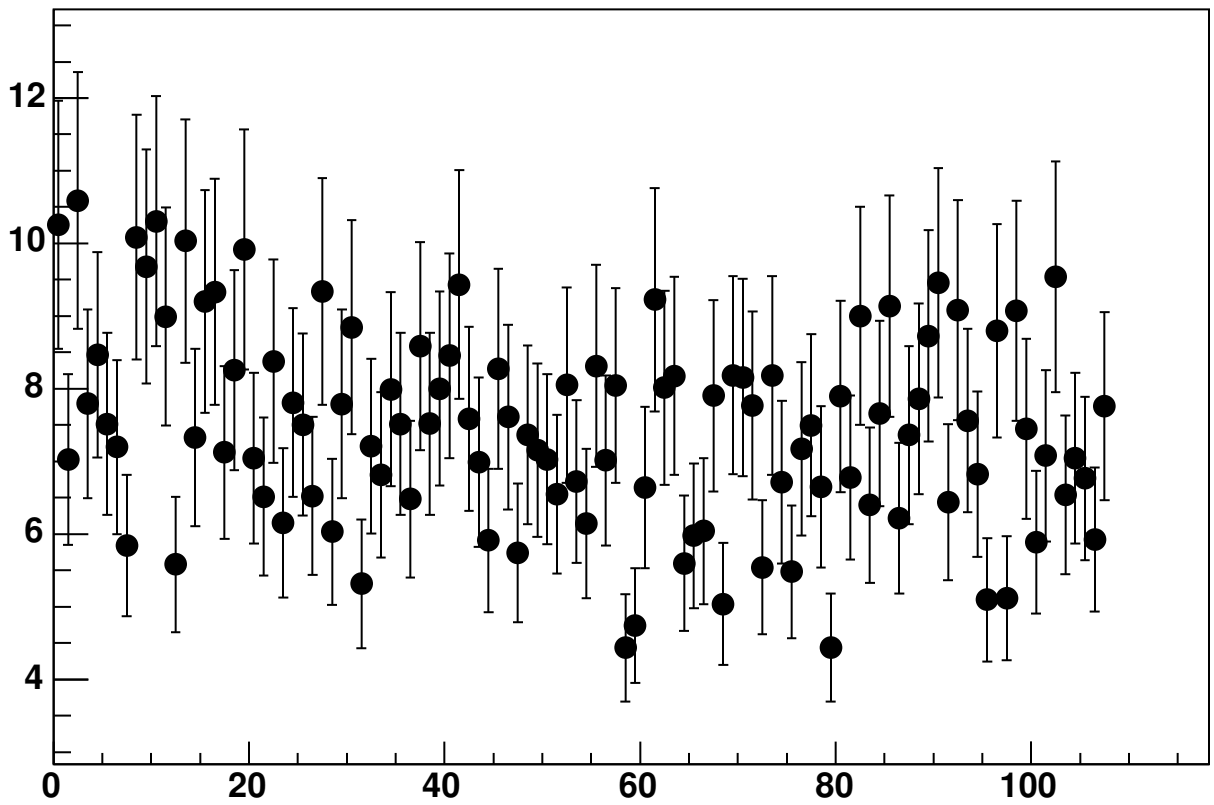
Enable 3, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



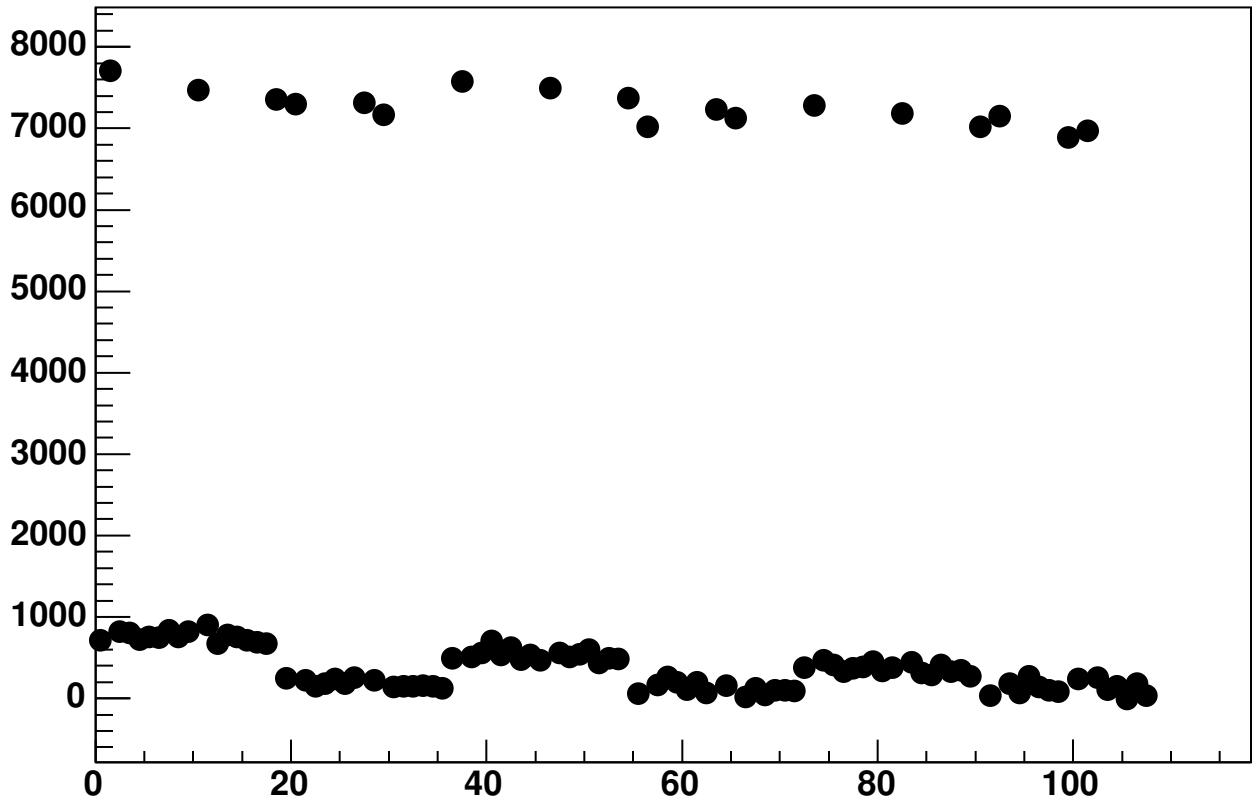
Enable 3, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



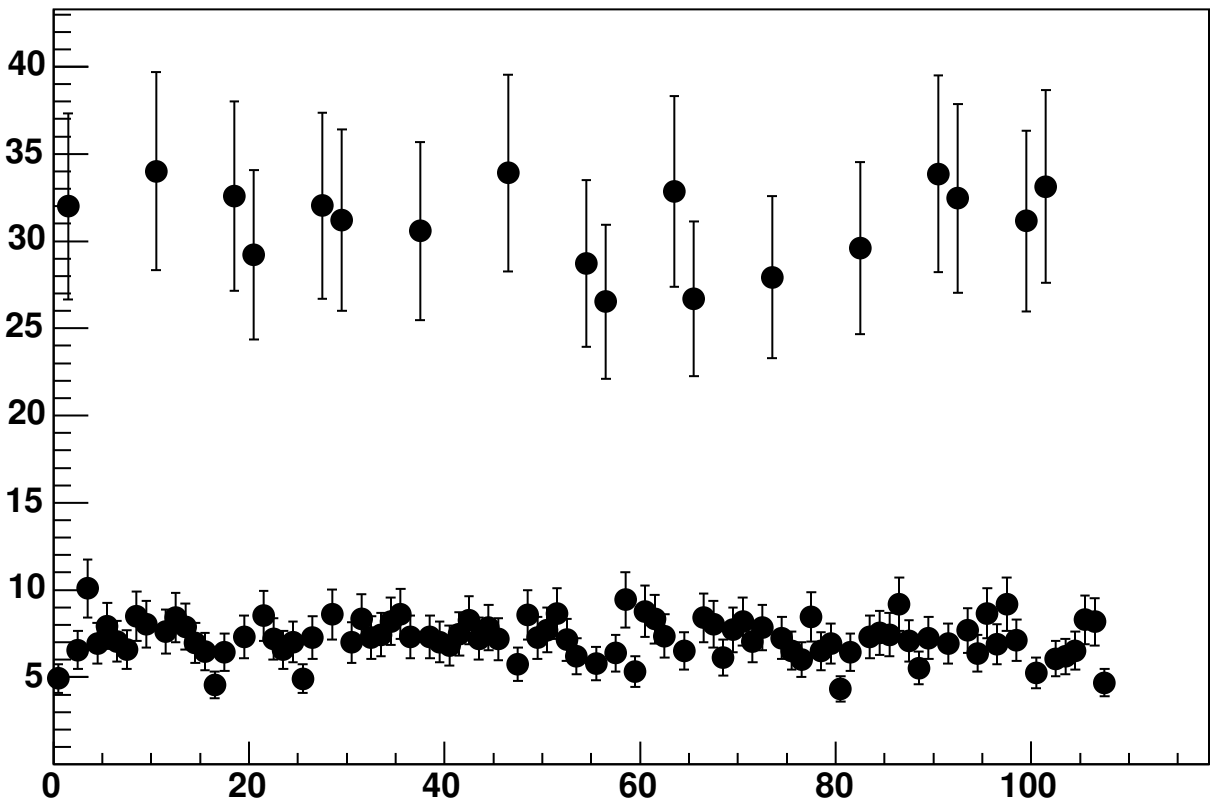
Enable 3, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



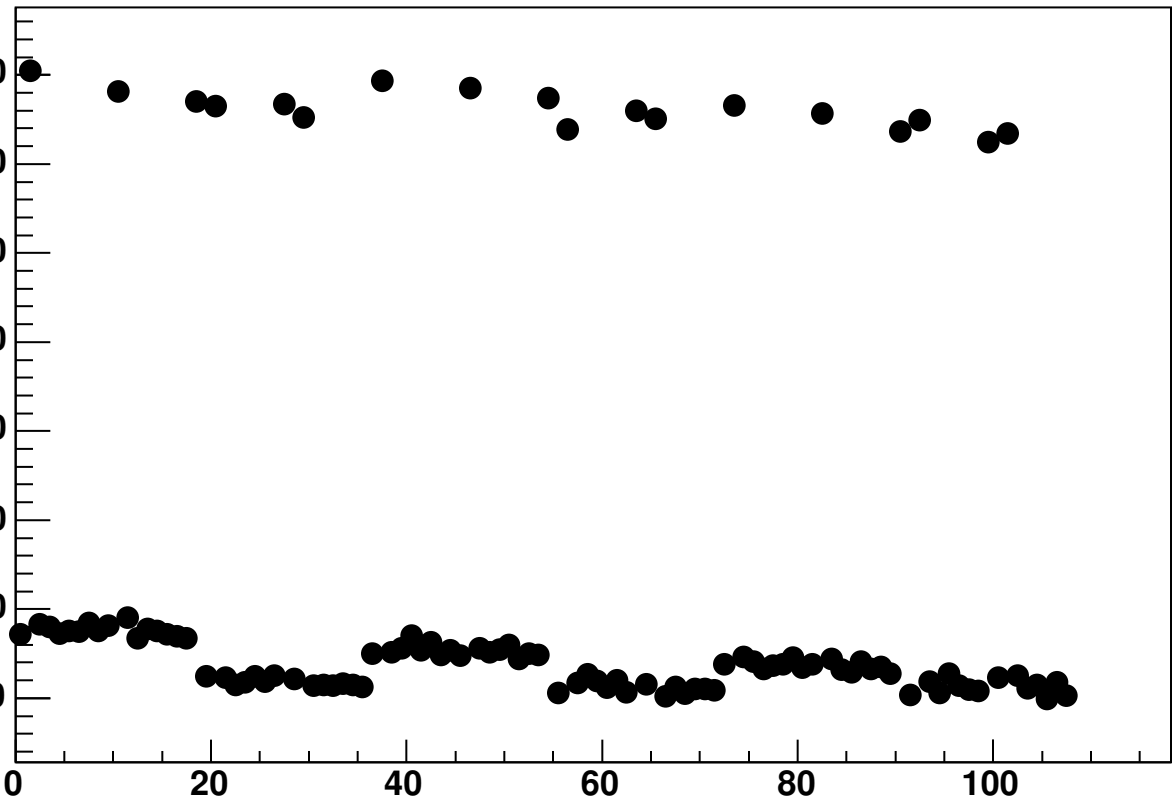
Enable 3, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



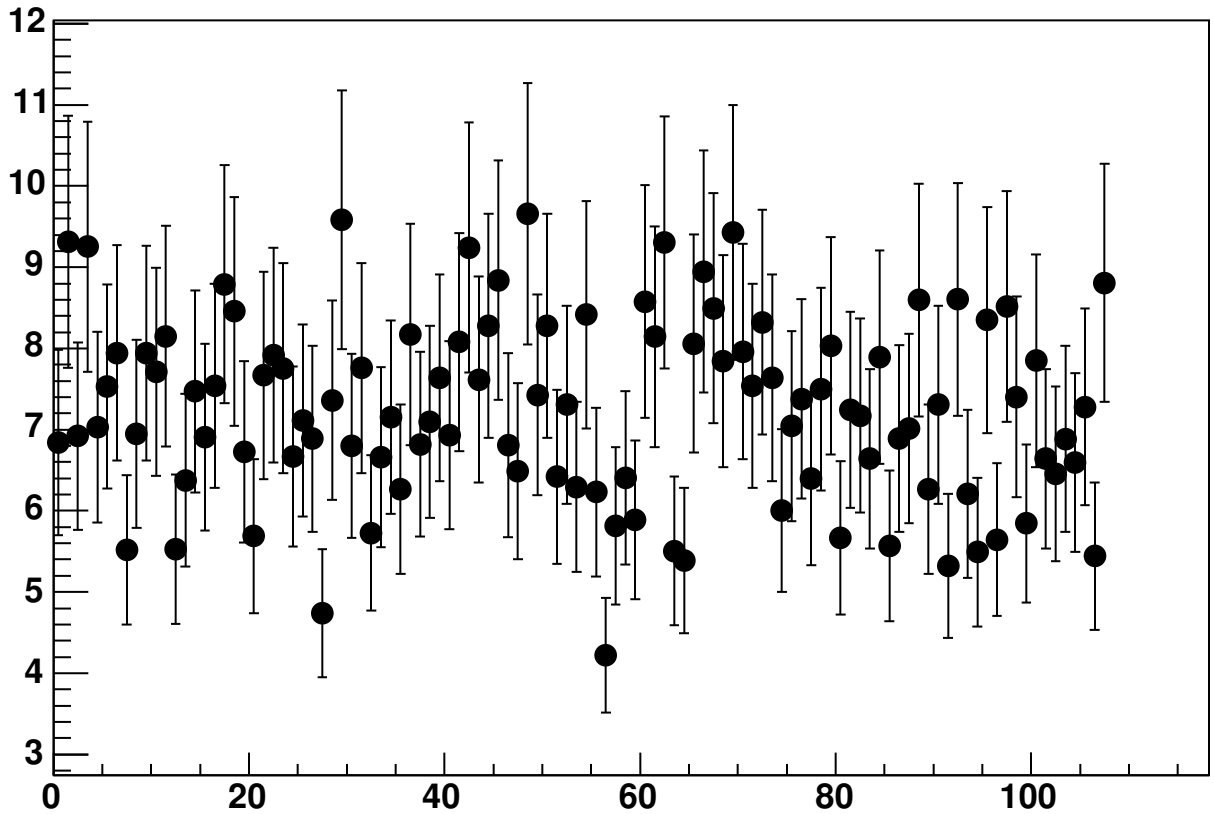
Enable 3, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



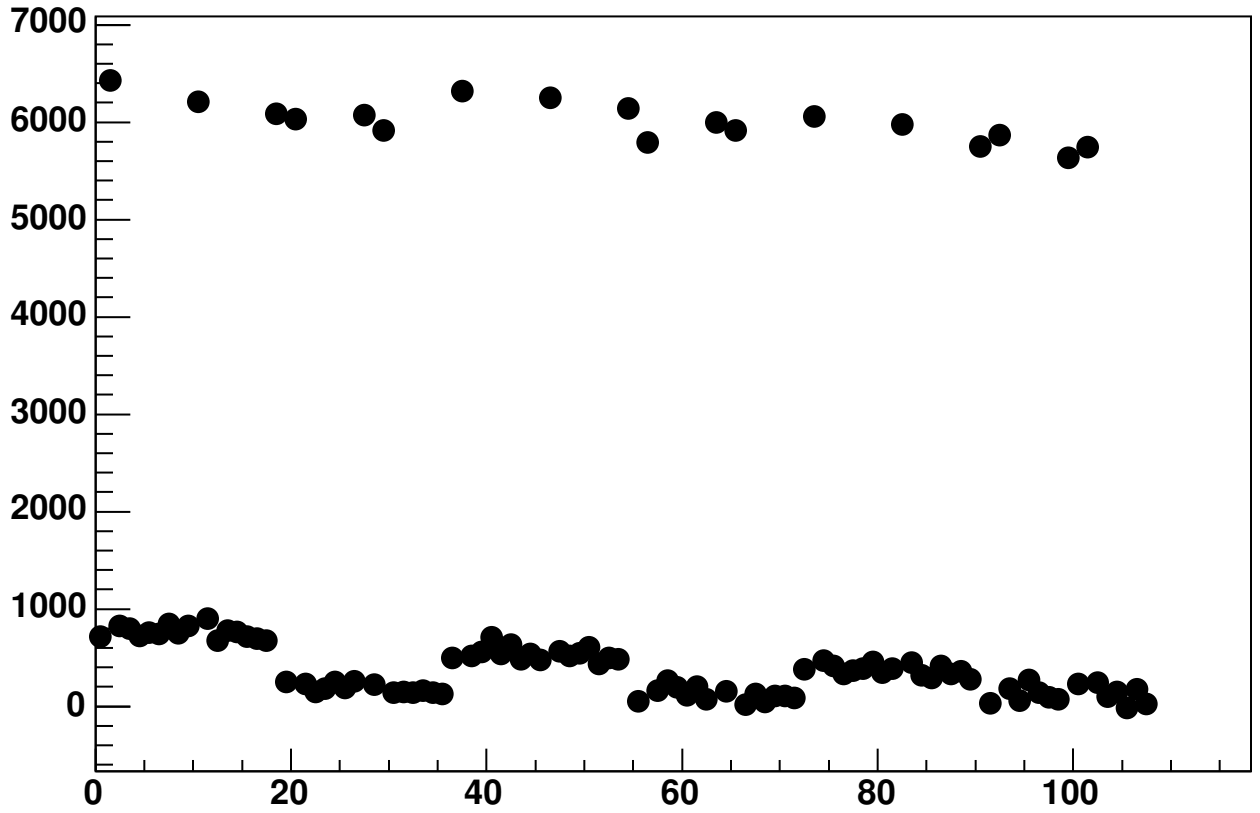
Enable 3, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



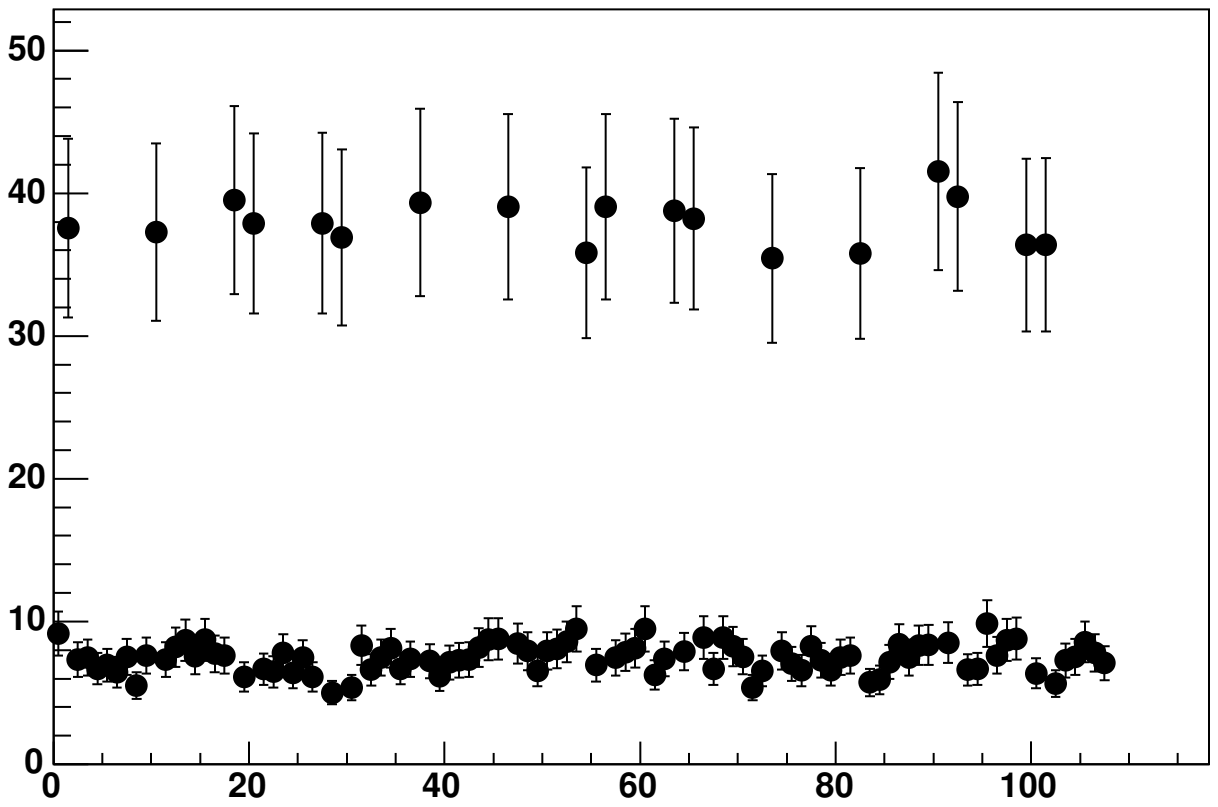
Enable 3, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



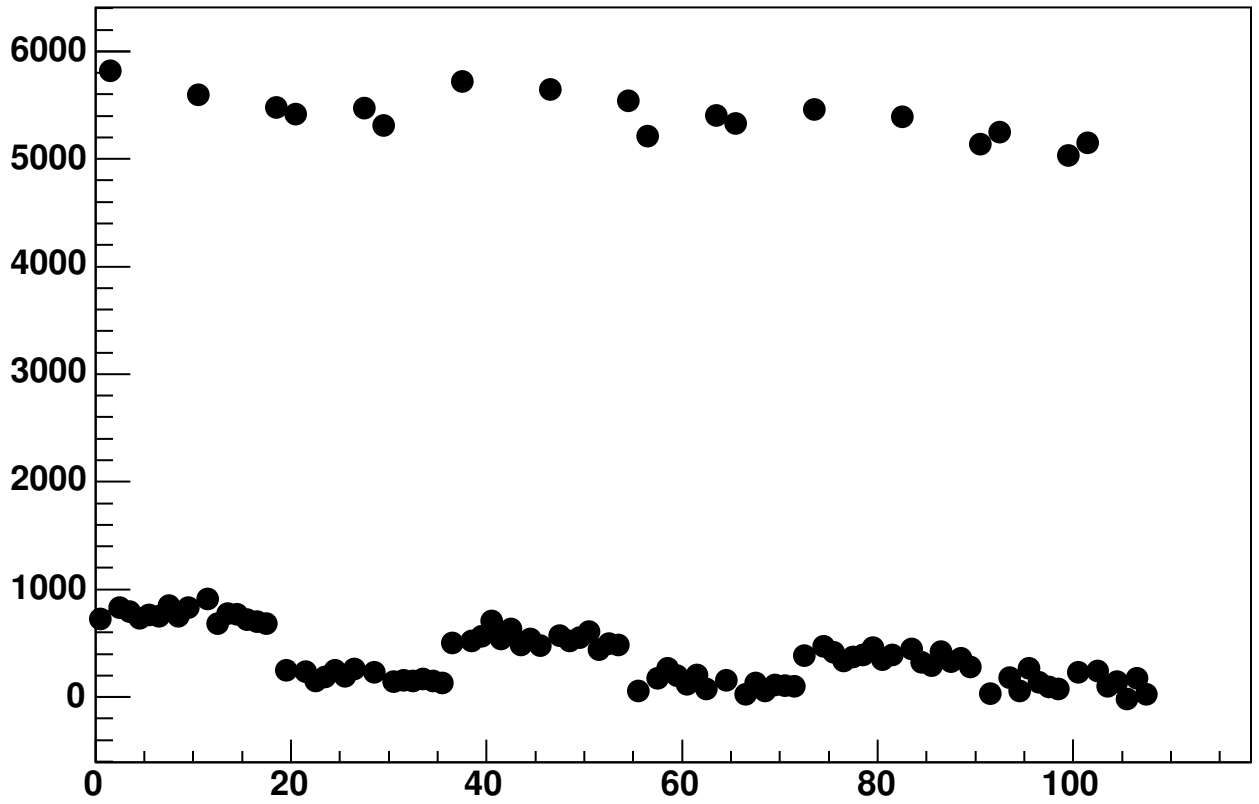
Enable 3, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



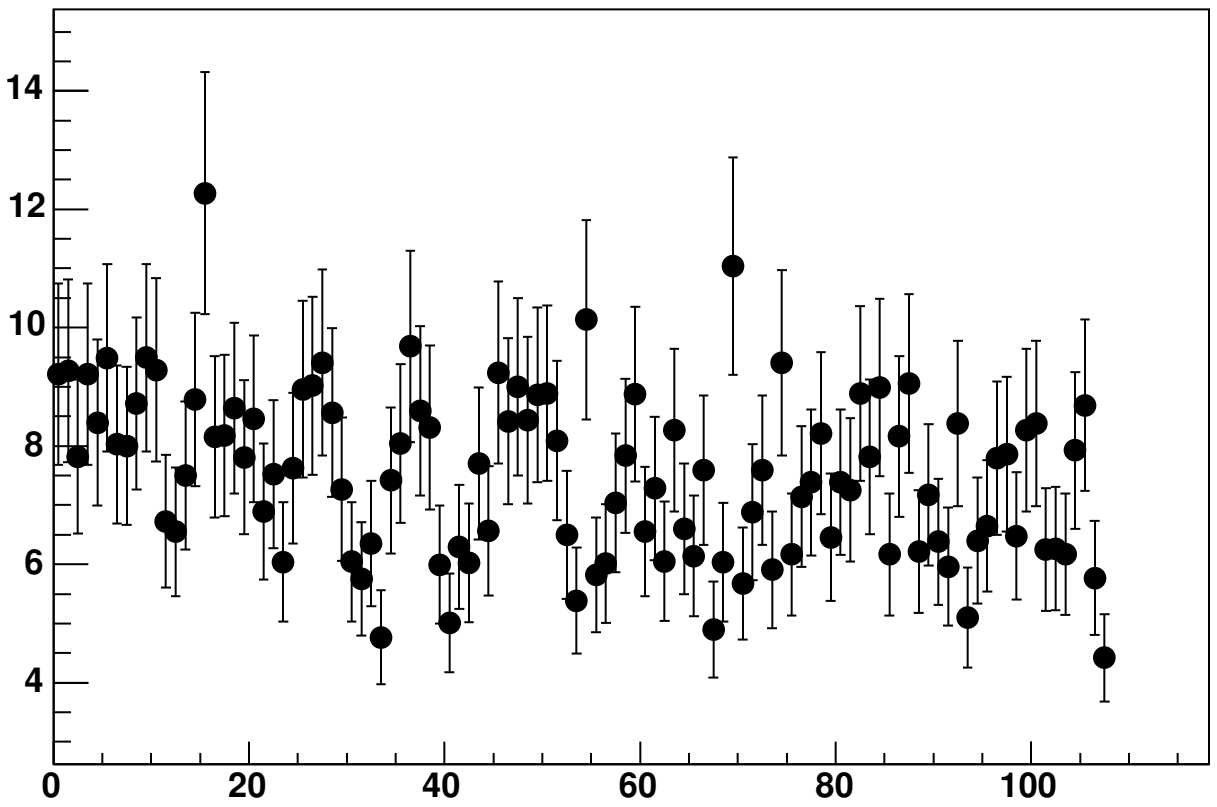
Enable 3, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



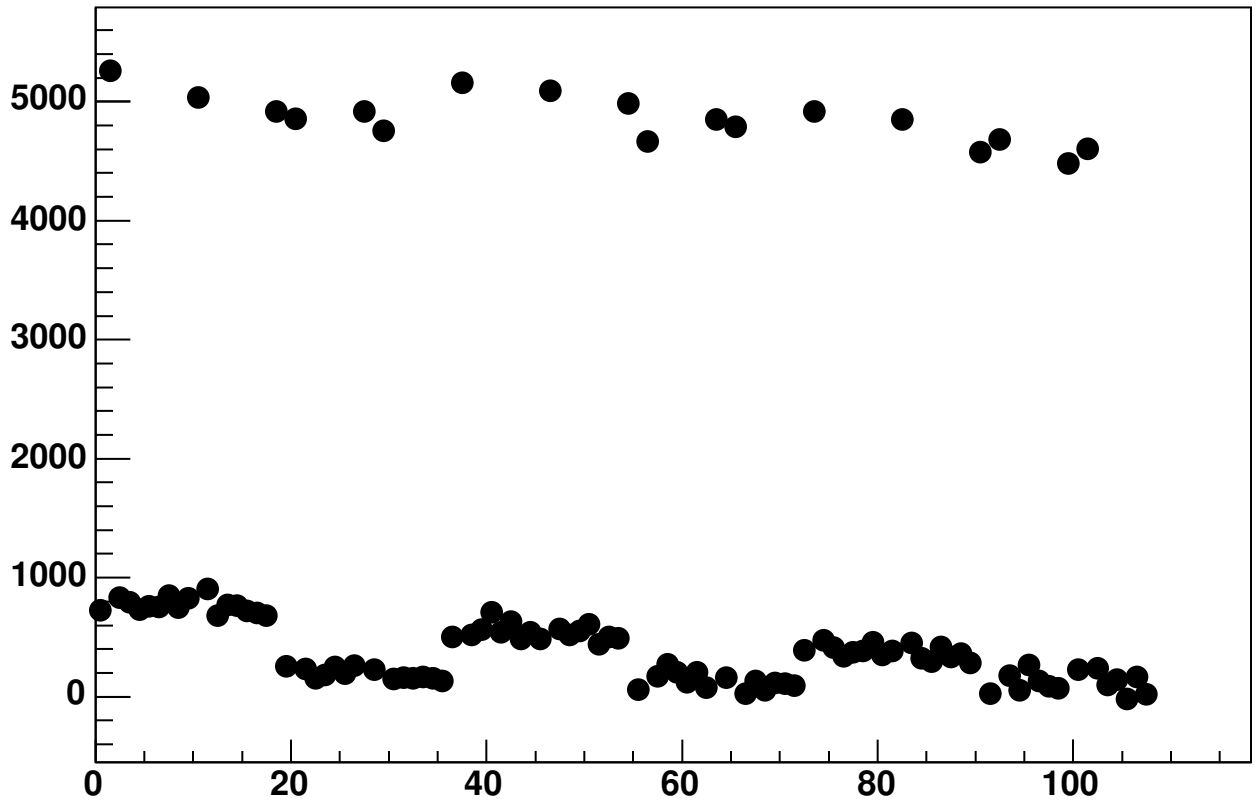
Enable 3, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



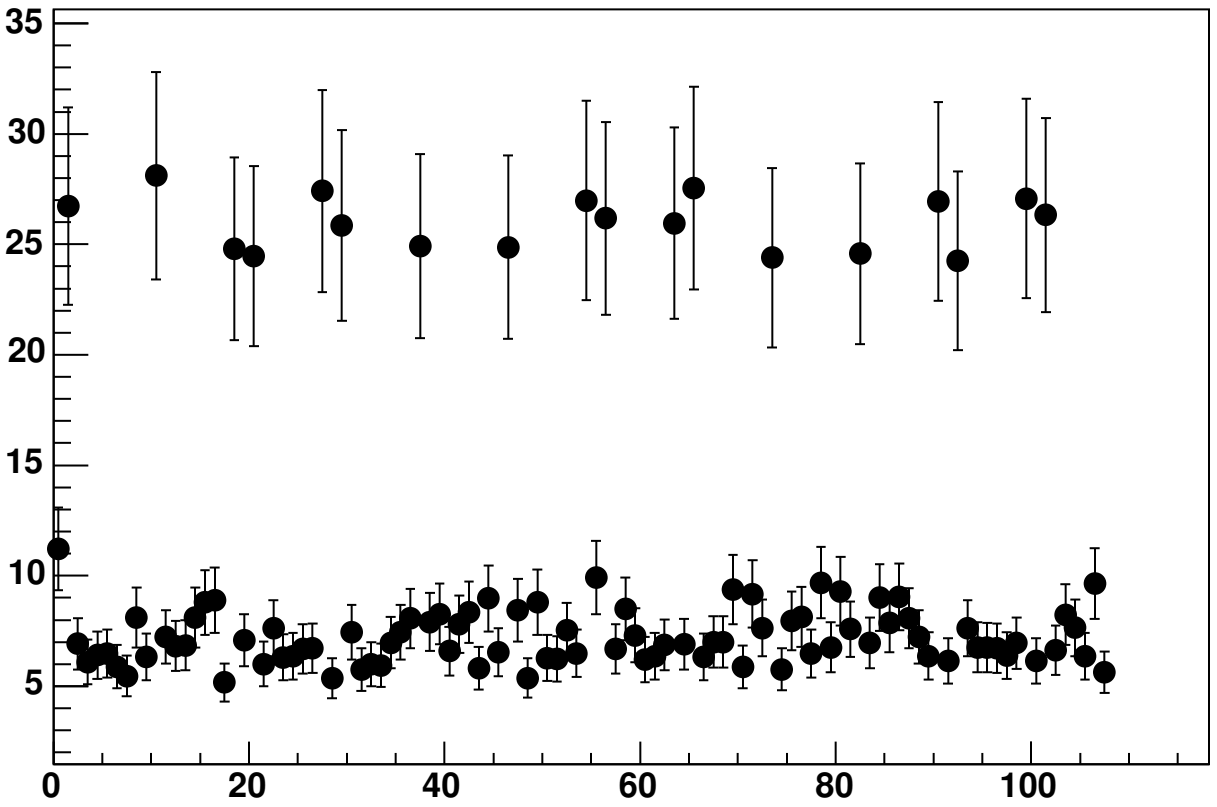
Enable 3, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



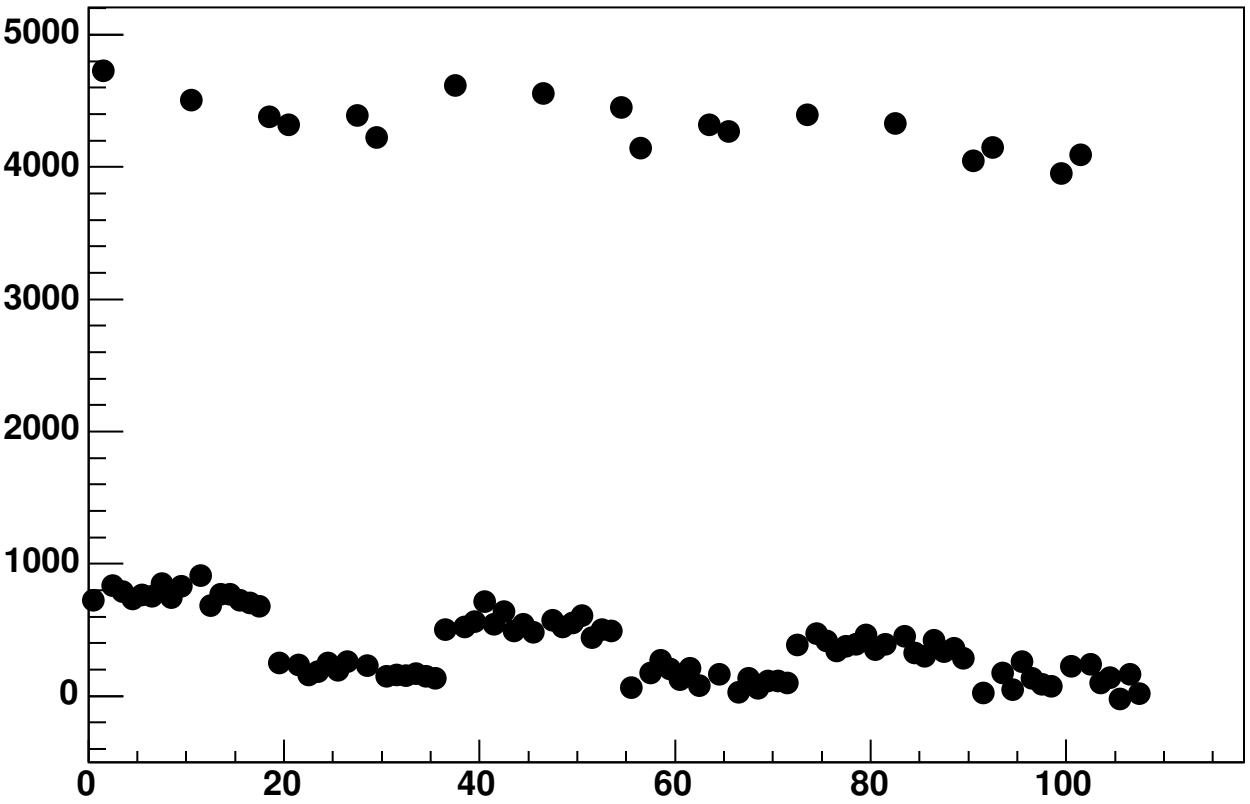
Enable 3, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



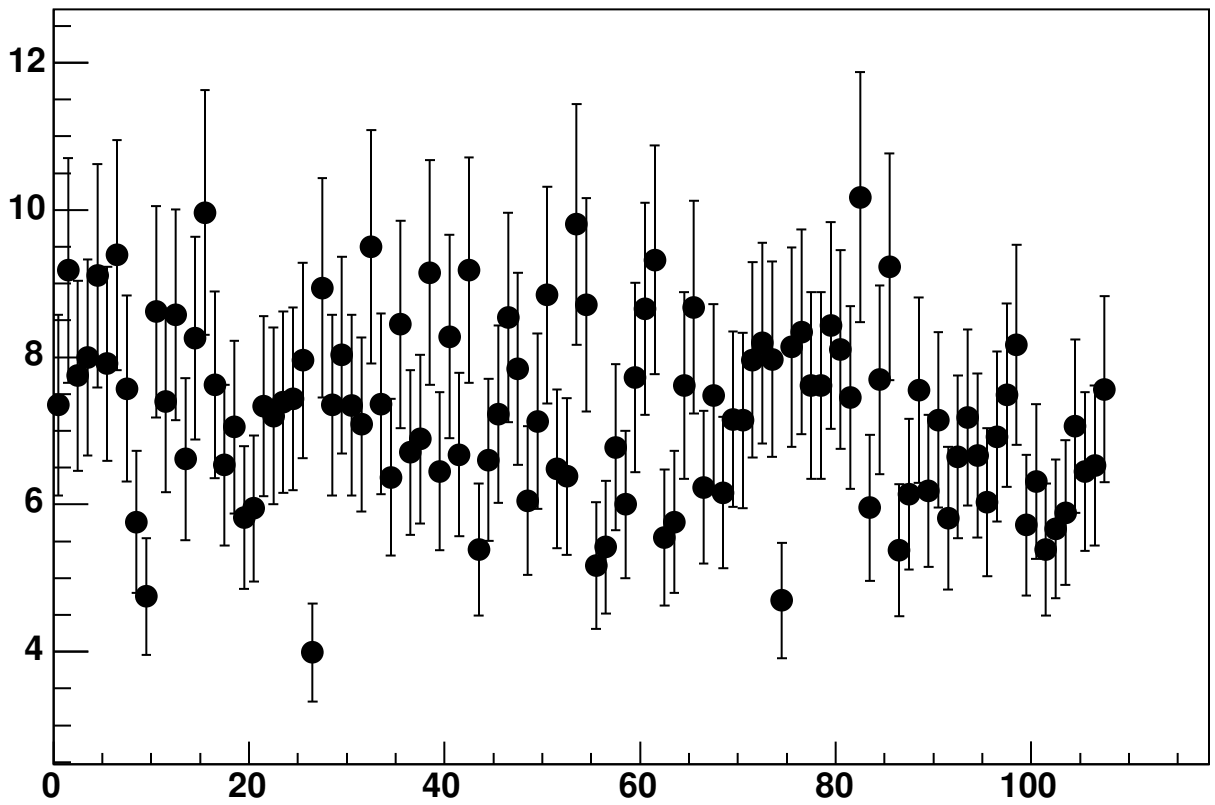
Enable 3, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



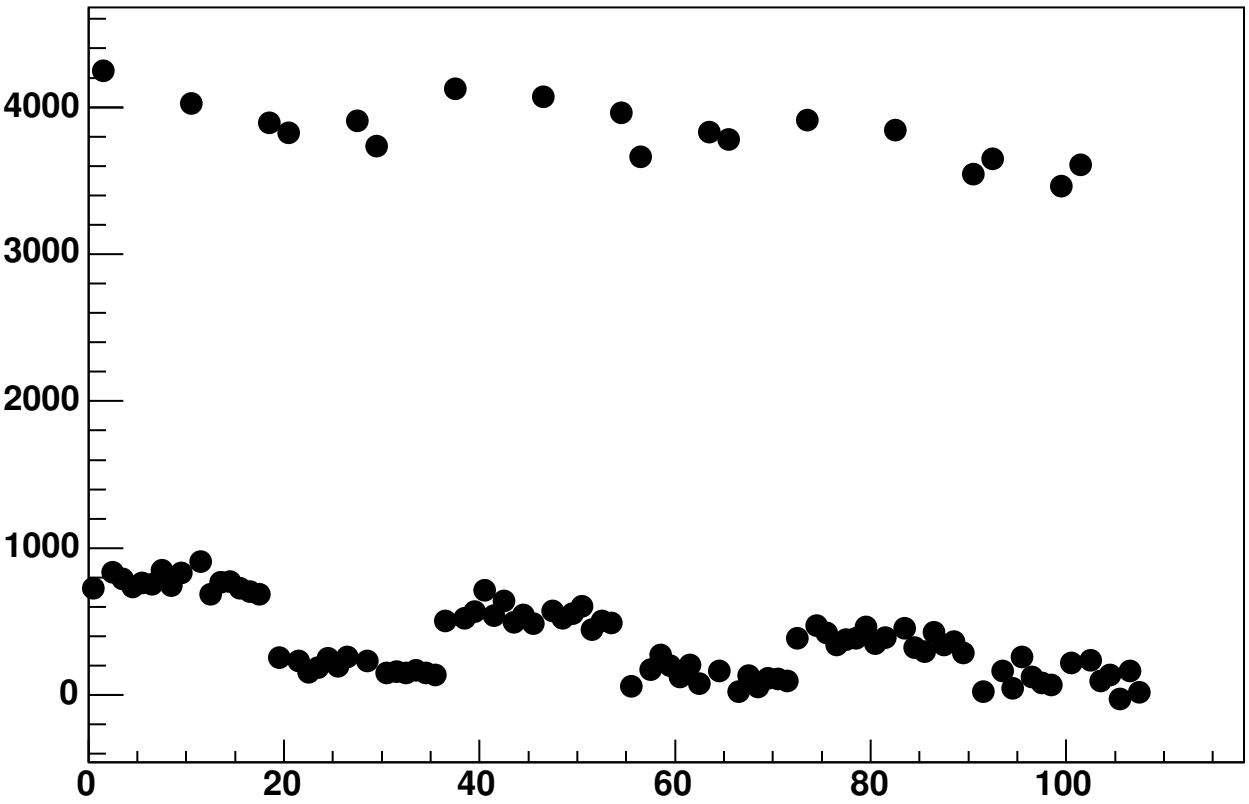
Enable 3, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



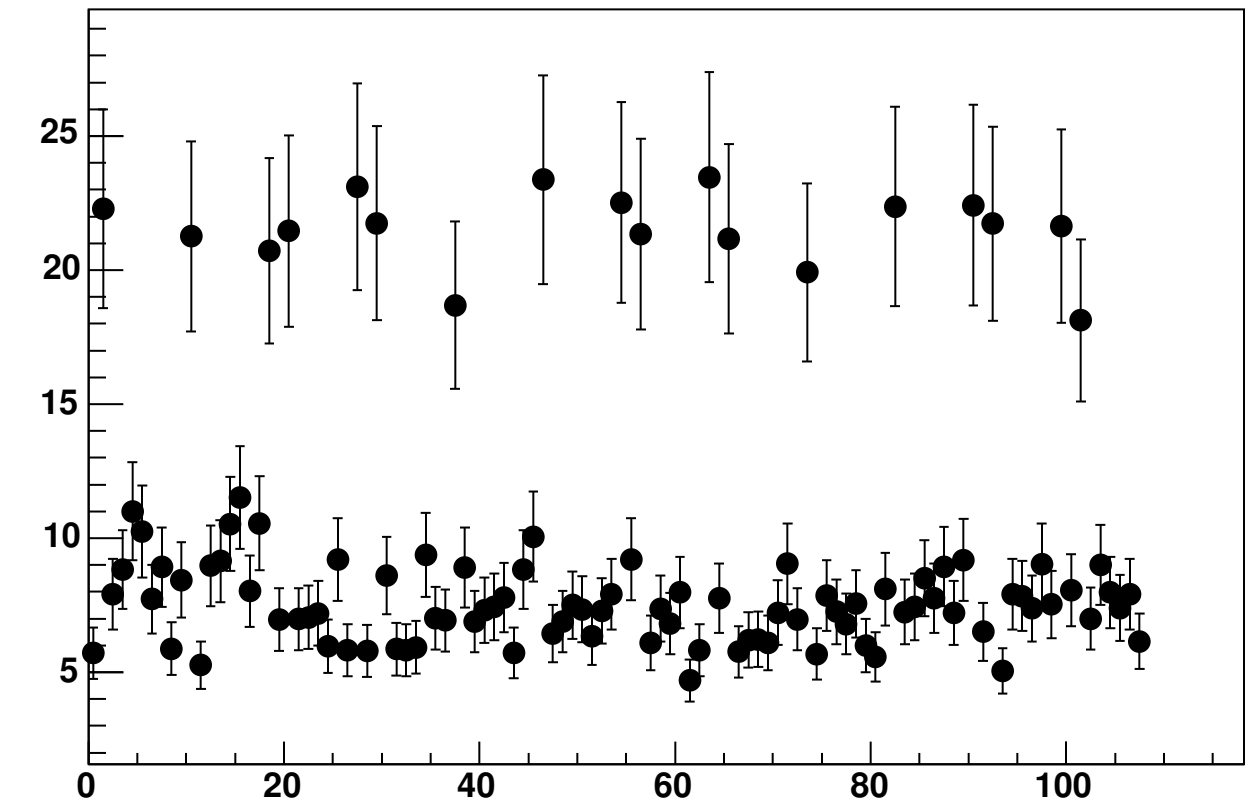
Enable 3, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



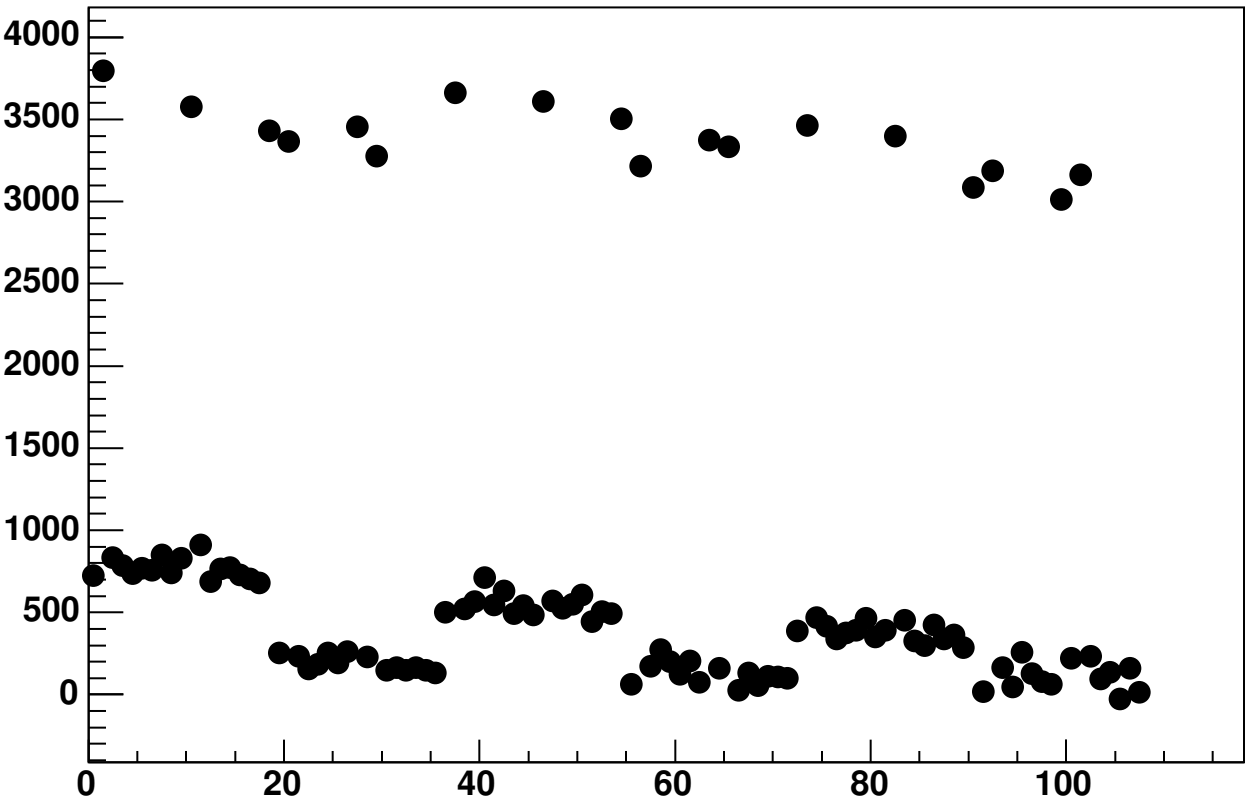
Enable 3, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



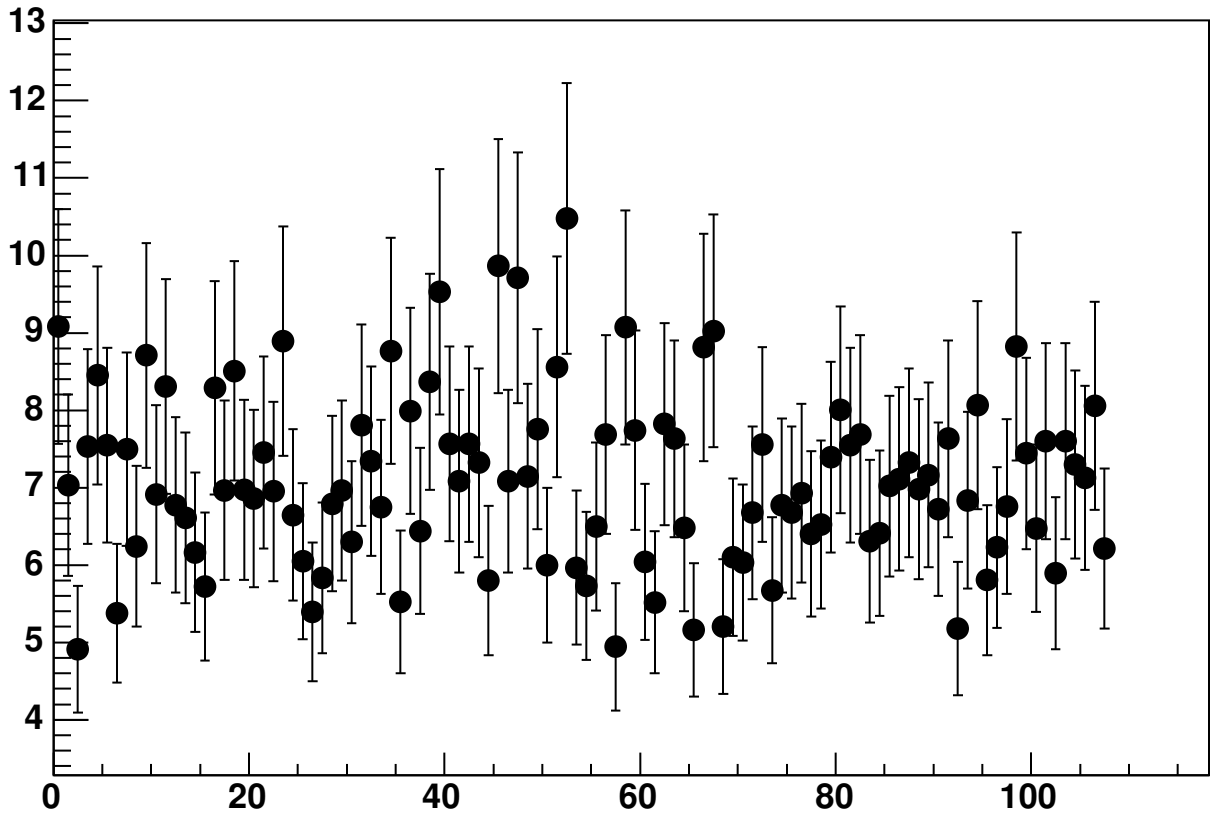
Enable 3, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



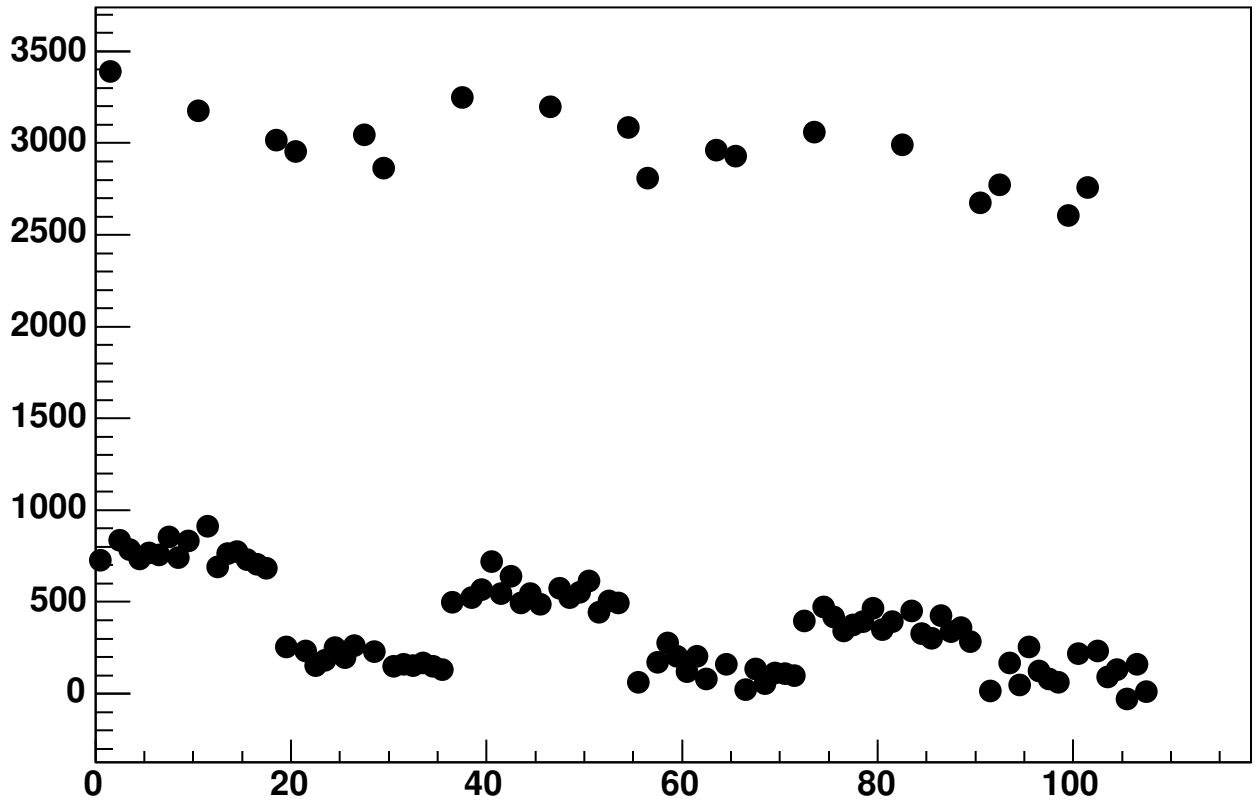
Enable 3, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



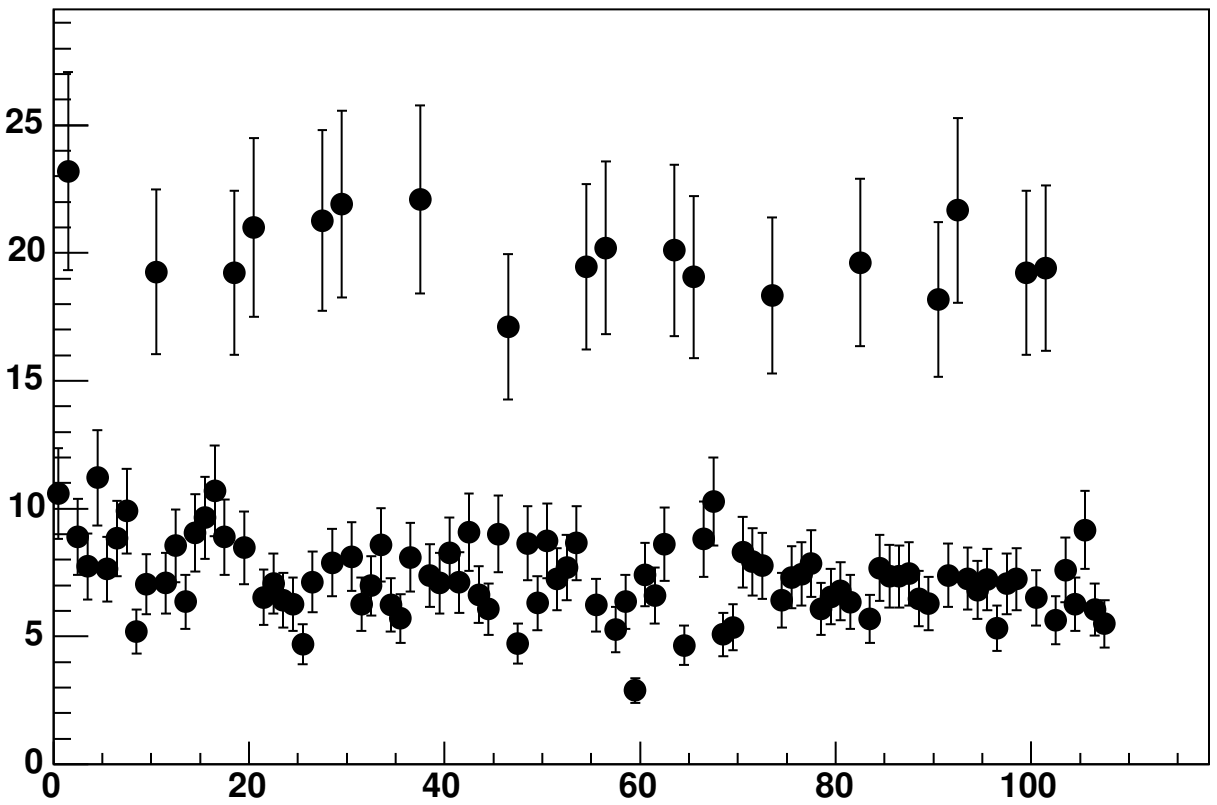
Enable 3, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



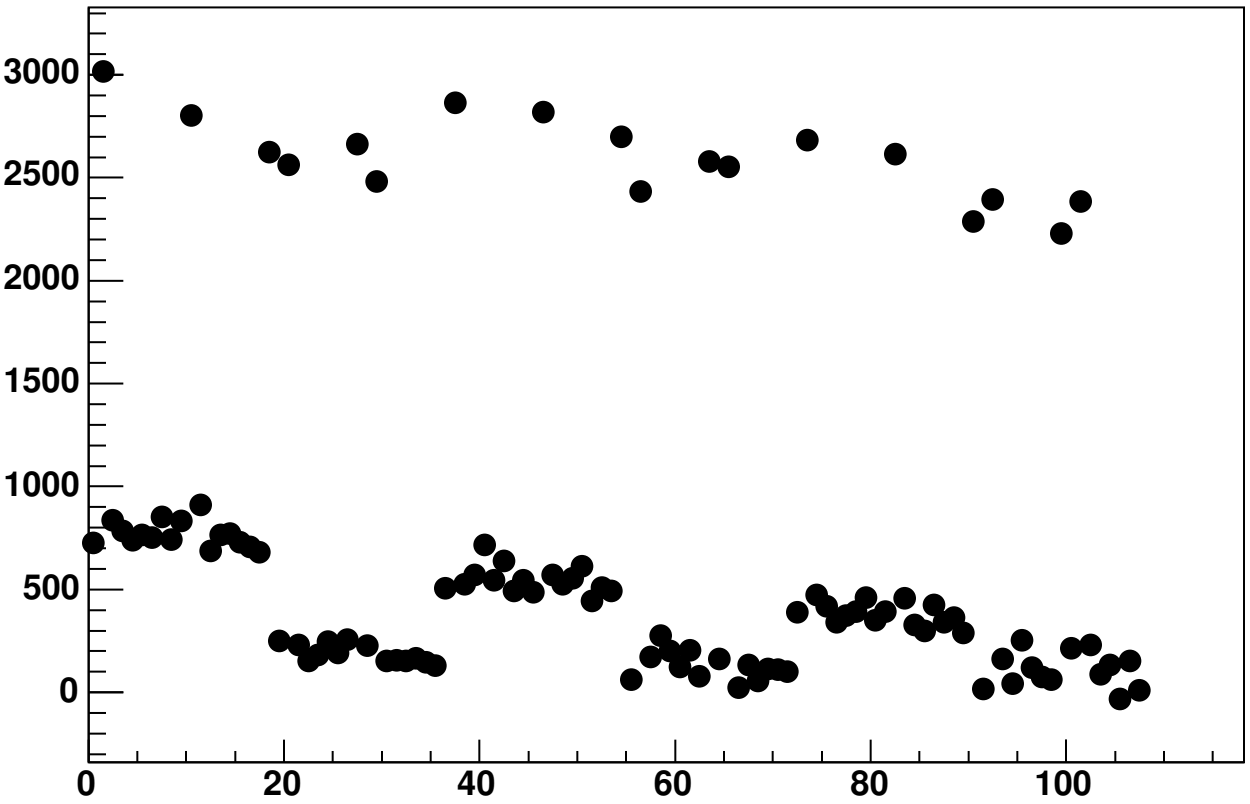
Enable 3, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



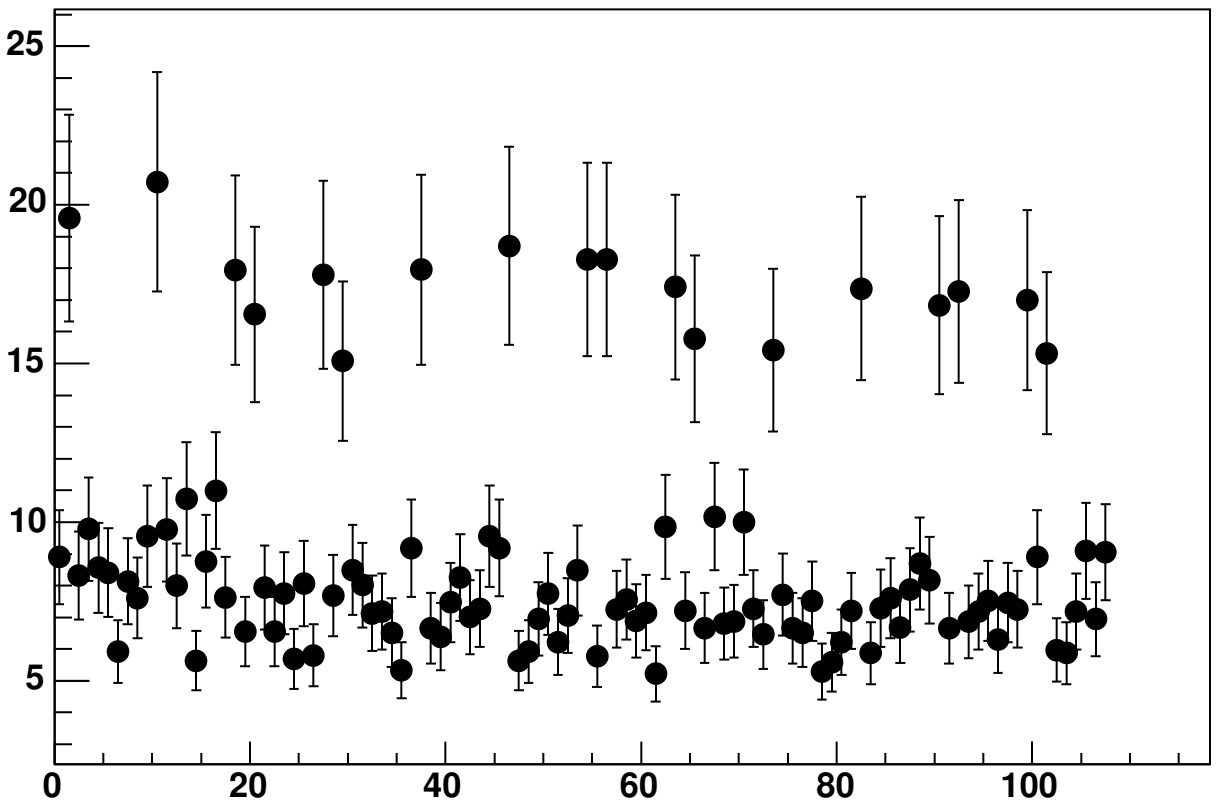
Enable 3, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



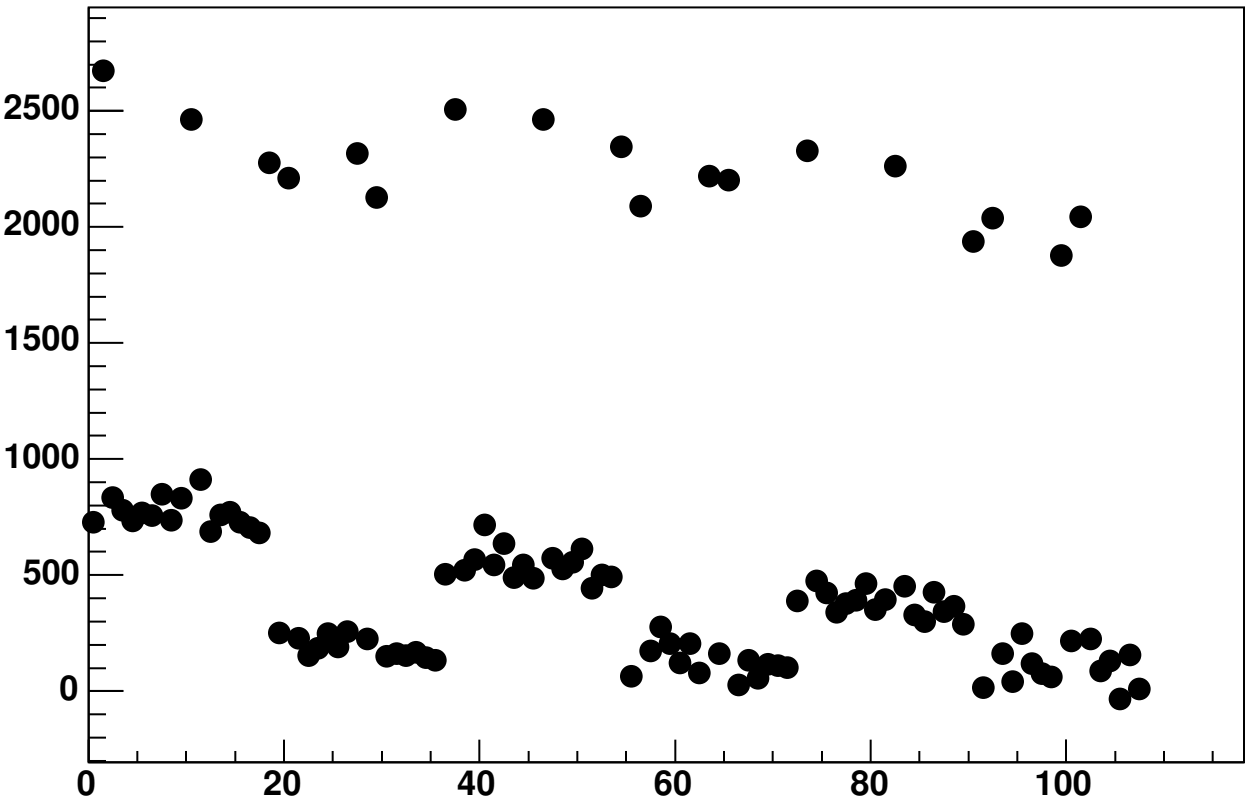
Enable 3, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



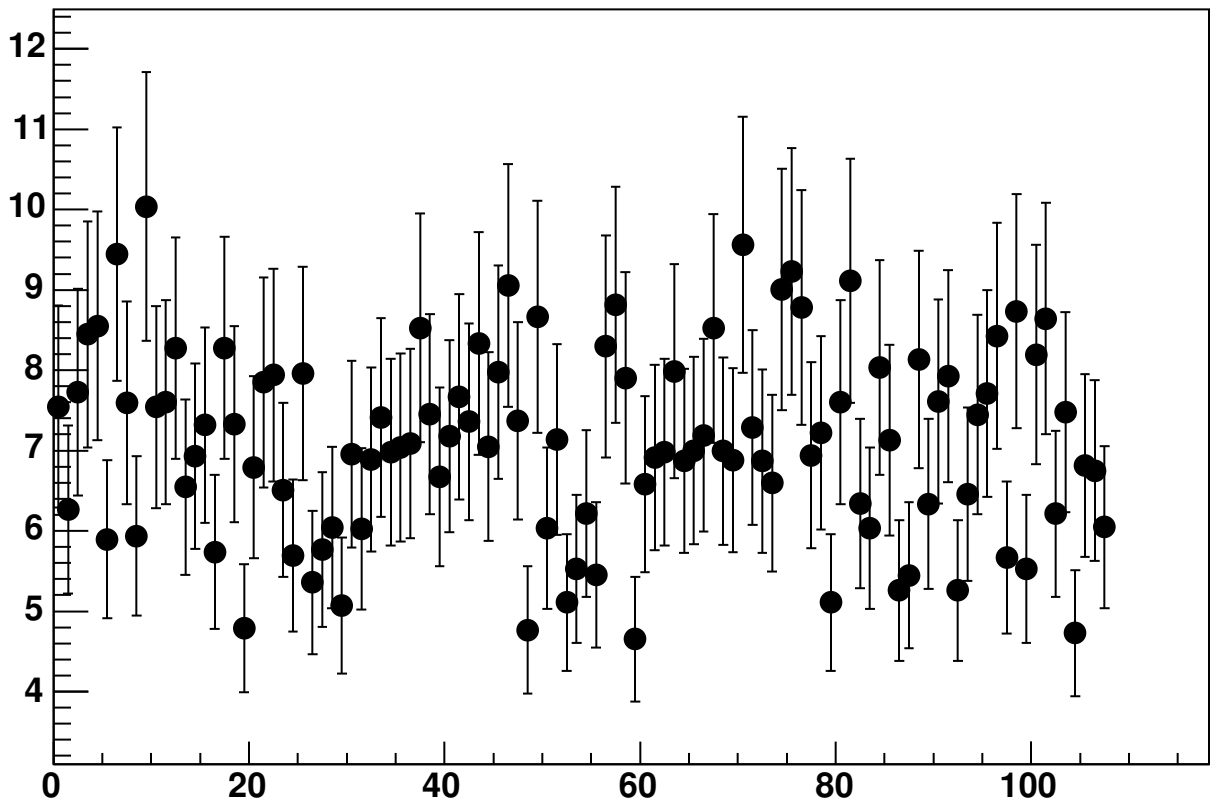
Enable 3, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



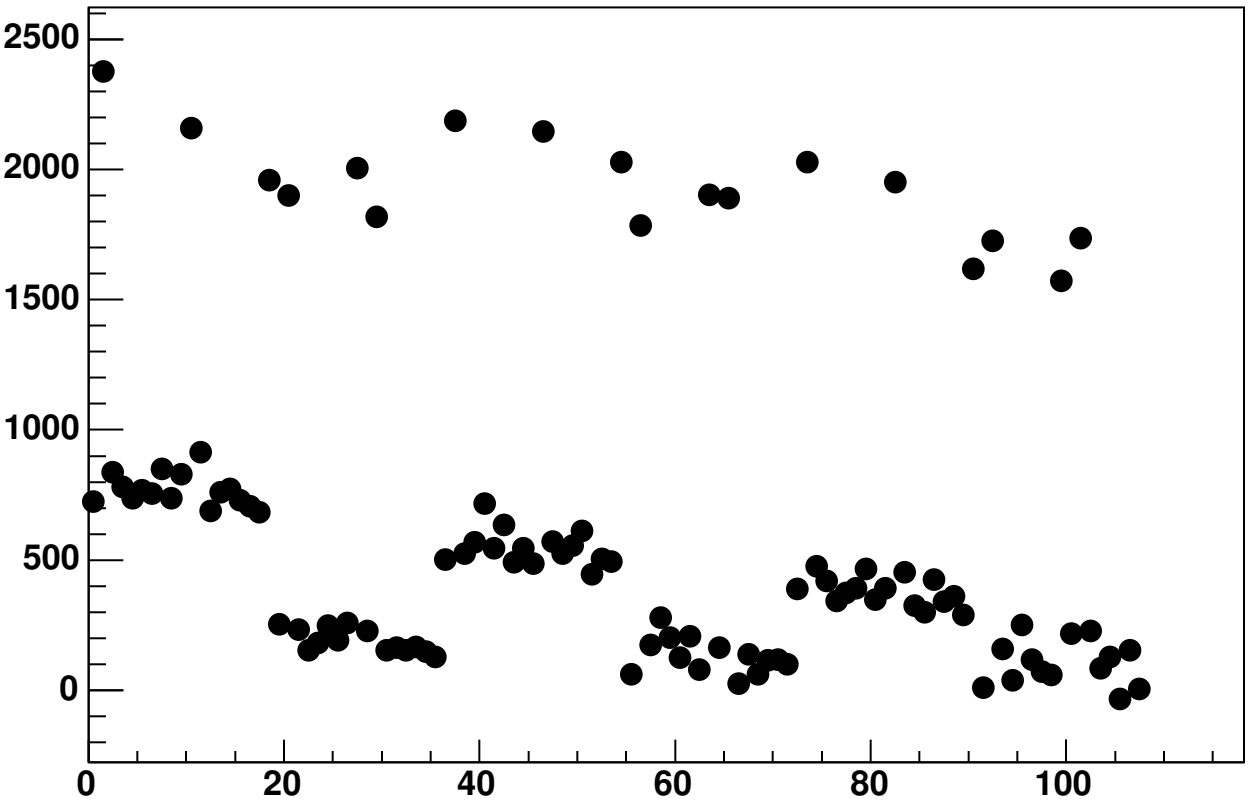
Enable 3, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



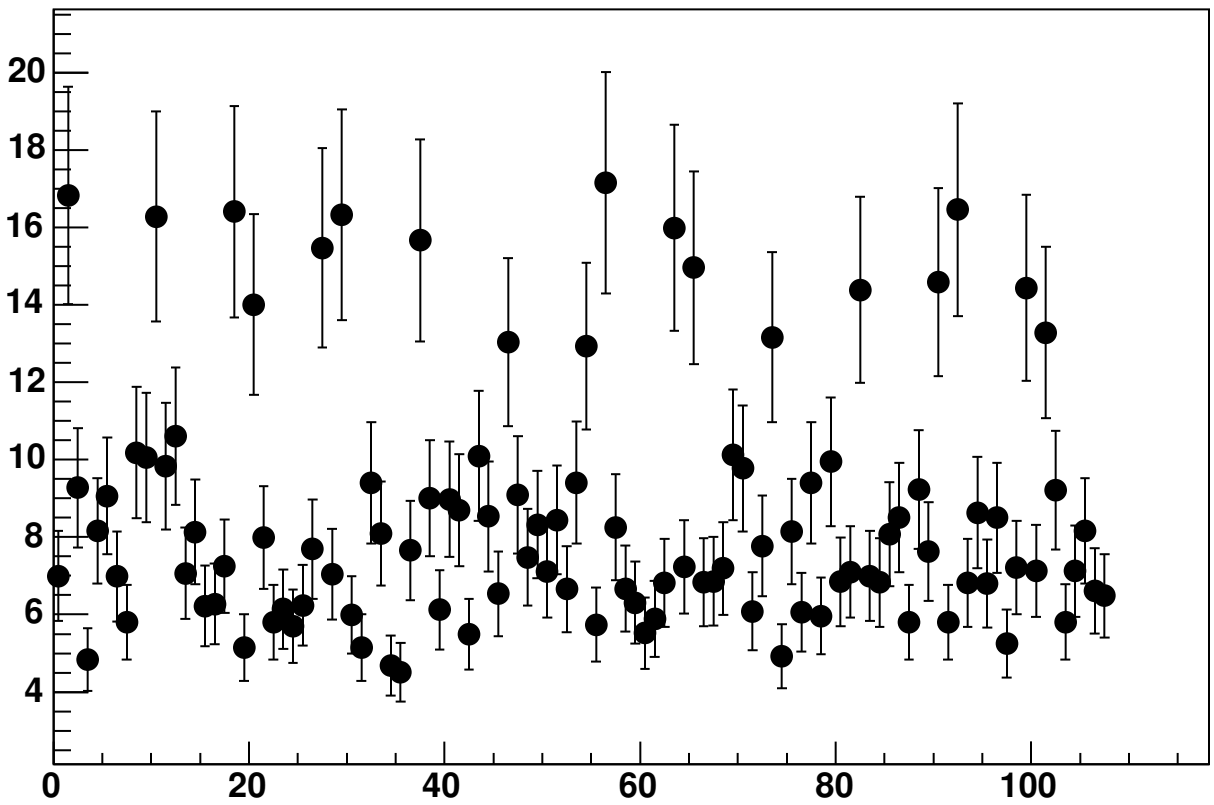
Enable 3, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



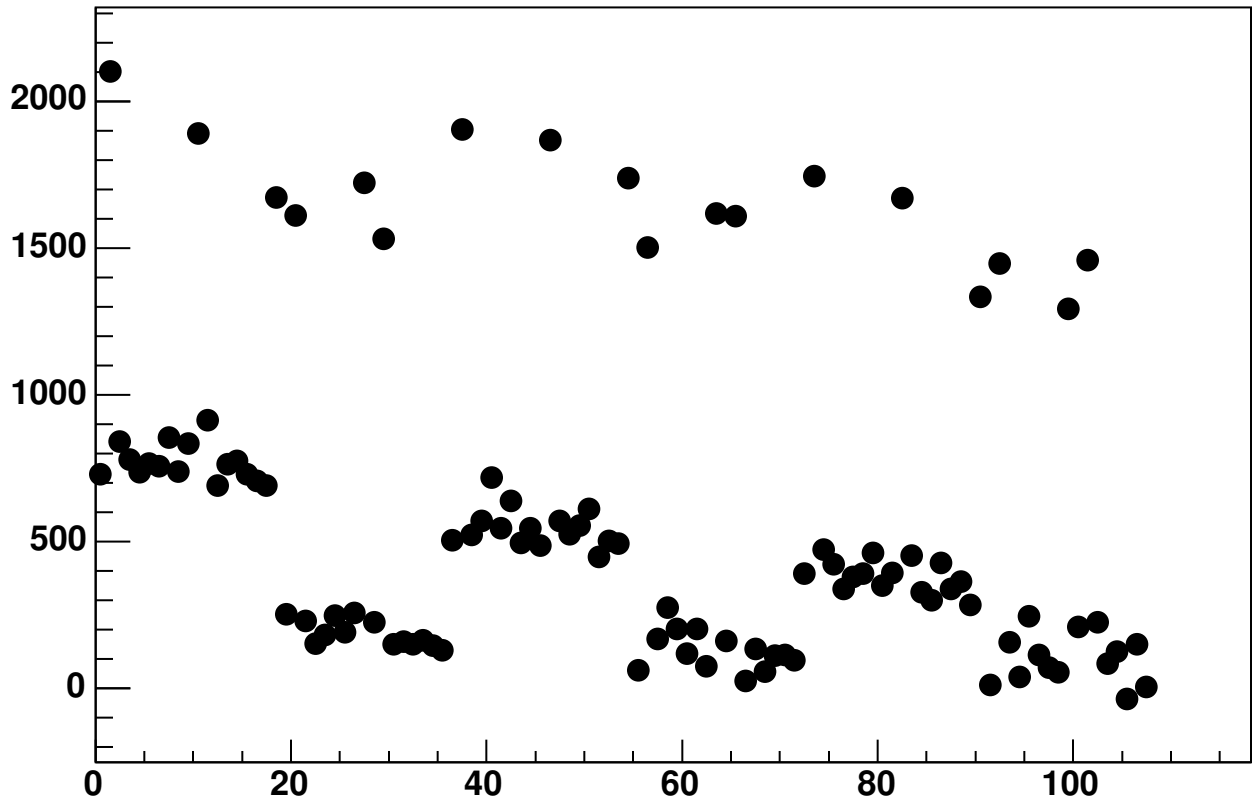
Enable 3, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



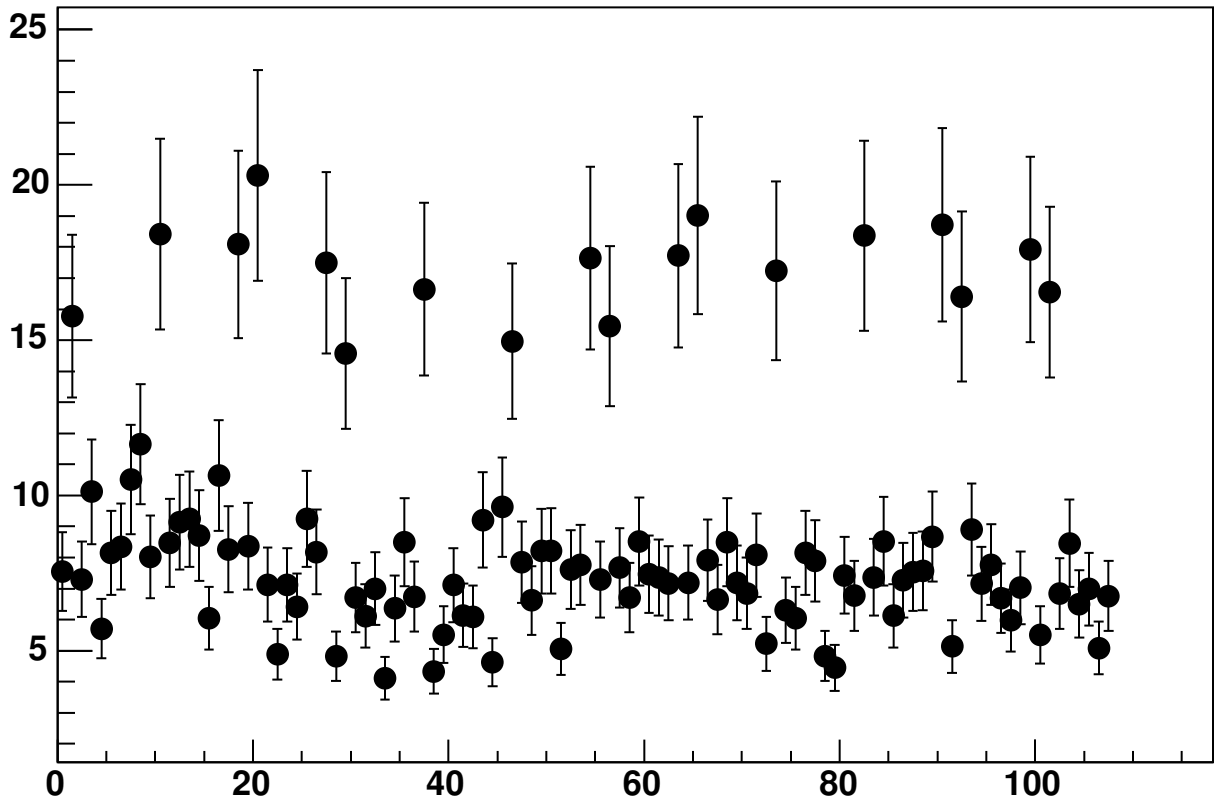
Enable 3, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



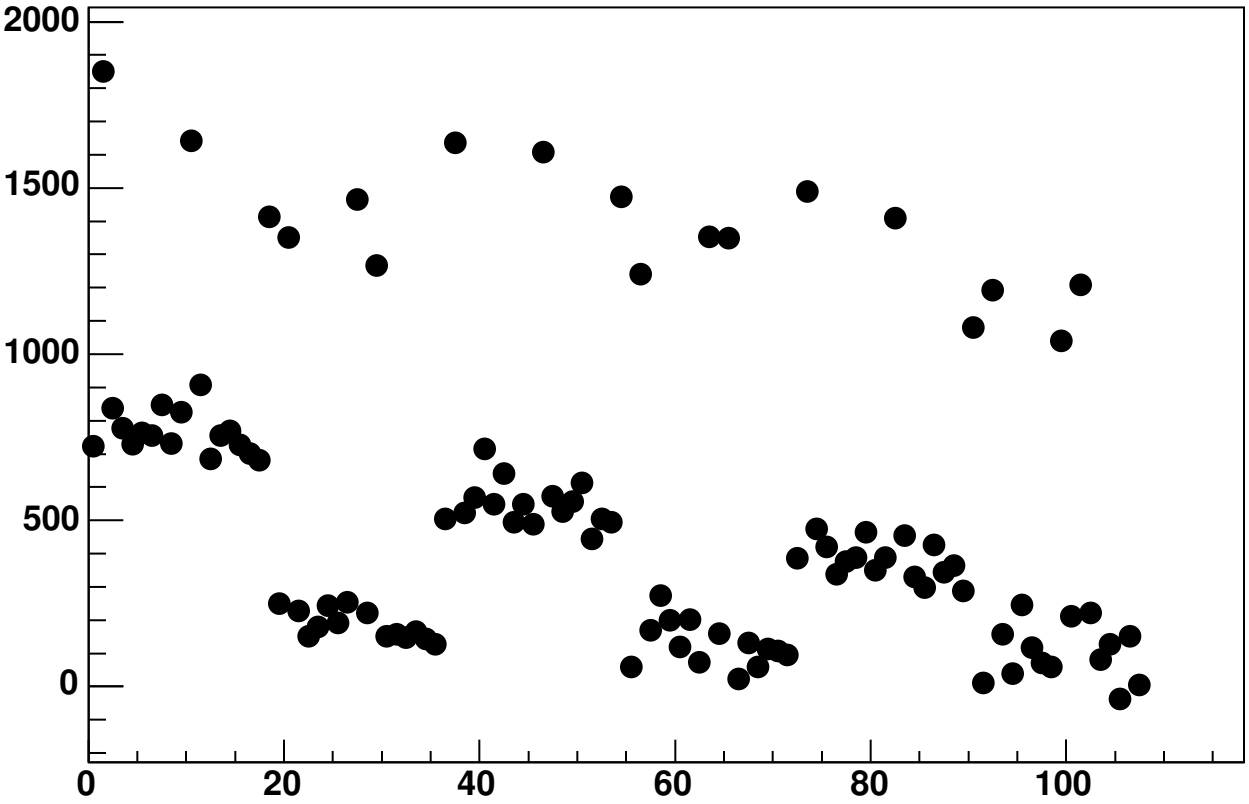
Enable 3, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



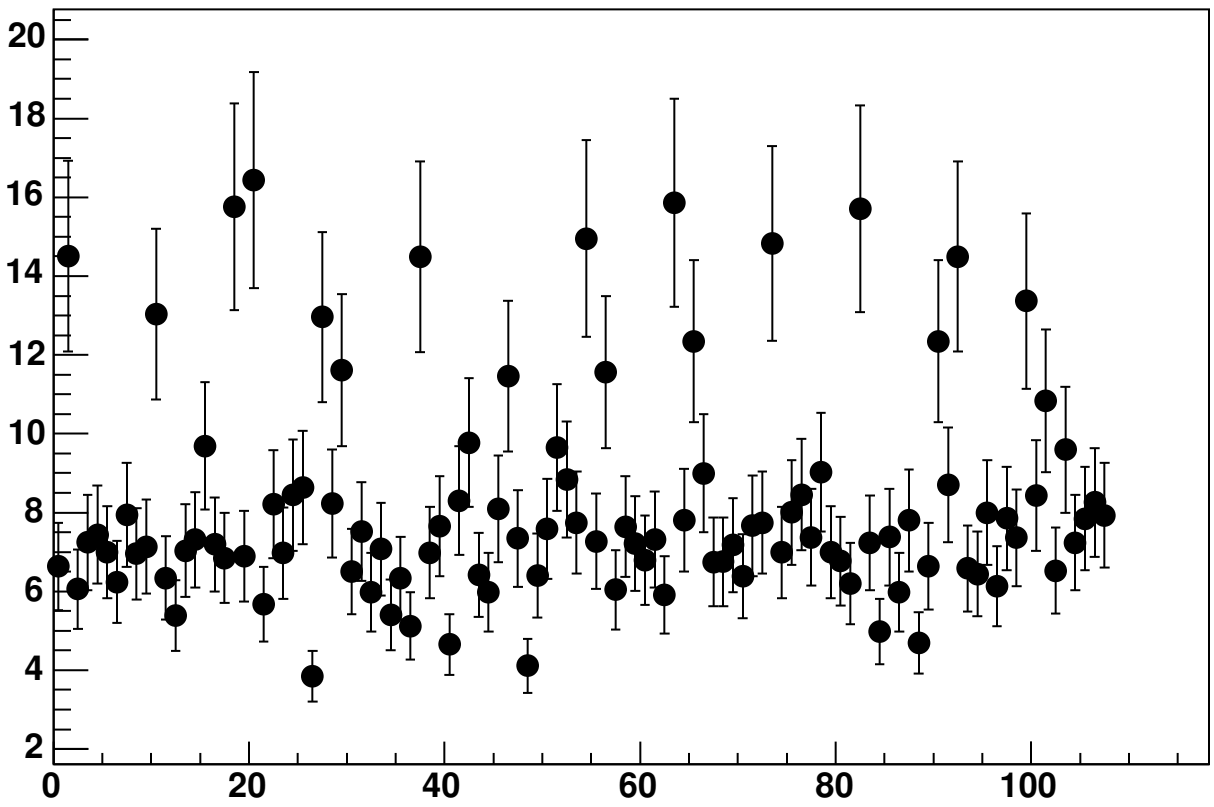
Enable 3, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



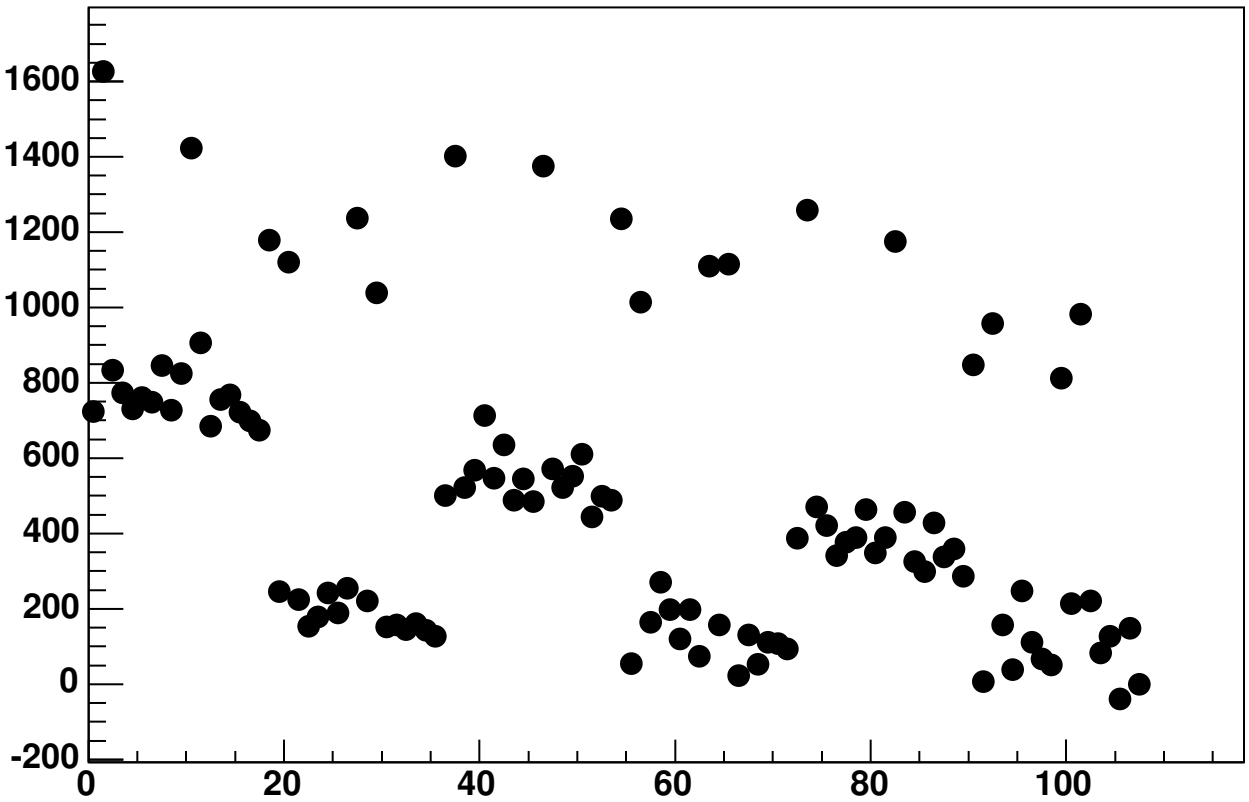
Enable 3, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



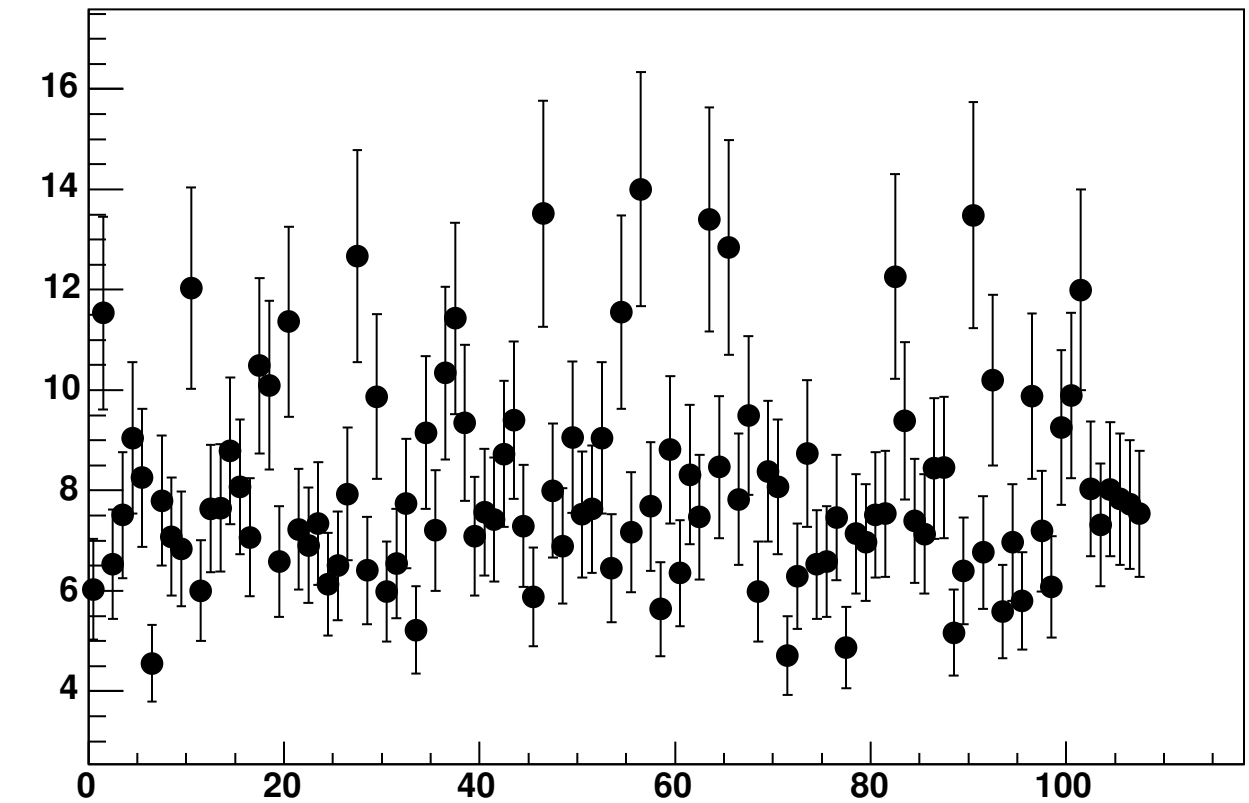
Enable 3, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



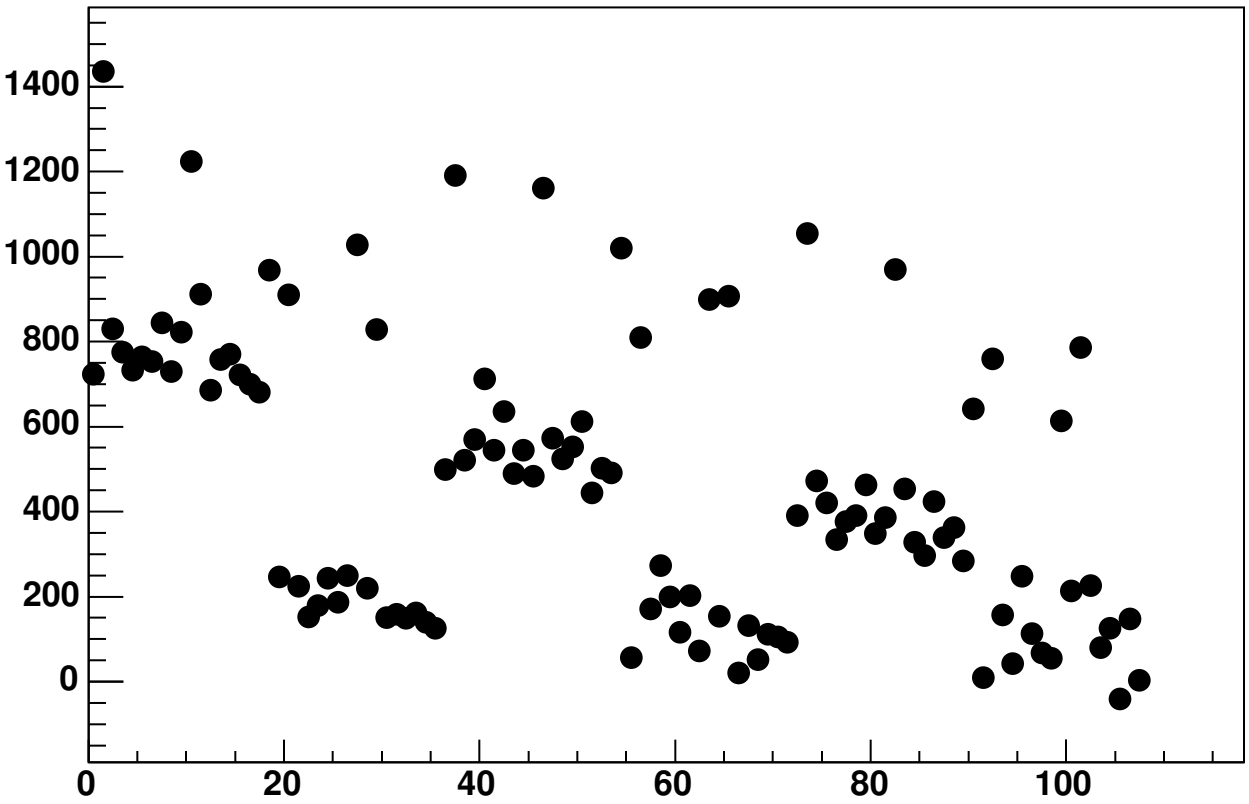
Enable 3, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



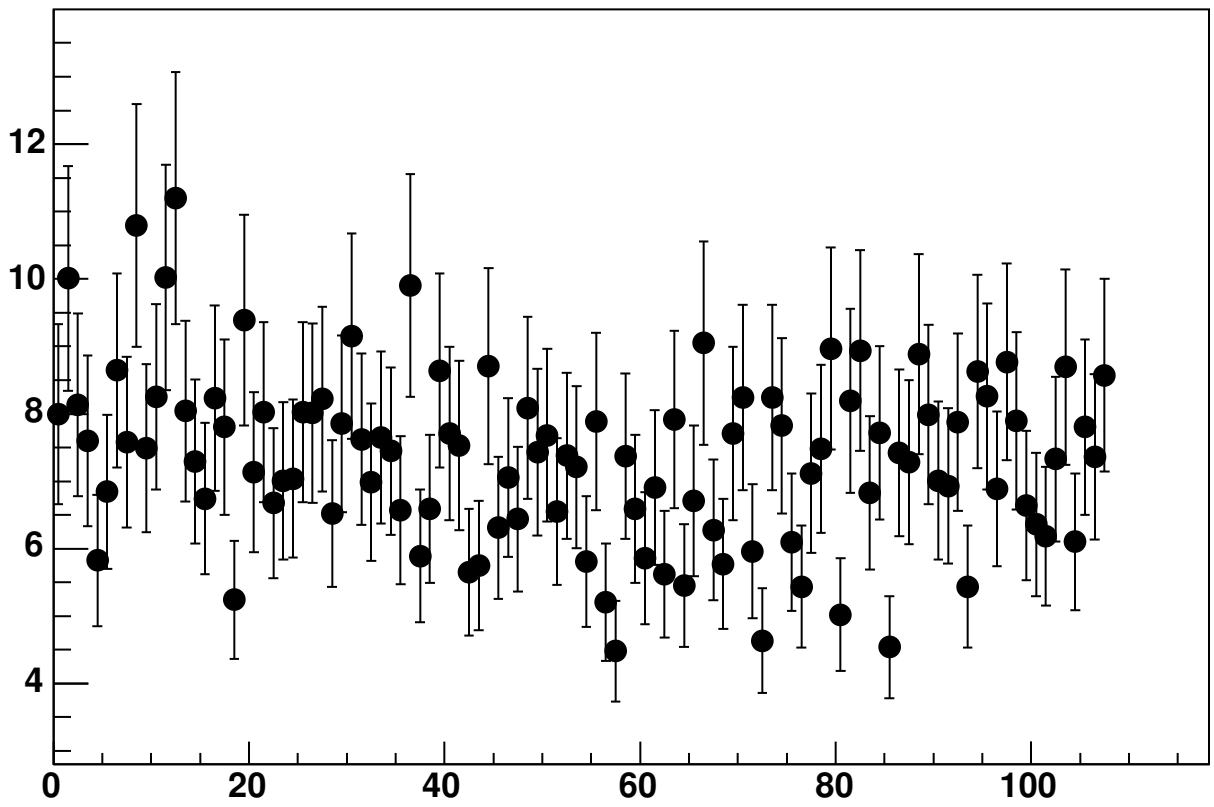
Enable 3, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



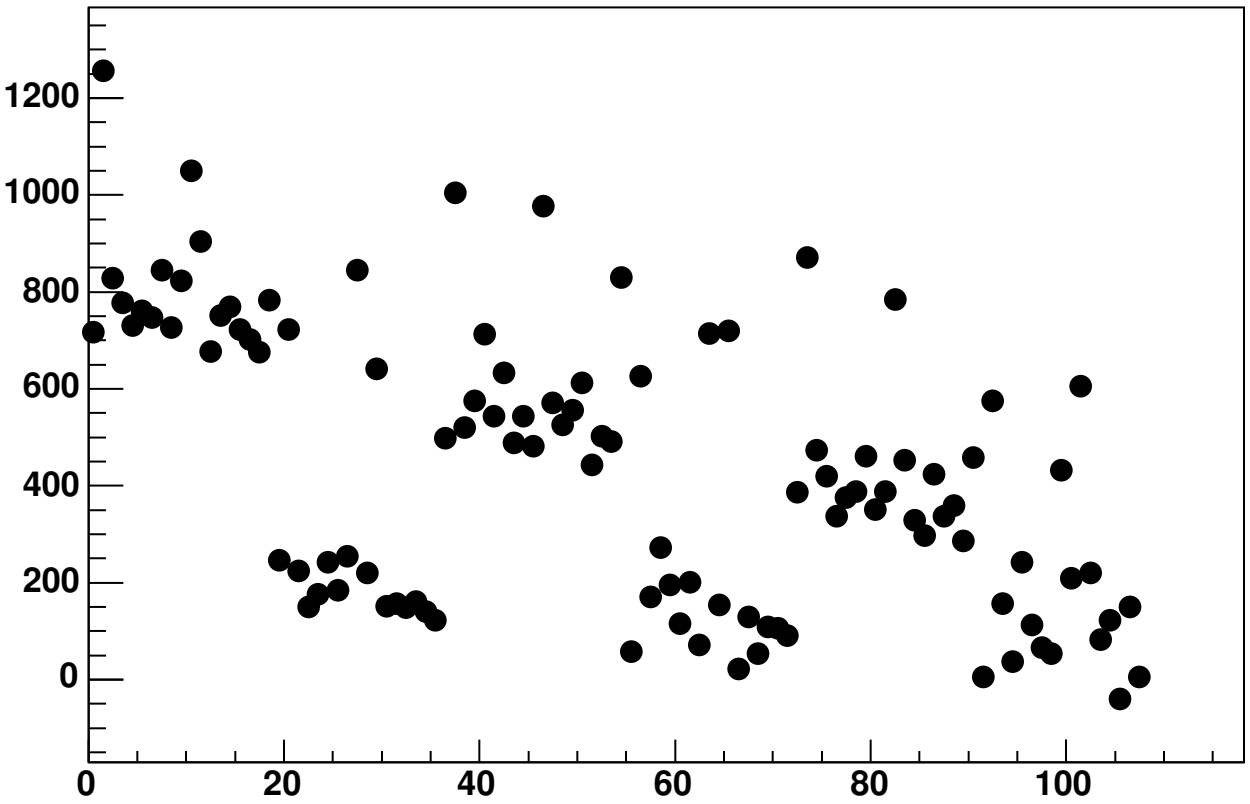
Enable 3, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



Enable 3, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



Enable 3, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



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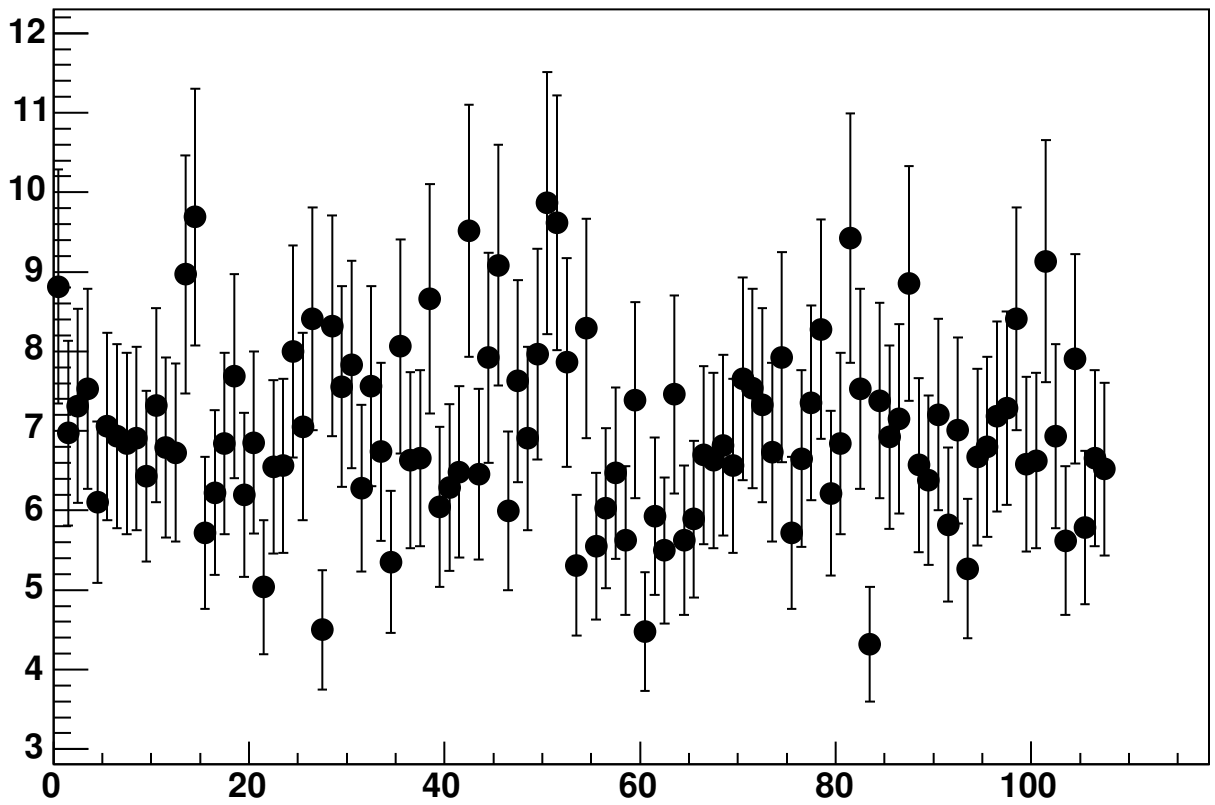
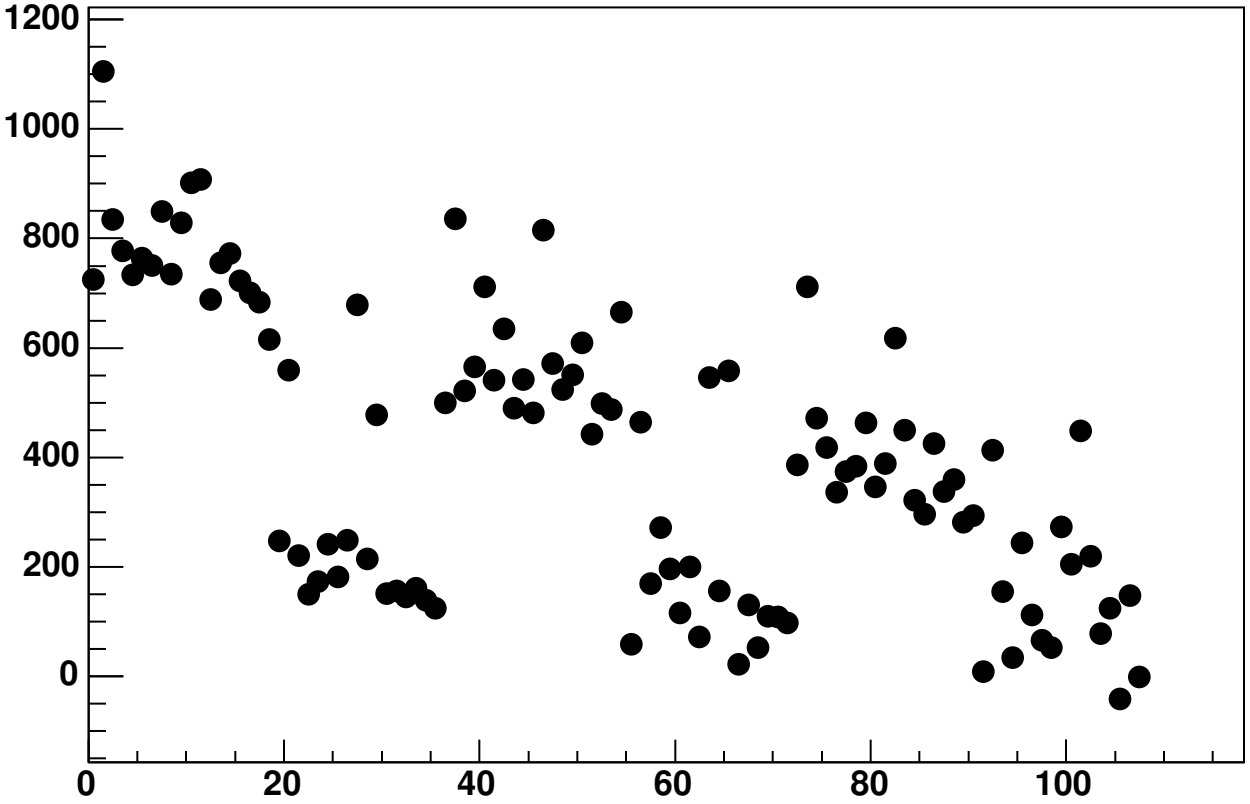
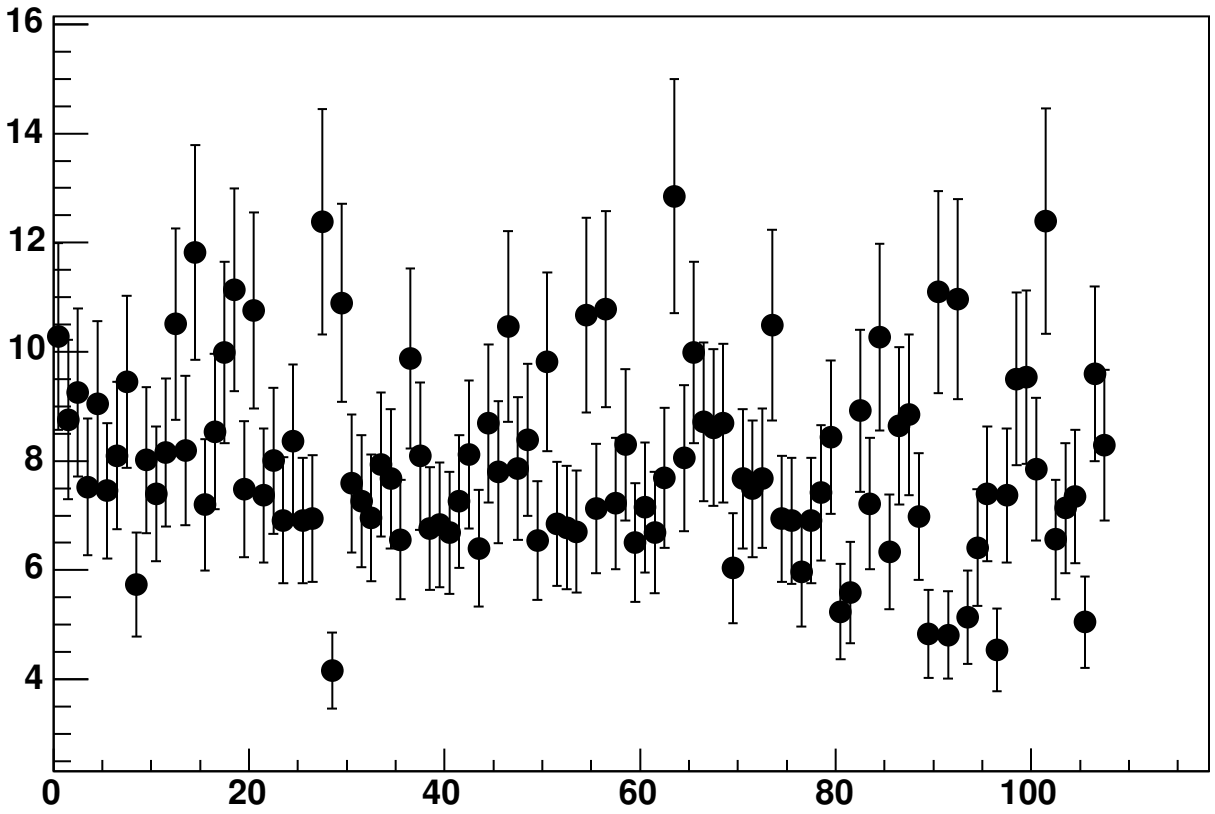


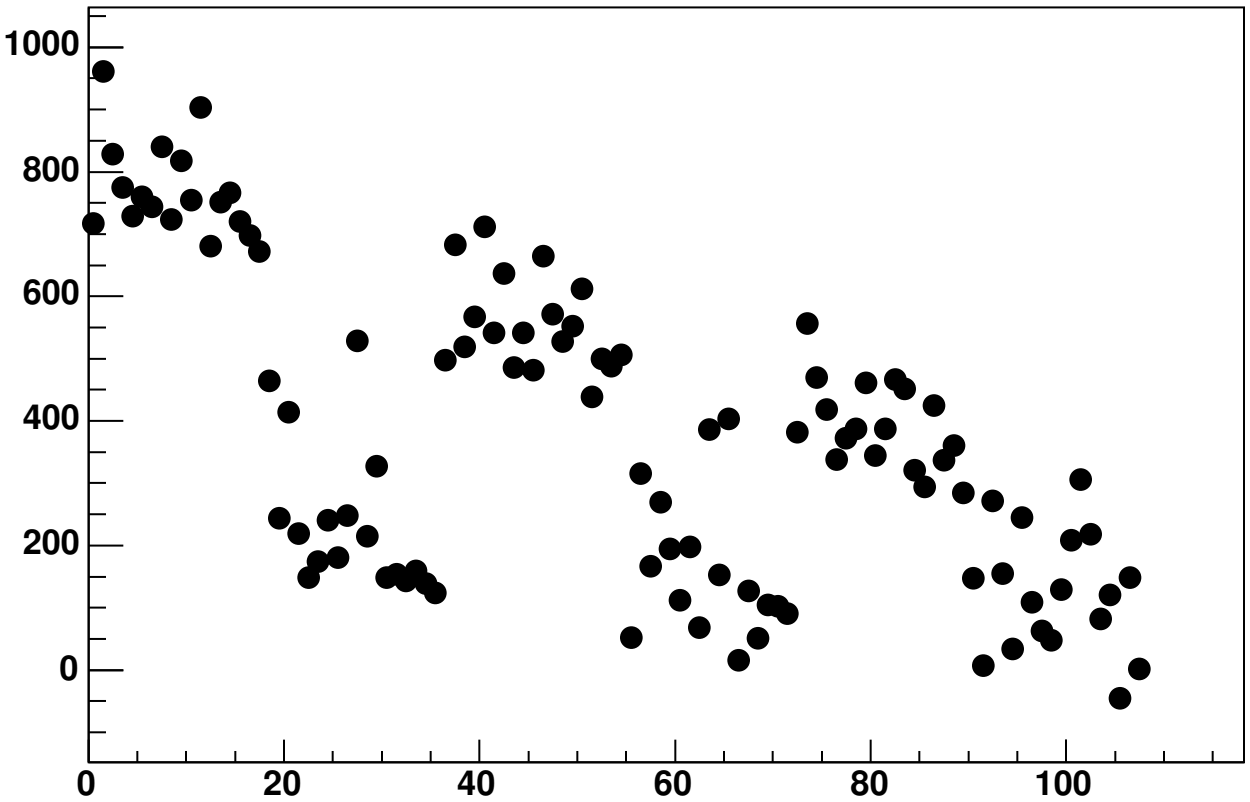
Enable 3, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



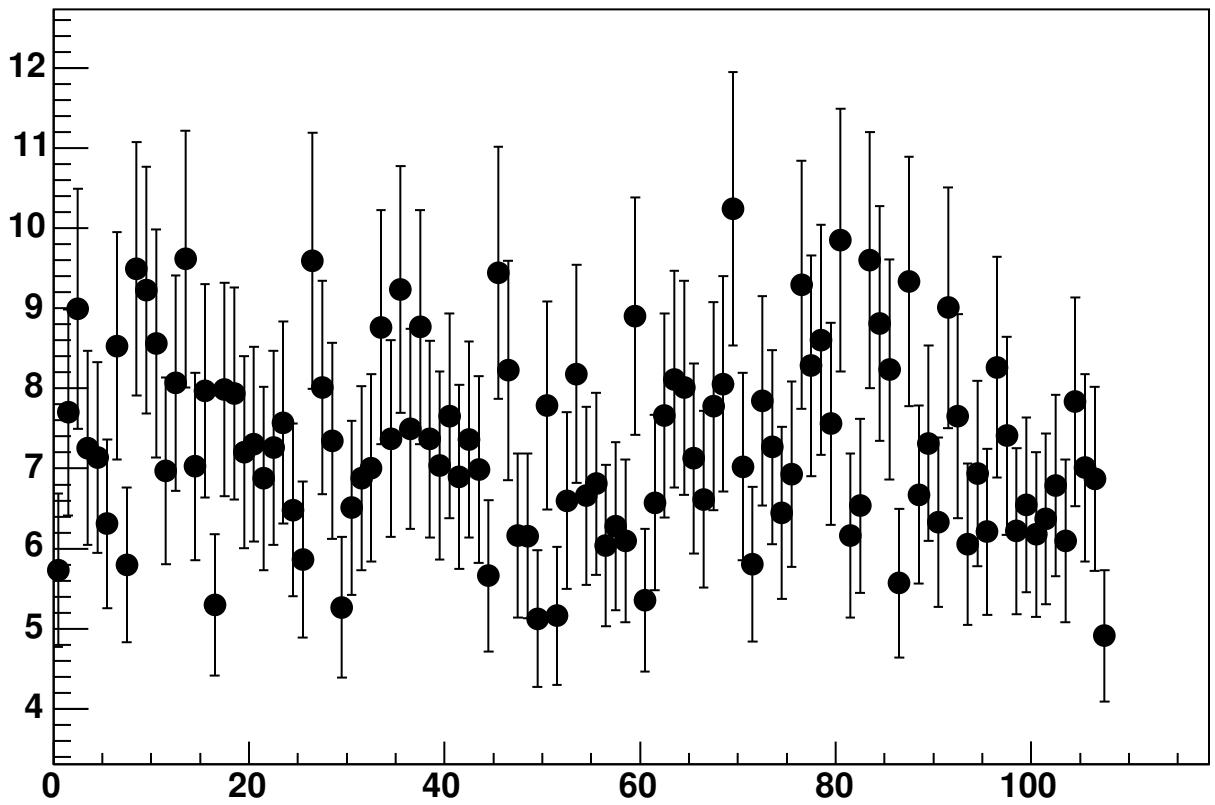
Enable 3, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



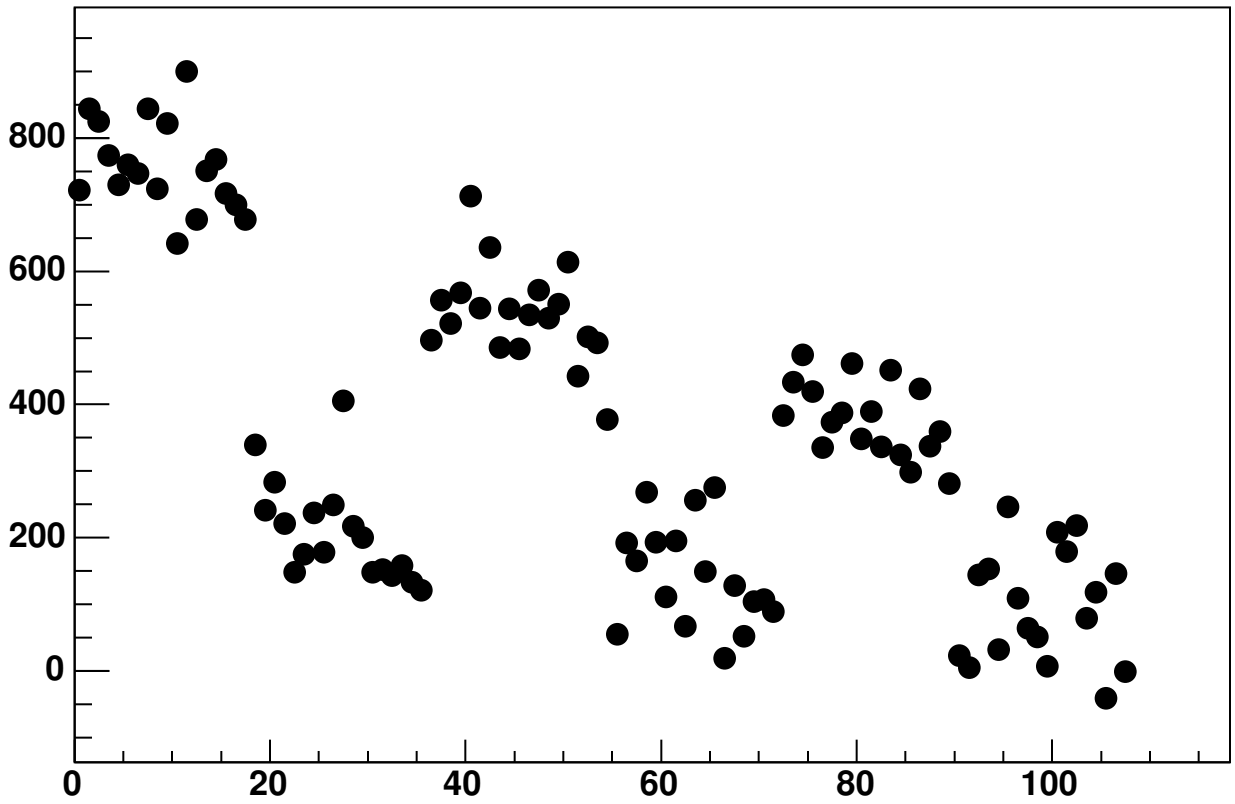
Enable 3, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



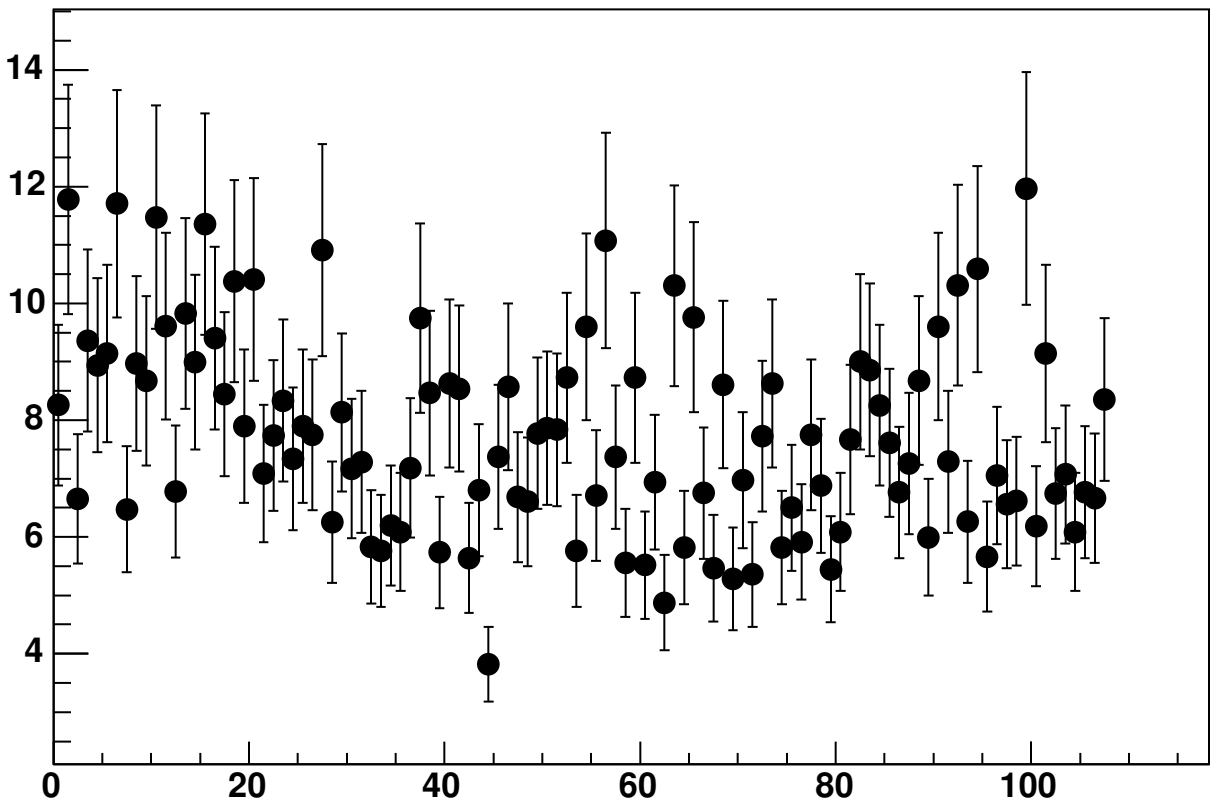
Enable 3, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



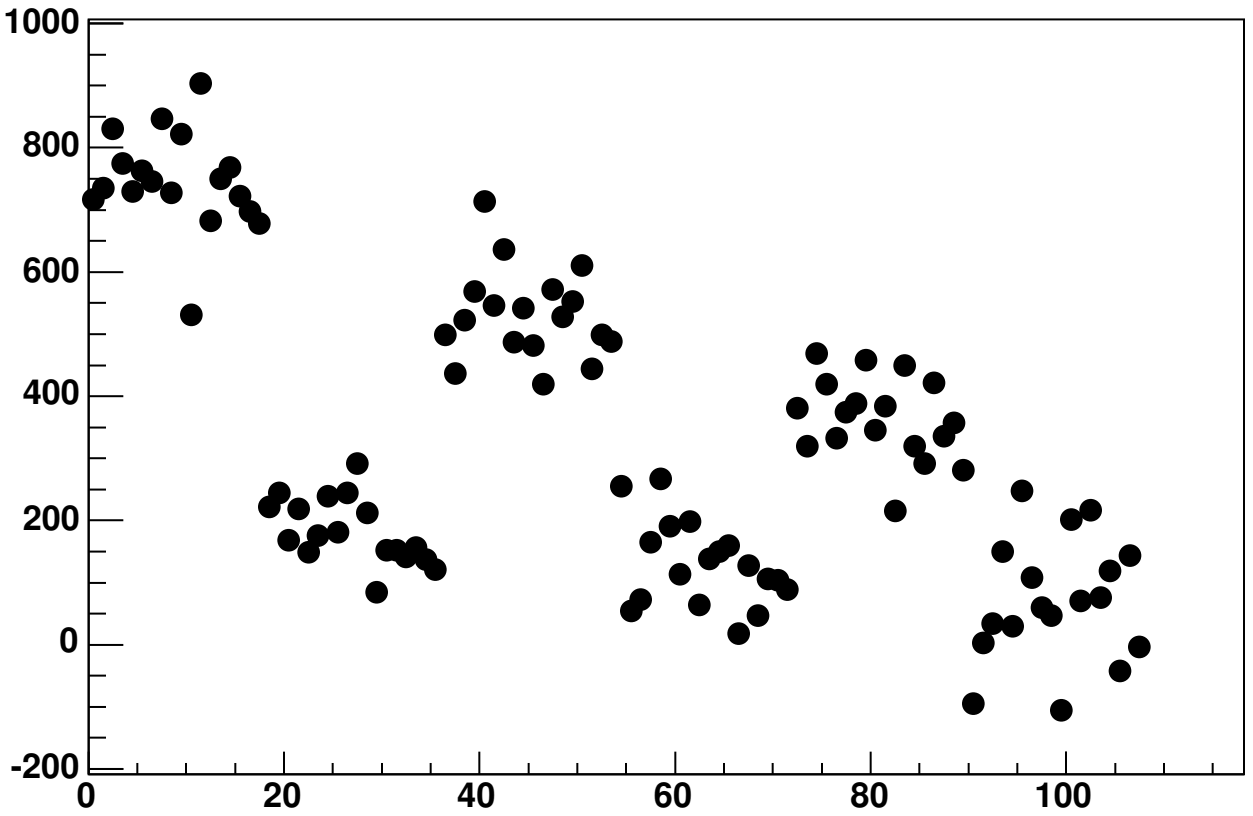
Enable 3, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



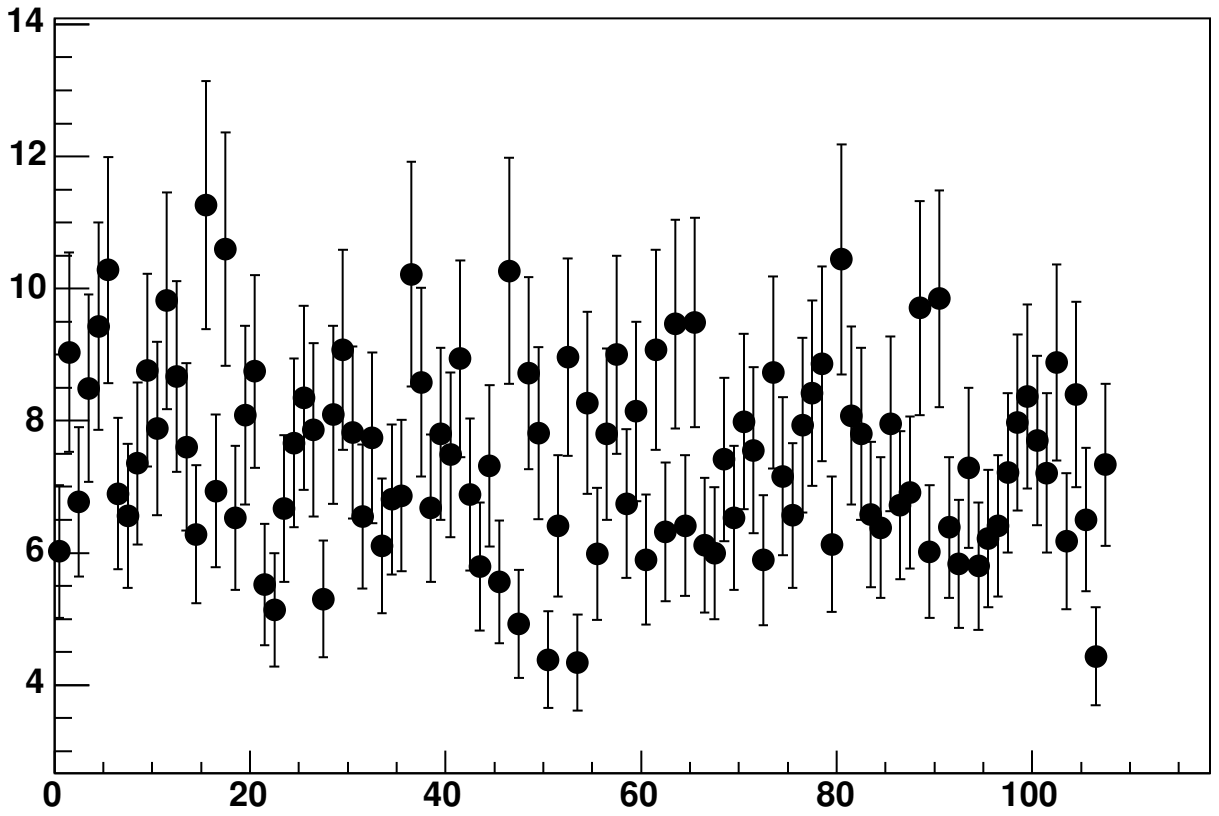
Enable 3, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



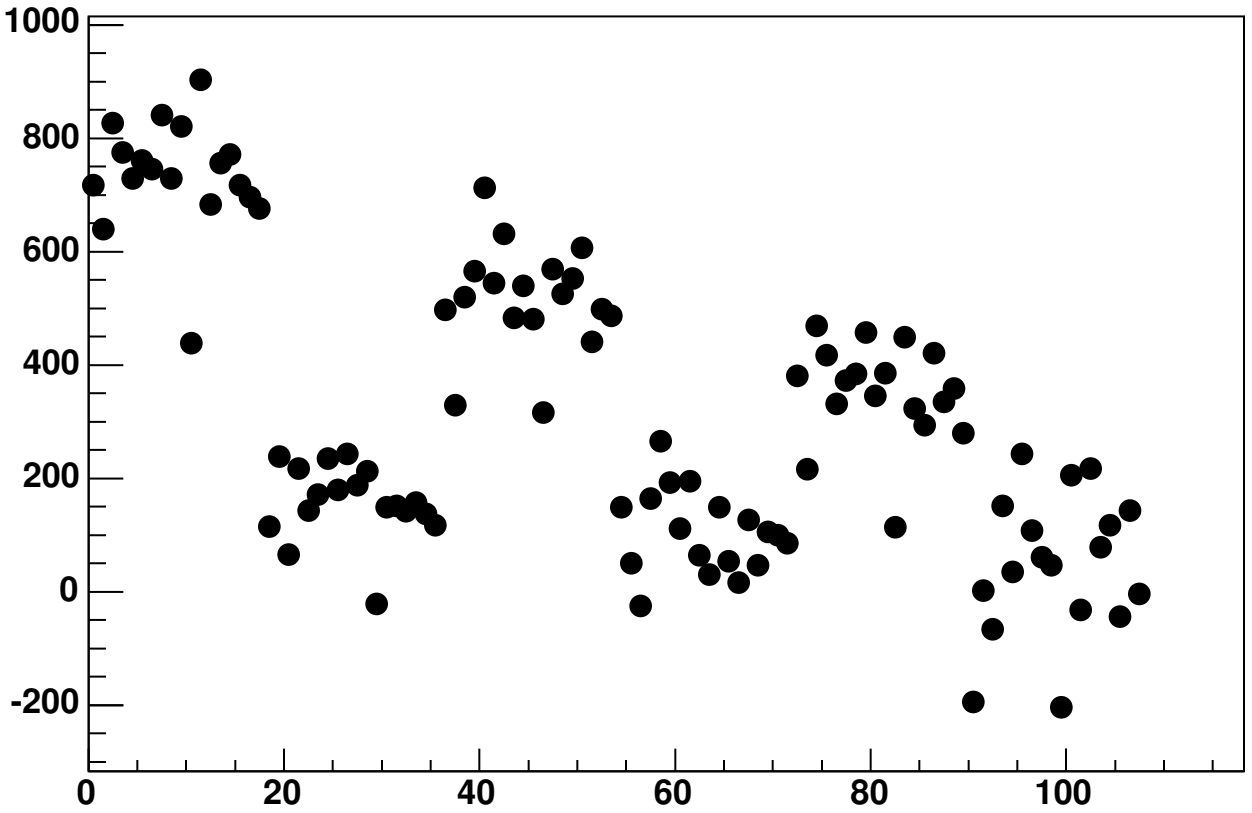
Enable 3, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



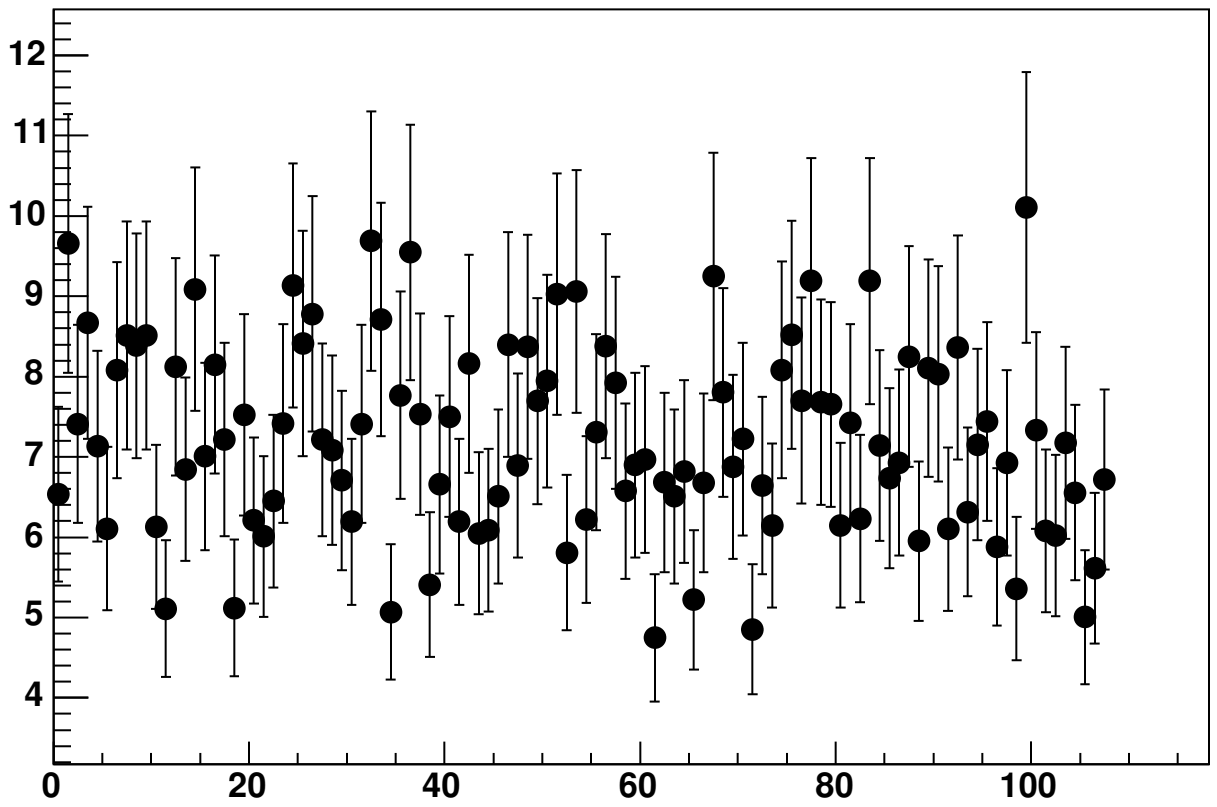
Enable 3, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



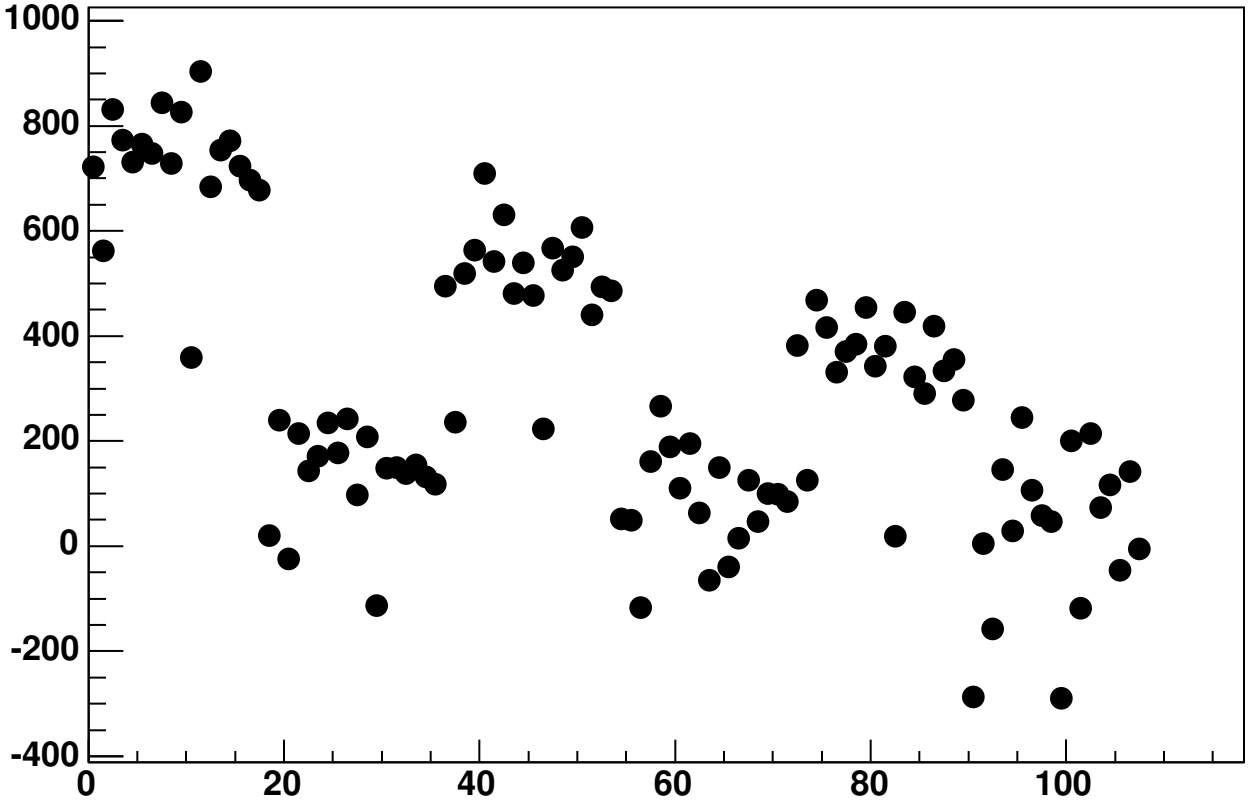
Enable 3, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



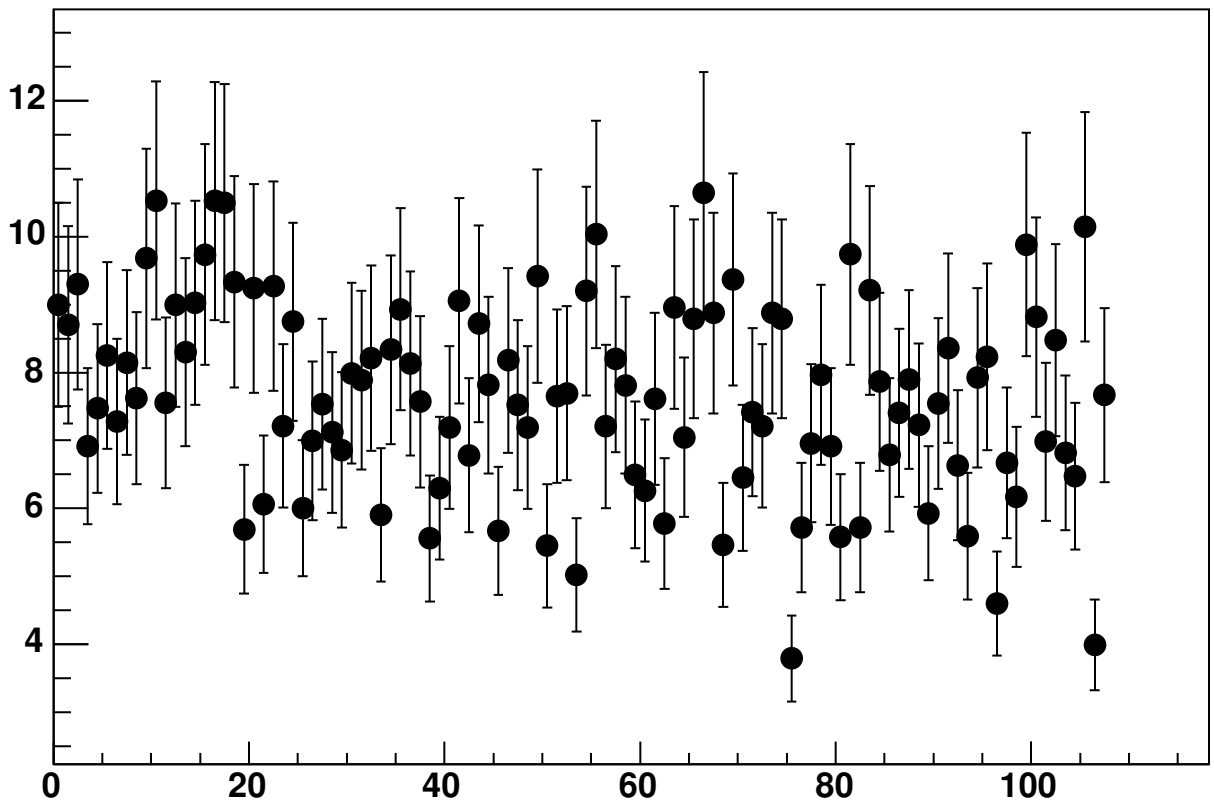
Enable 3, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



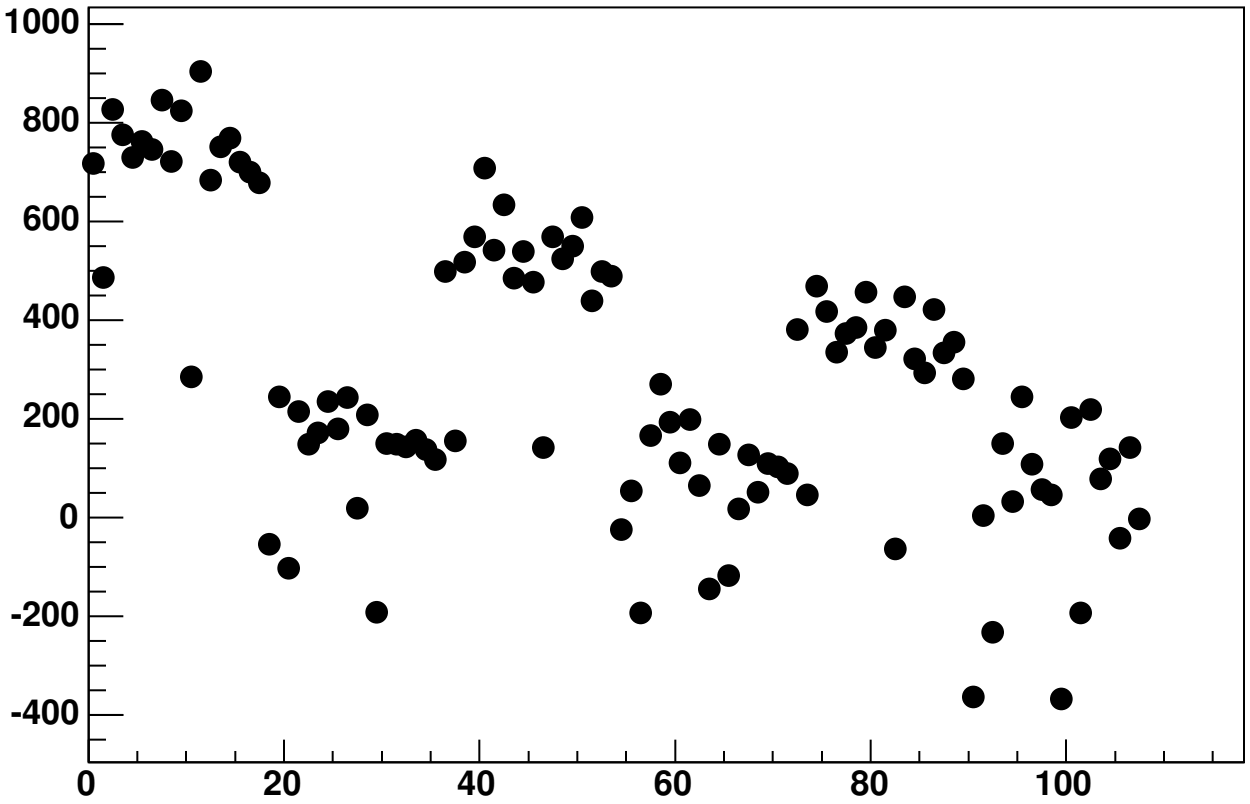
Enable 3, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



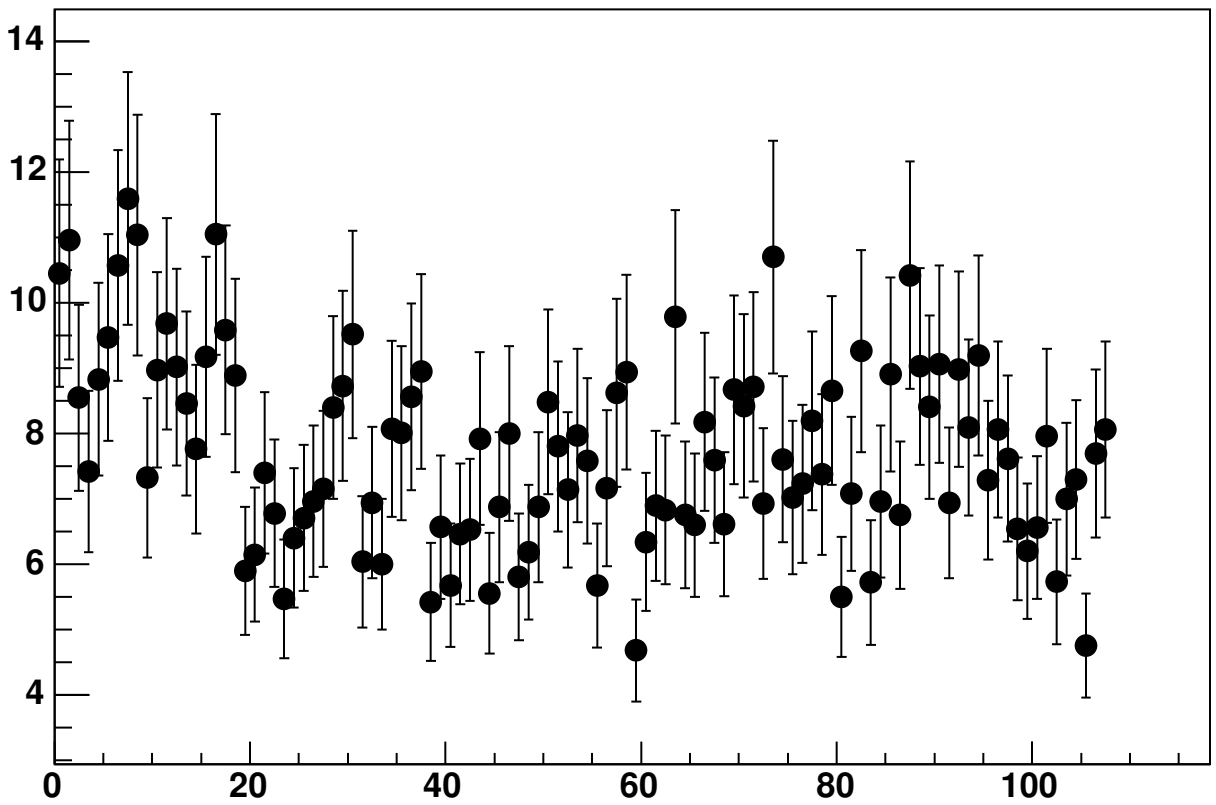
Enable 3, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



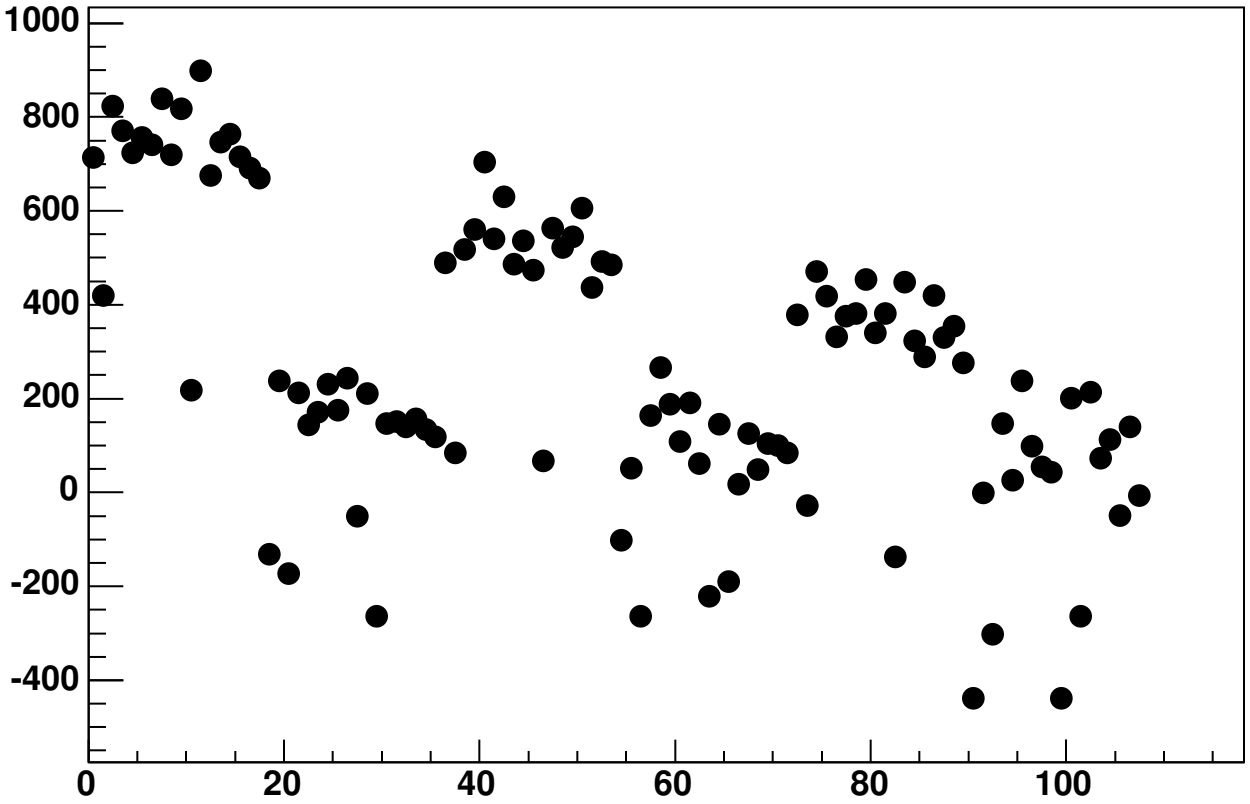
Enable 3, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



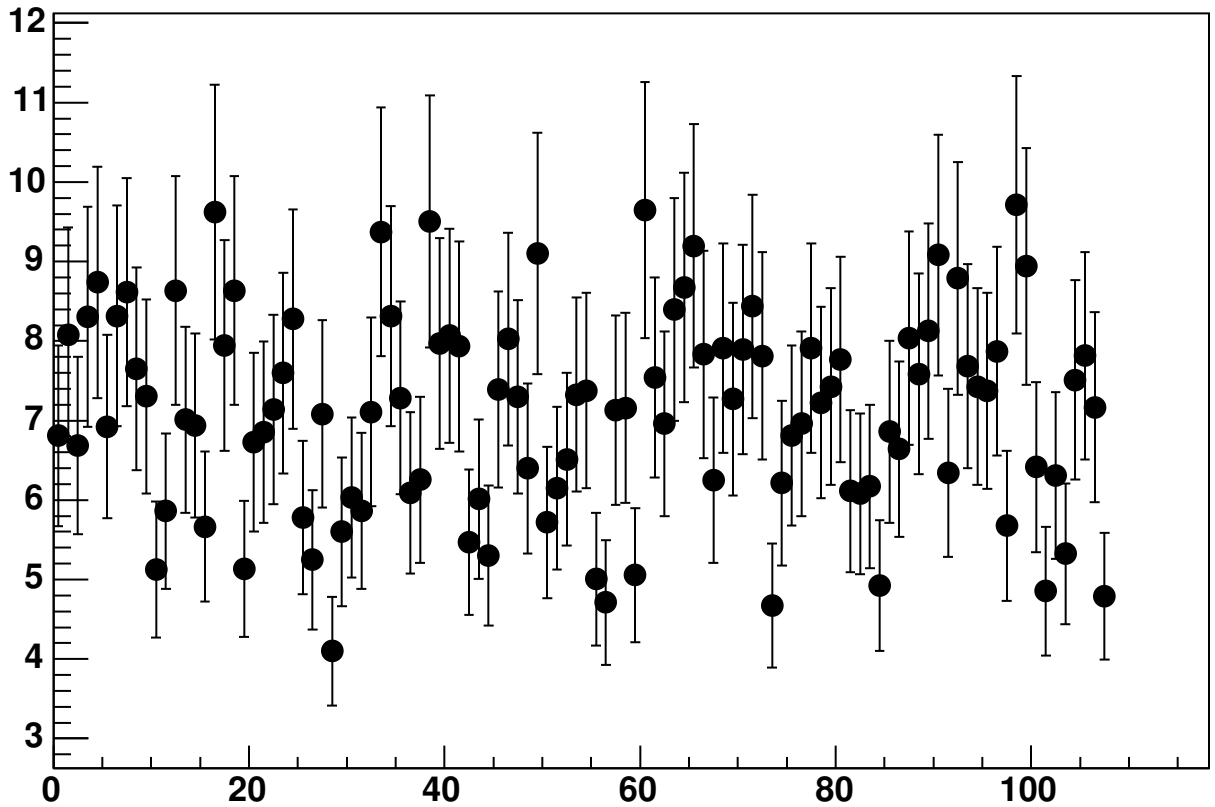
Enable 3, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



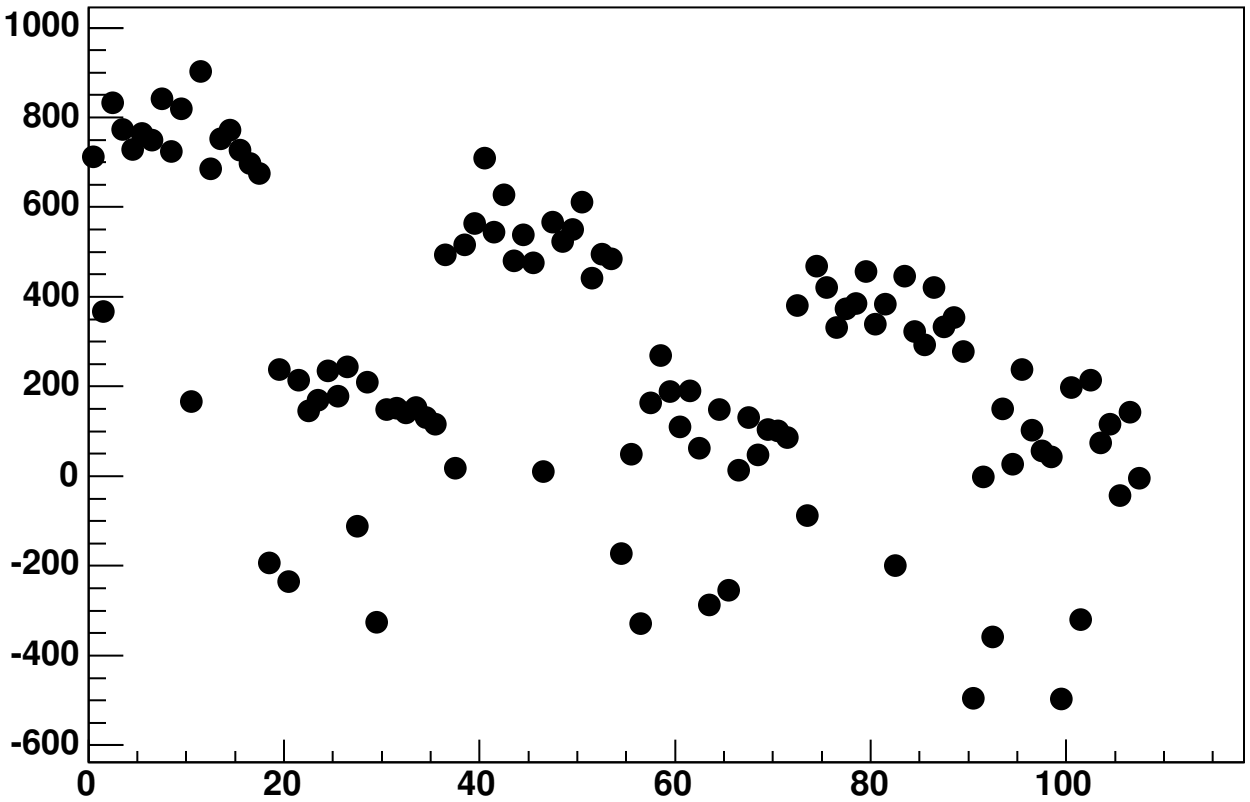
Enable 3, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



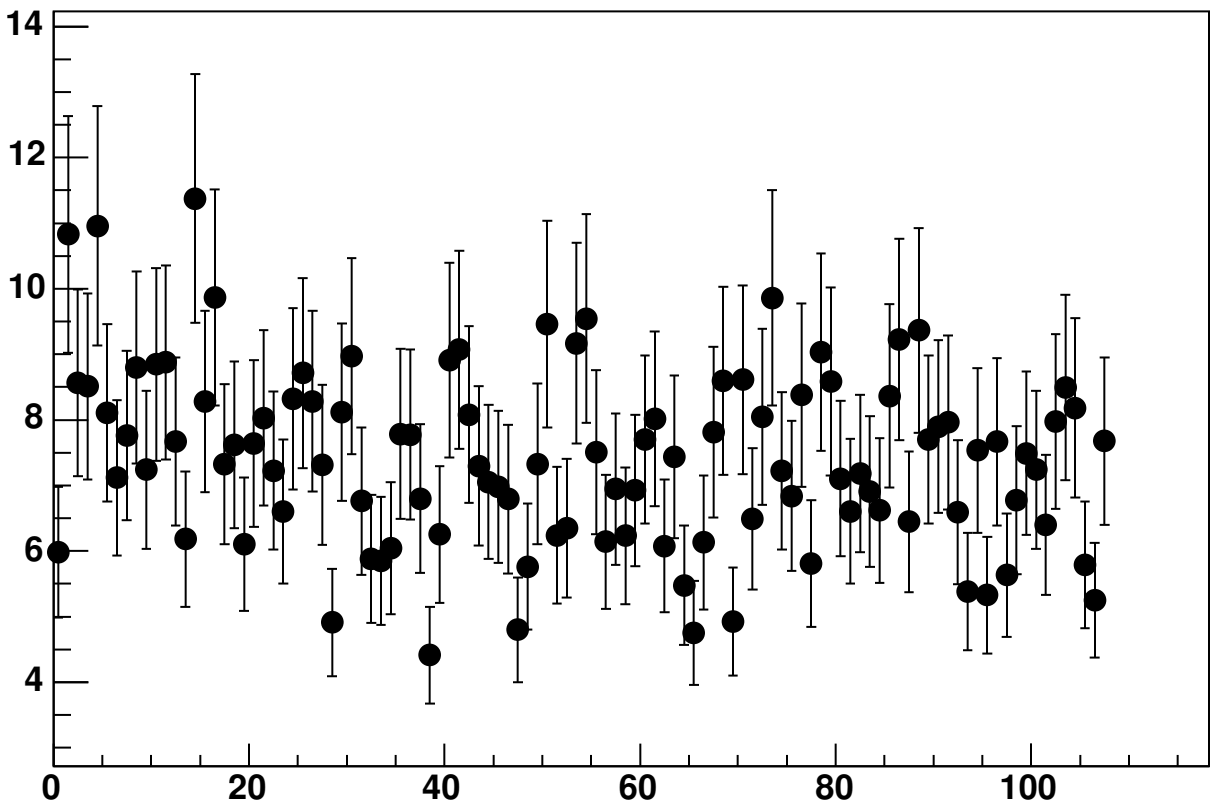
Enable 3, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



Enable 3, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



Enable 3, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



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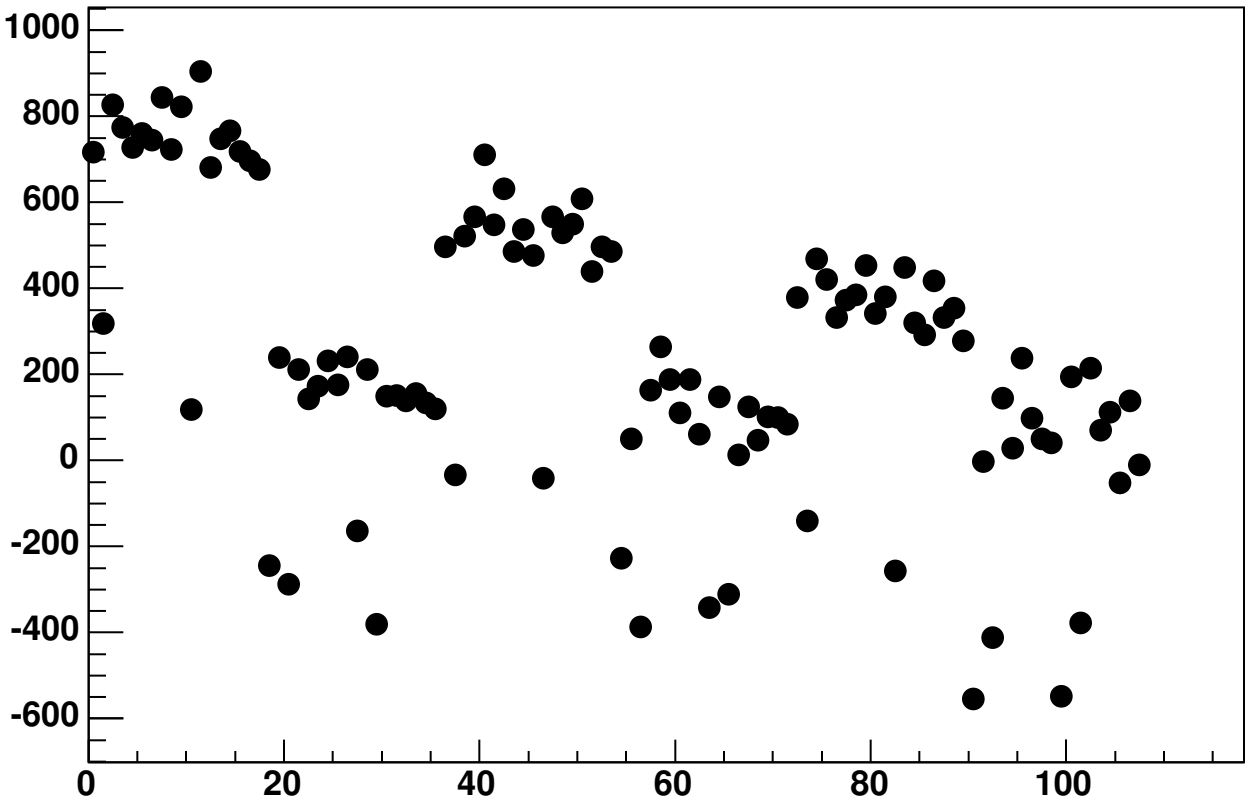

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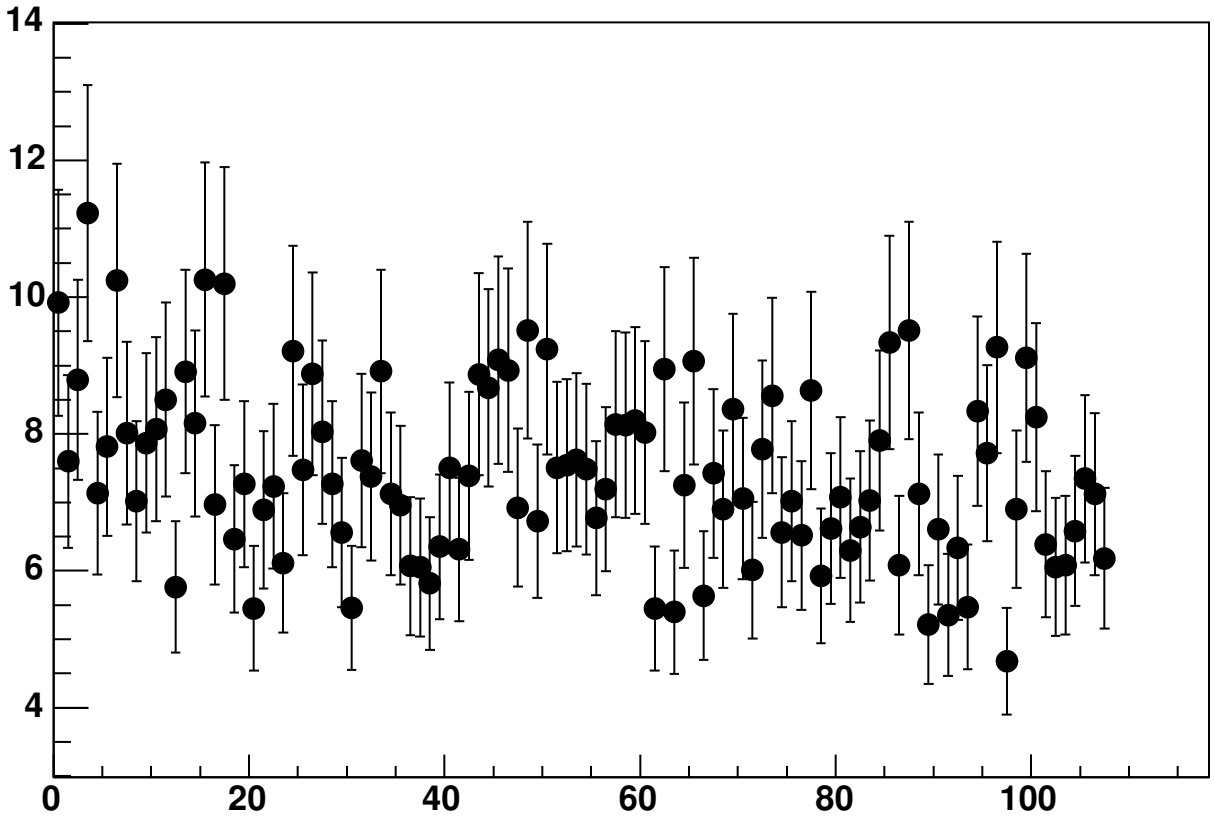

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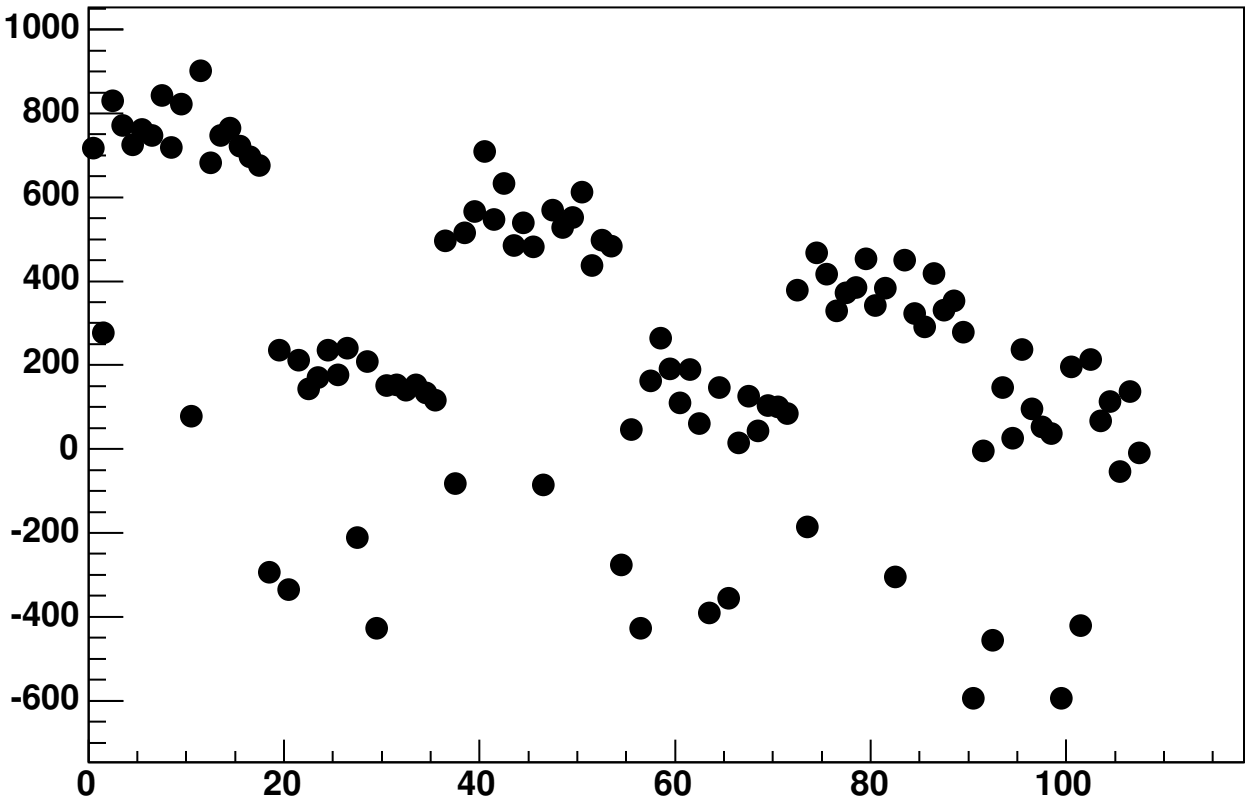
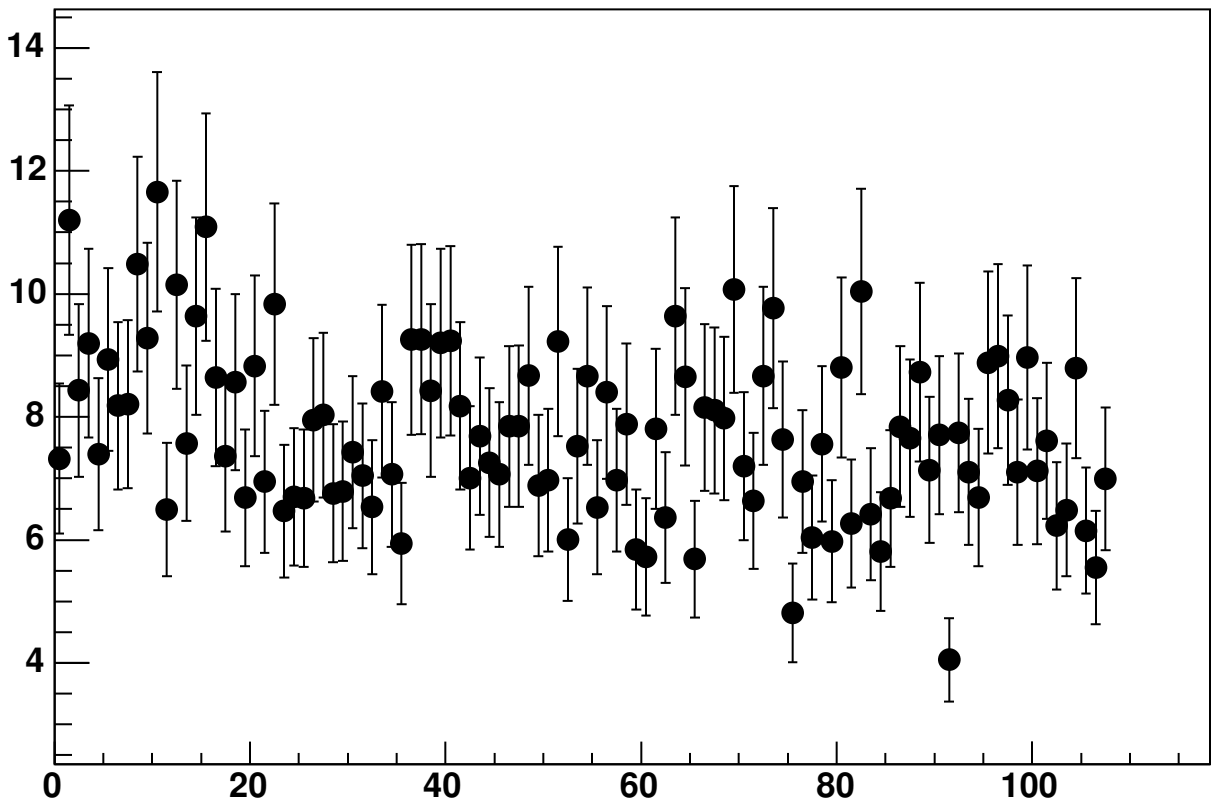
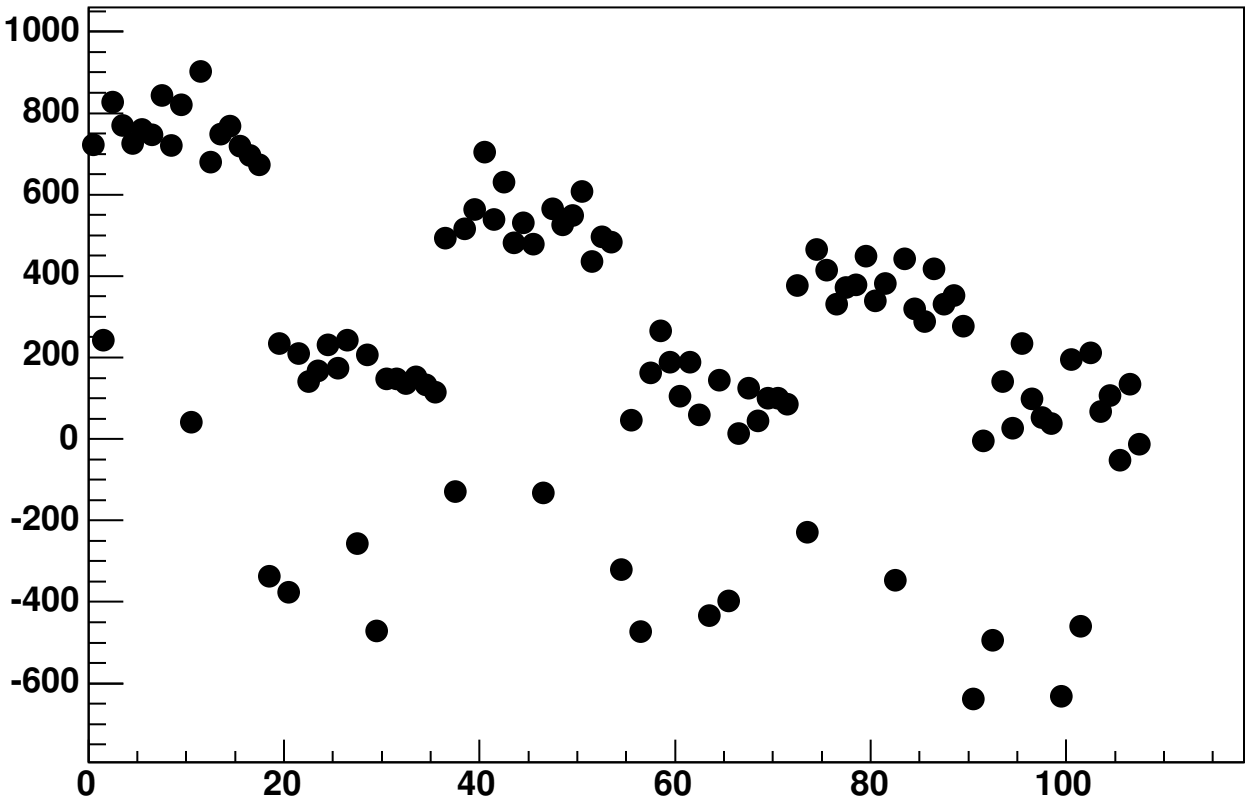


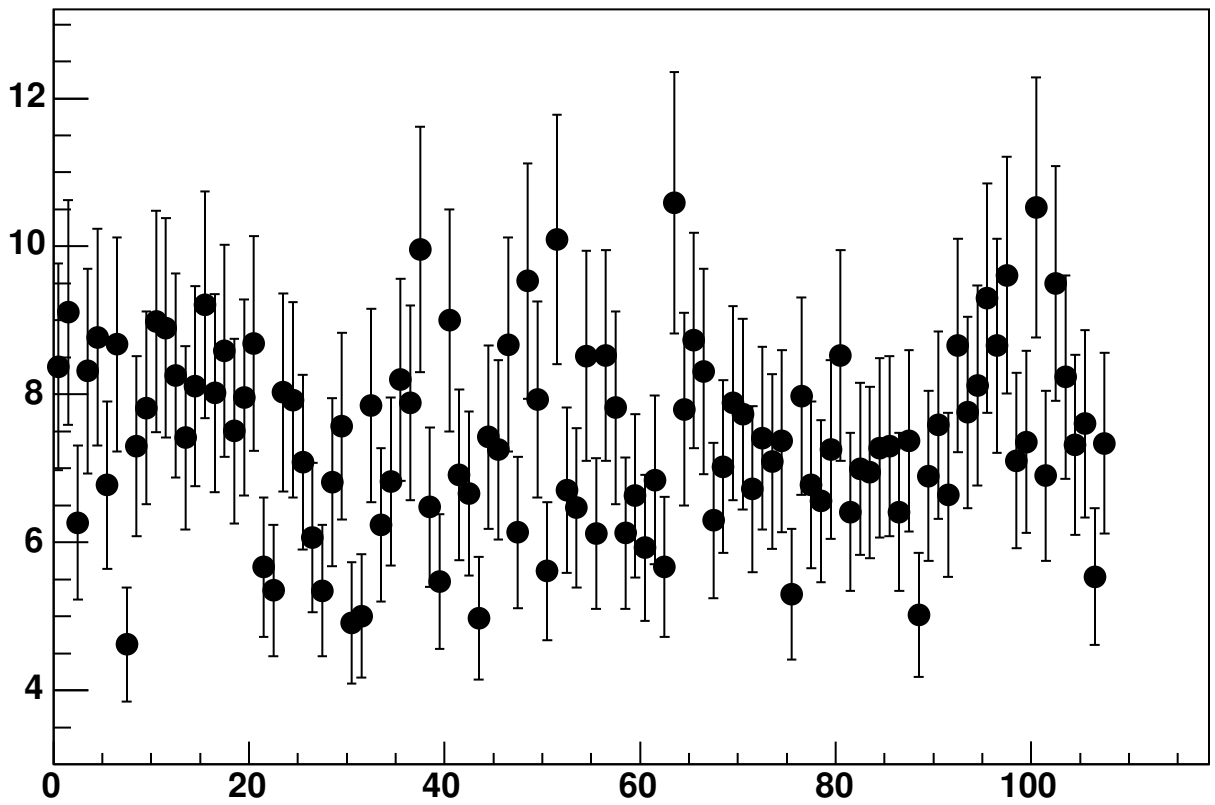
Enable 3, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



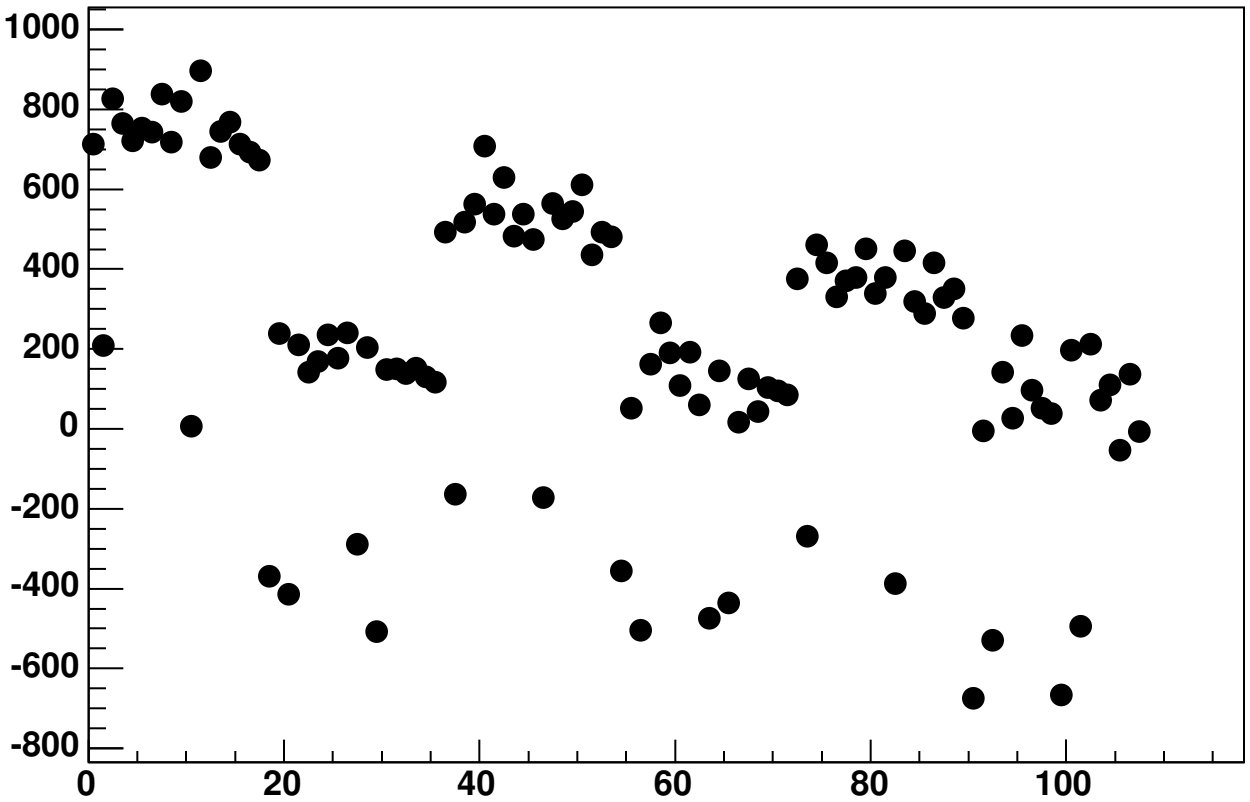
Enable 3, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



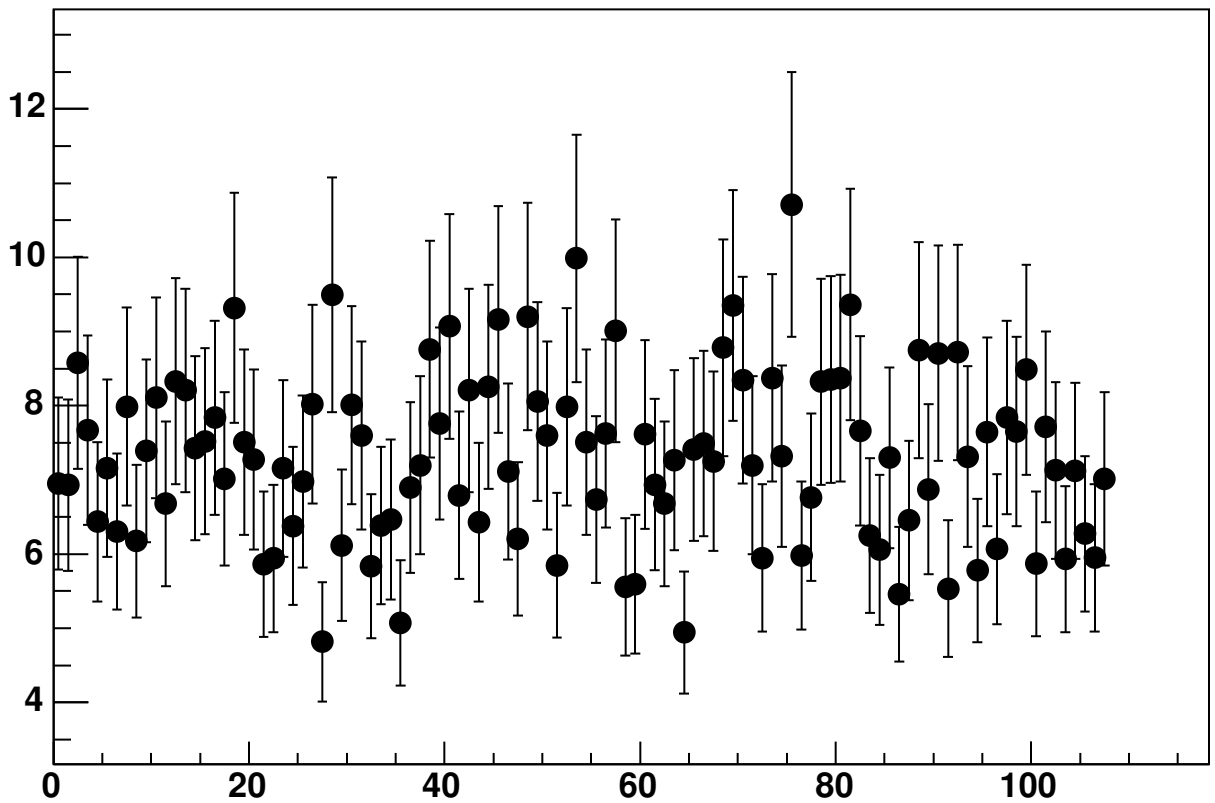
Enable 3, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



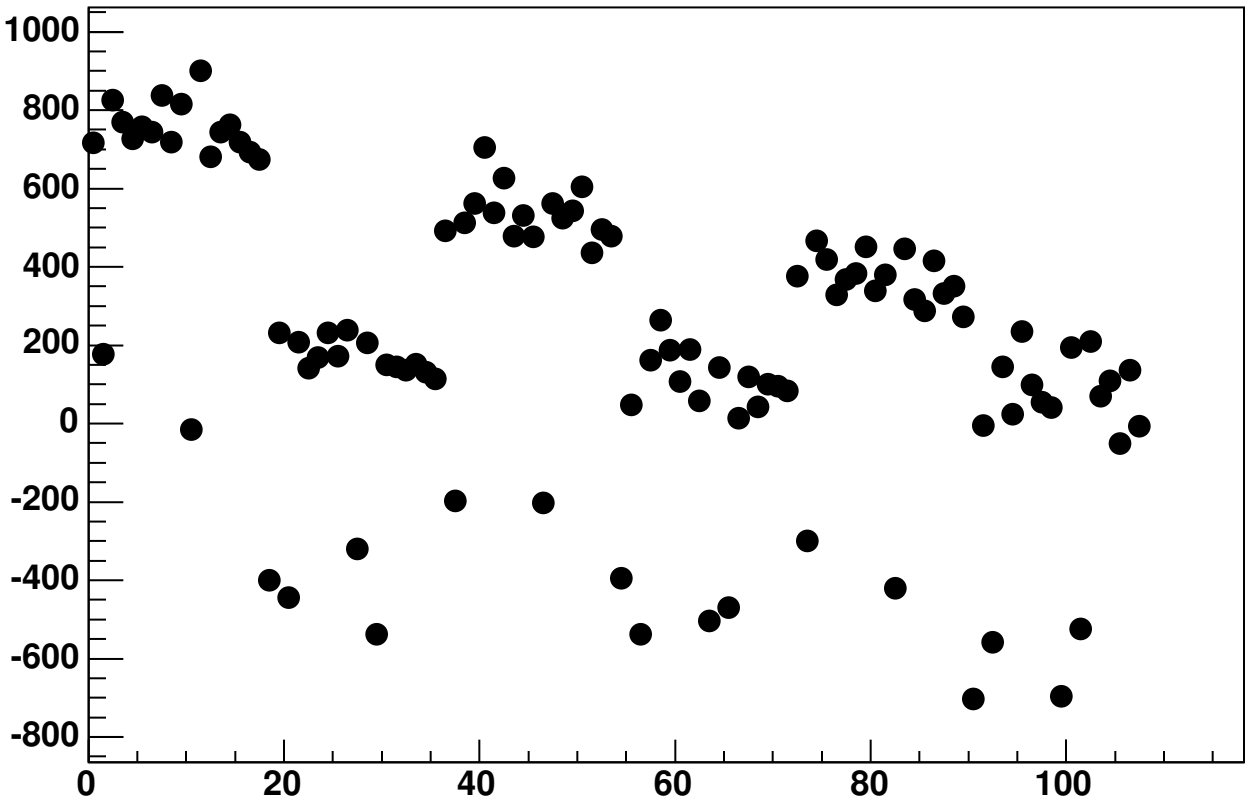
Enable 3, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



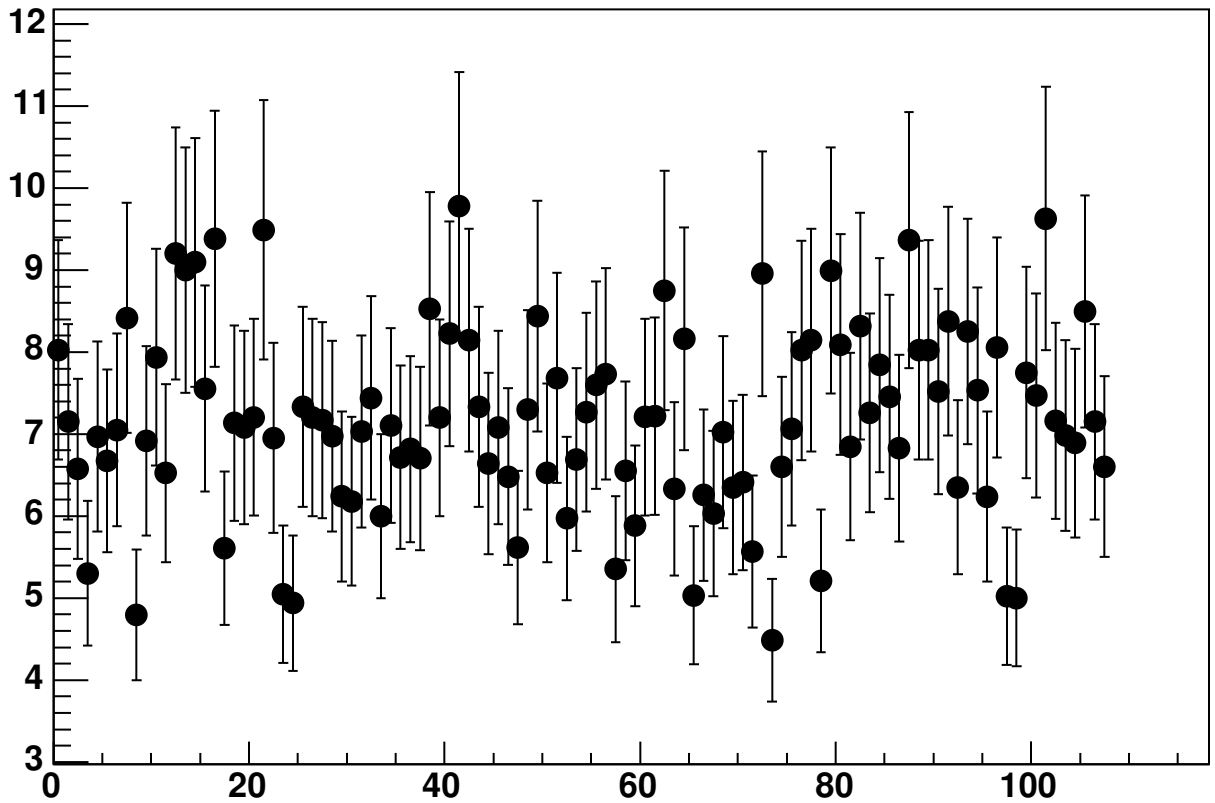
Enable 3, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



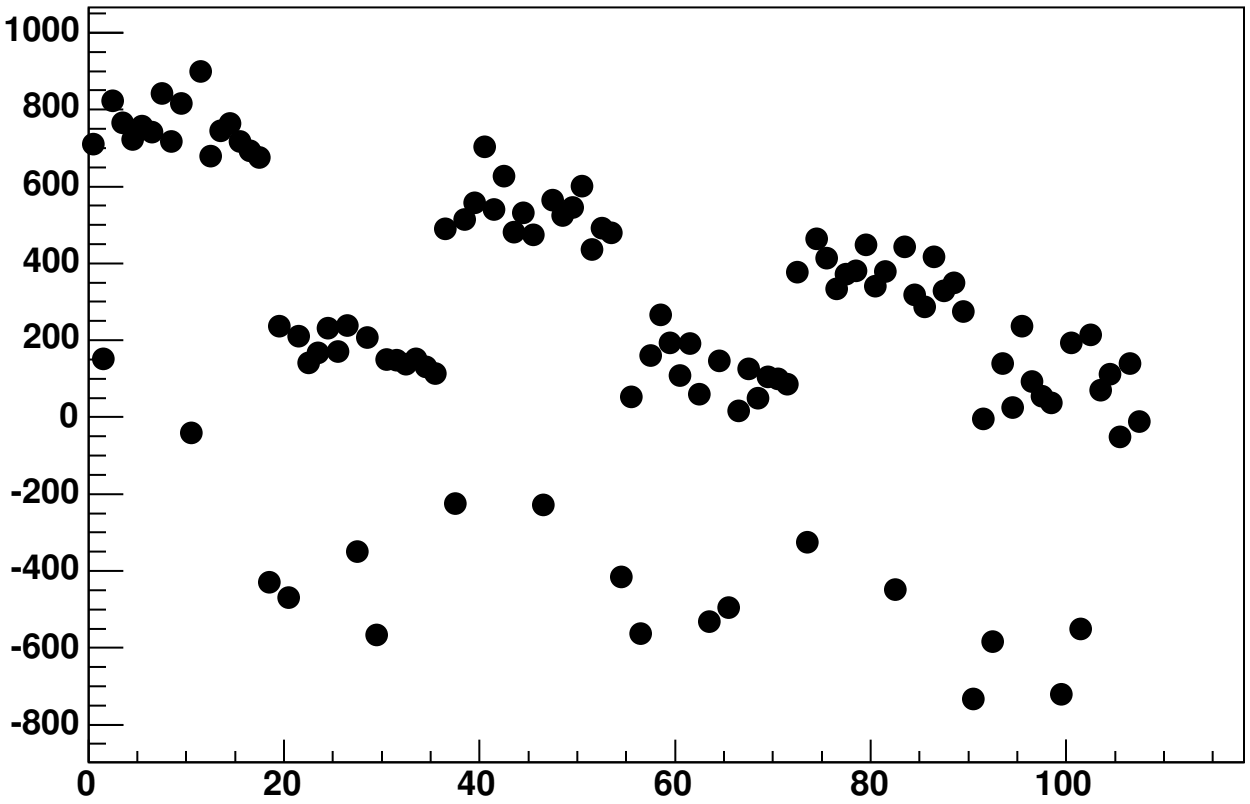
Enable 3, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



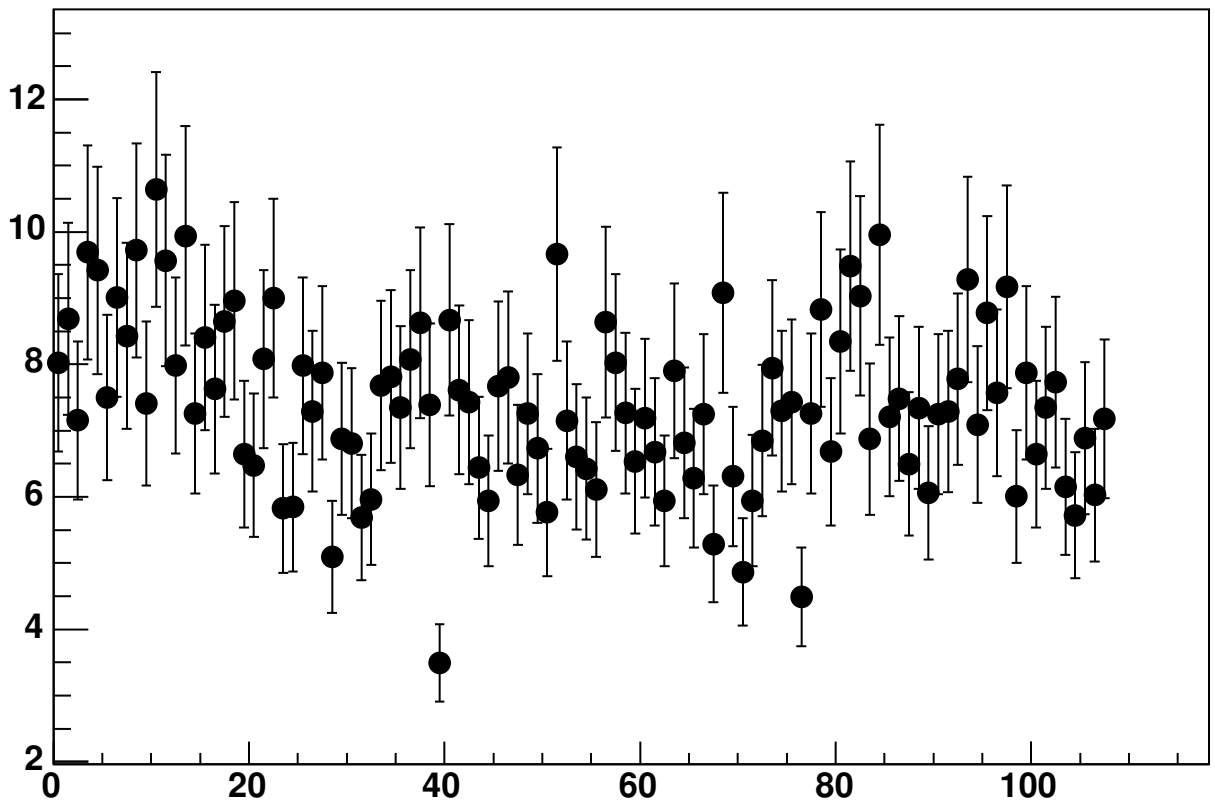
Enable 3, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



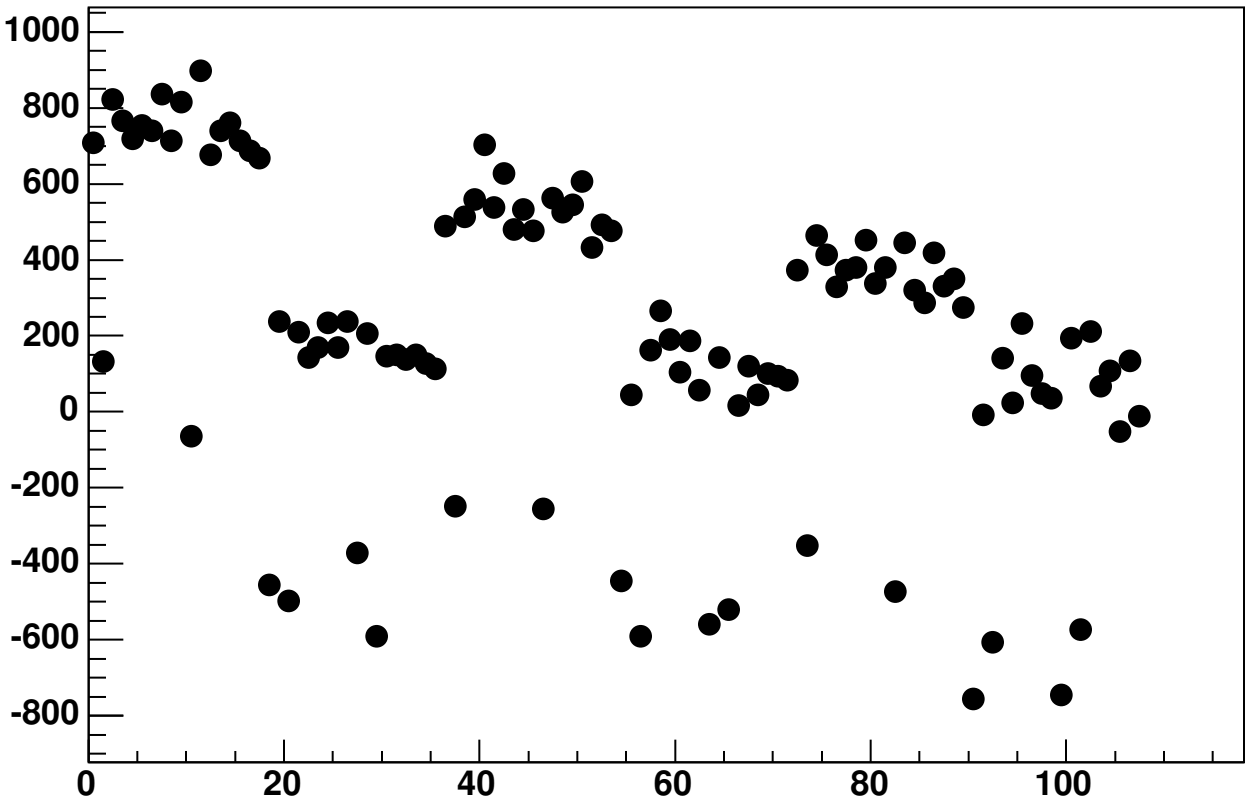
Enable 3, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



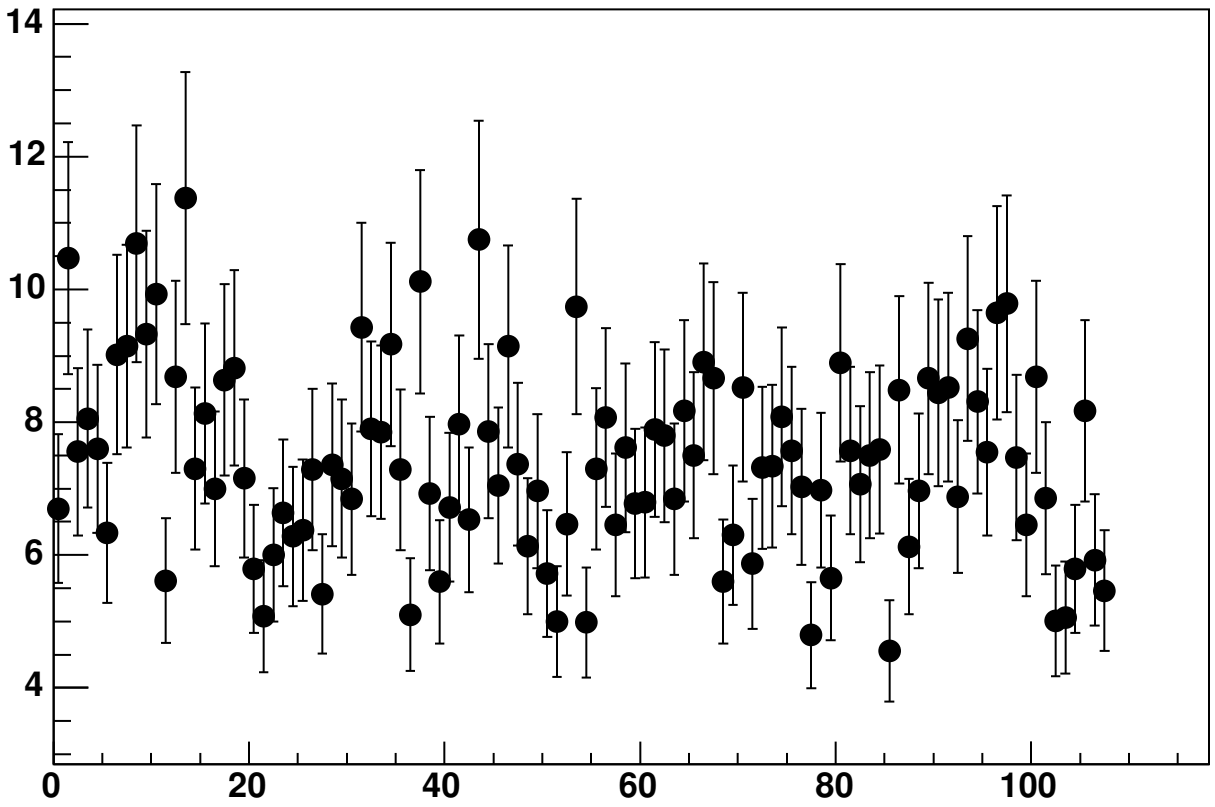
Enable 3, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



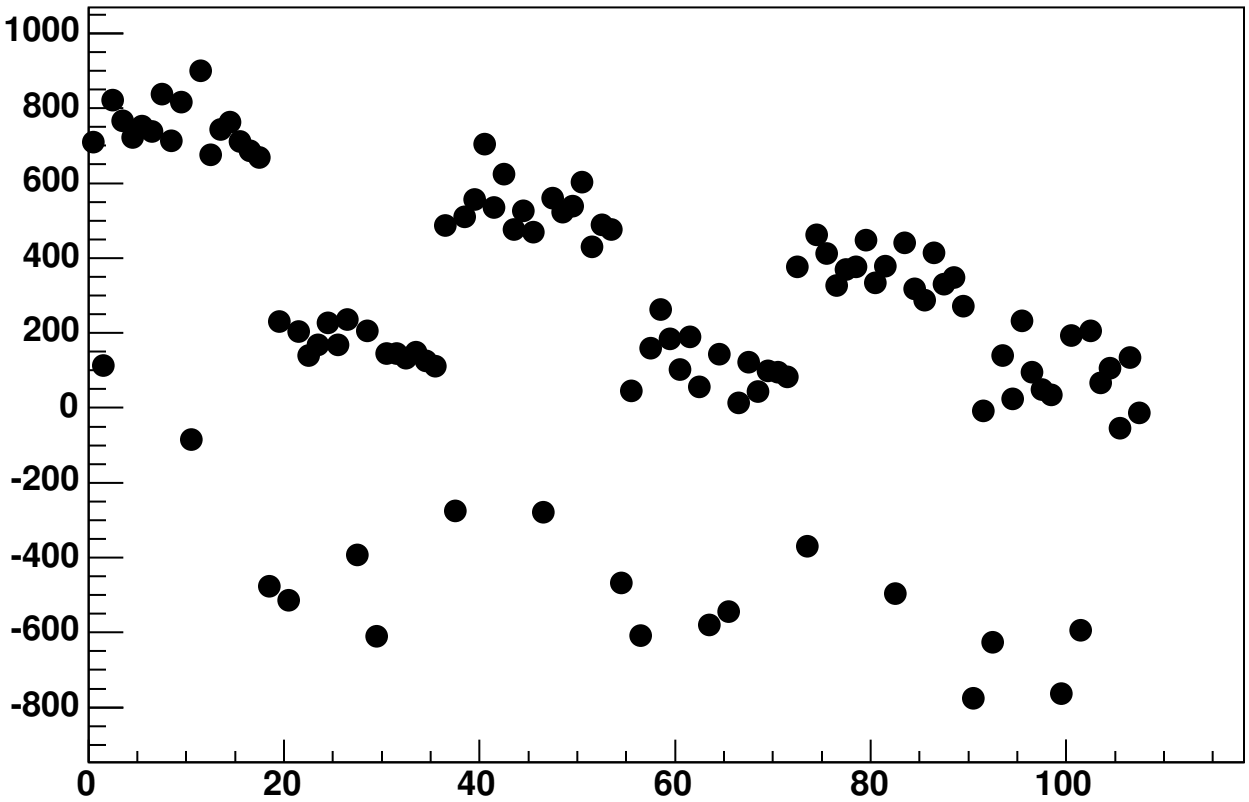
Enable 3, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



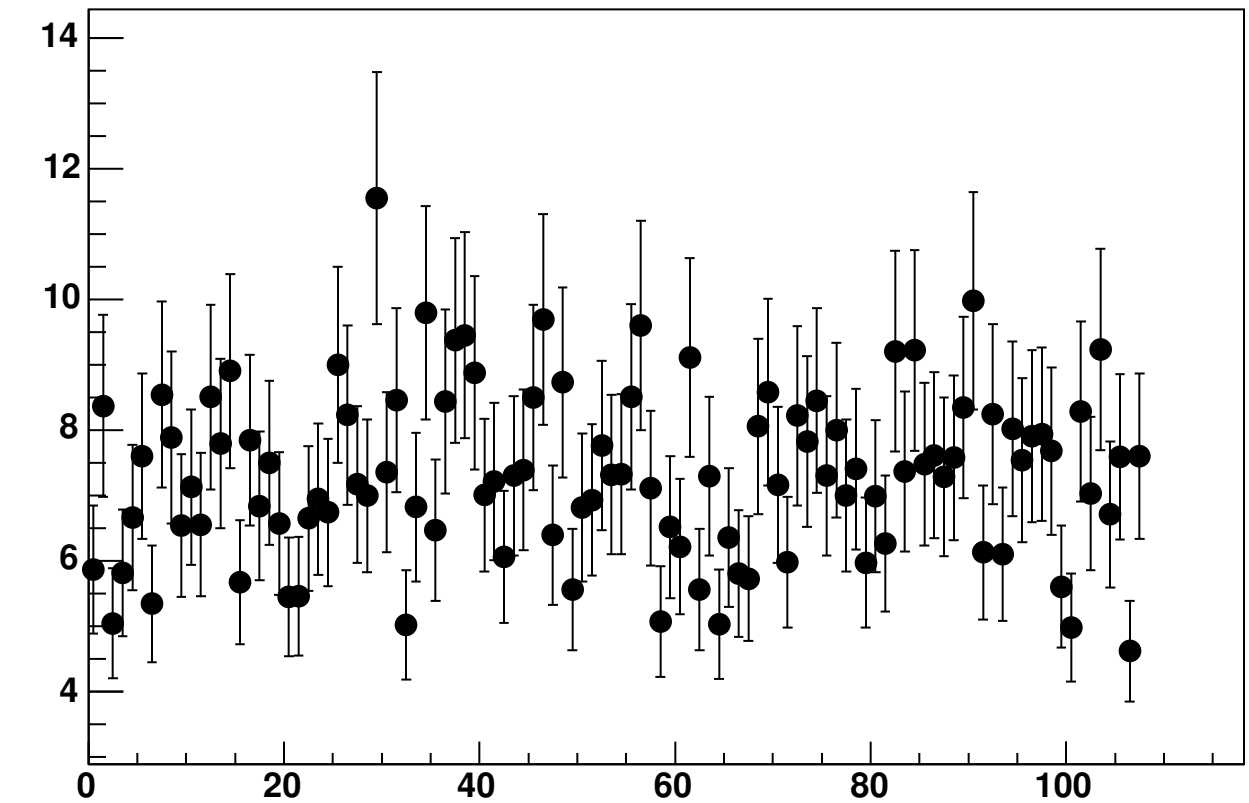
Enable 3, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



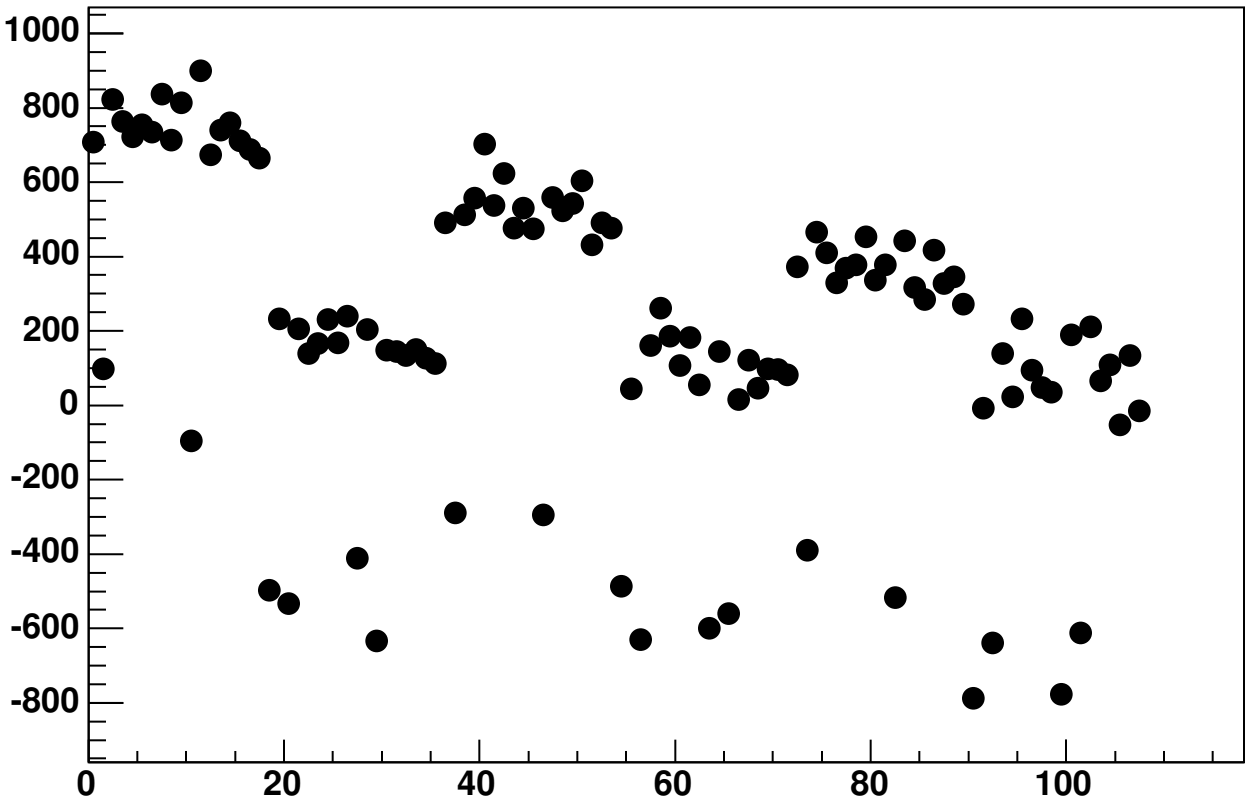
Enable 3, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



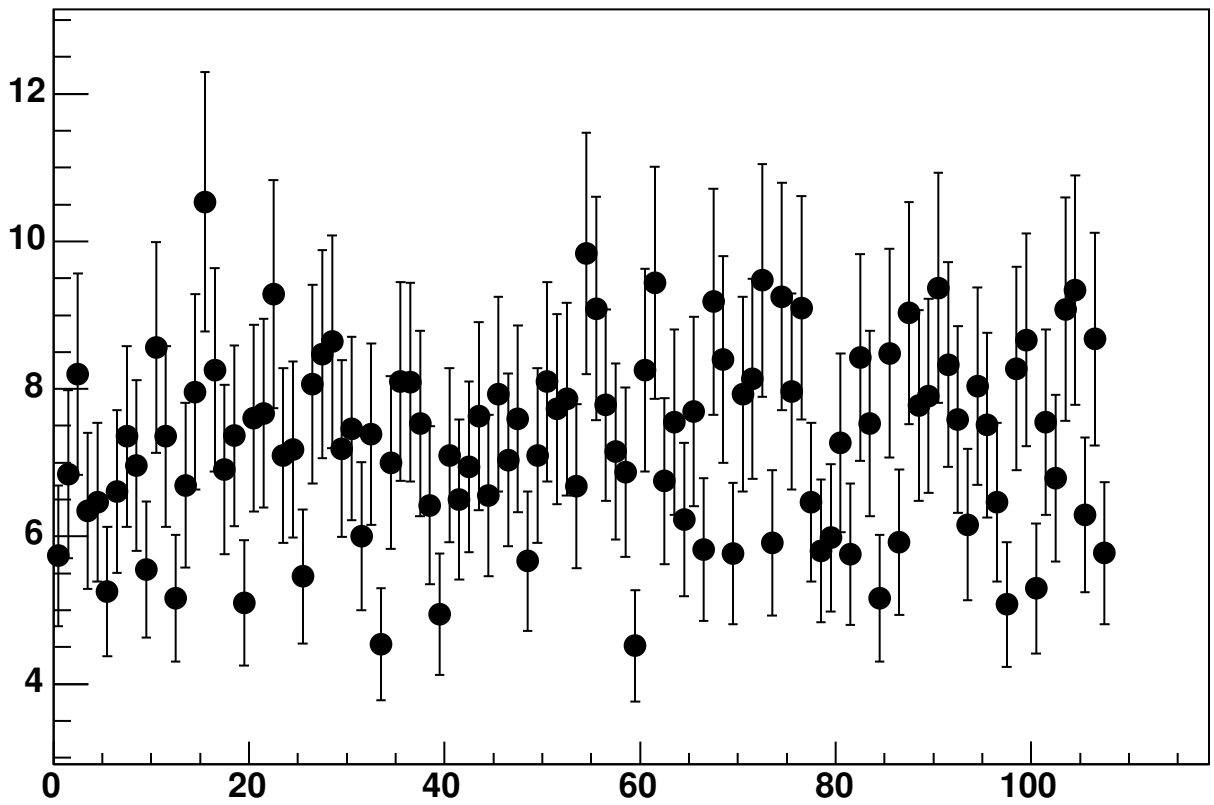
Enable 3, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



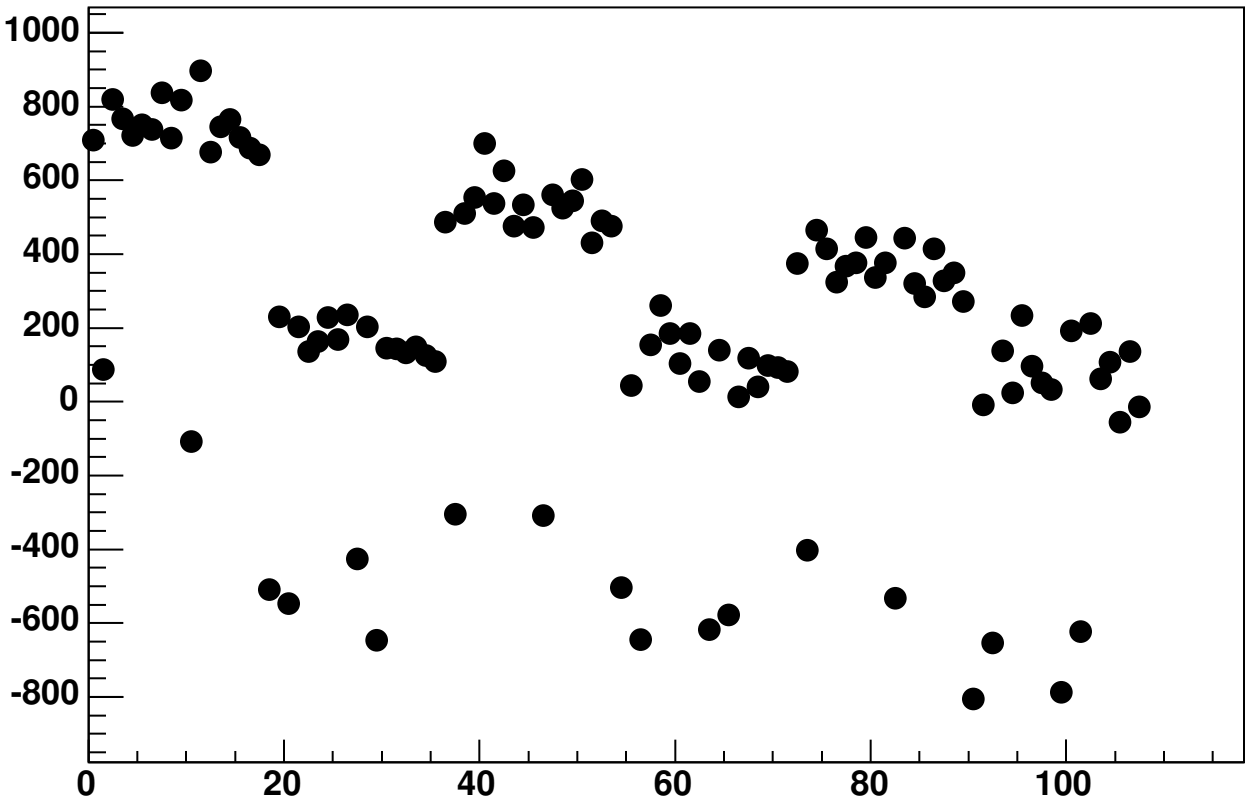
Enable 3, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



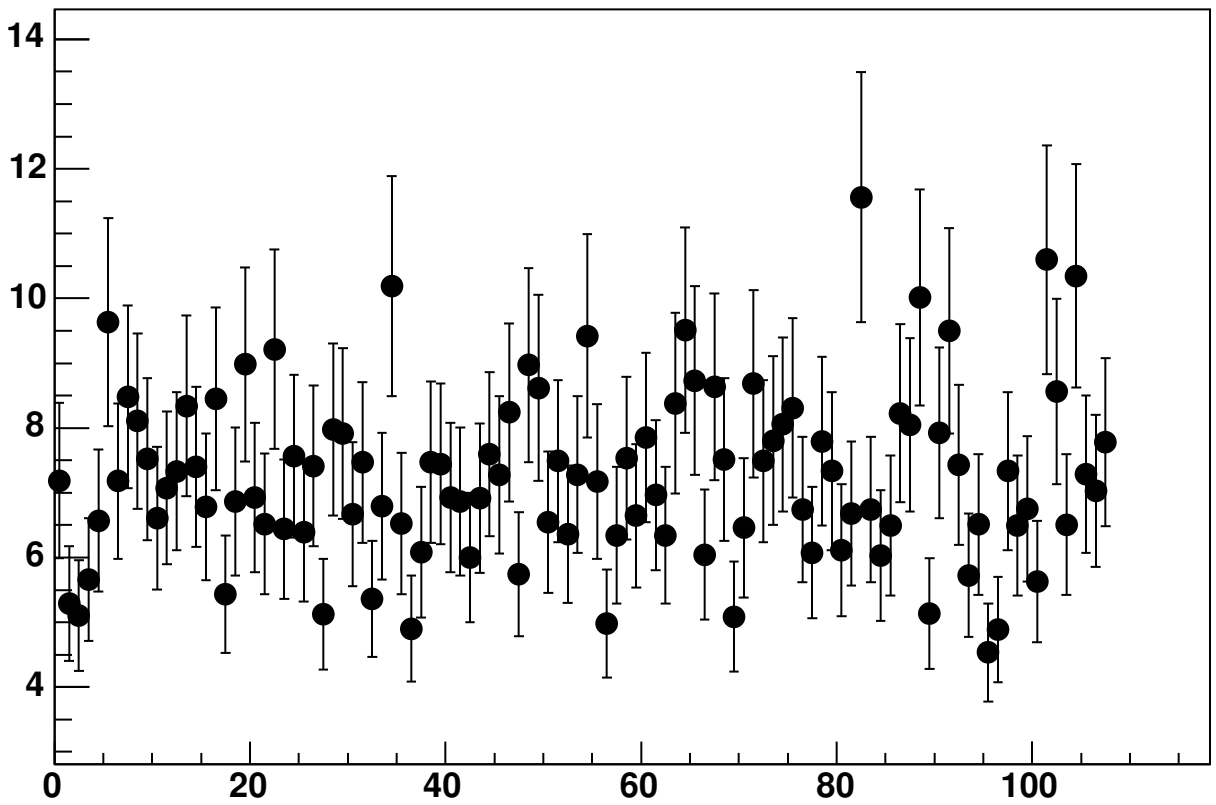
Enable 3, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan



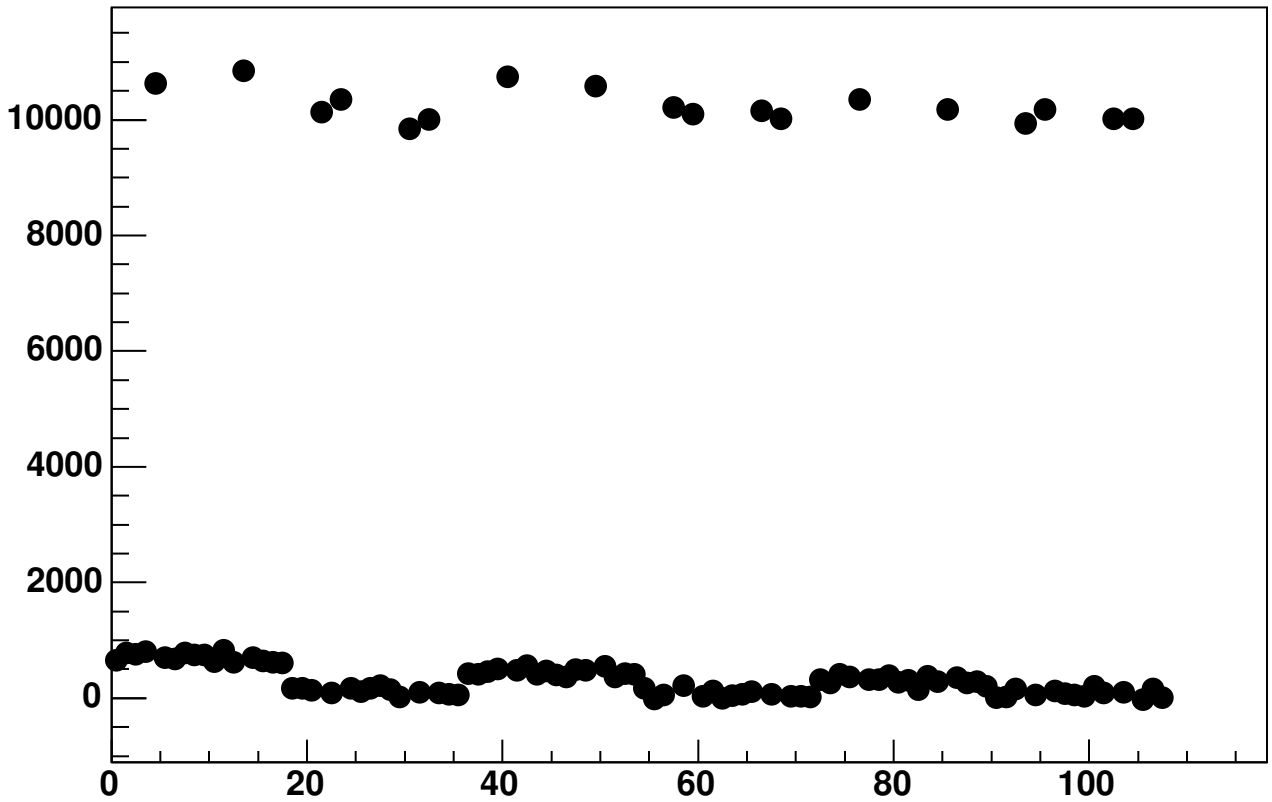
Enable 3, DAC=4000, Hold=245, ADC Mean vs 18*Chip+Chan



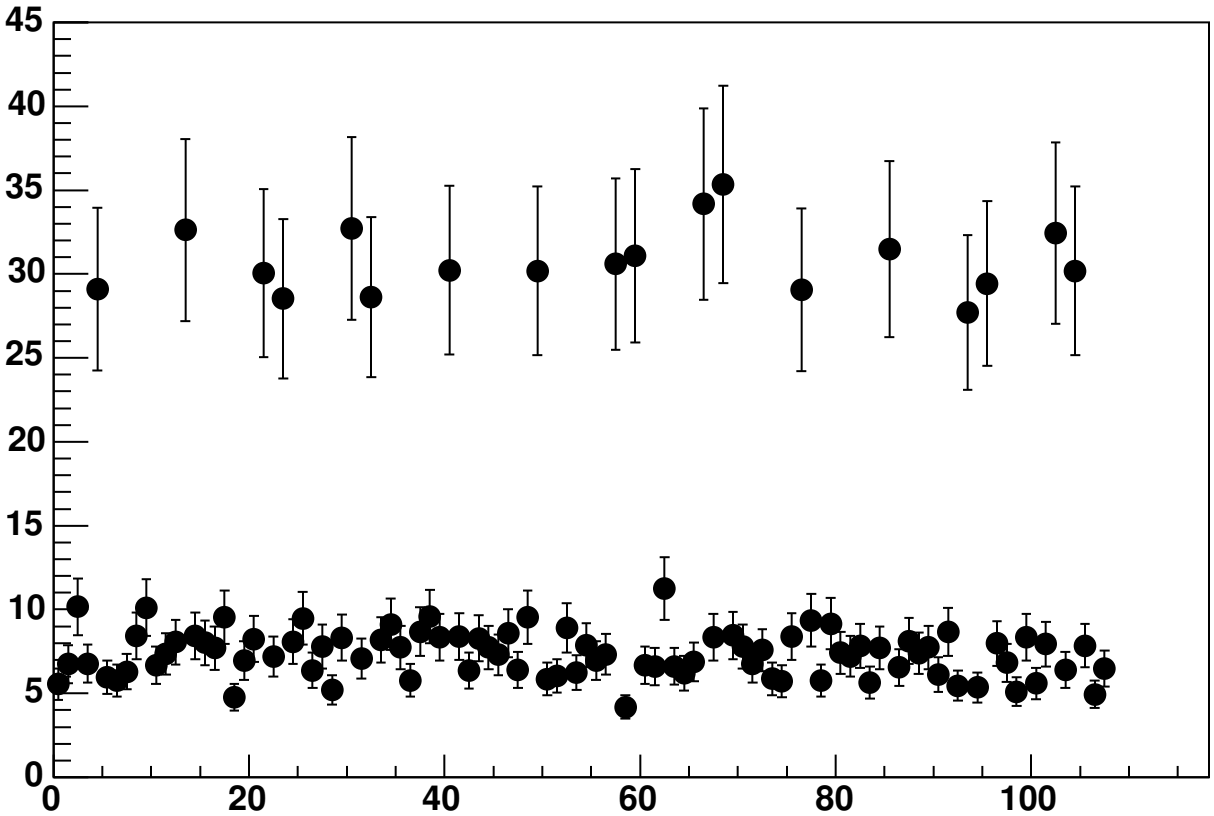
Enable 3, DAC=4000, Hold=245, ADC Noise vs 18*Chip+Chan



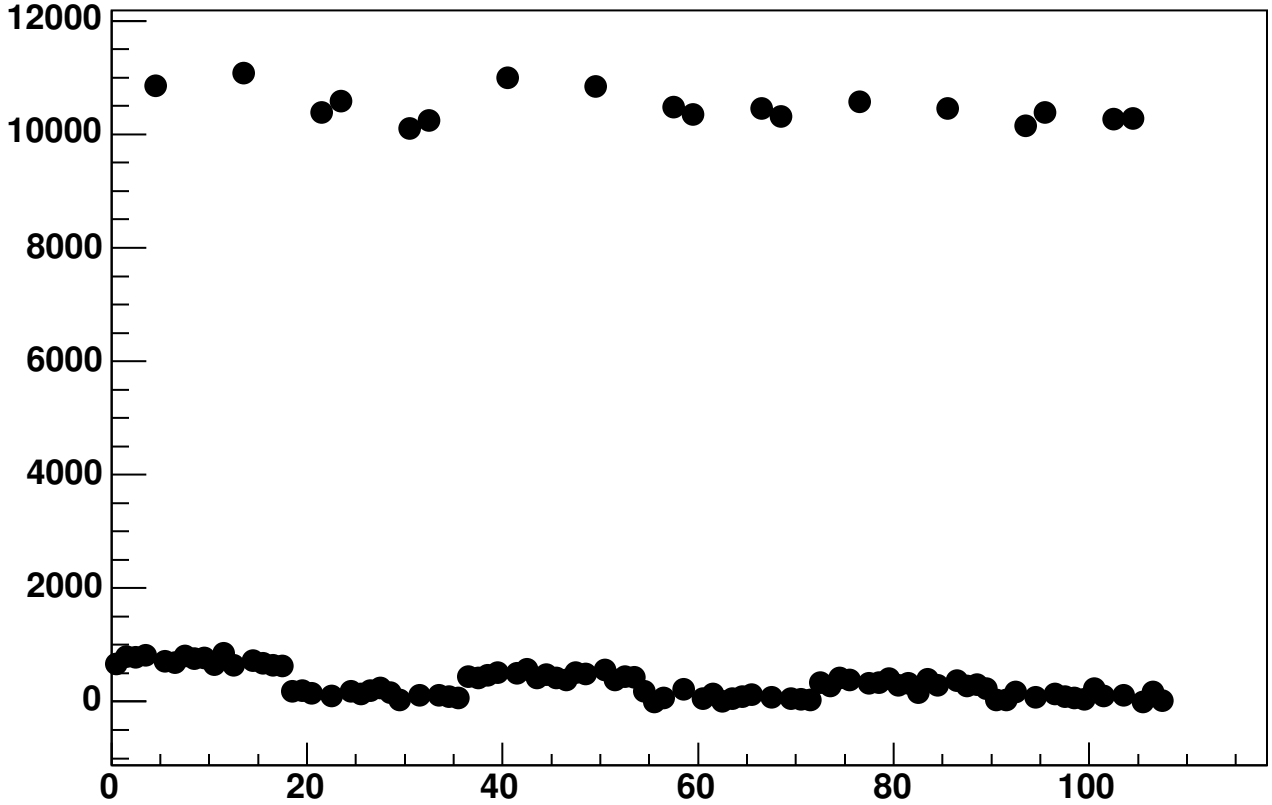
Enable 4, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



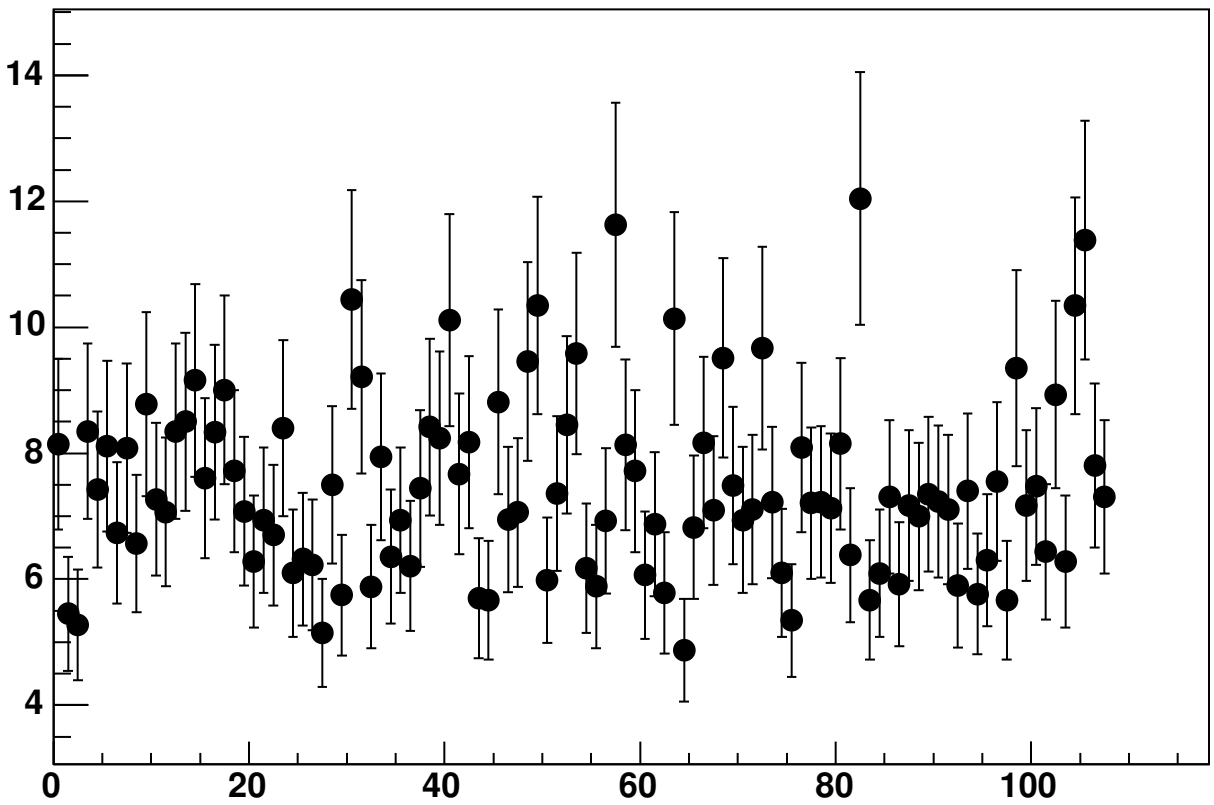
Enable 4, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



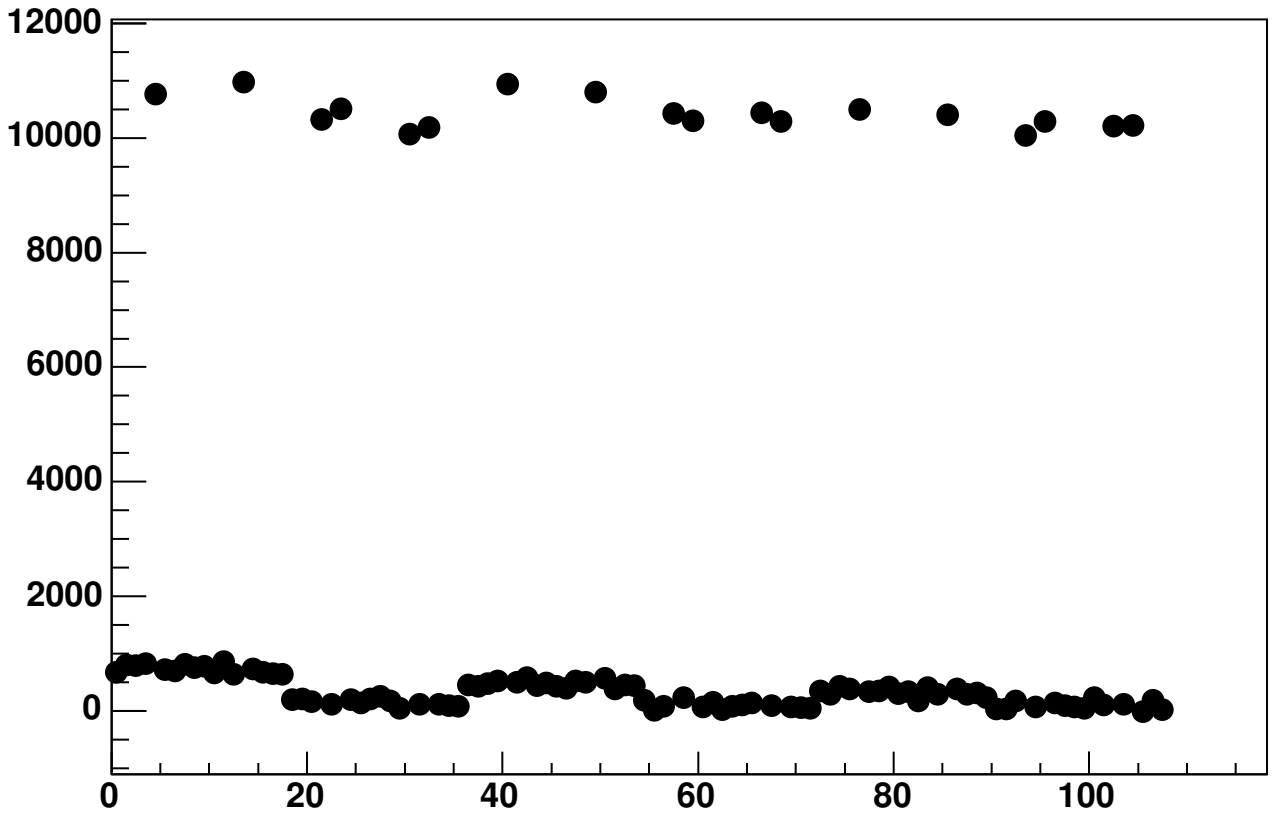
Enable 4, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



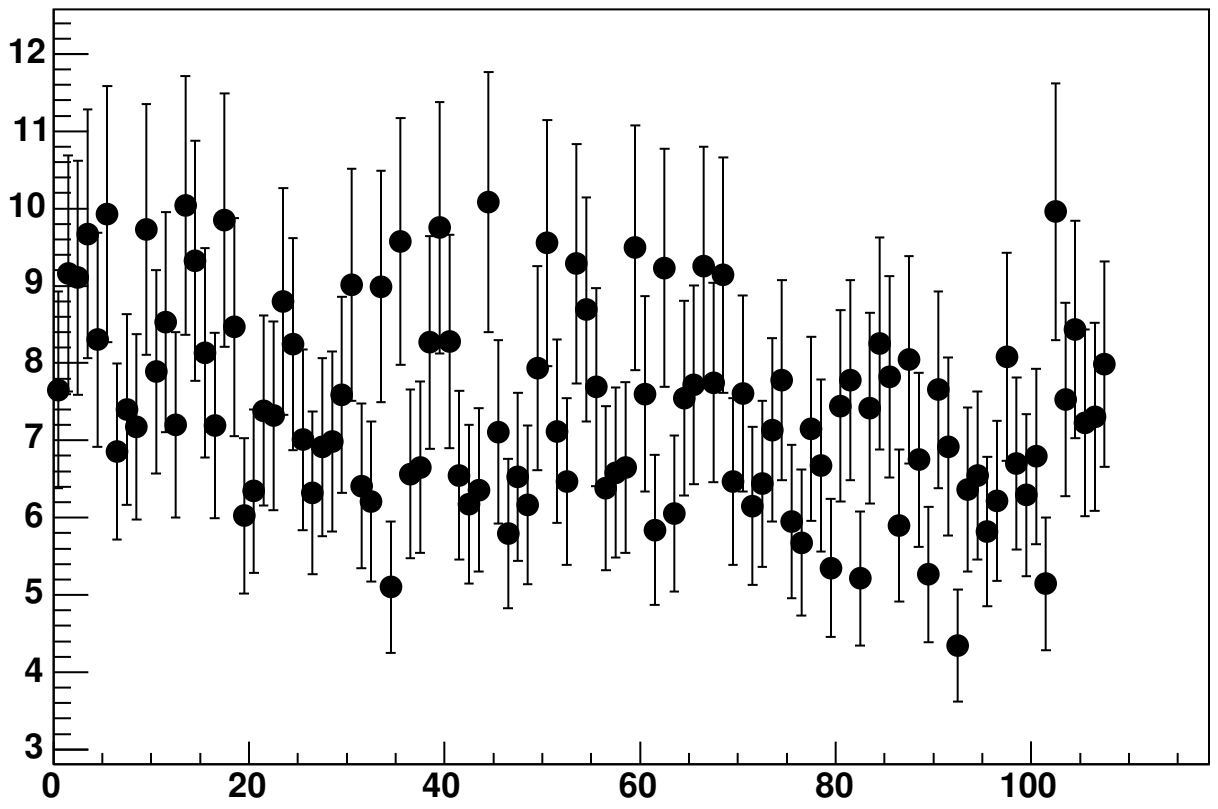
Enable 4, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



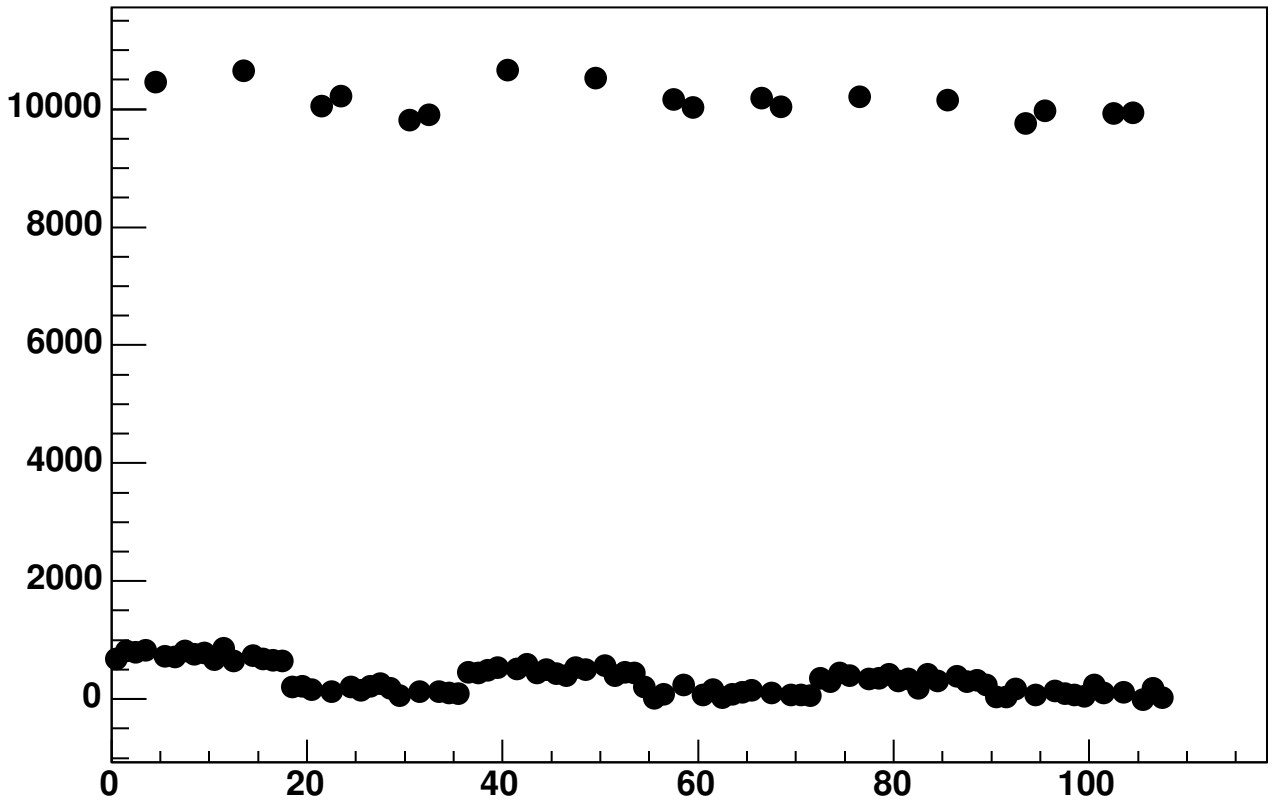
Enable 4, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



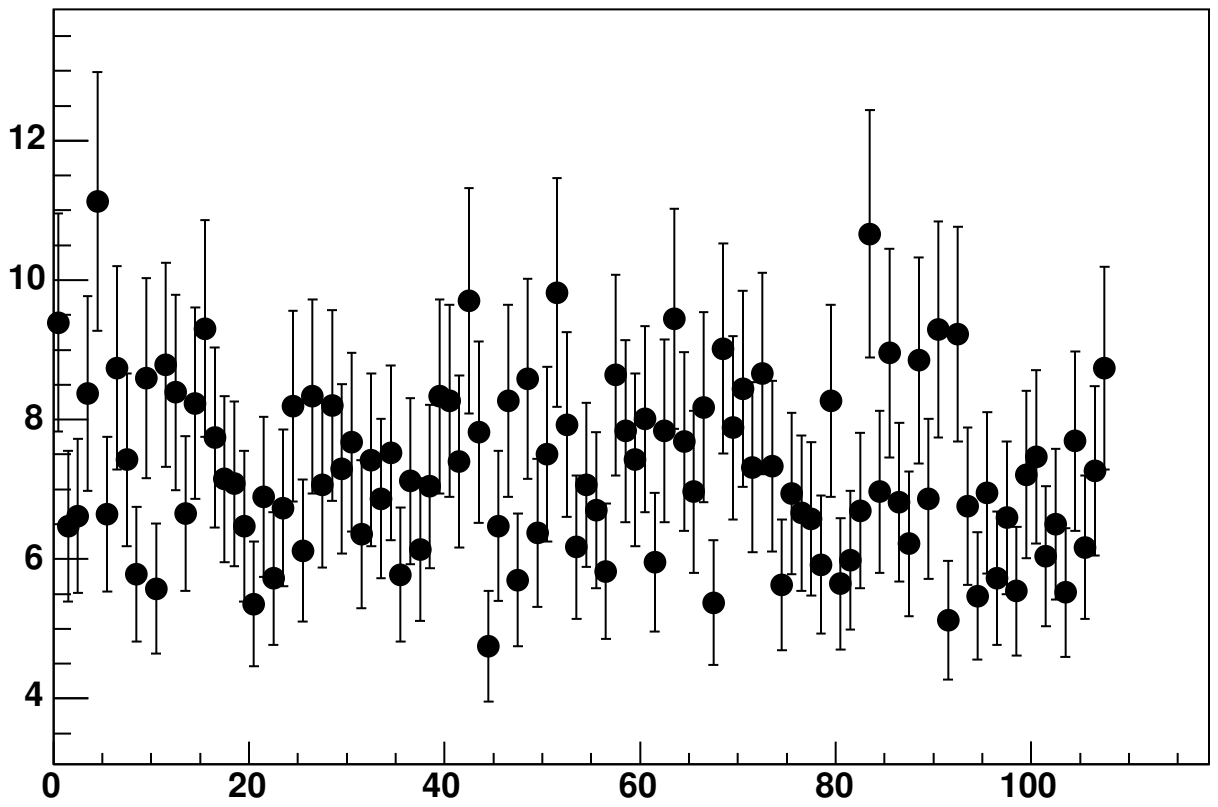
Enable 4, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



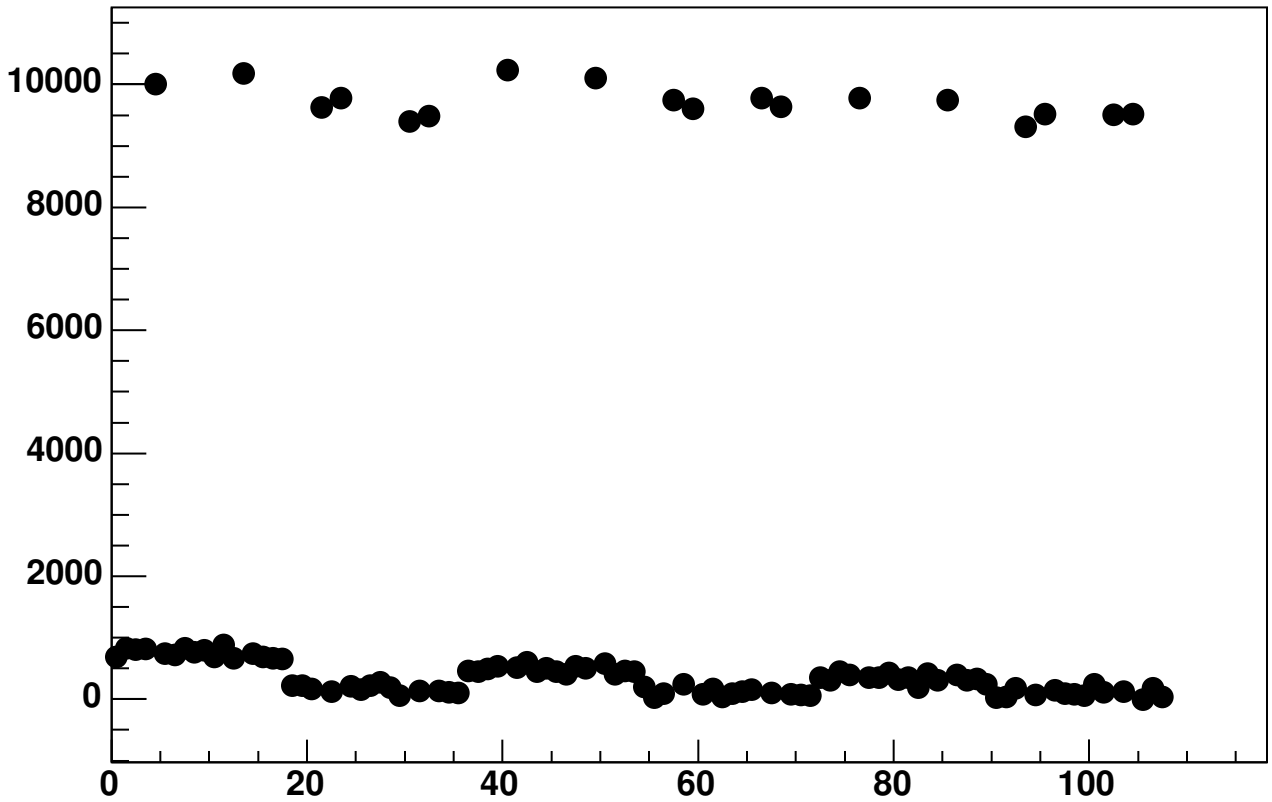
Enable 4, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



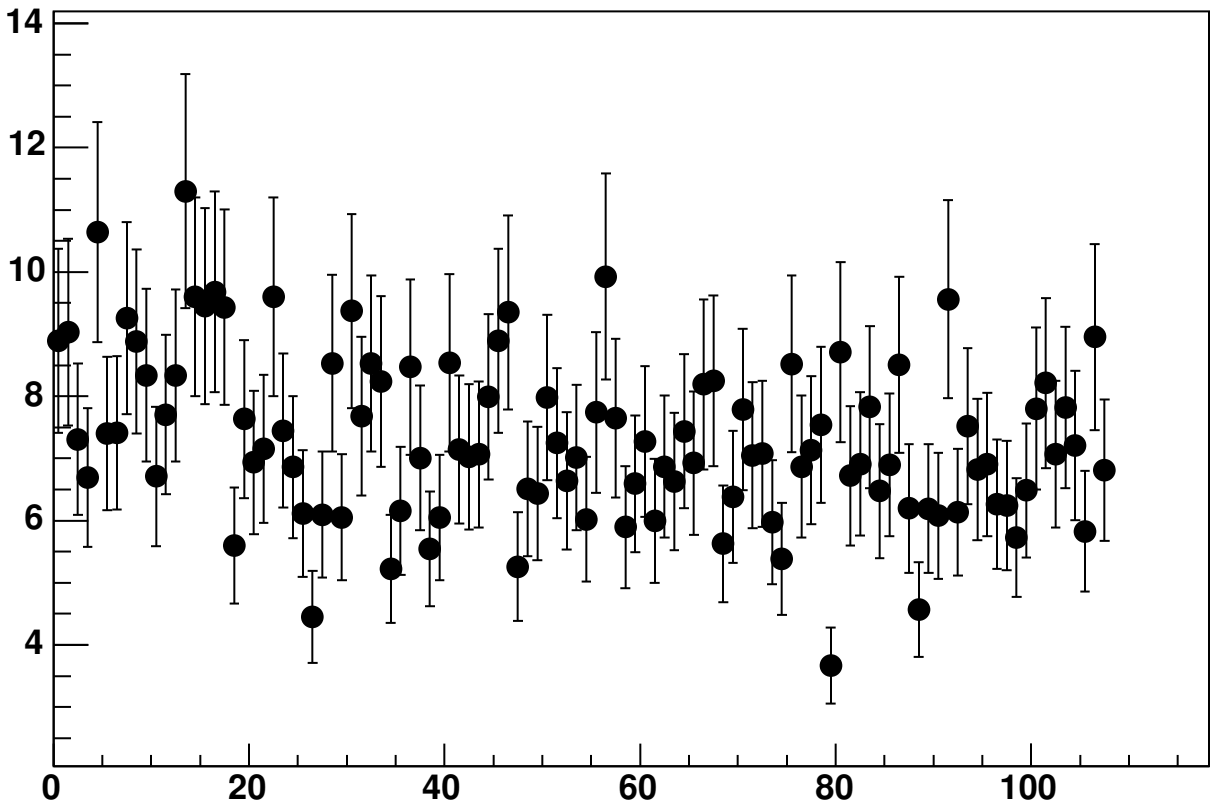
Enable 4, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



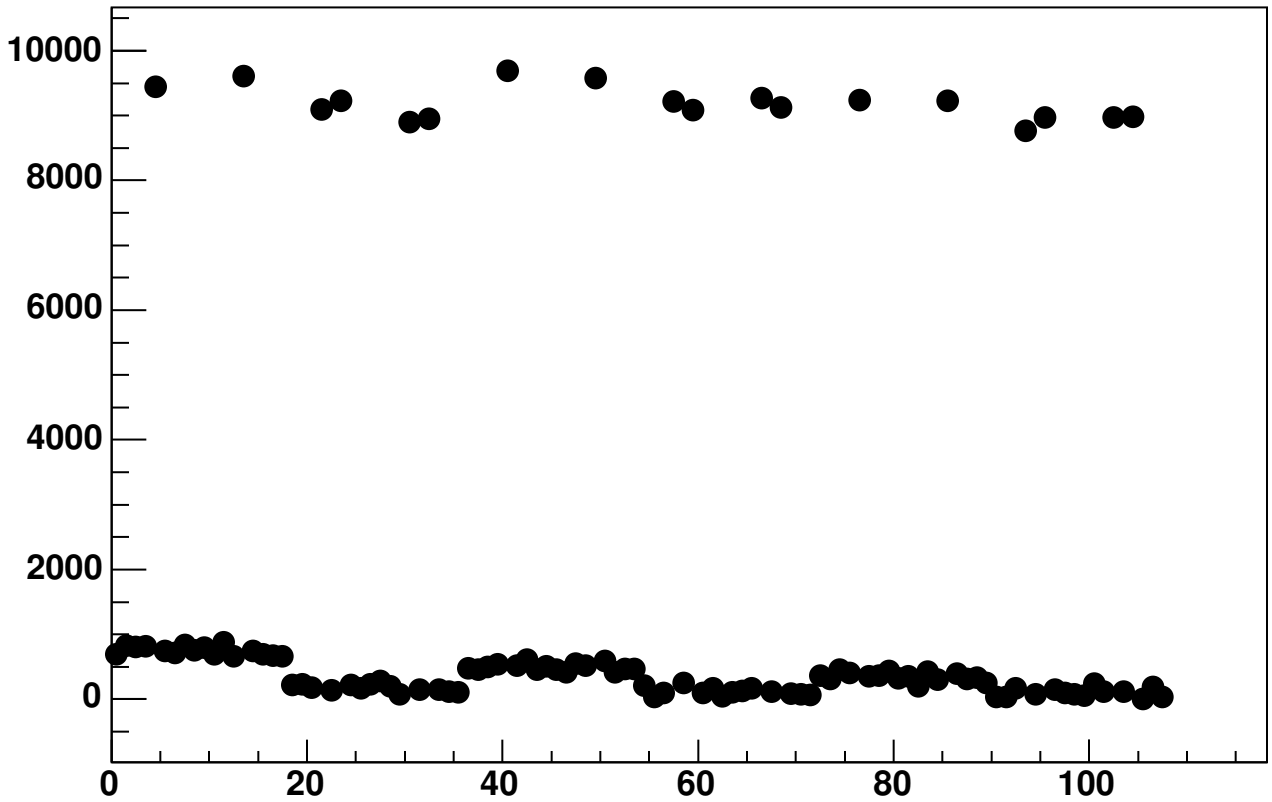
Enable 4, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



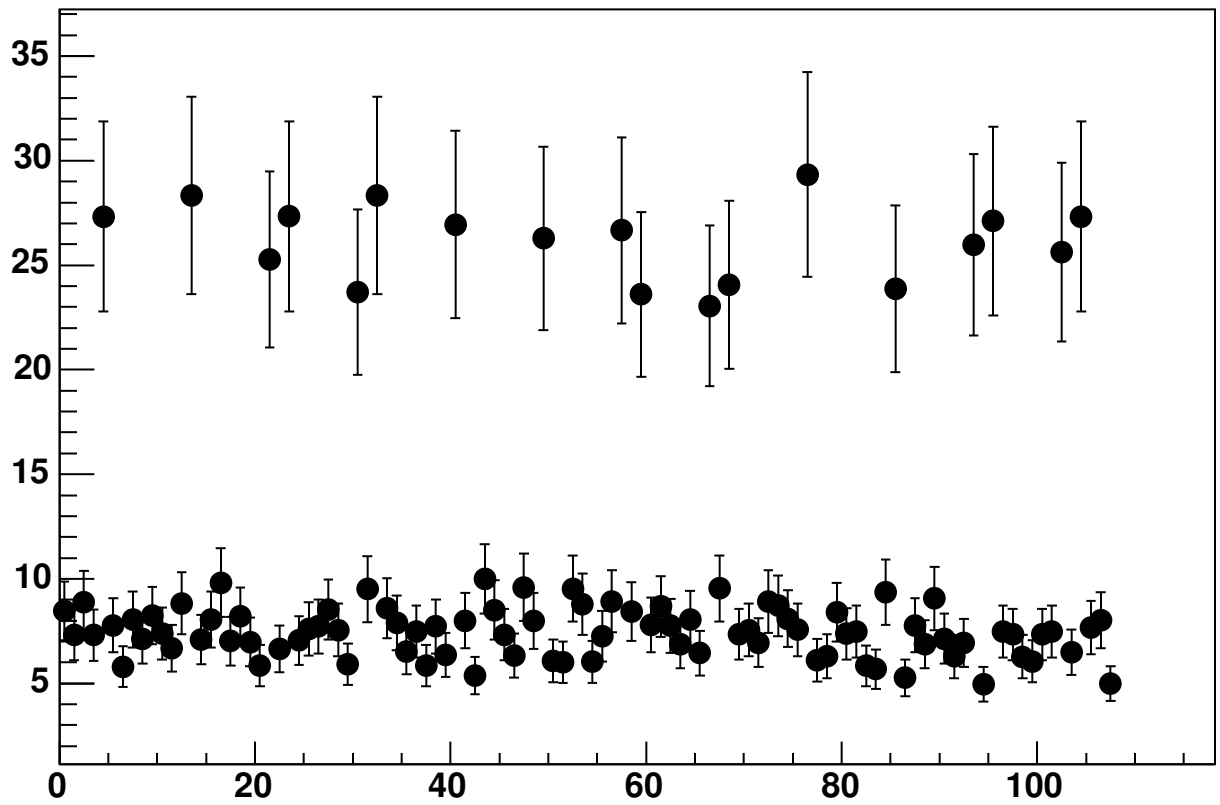
Enable 4, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



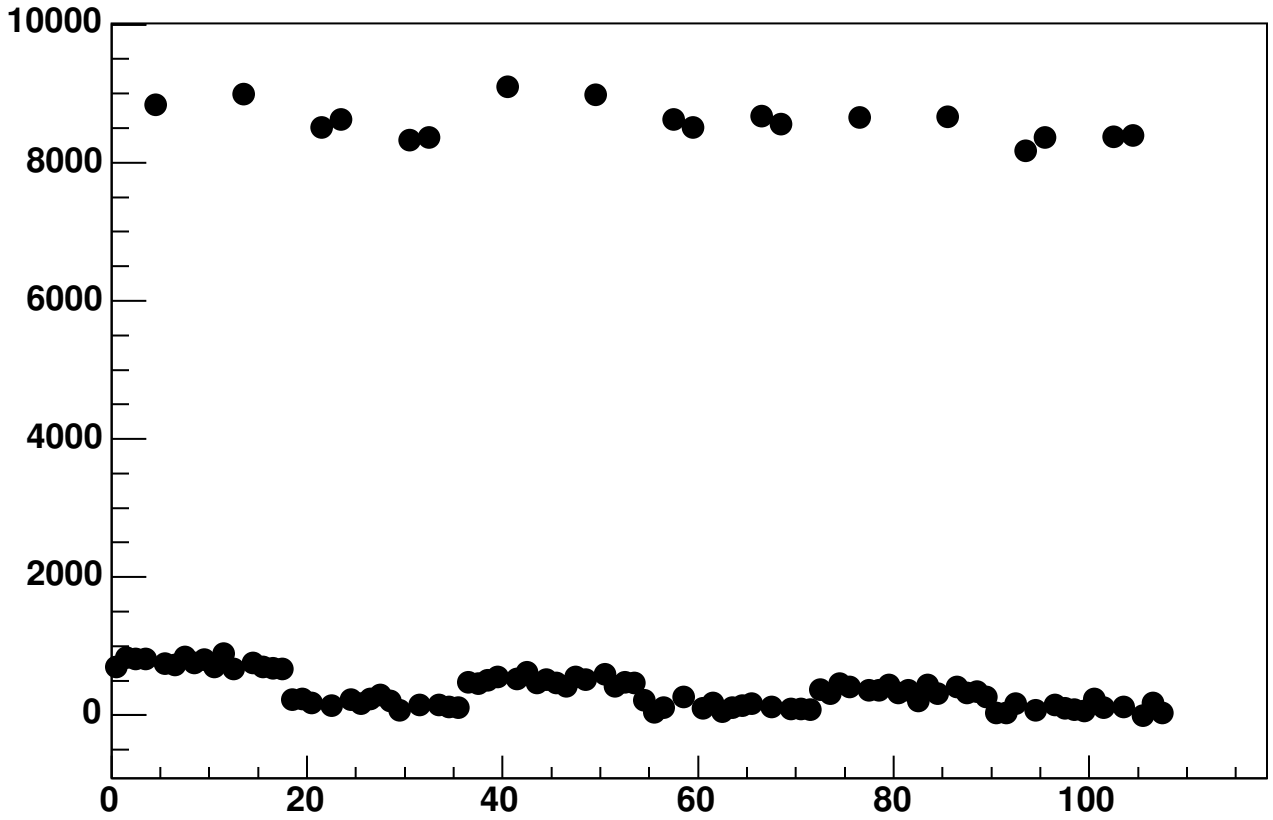
Enable 4, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



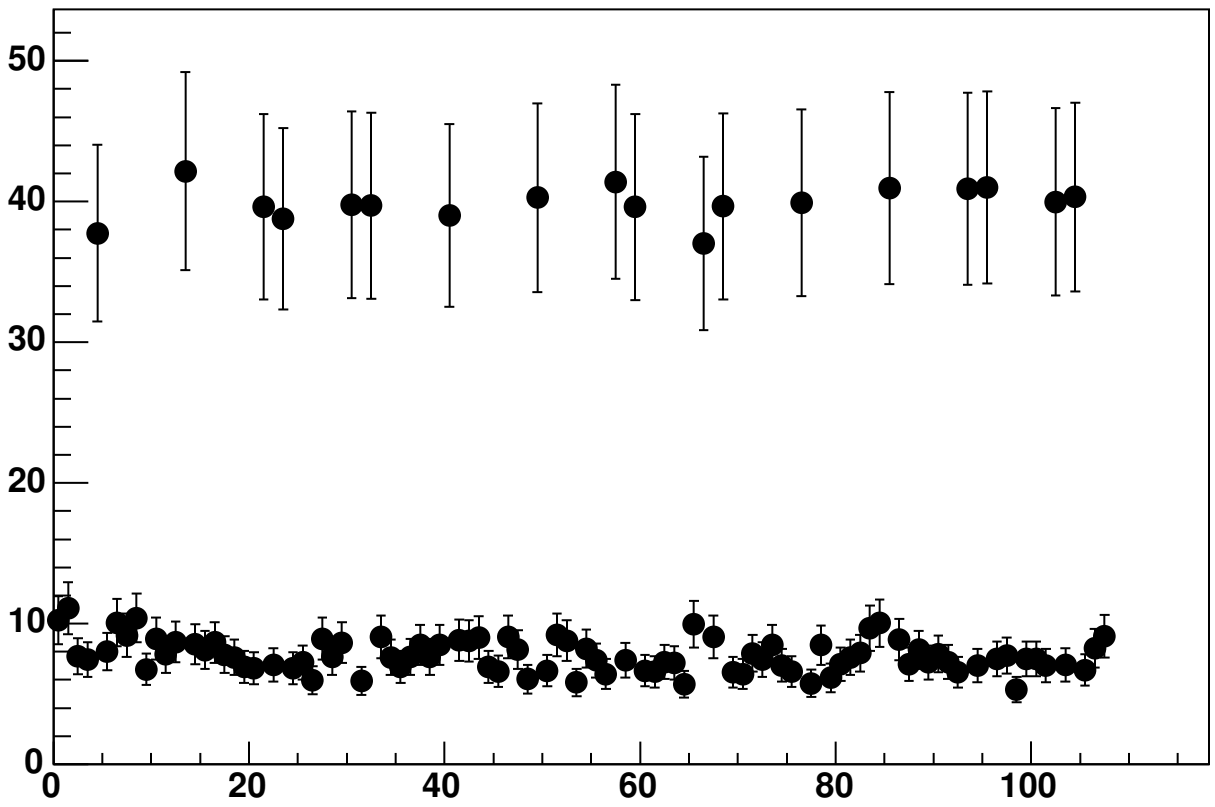
Enable 4, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



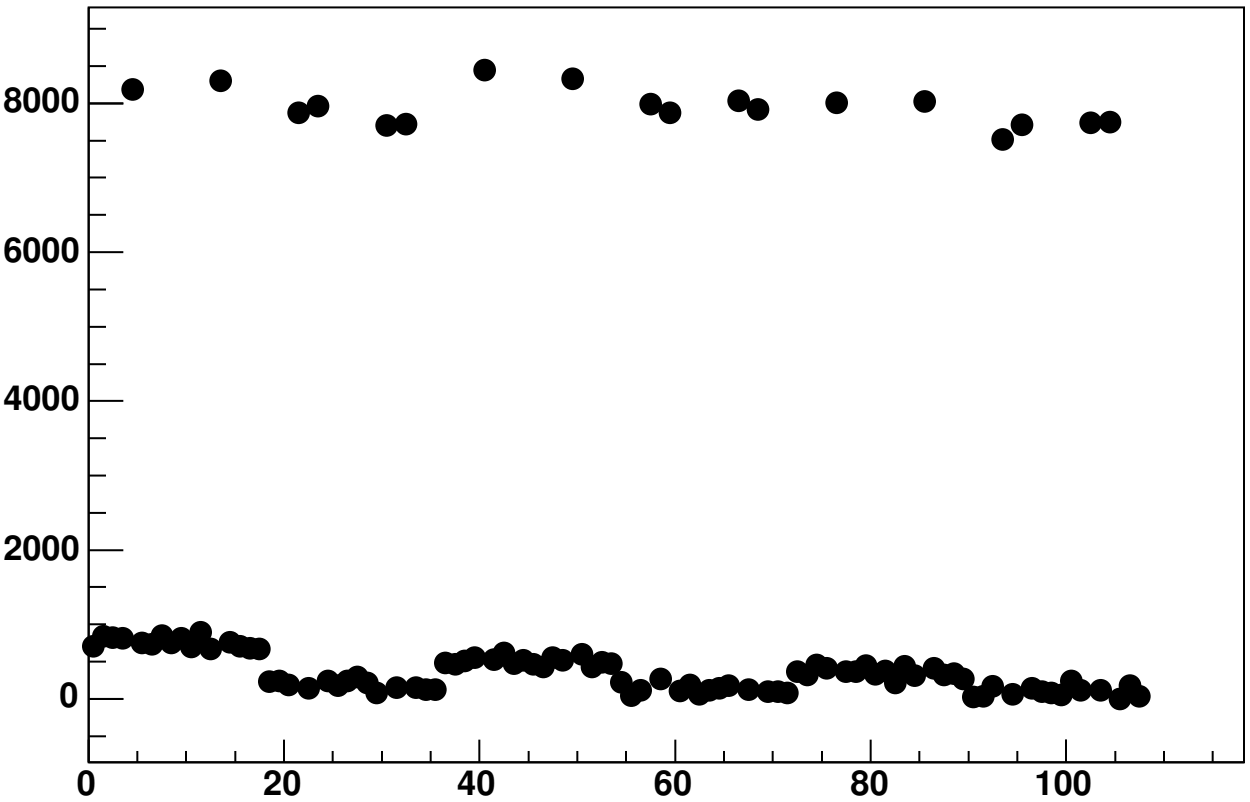
Enable 4, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



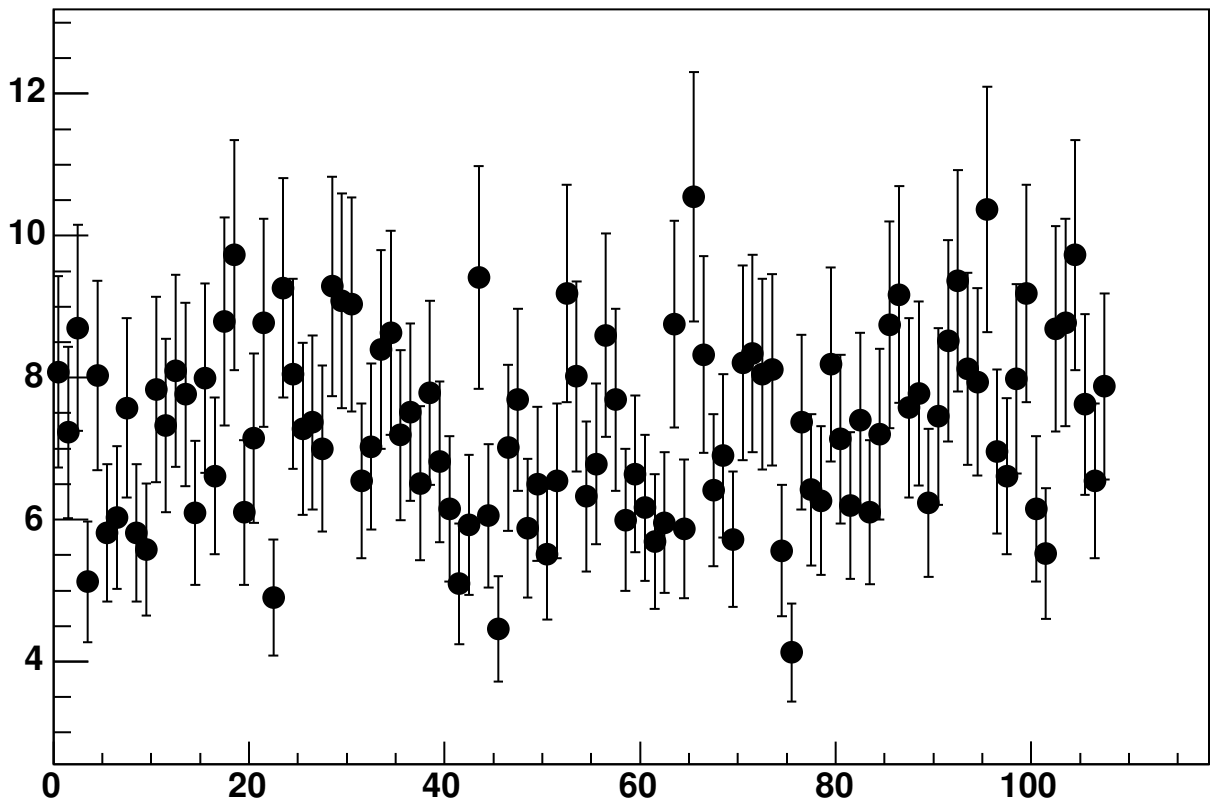
Enable 4, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



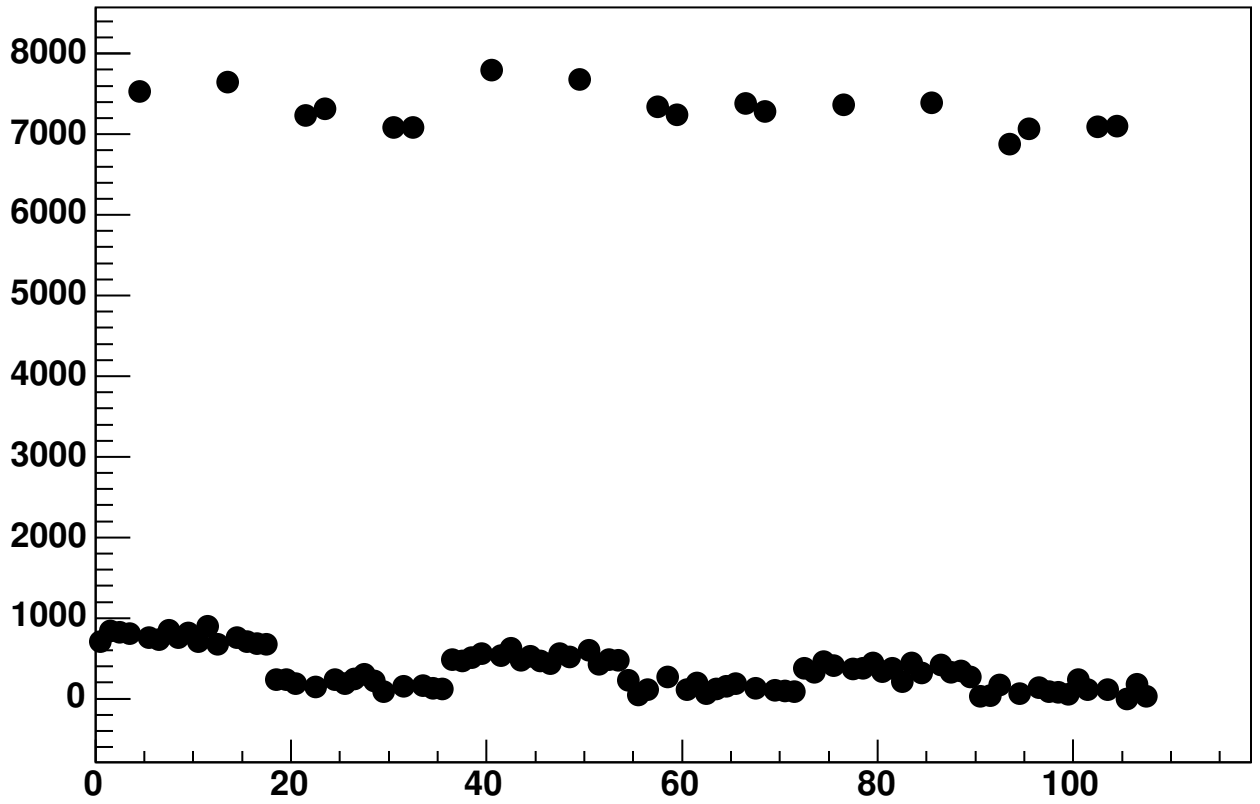
Enable 4, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



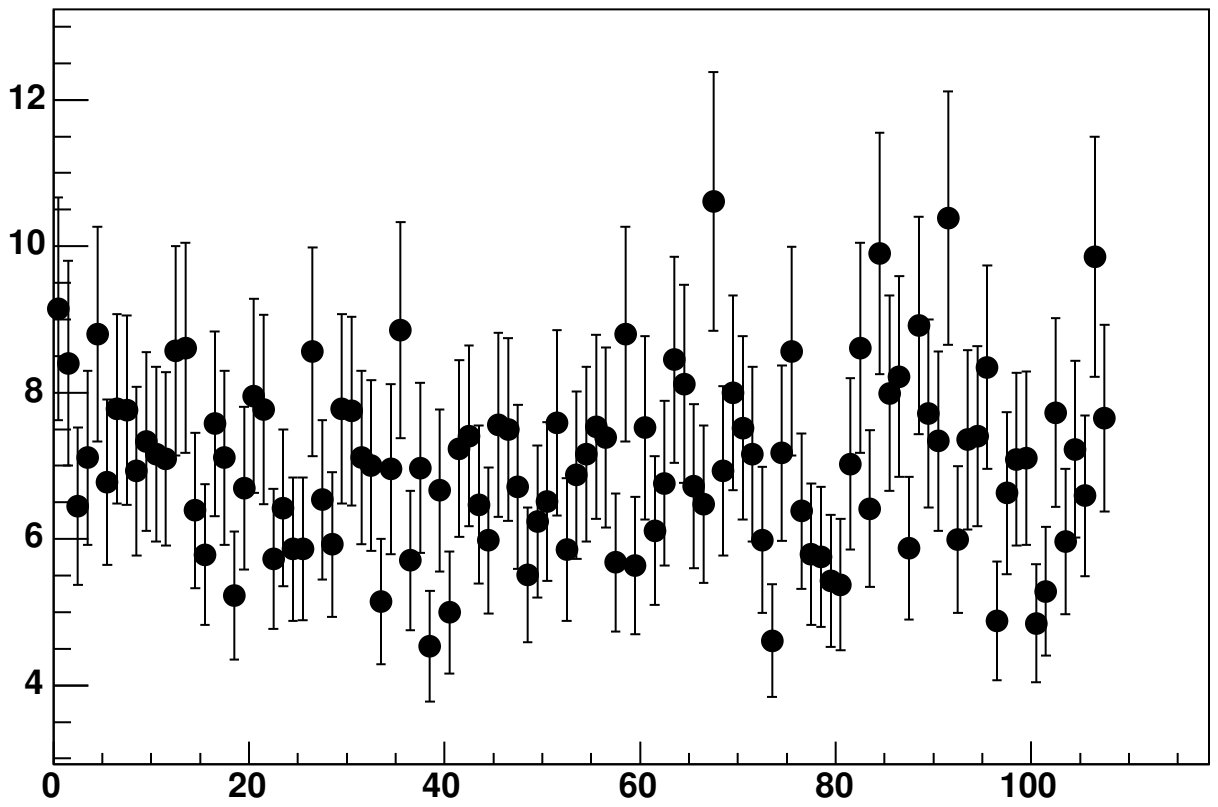
Enable 4, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



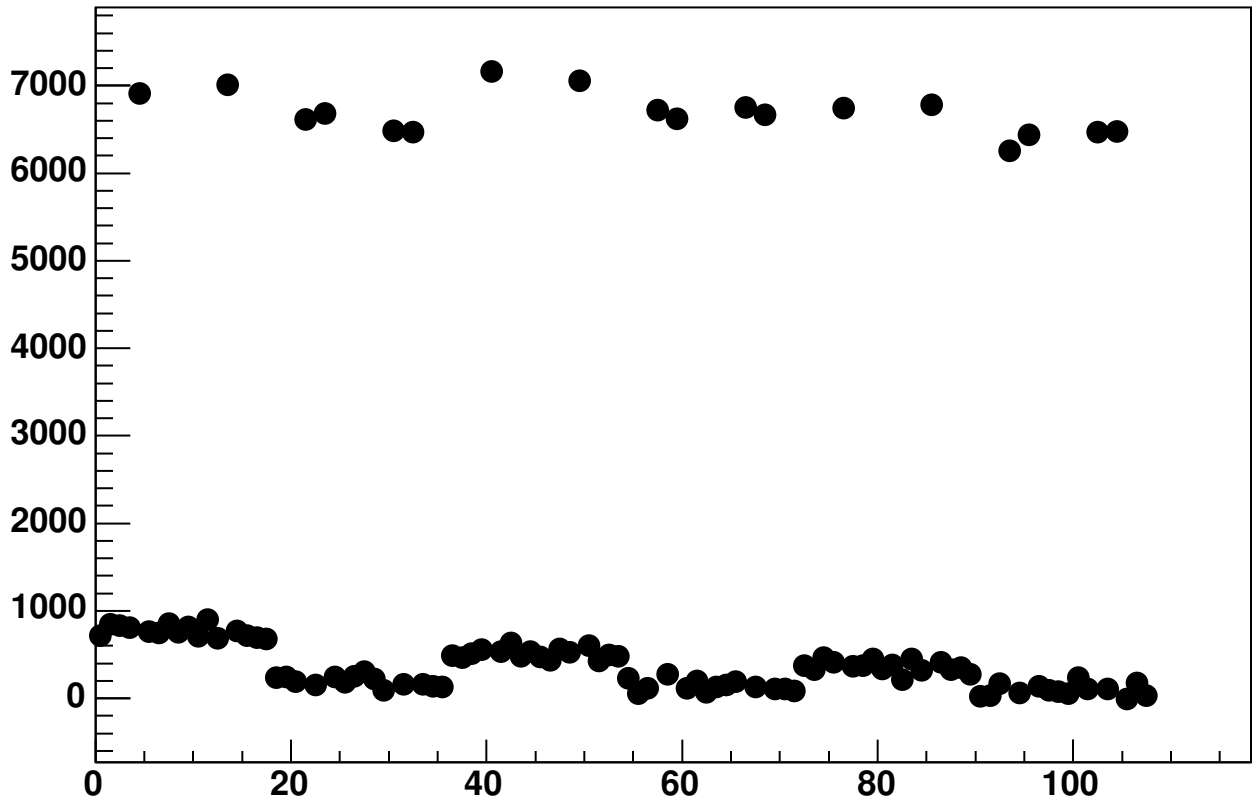
Enable 4, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



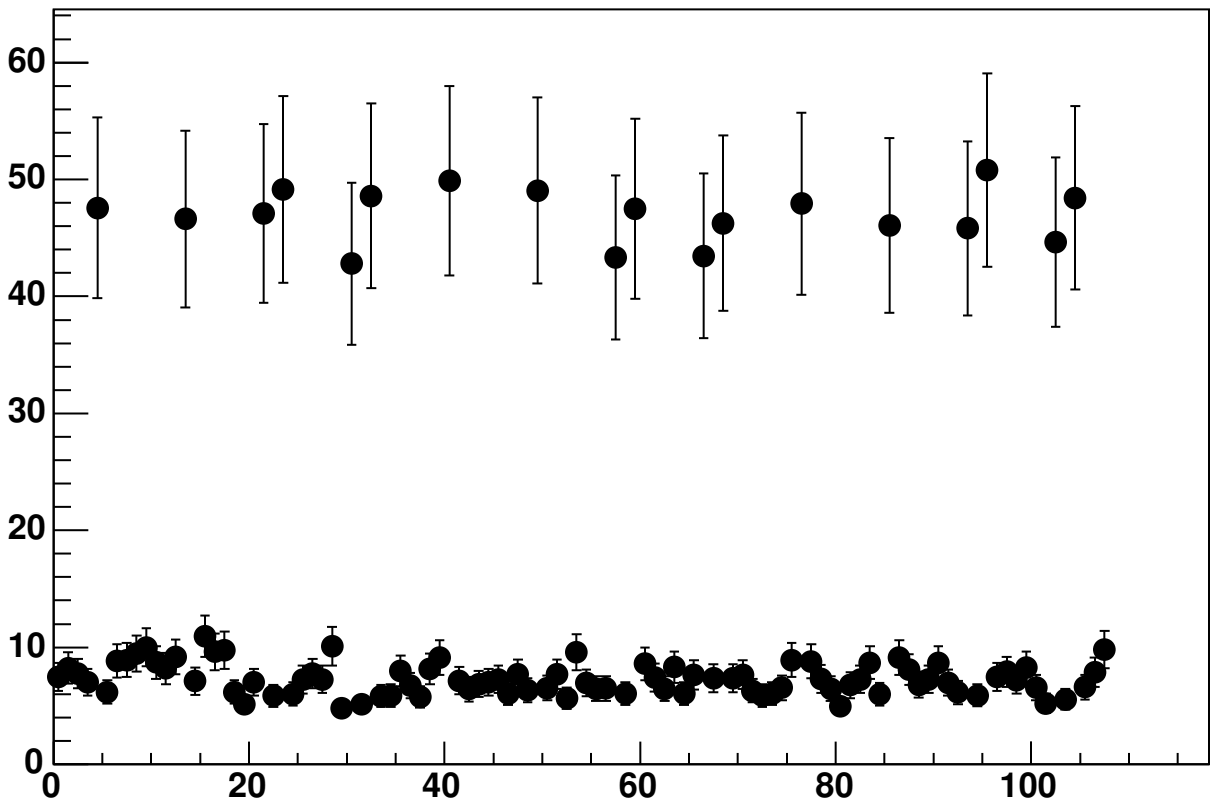
Enable 4, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



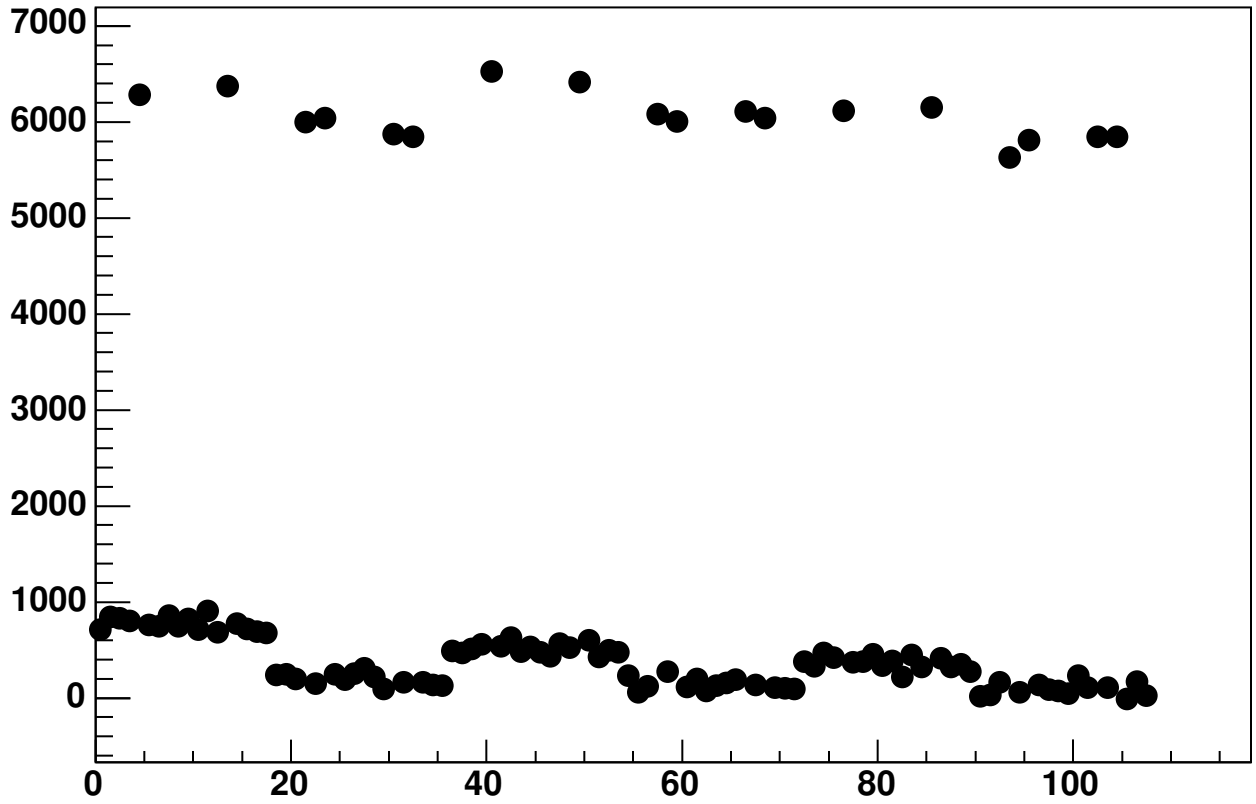
Enable 4, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



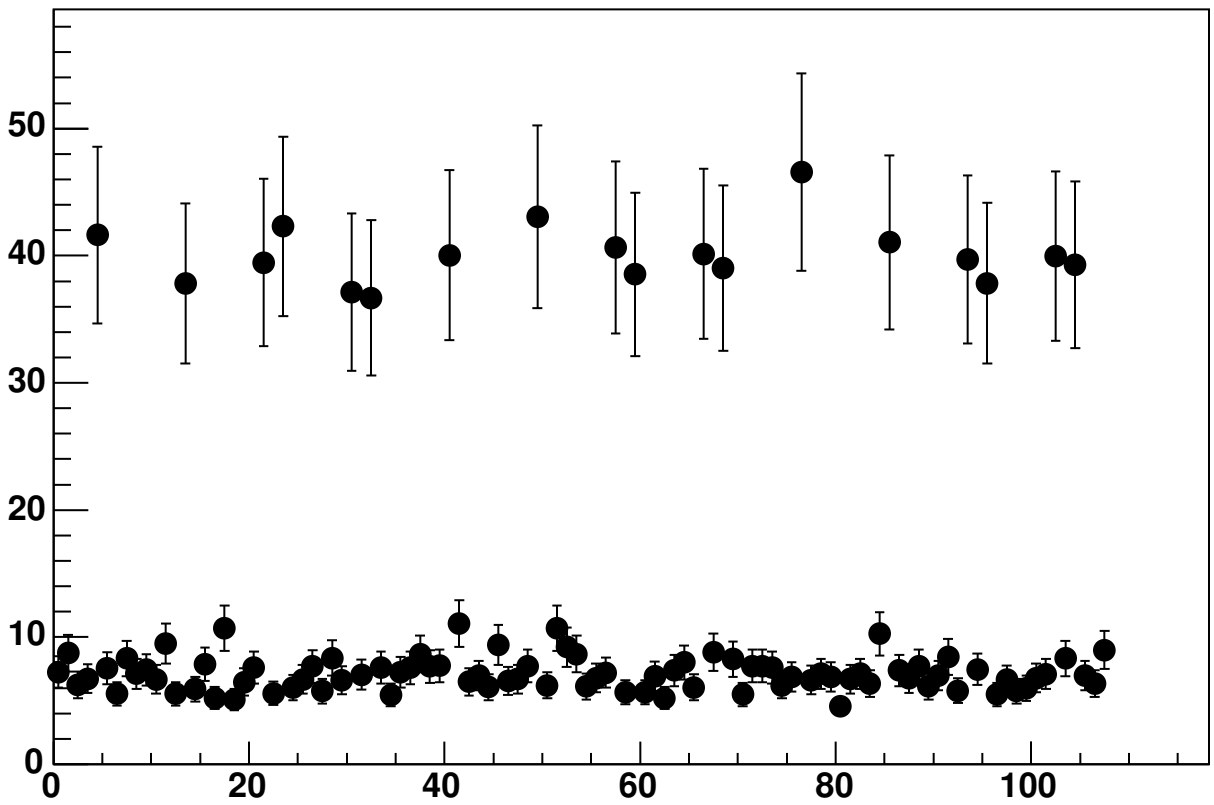
Enable 4, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



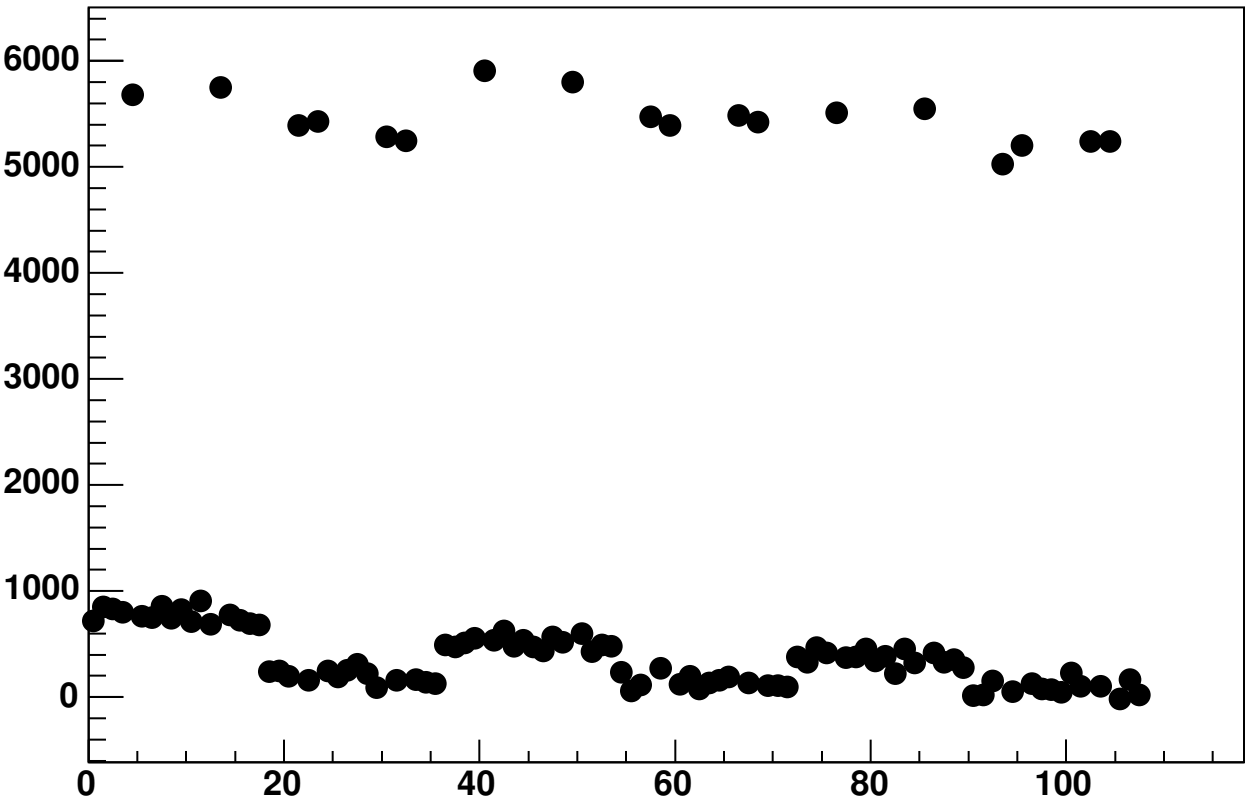
Enable 4, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



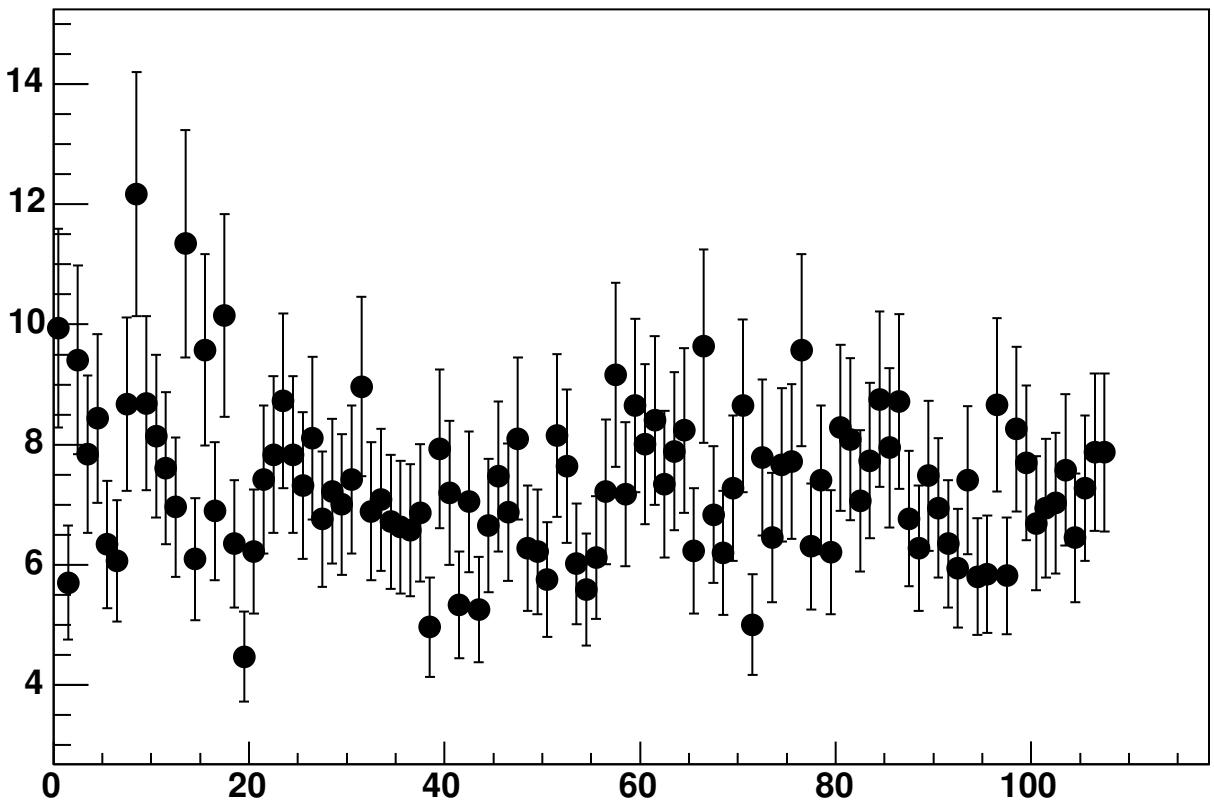
Enable 4, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



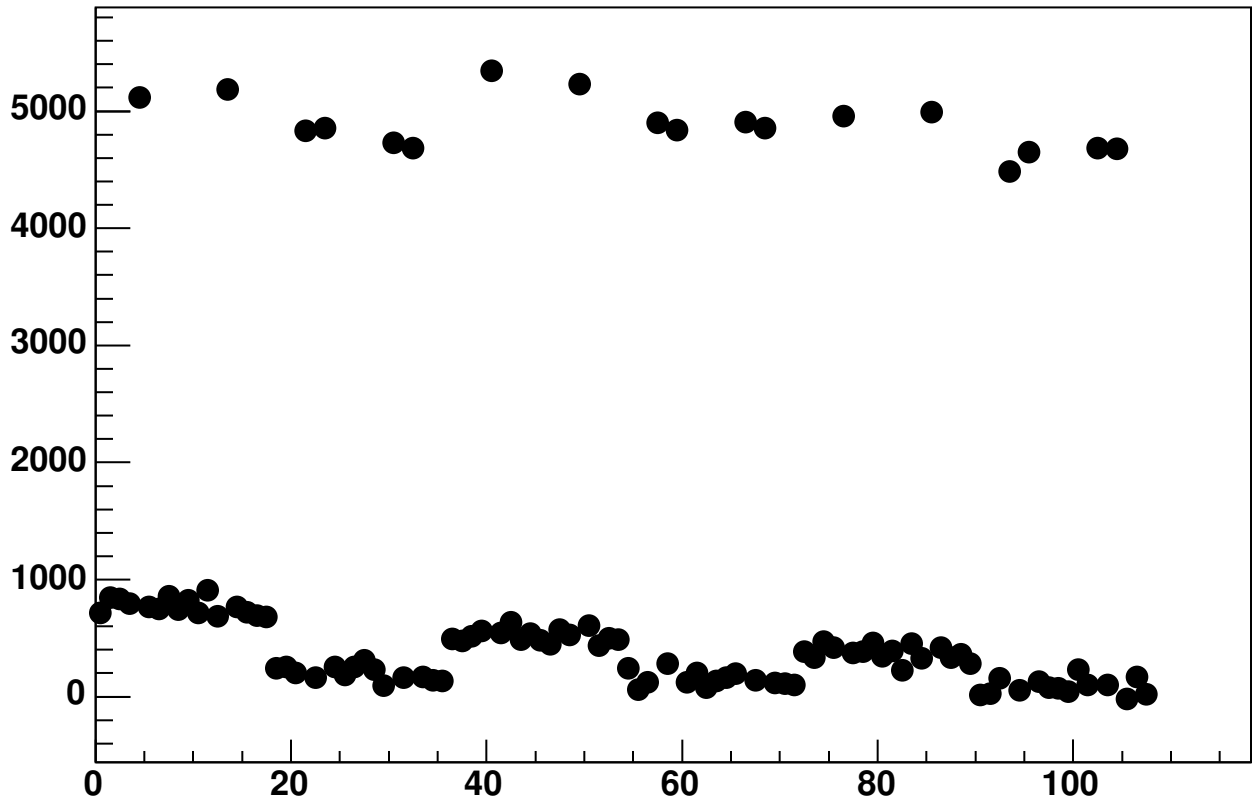
Enable 4, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



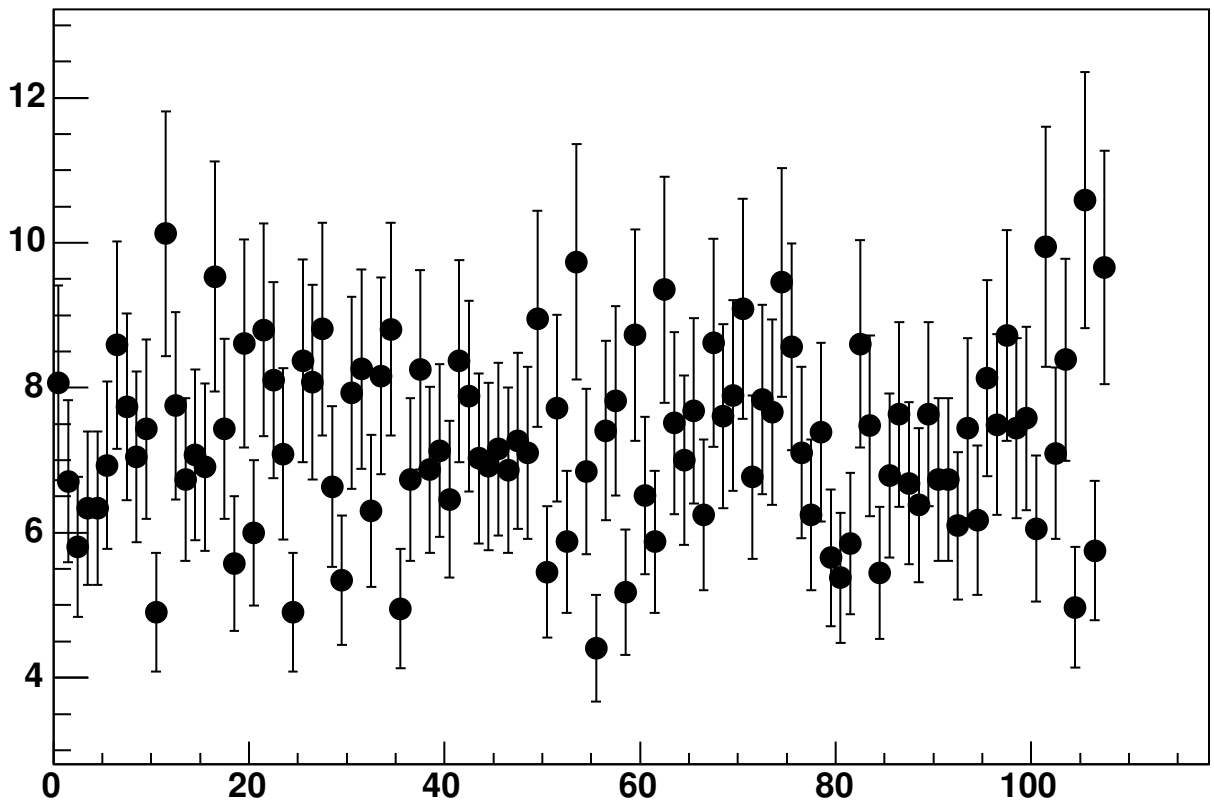
Enable 4, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



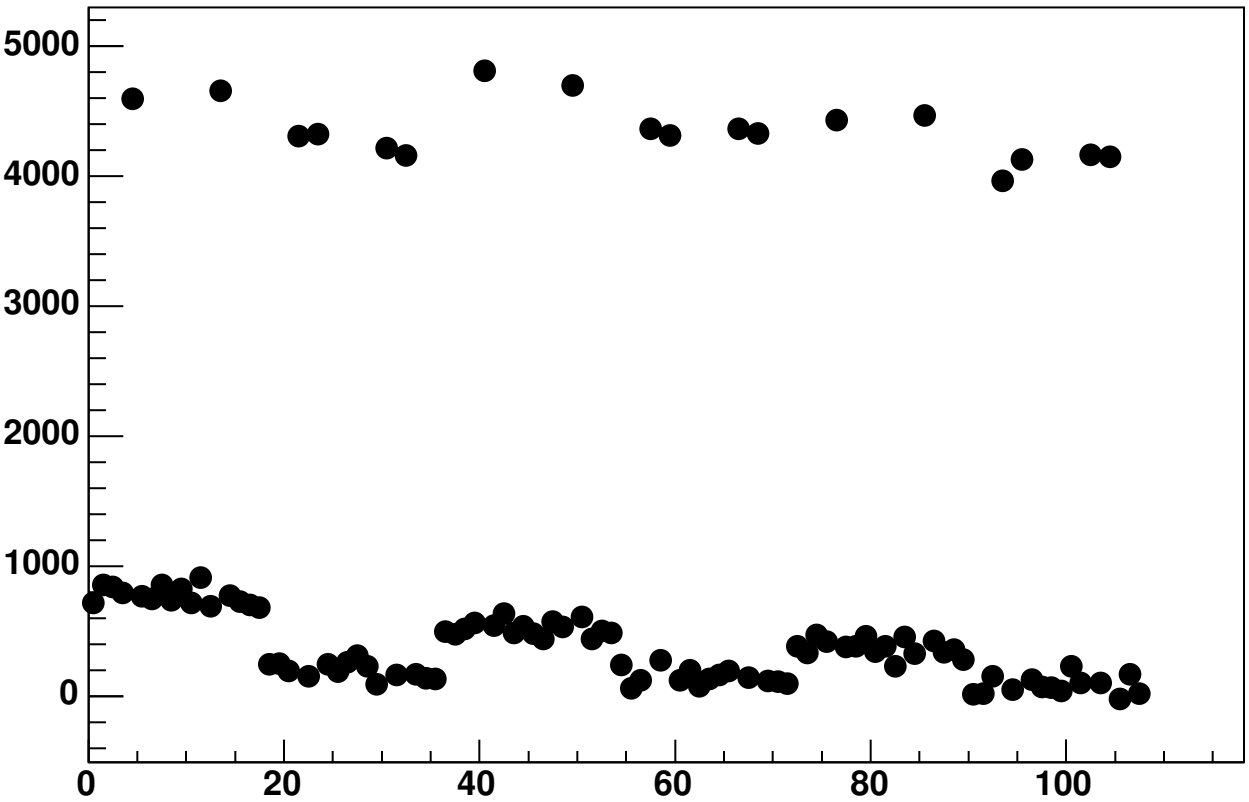
Enable 4, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



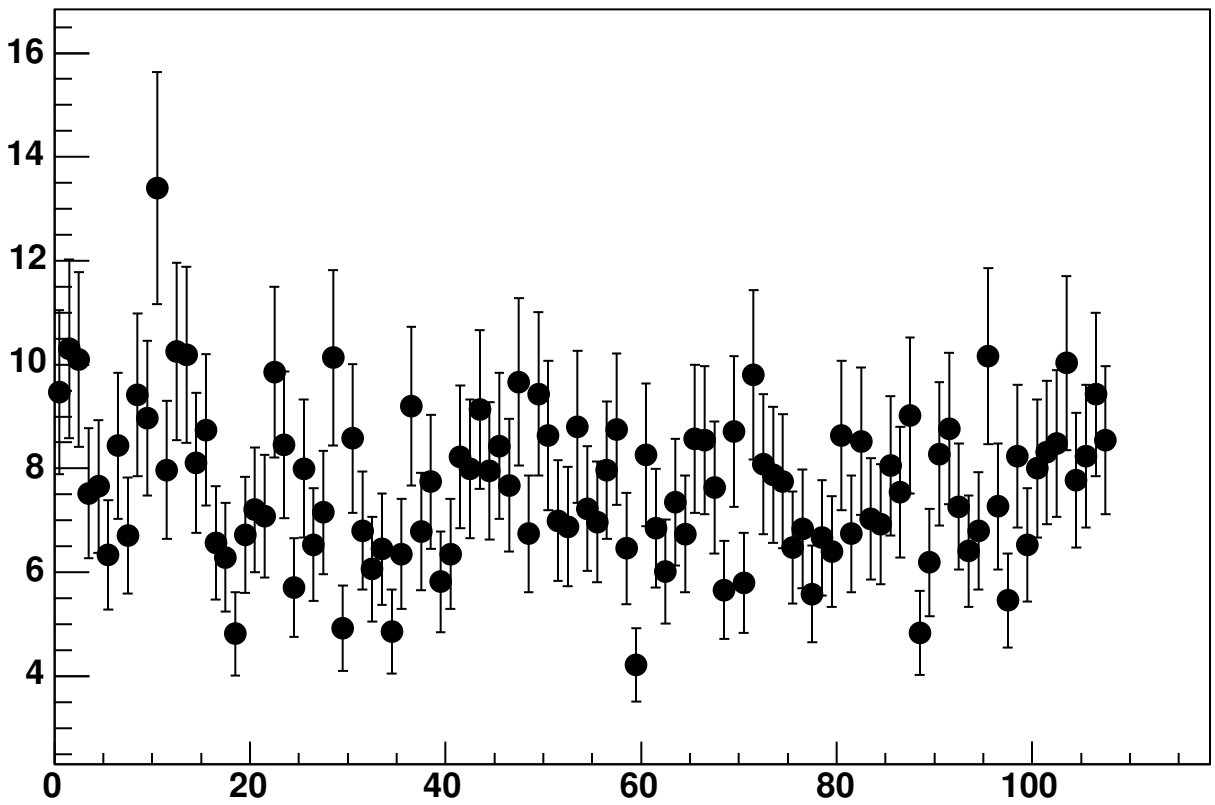
Enable 4, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



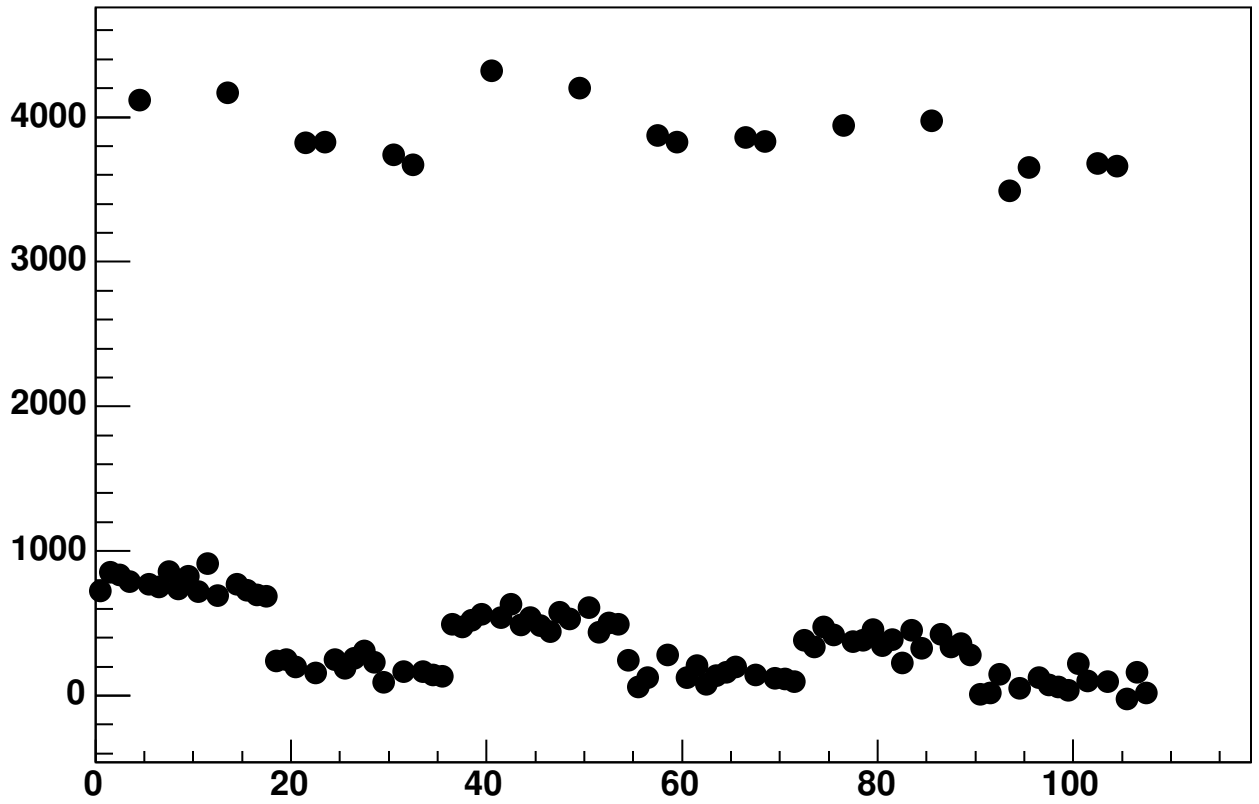
Enable 4, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



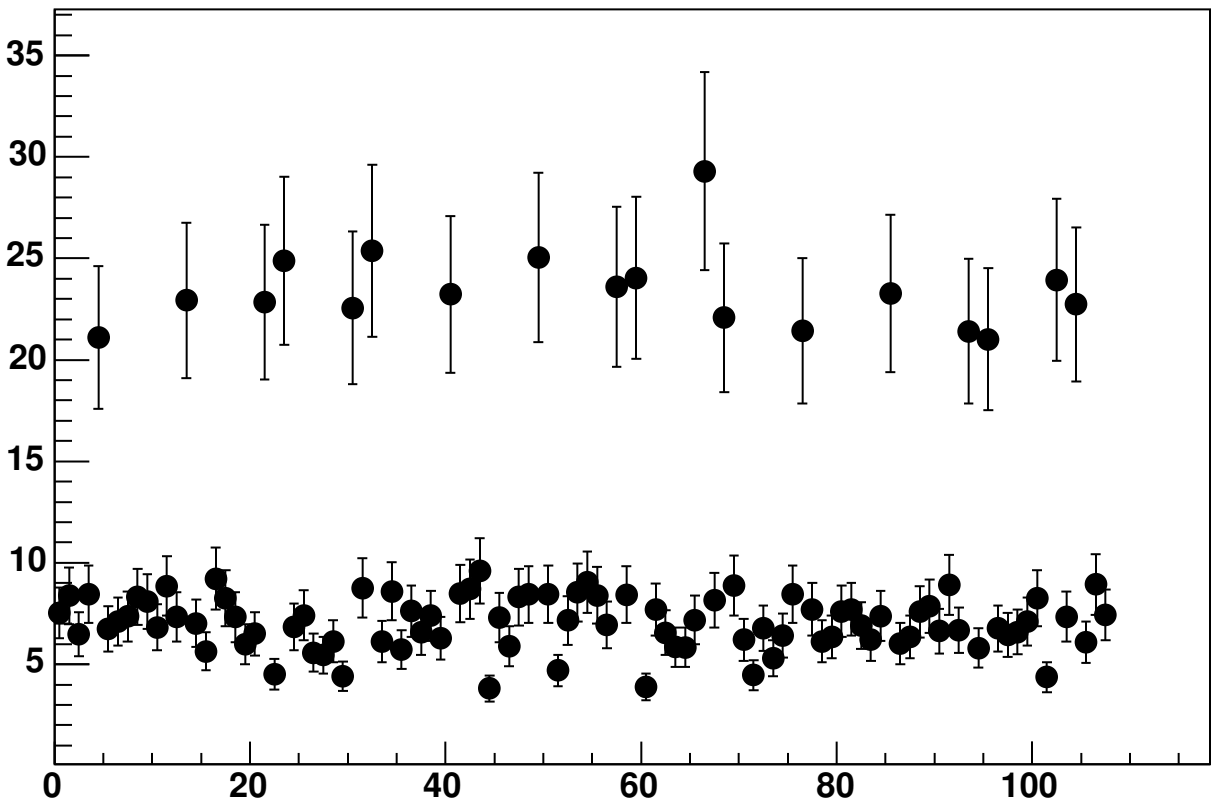
Enable 4, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



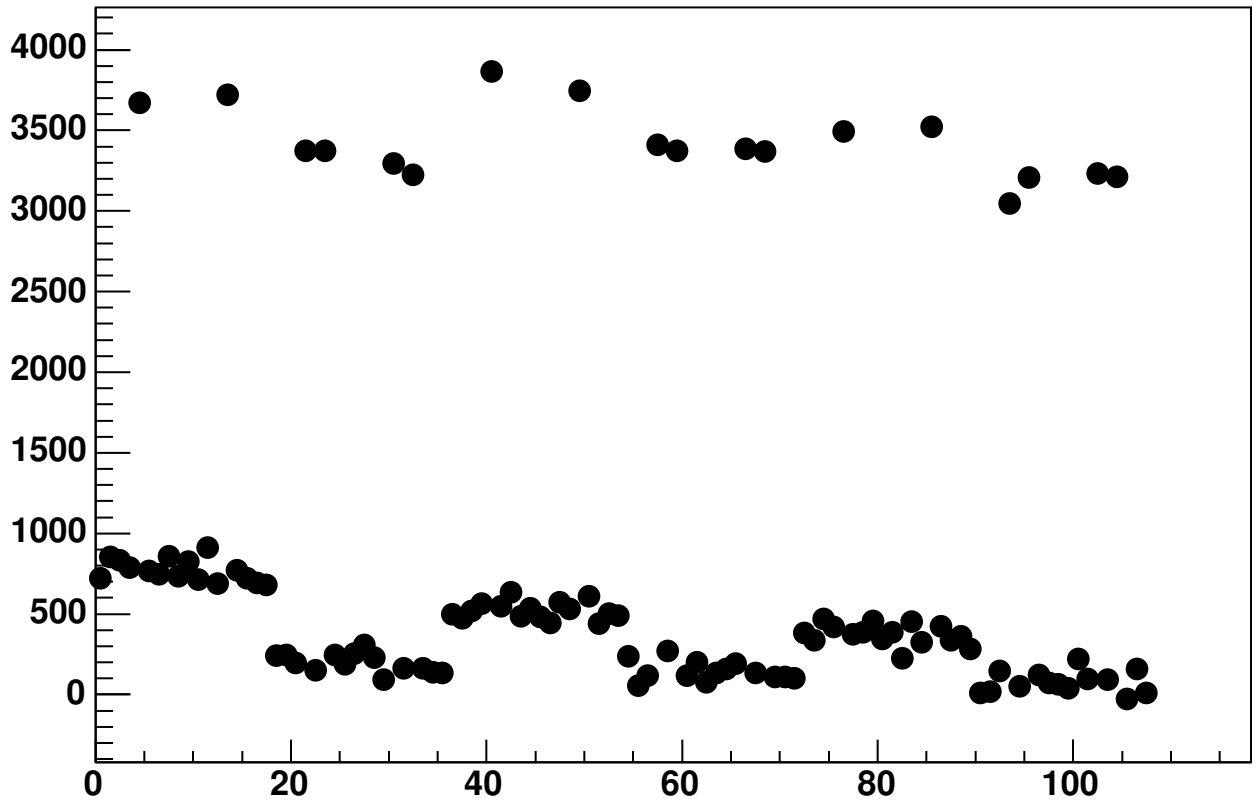
Enable 4, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



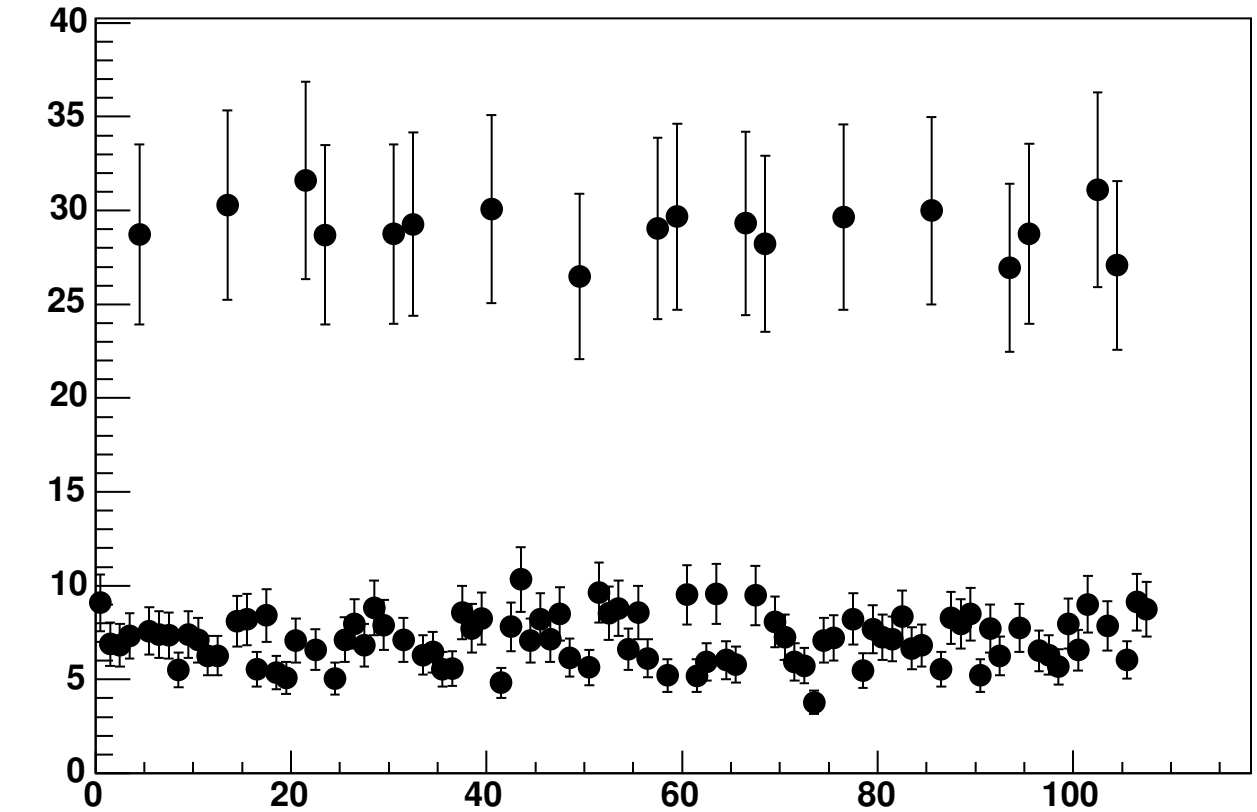
Enable 4, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



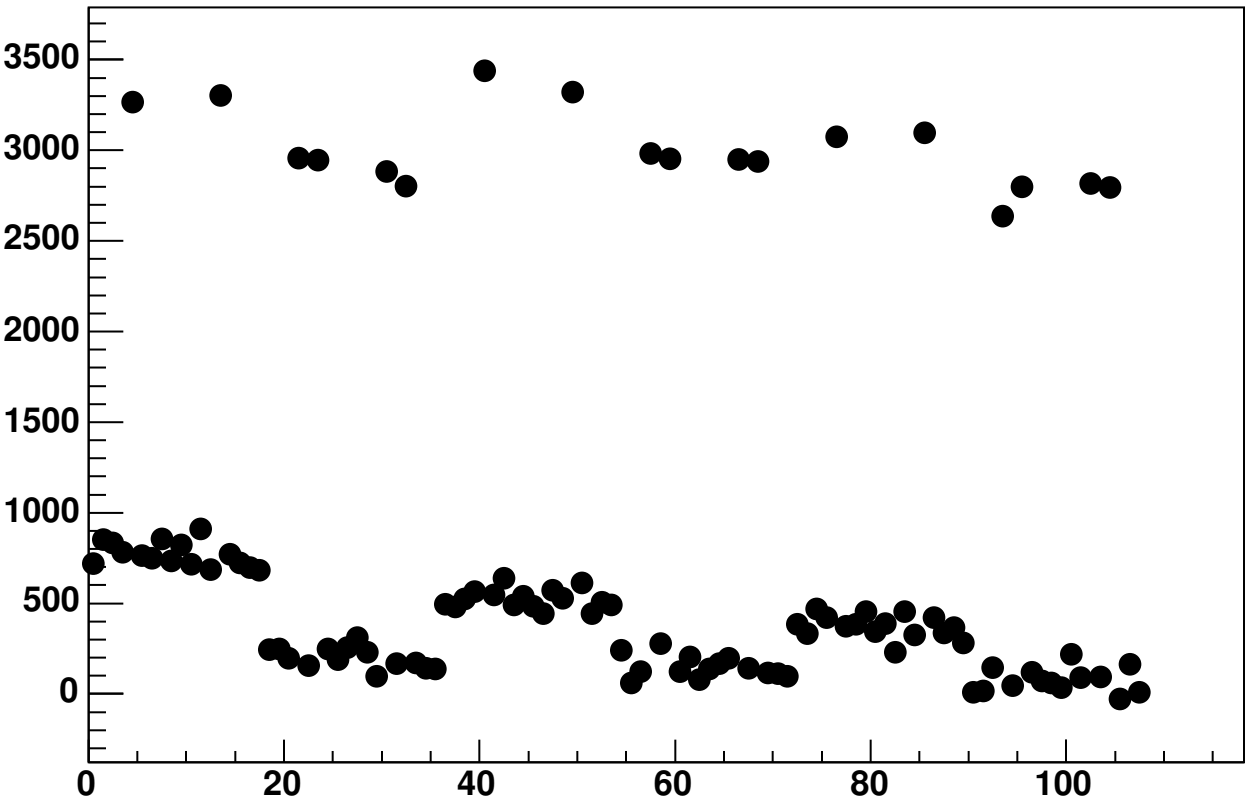
Enable 4, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



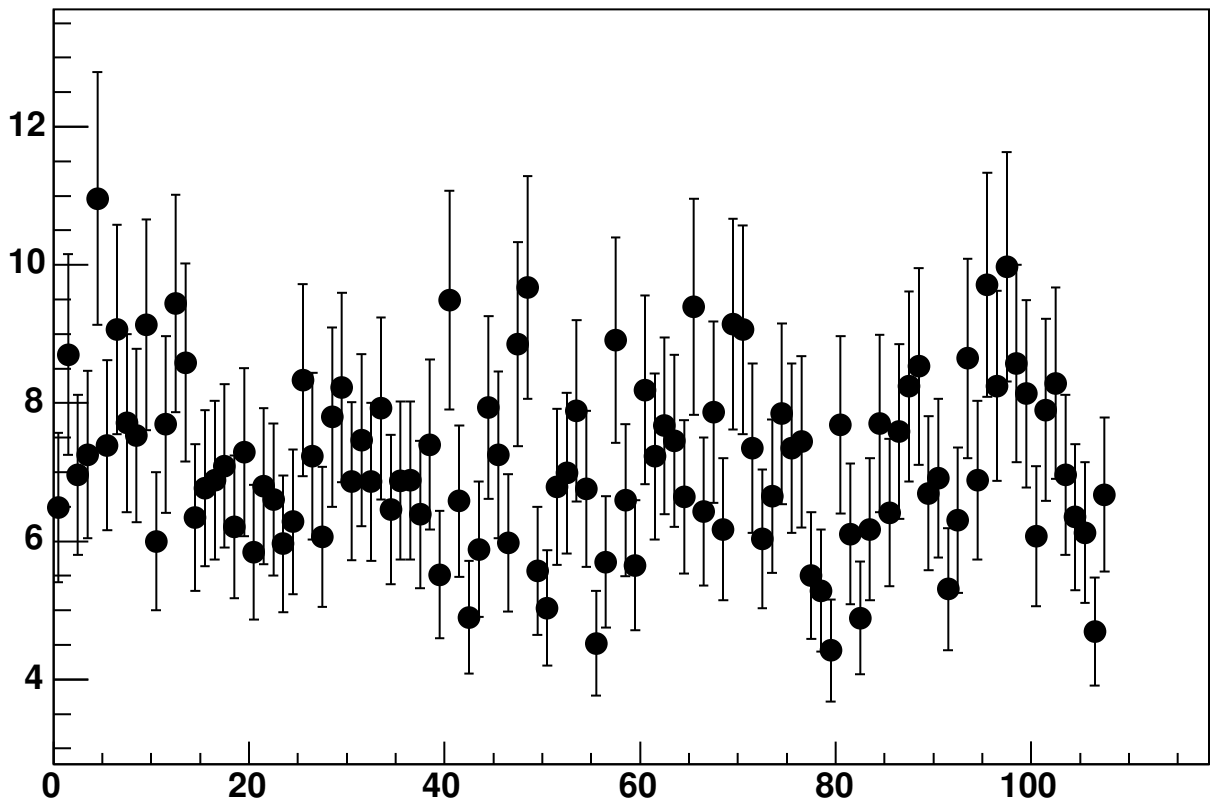
Enable 4, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



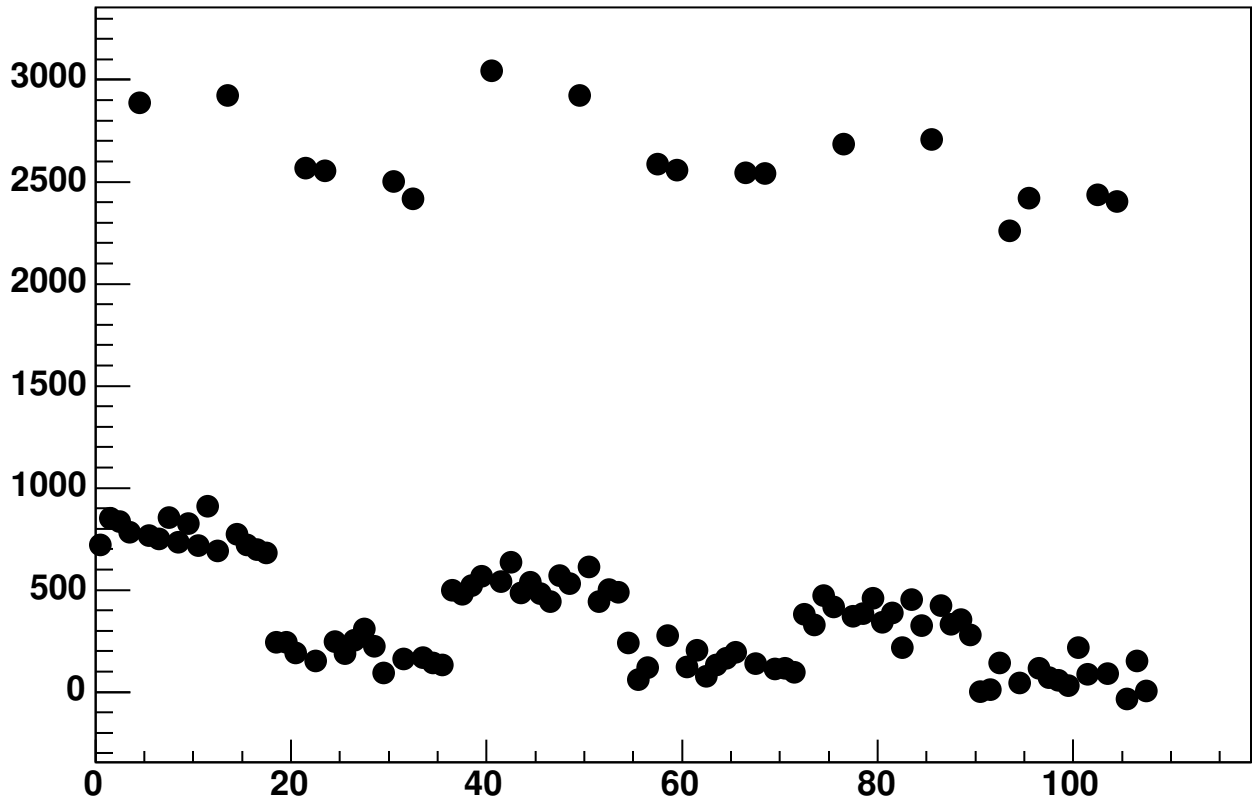
Enable 4, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



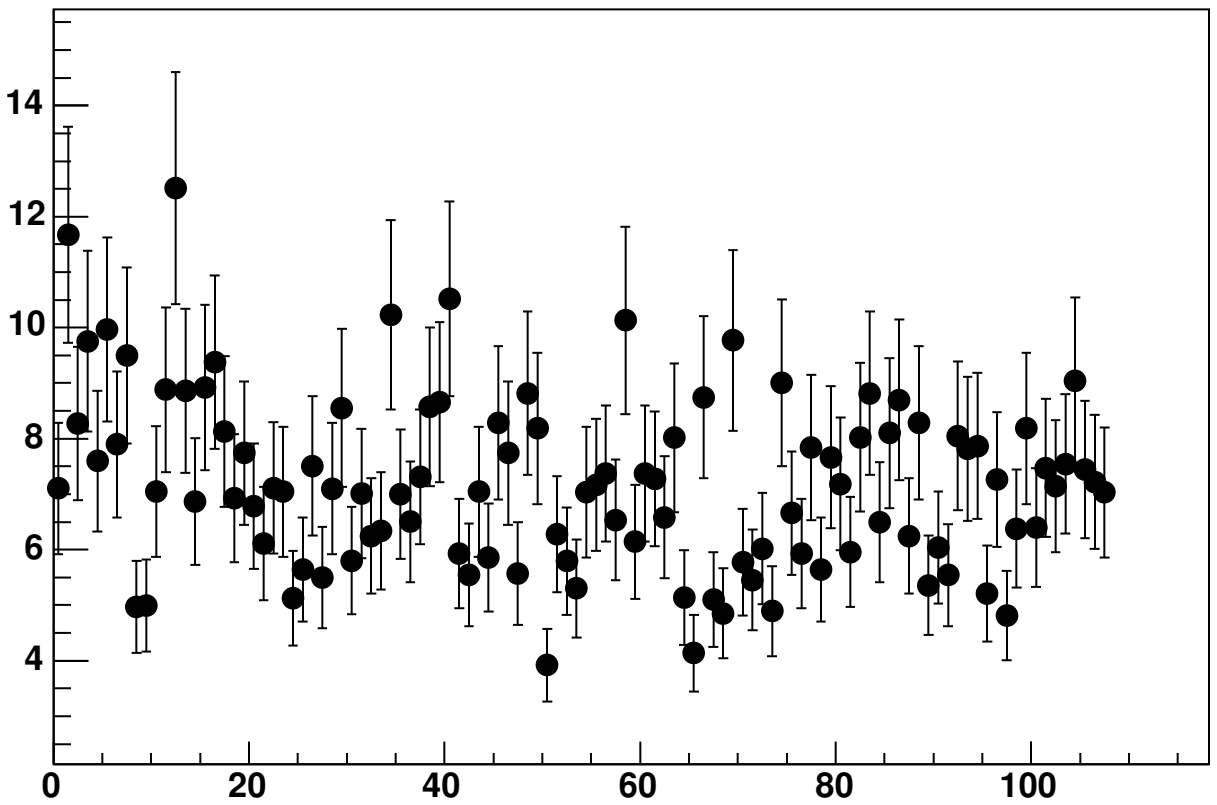
Enable 4, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



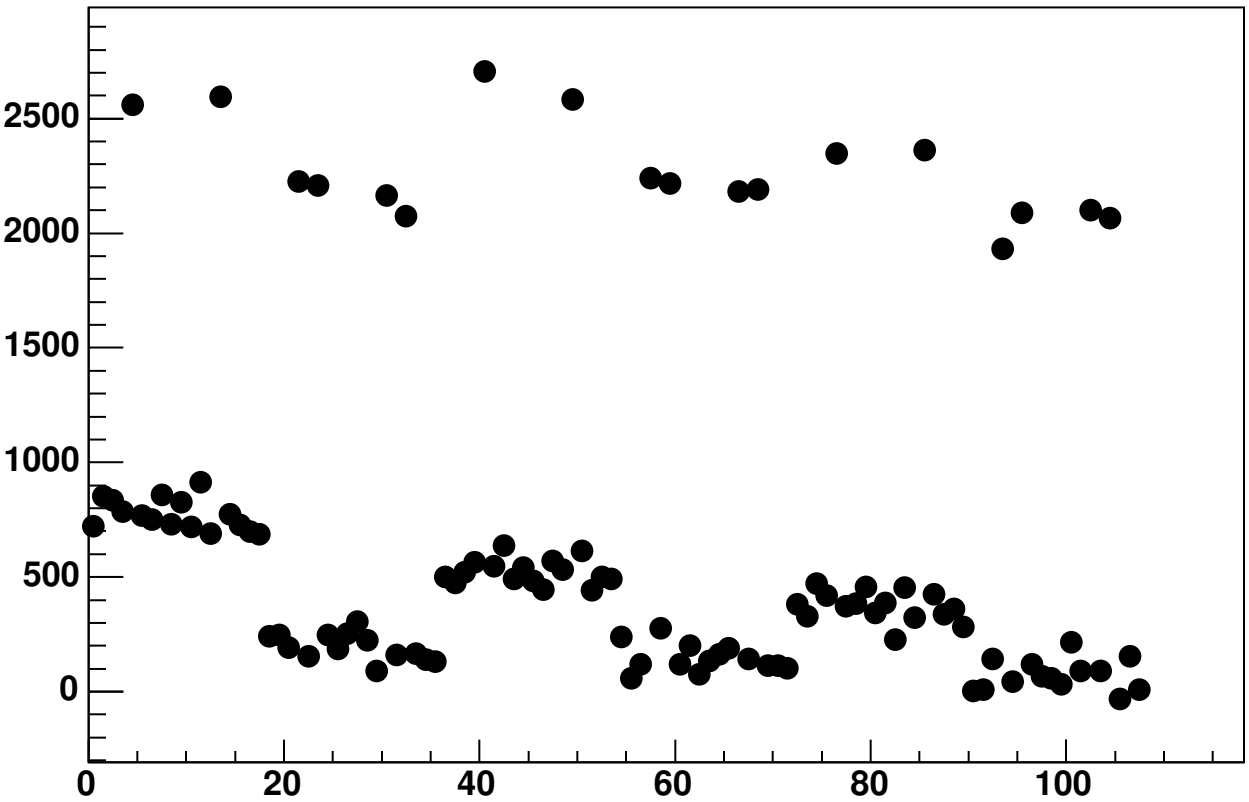
Enable 4, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



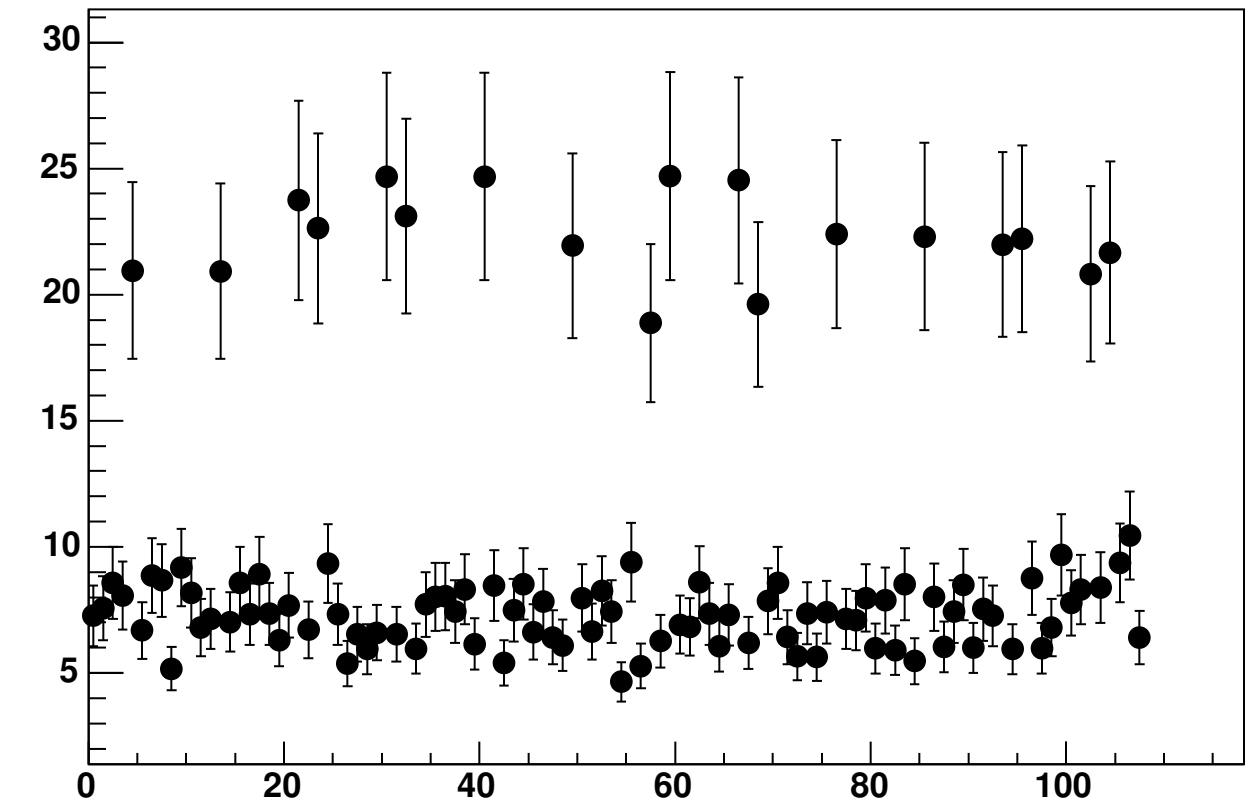
Enable 4, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



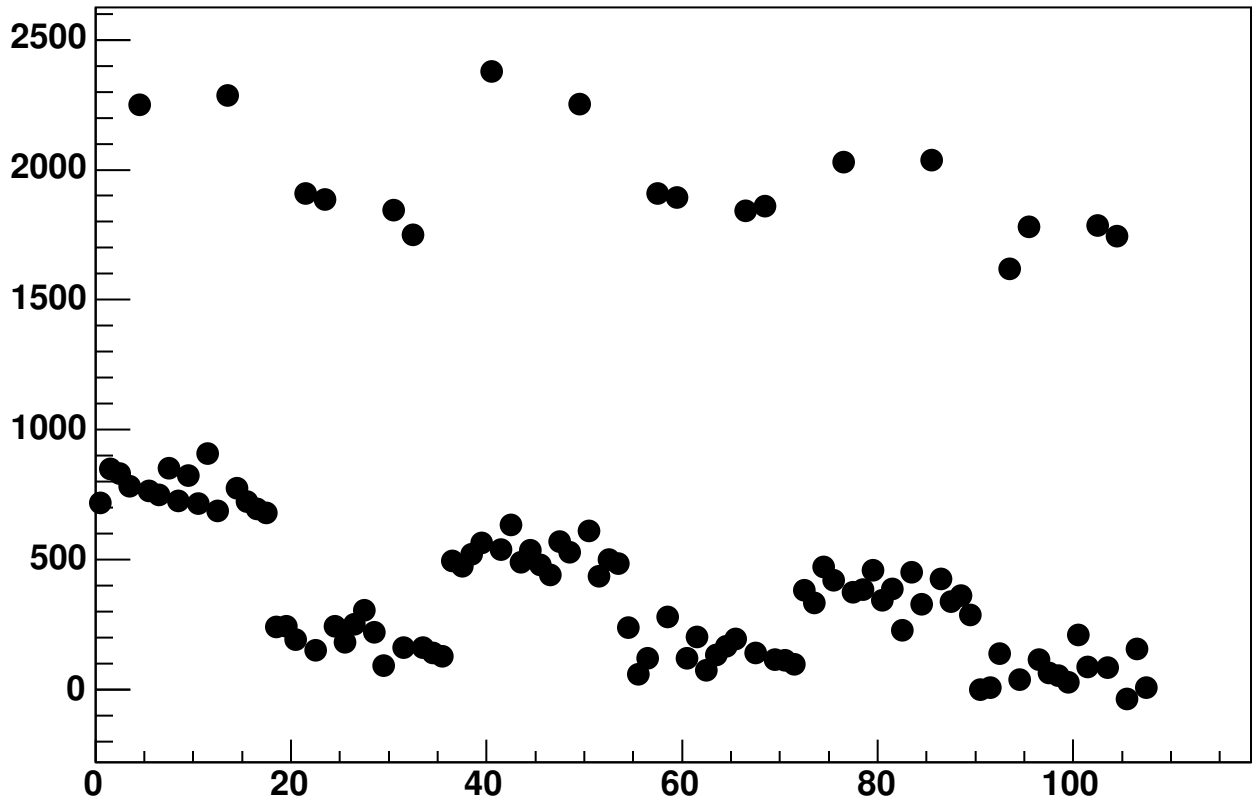
Enable 4, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



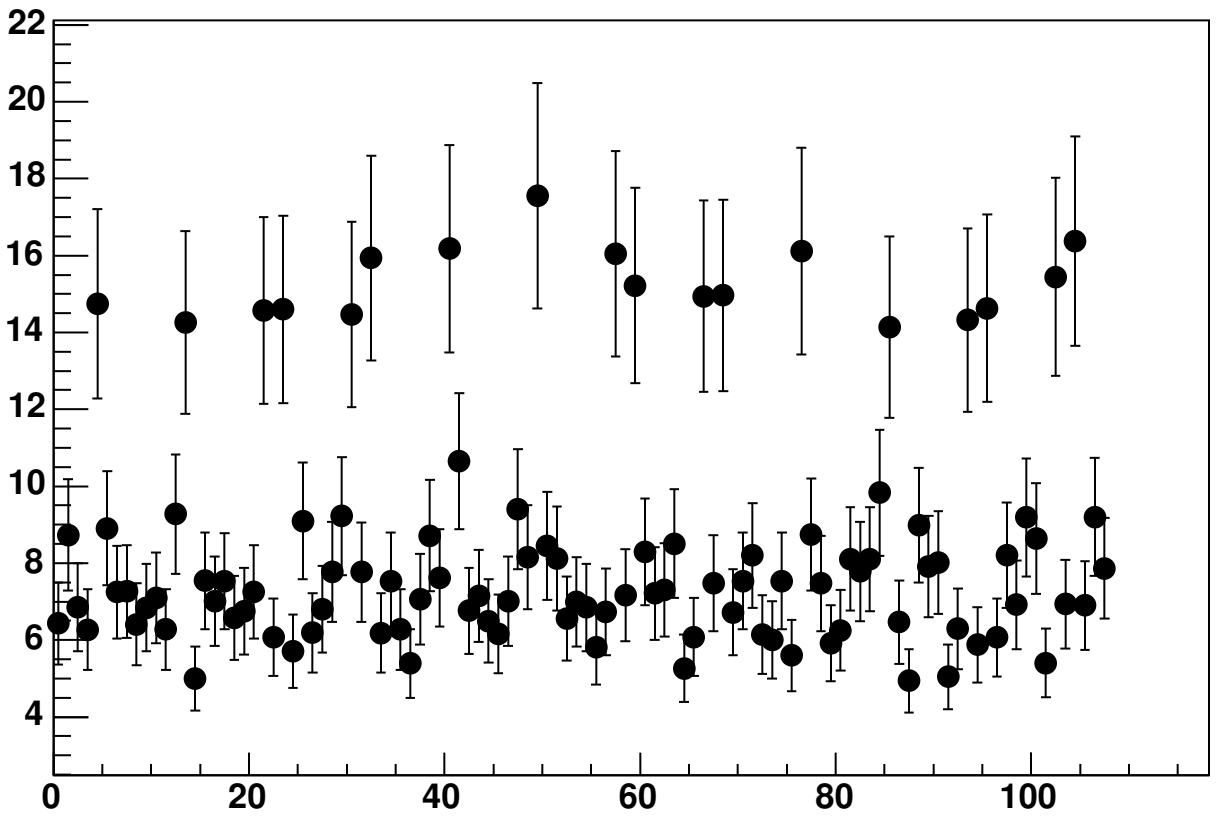
Enable 4, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



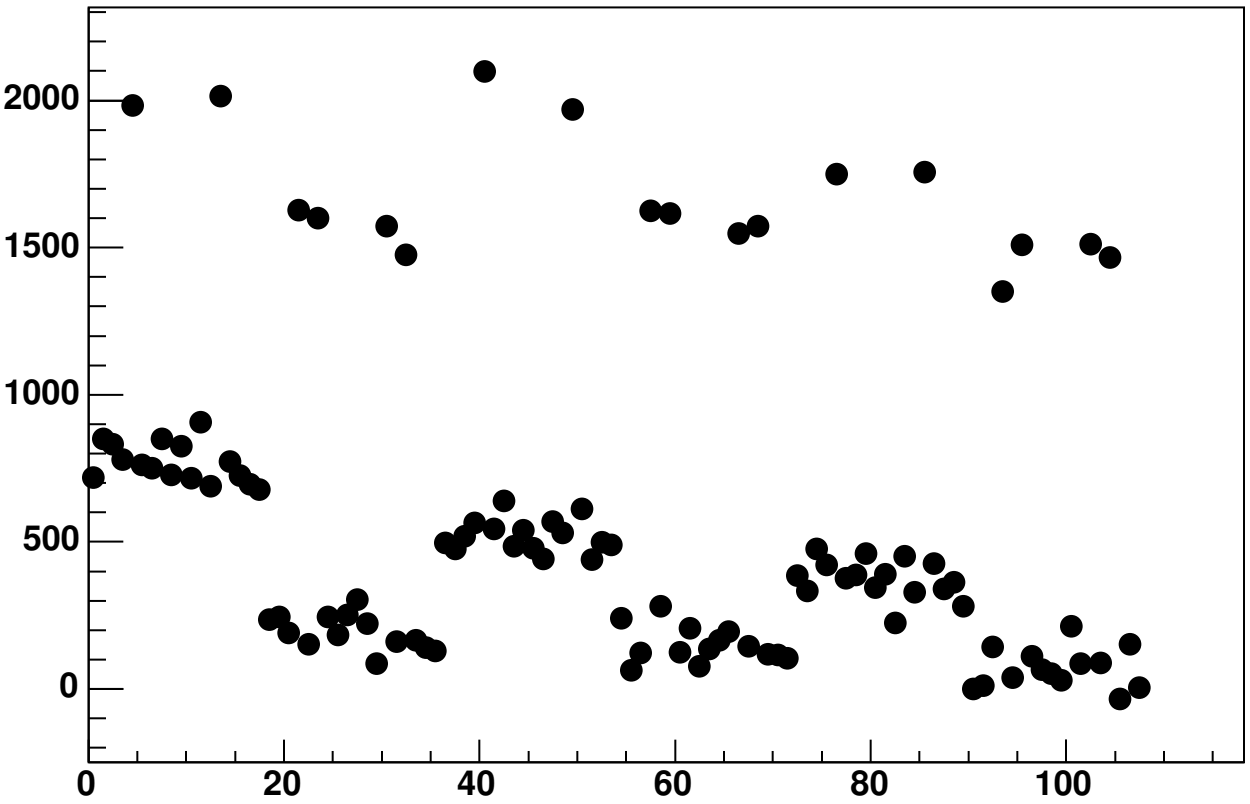
Enable 4, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



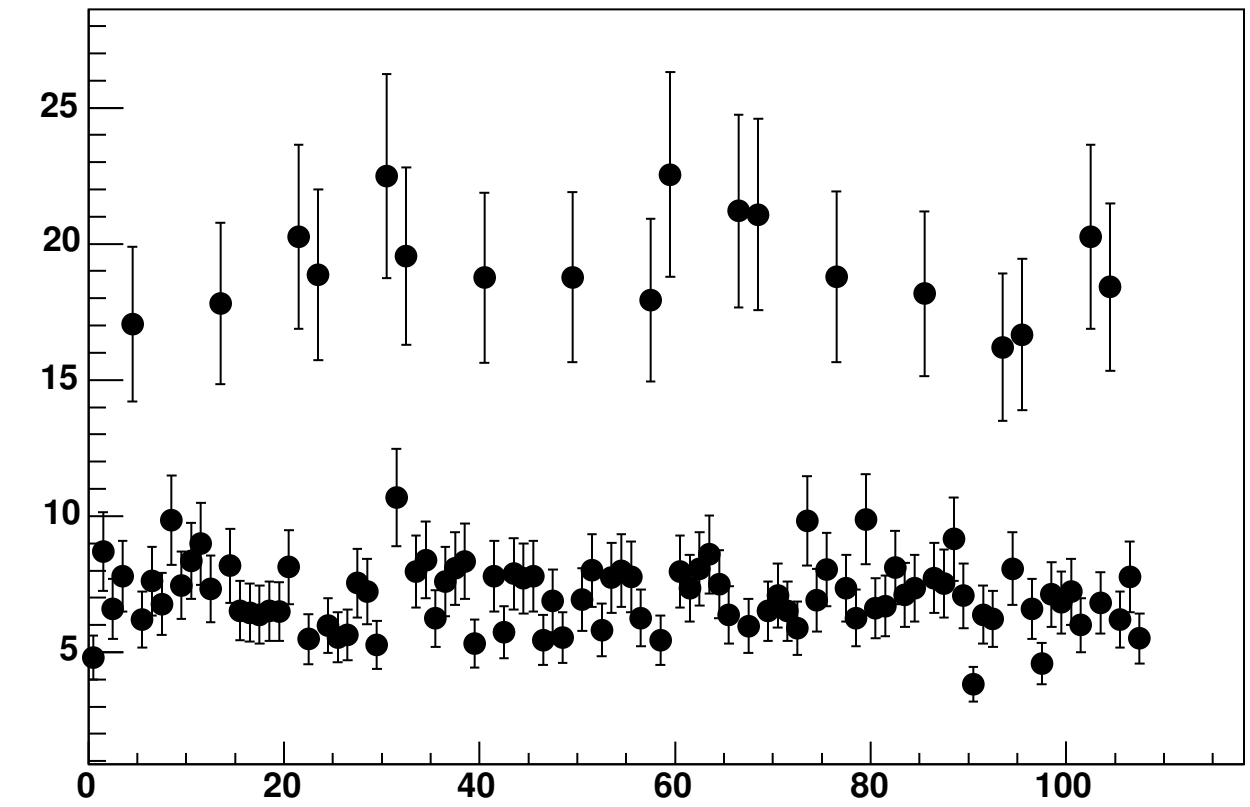
Enable 4, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



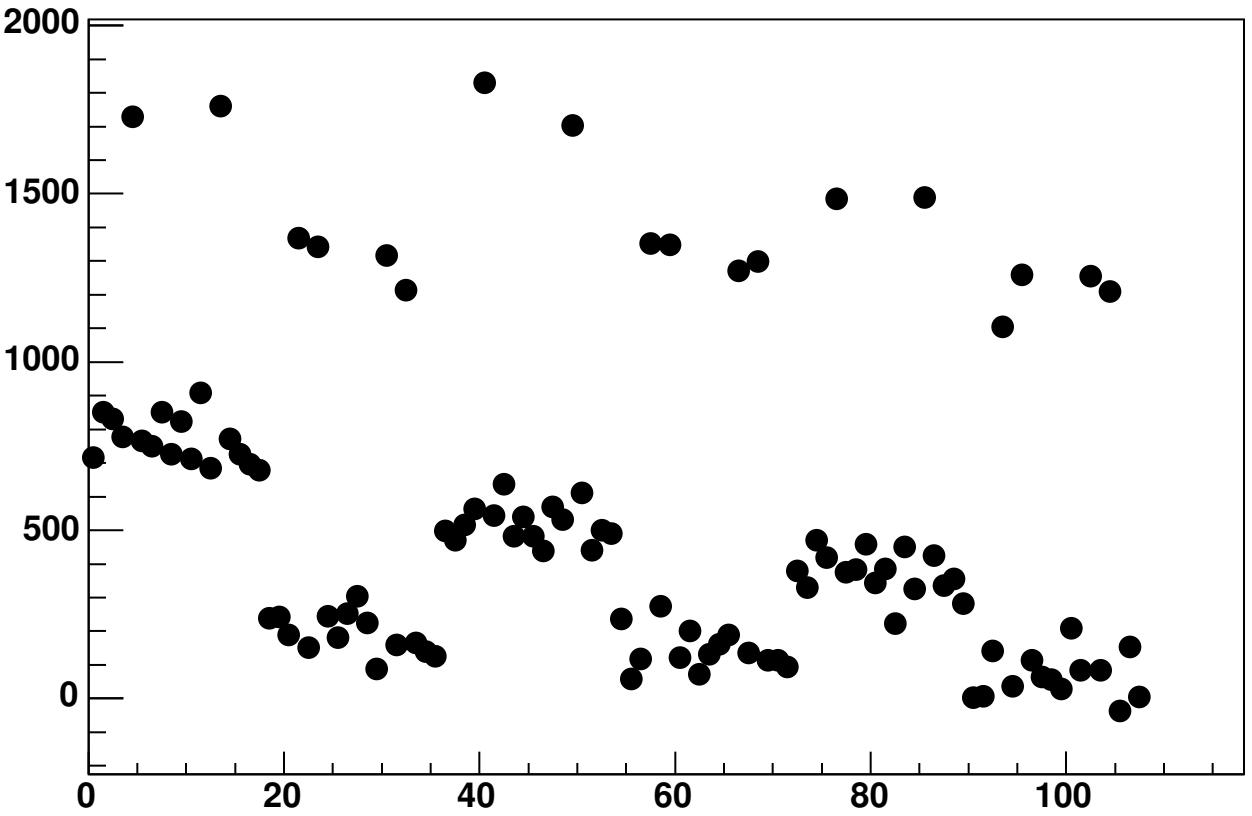
Enable 4, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



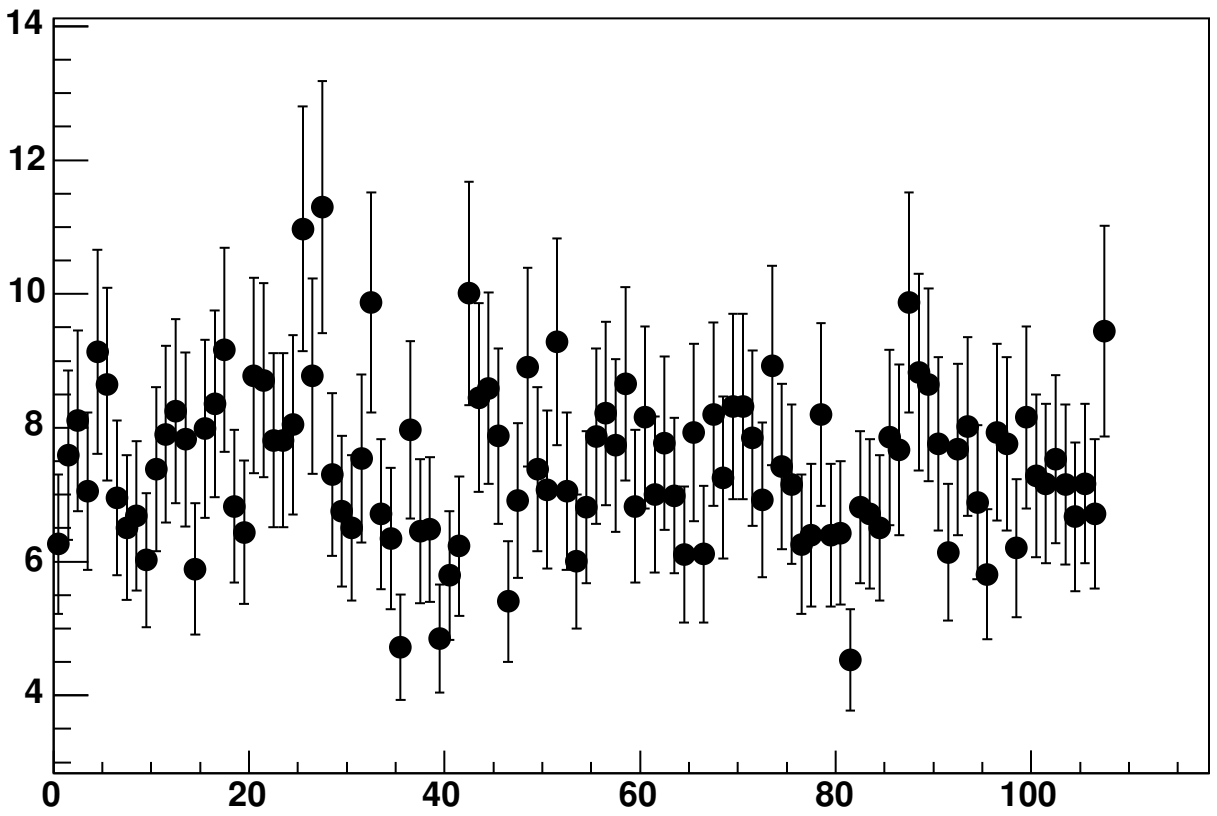
Enable 4, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



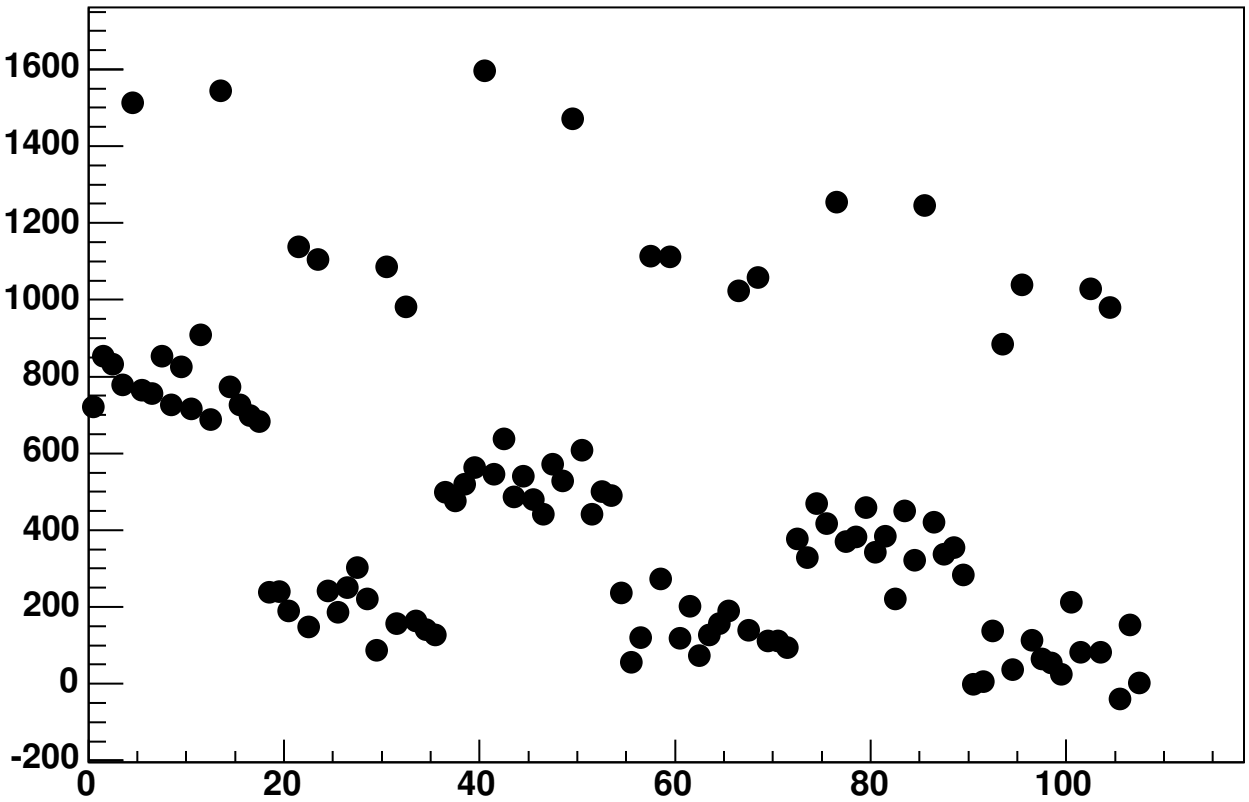
Enable 4, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



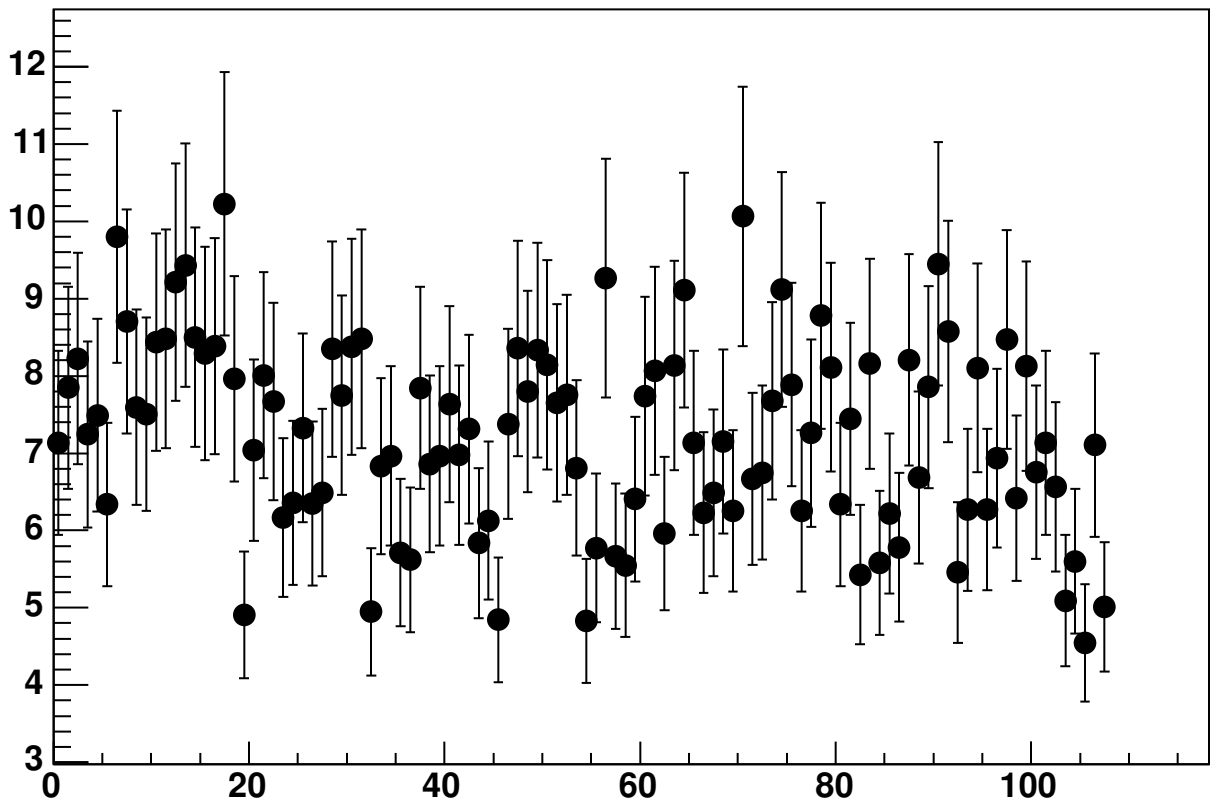
Enable 4, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



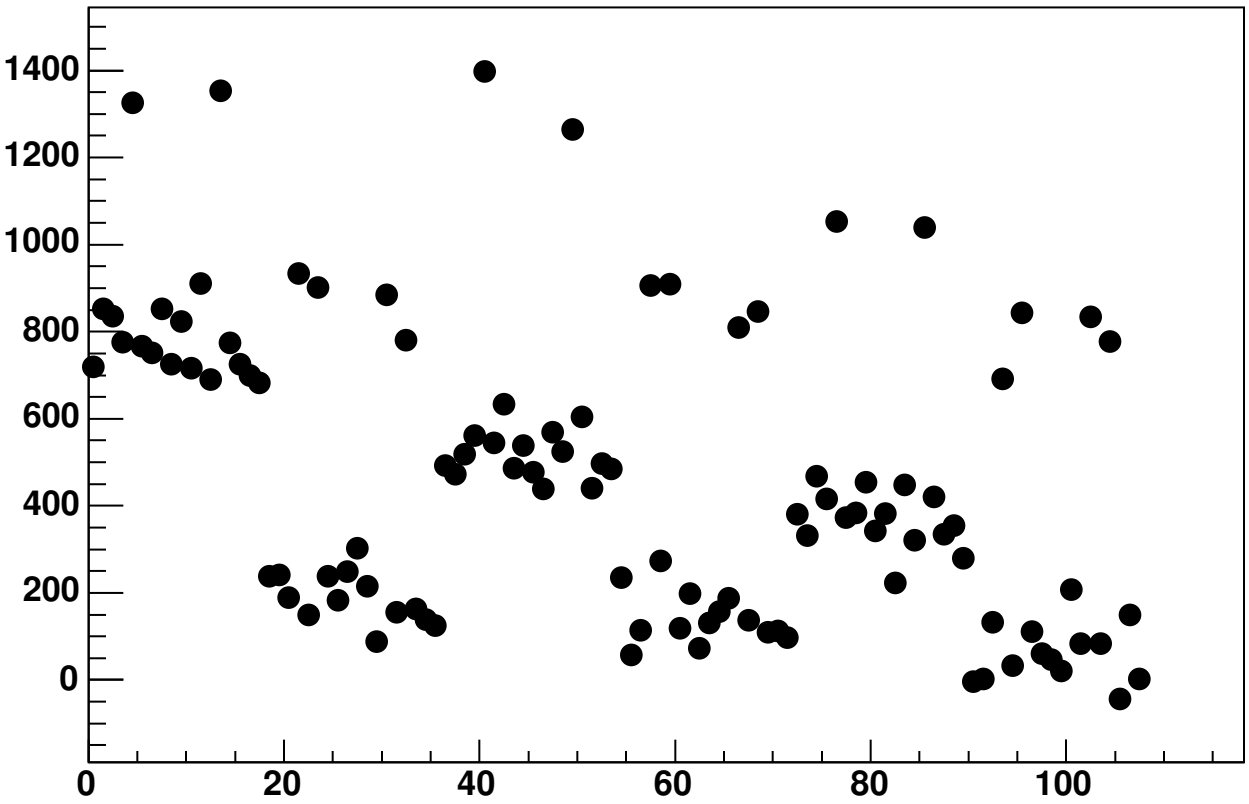
Enable 4, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



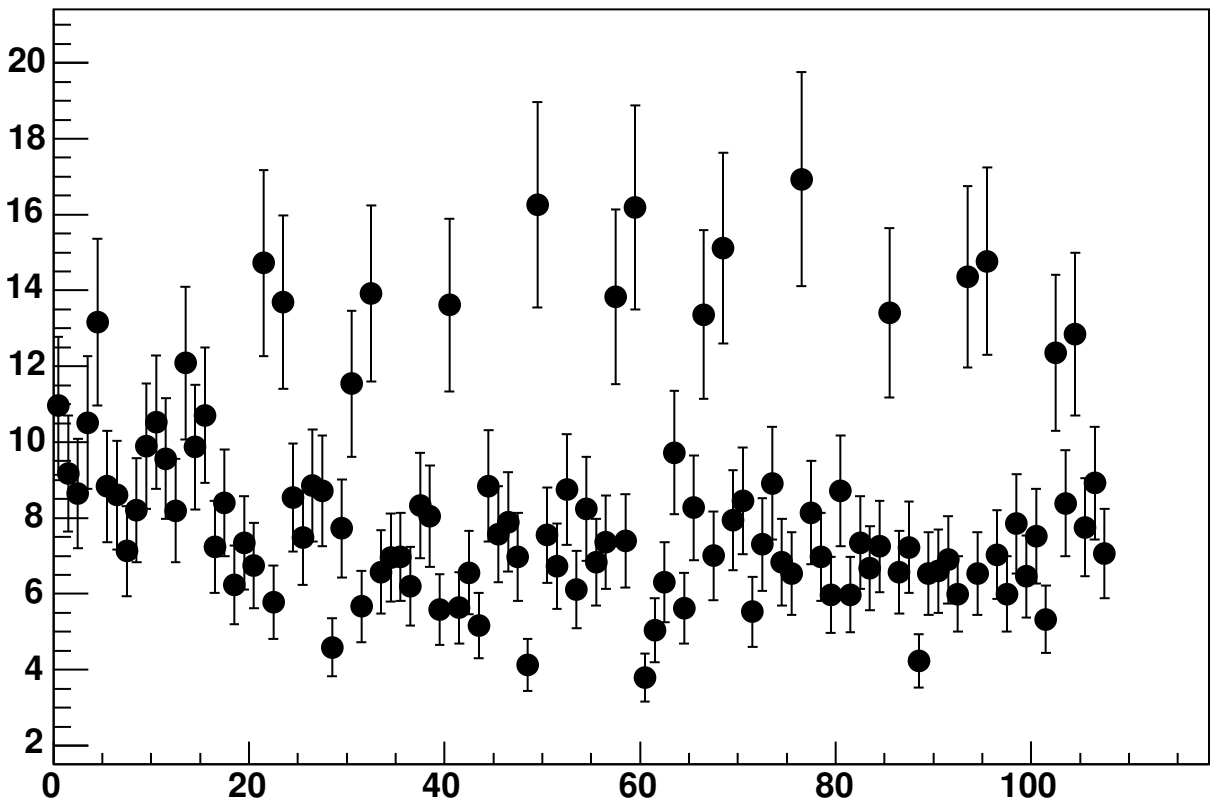
Enable 4, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



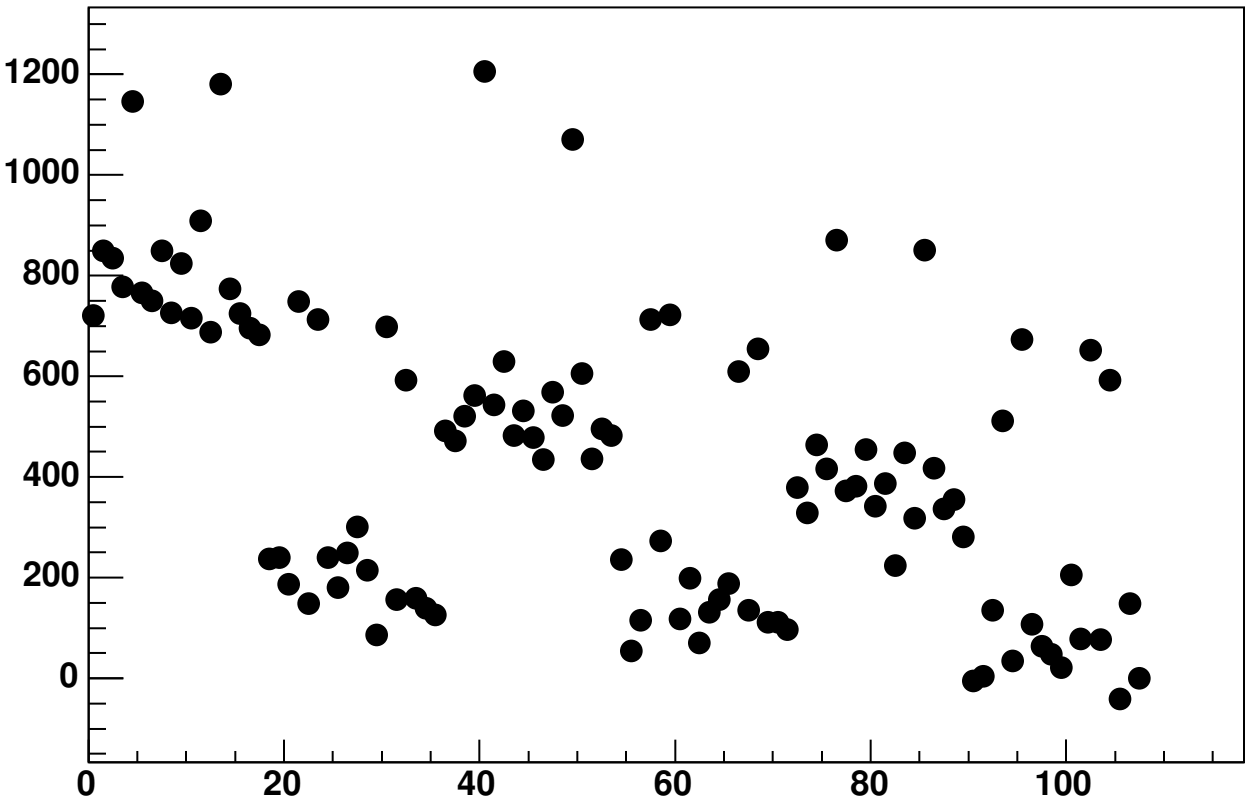
Enable 4, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



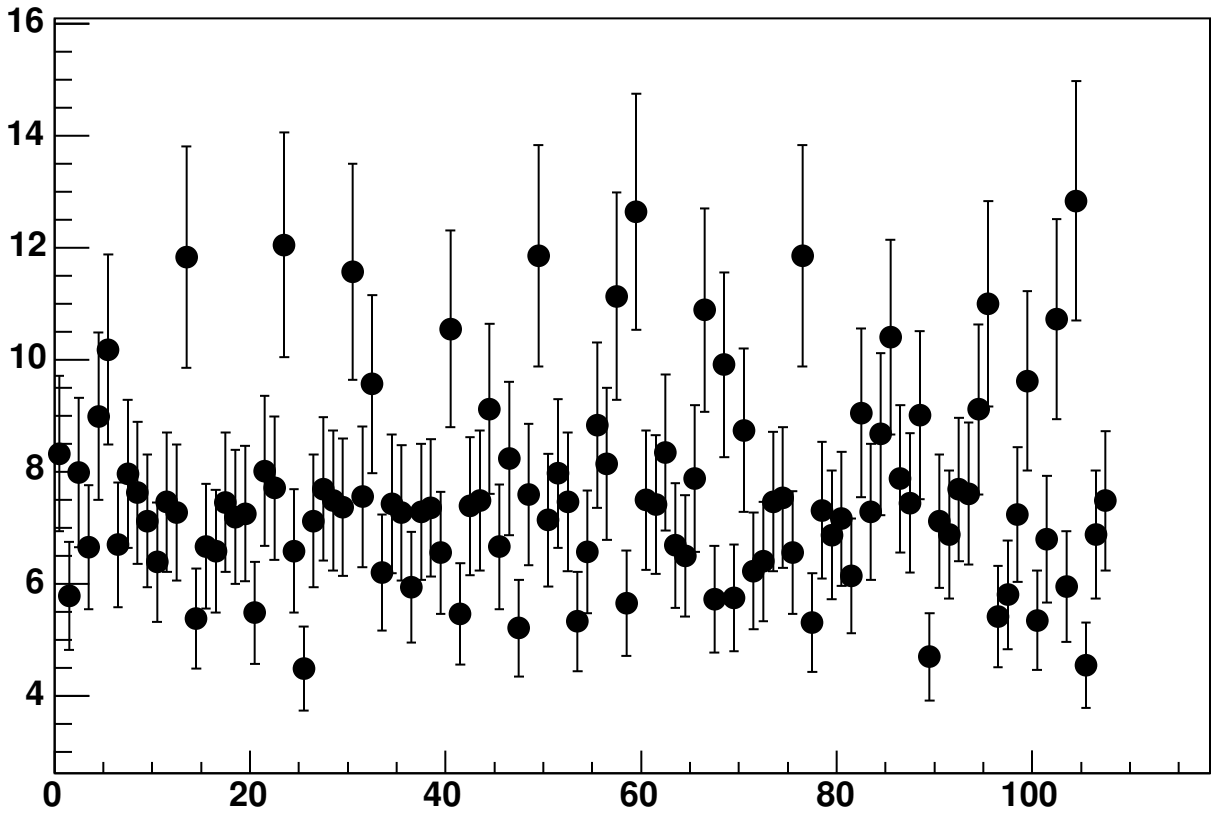
Enable 4, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



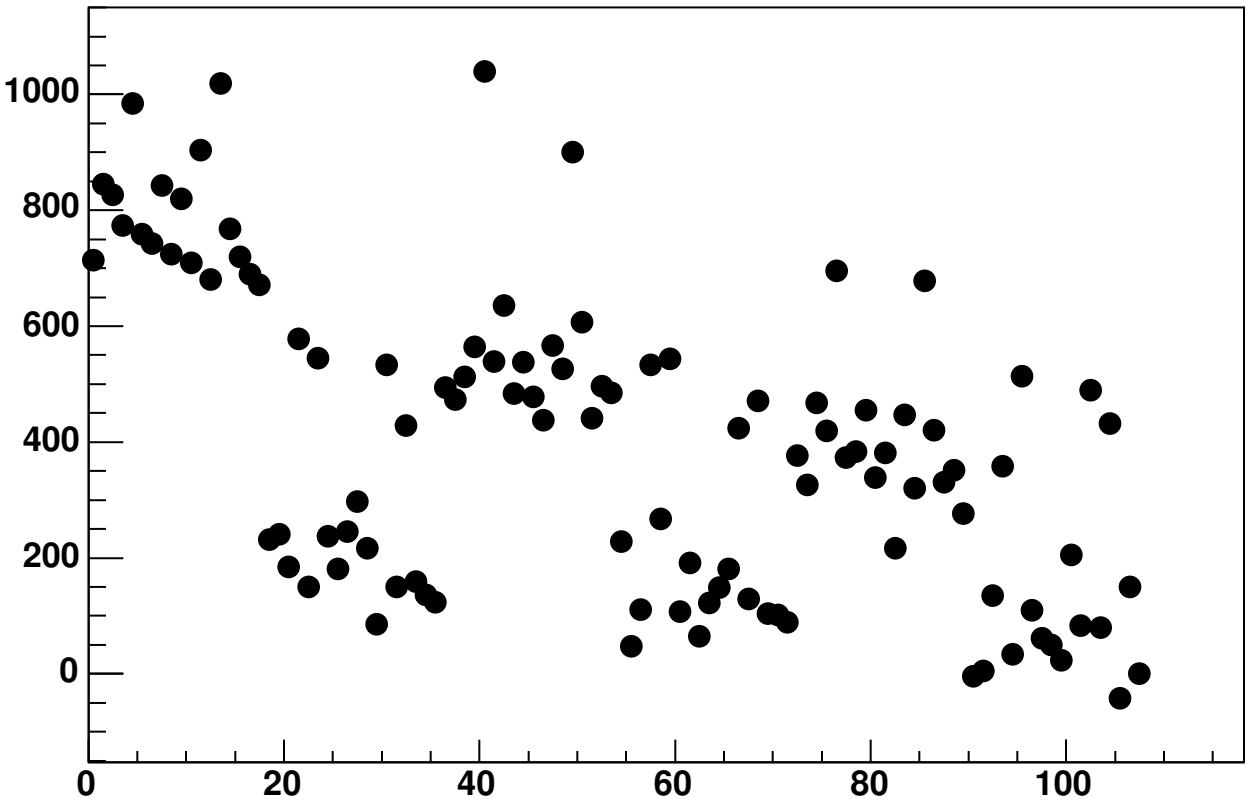
Enable 4, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



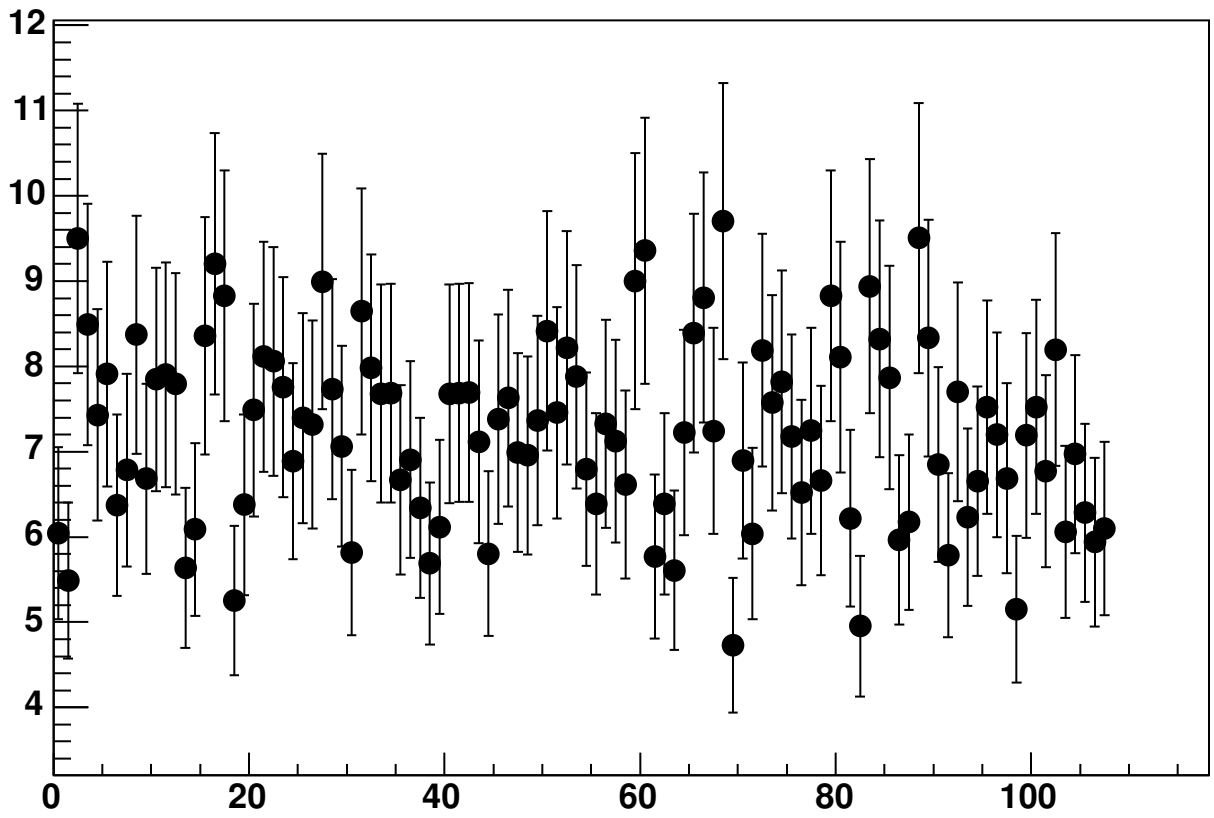
Enable 4, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



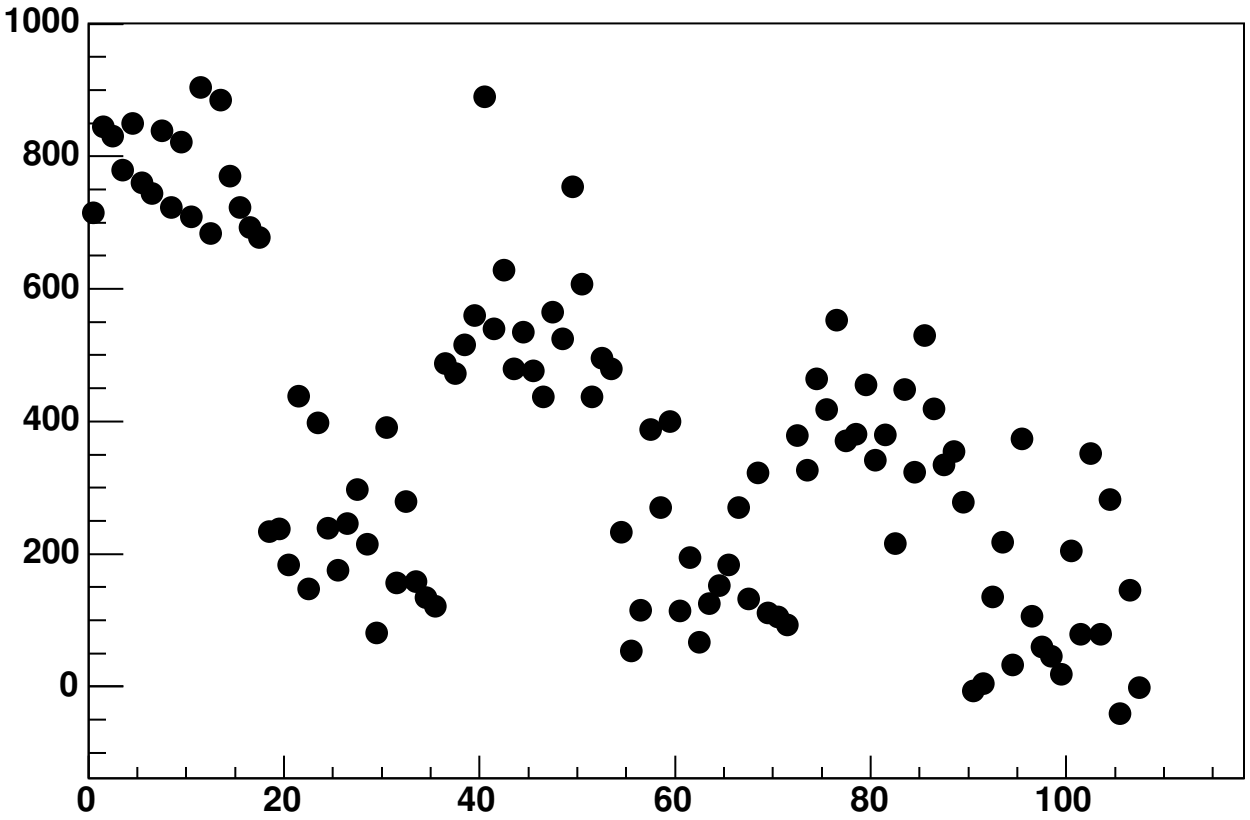
Enable 4, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



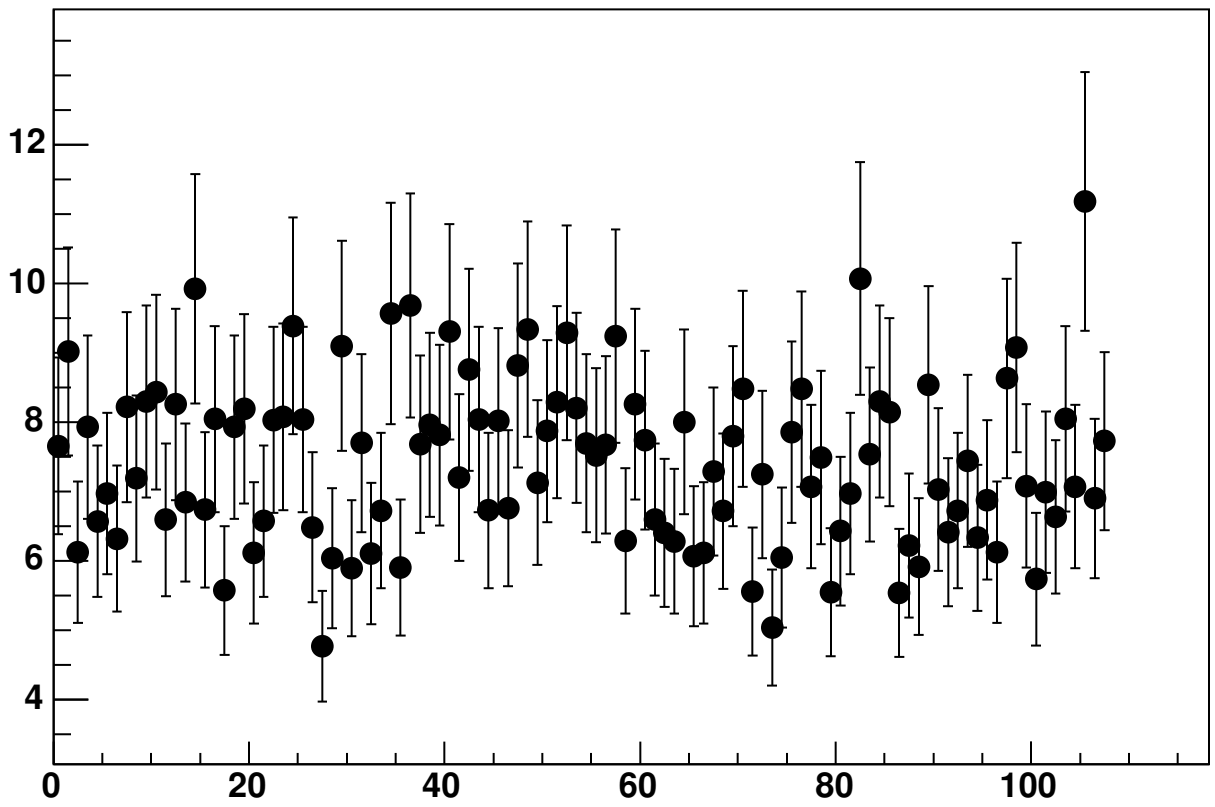
Enable 4, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



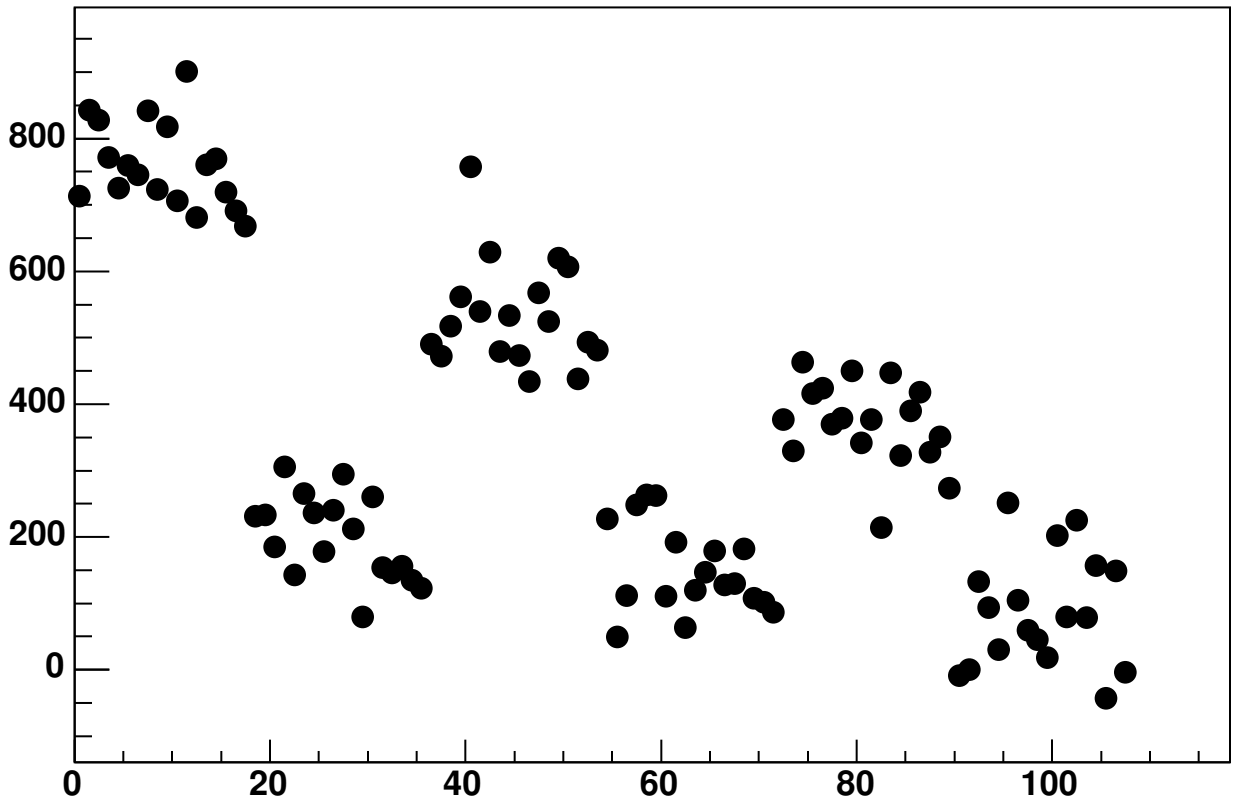
Enable 4, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



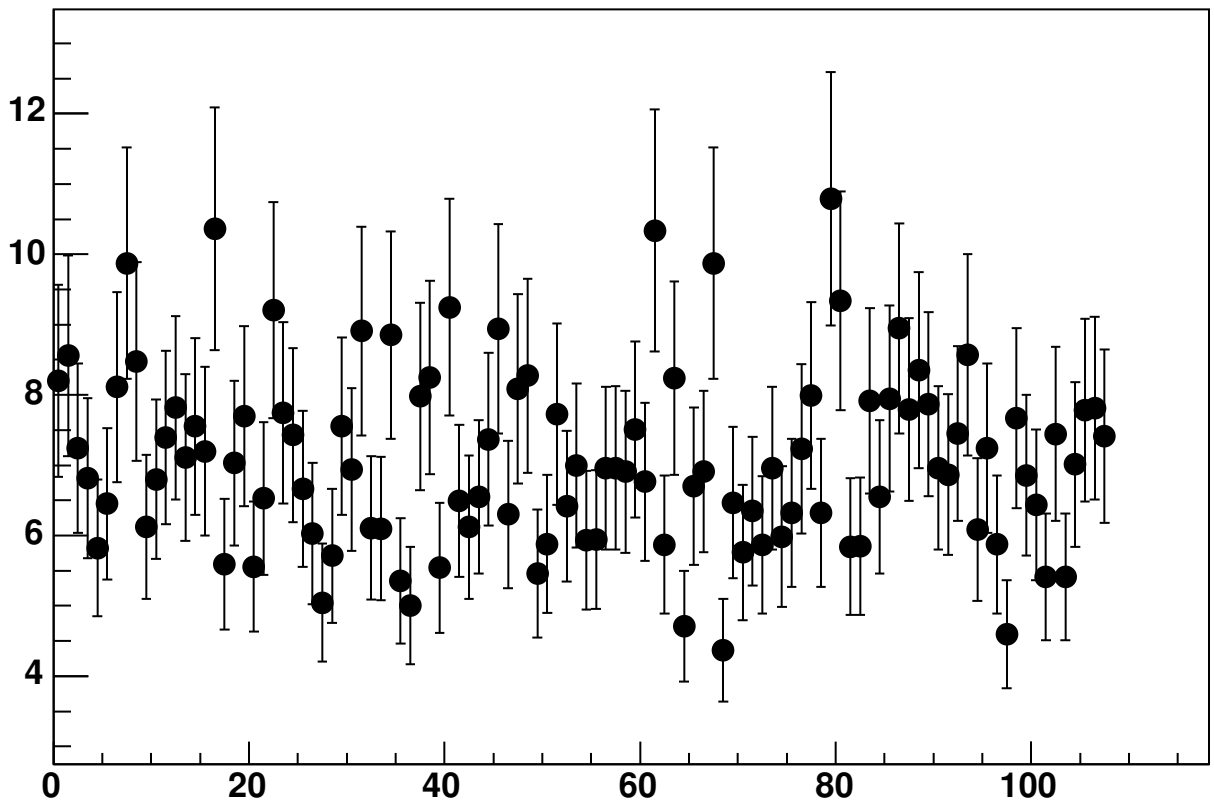
Enable 4, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



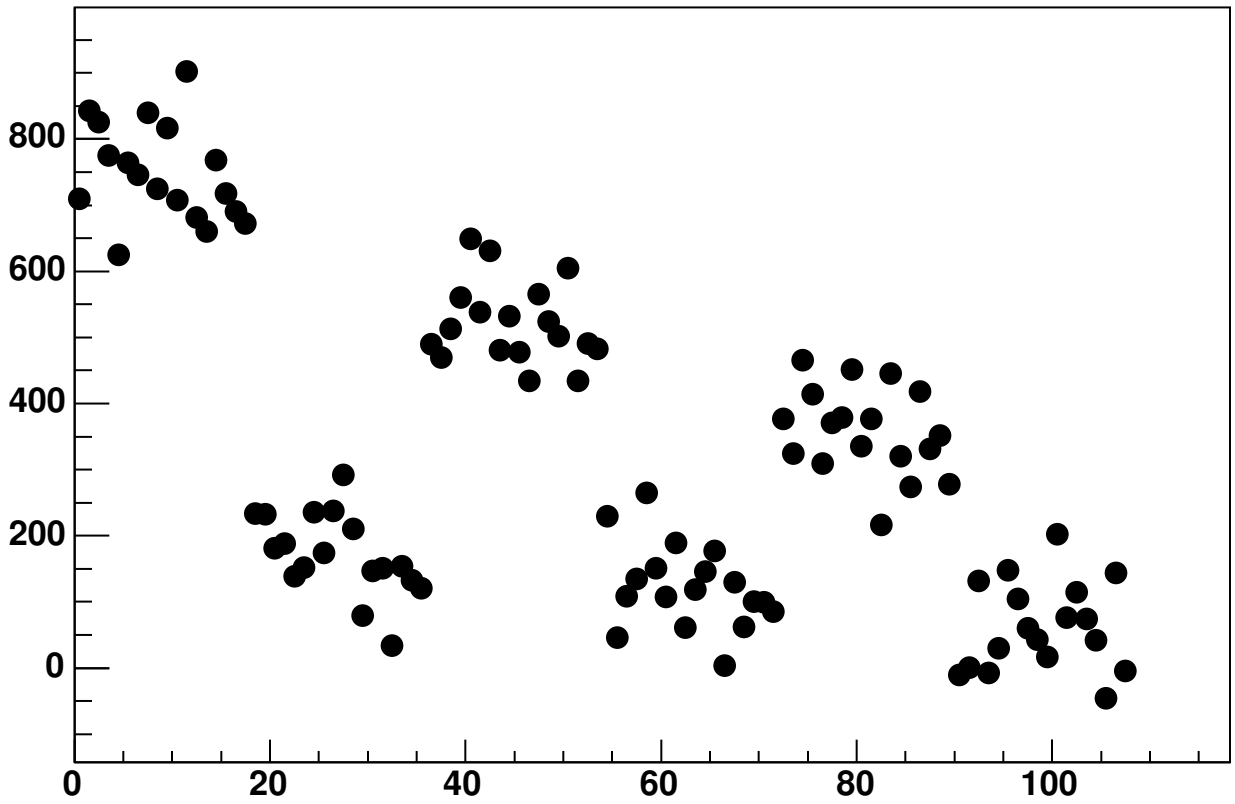
Enable 4, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



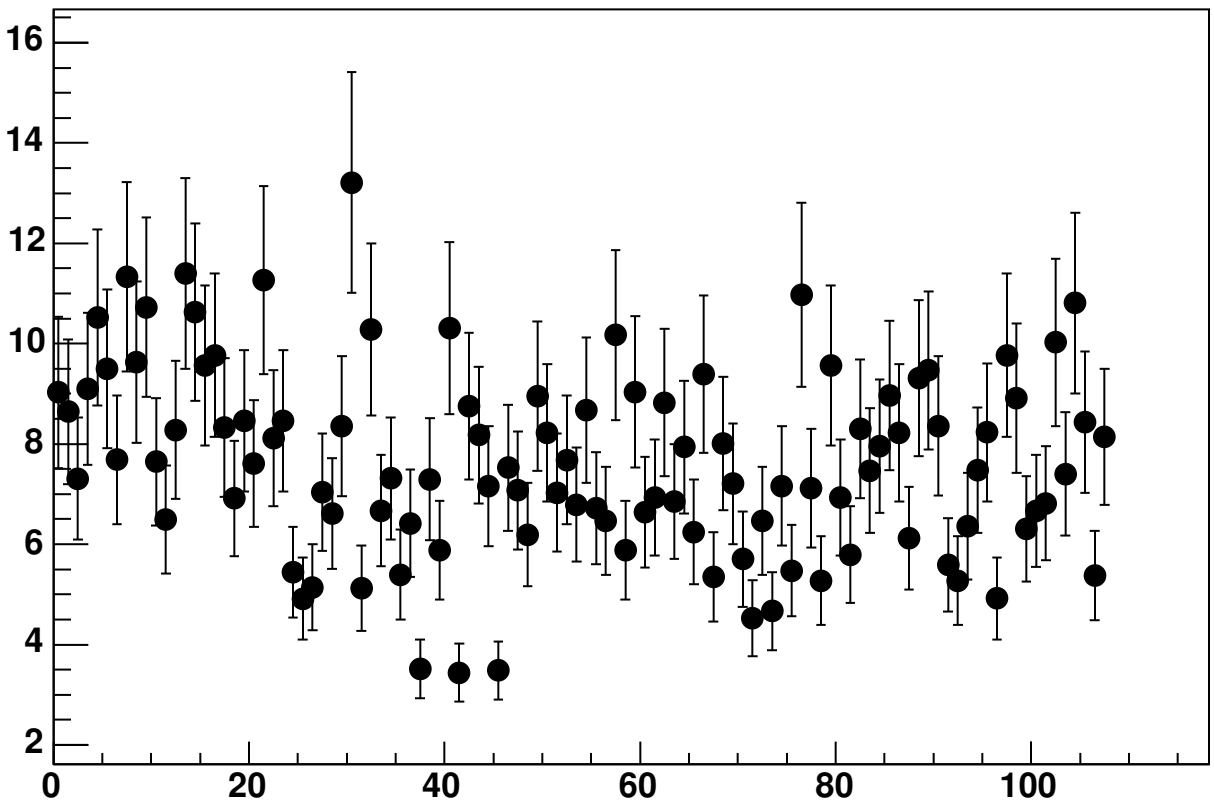
Enable 4, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



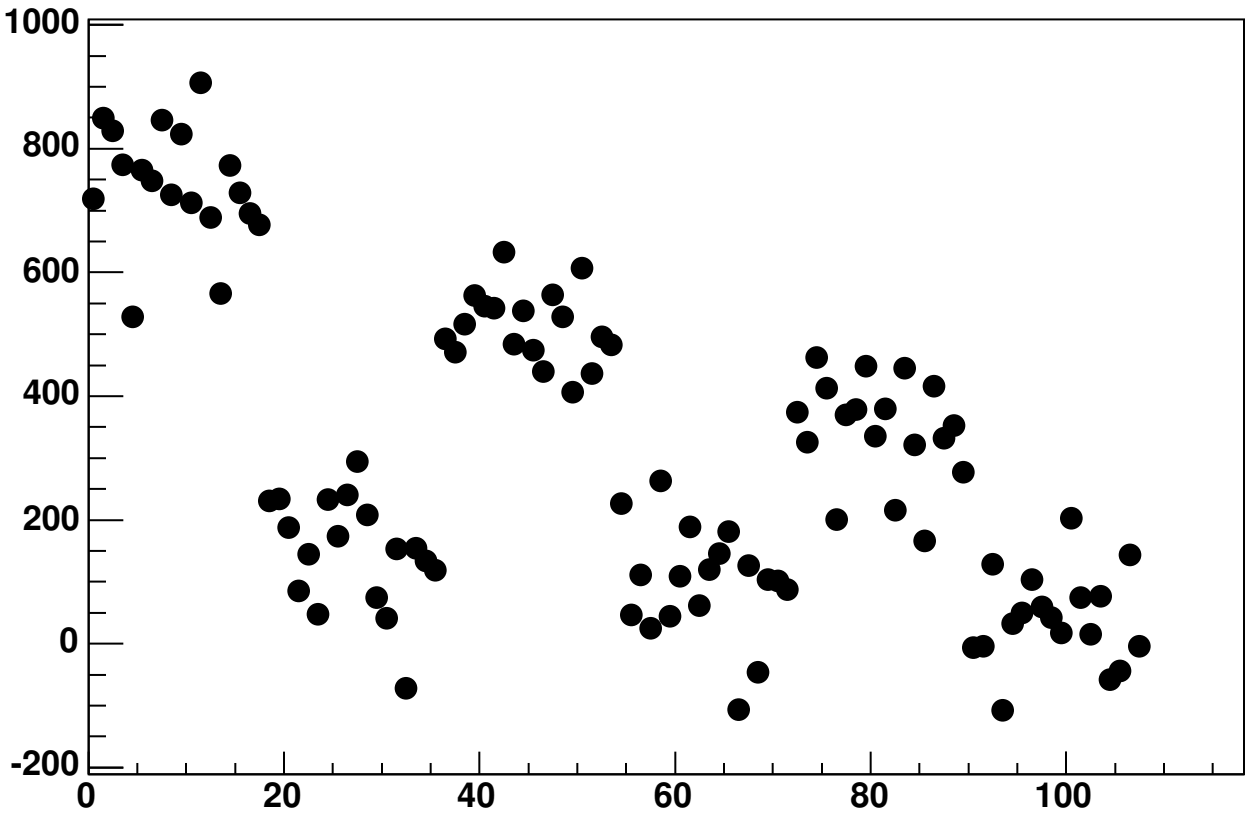
Enable 4, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



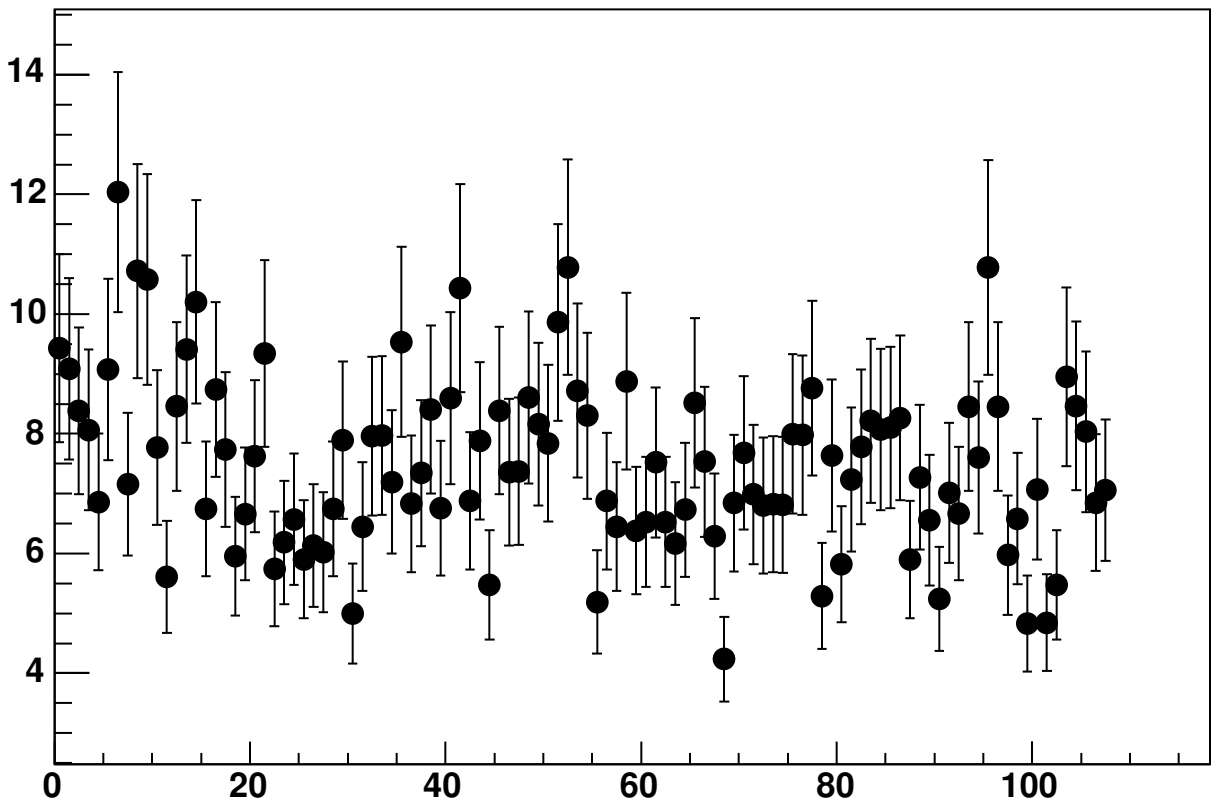
Enable 4, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



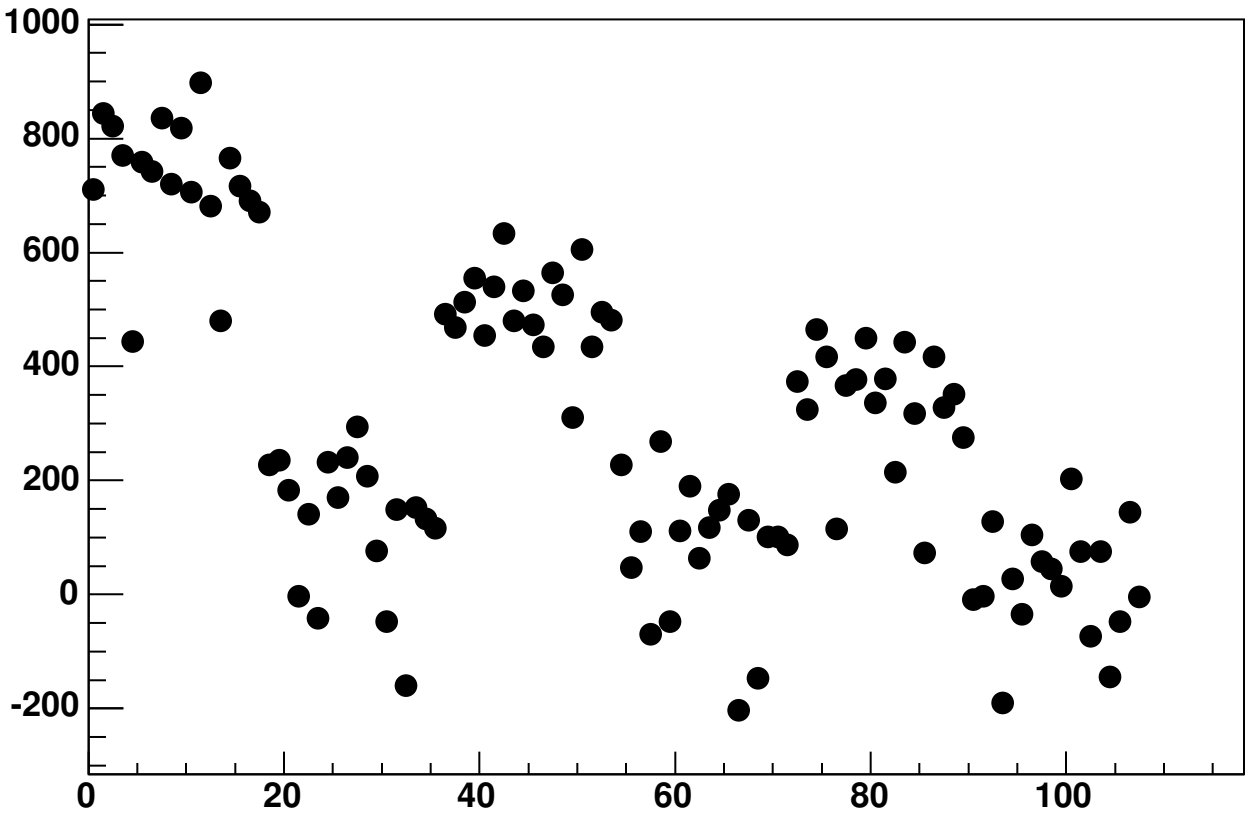
Enable 4, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



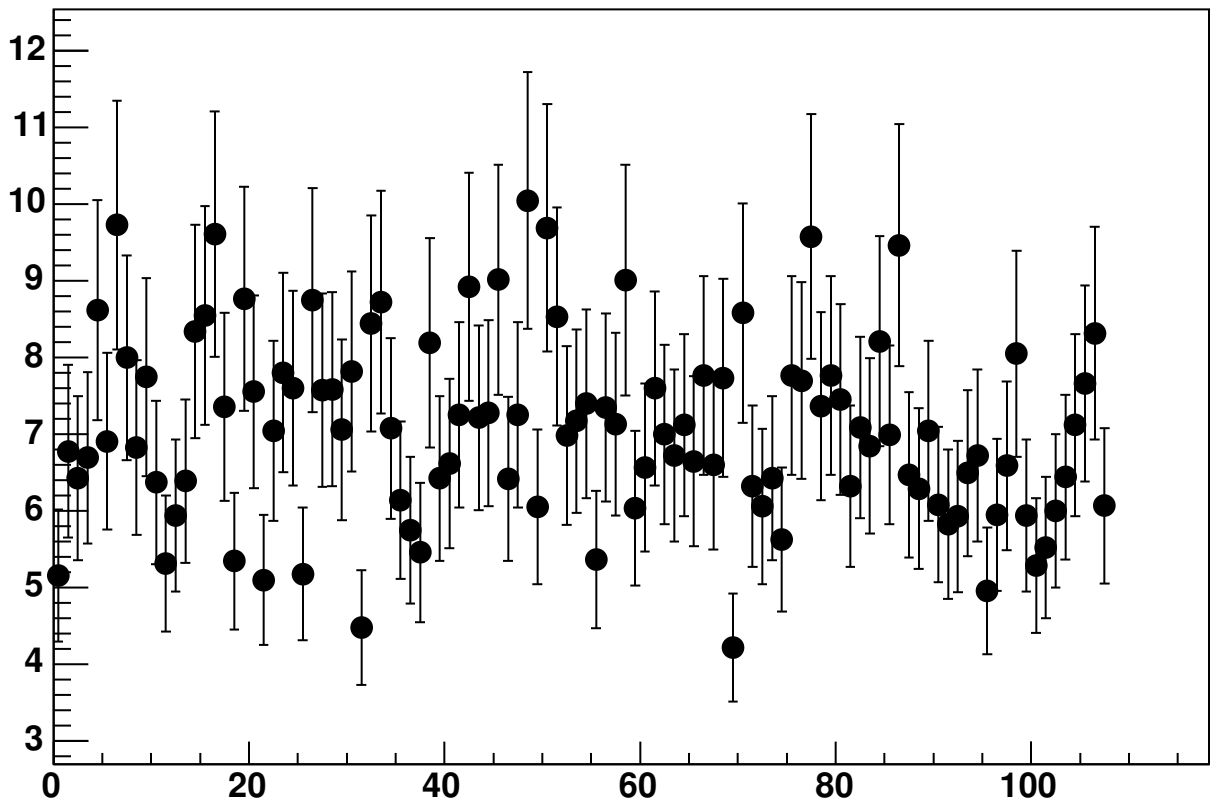
Enable 4, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



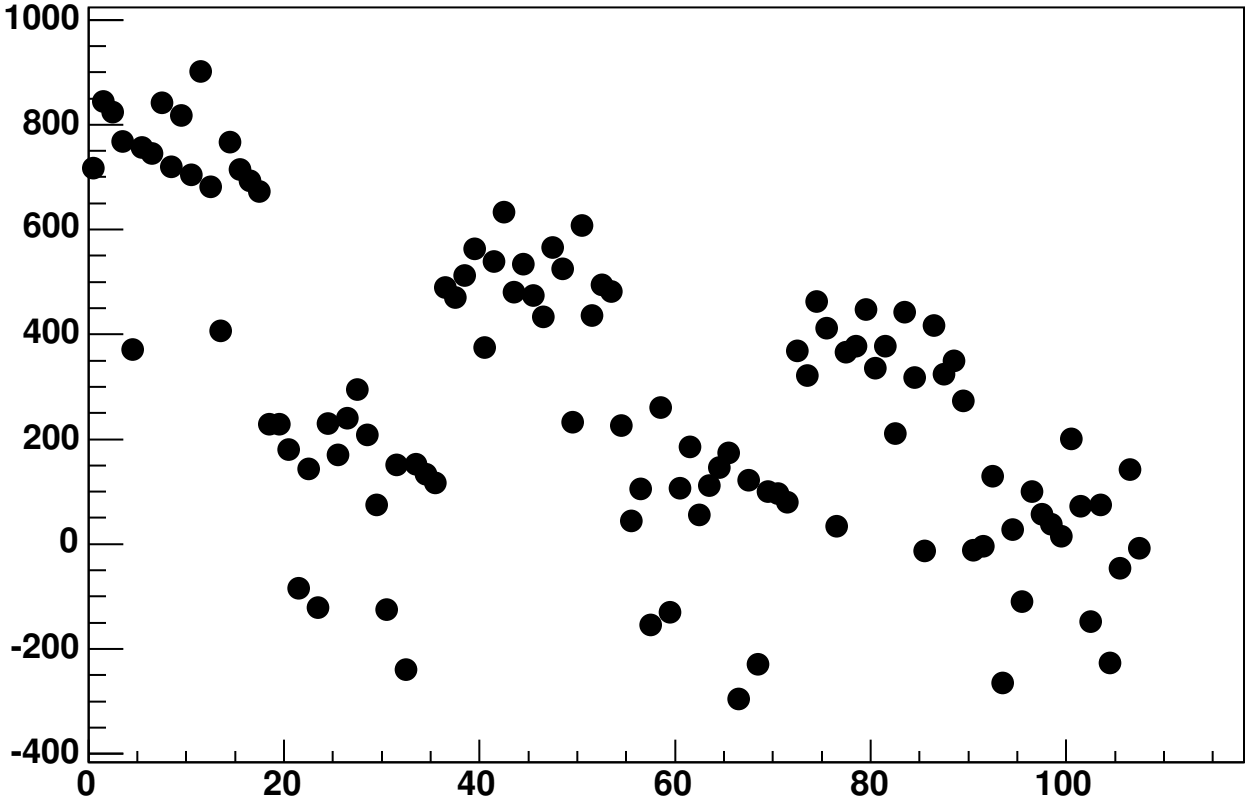
Enable 4, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



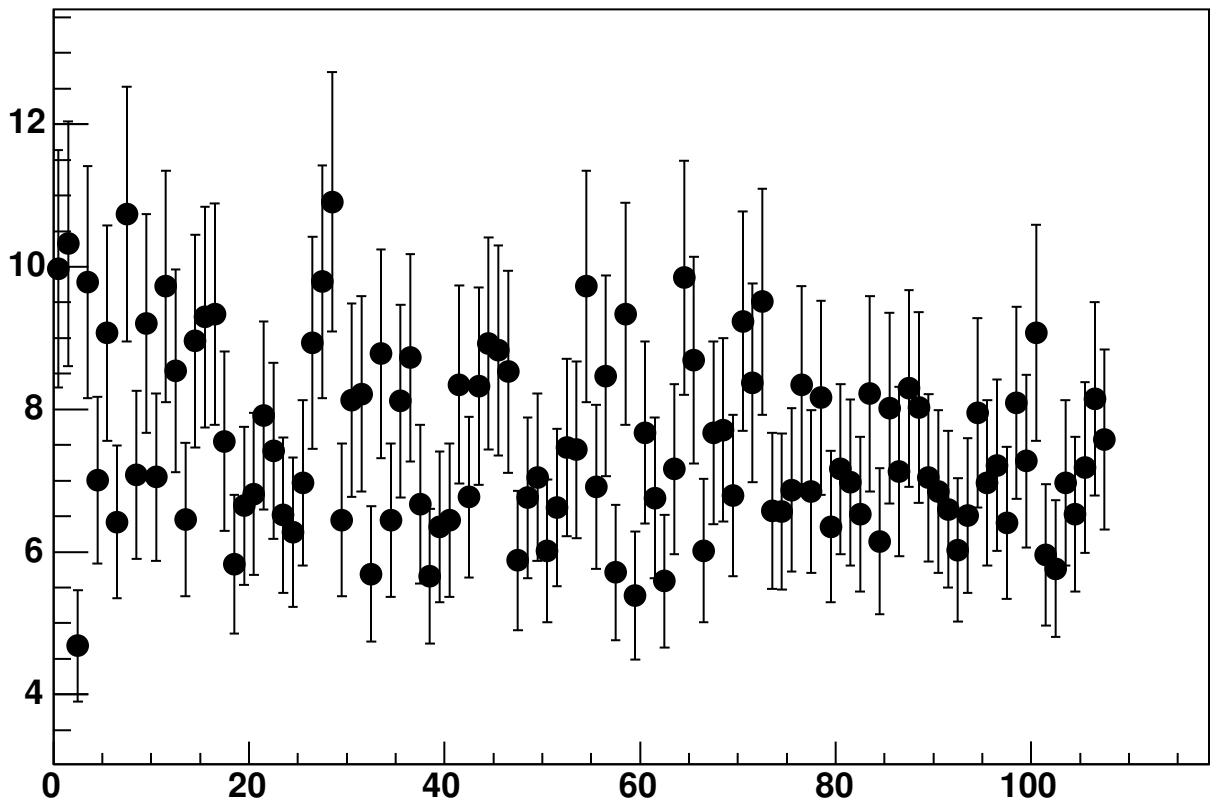
Enable 4, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



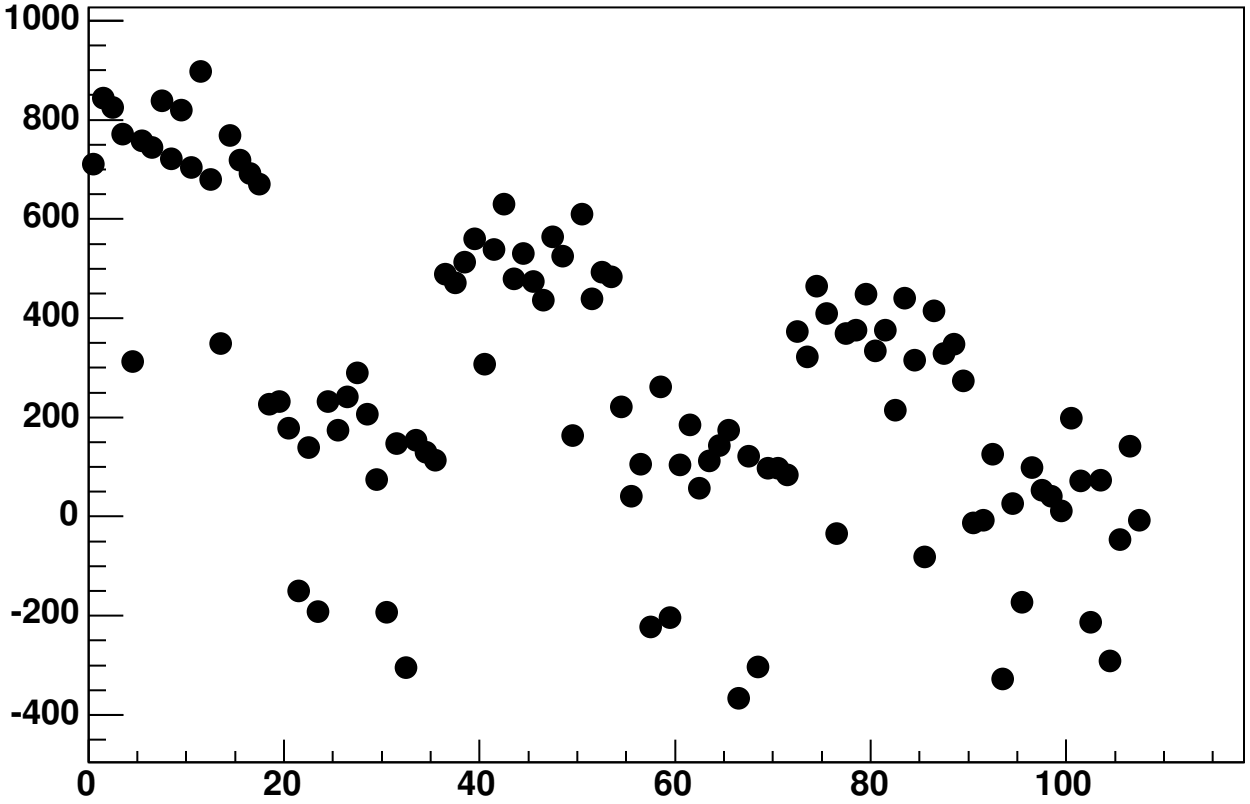
Enable 4, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



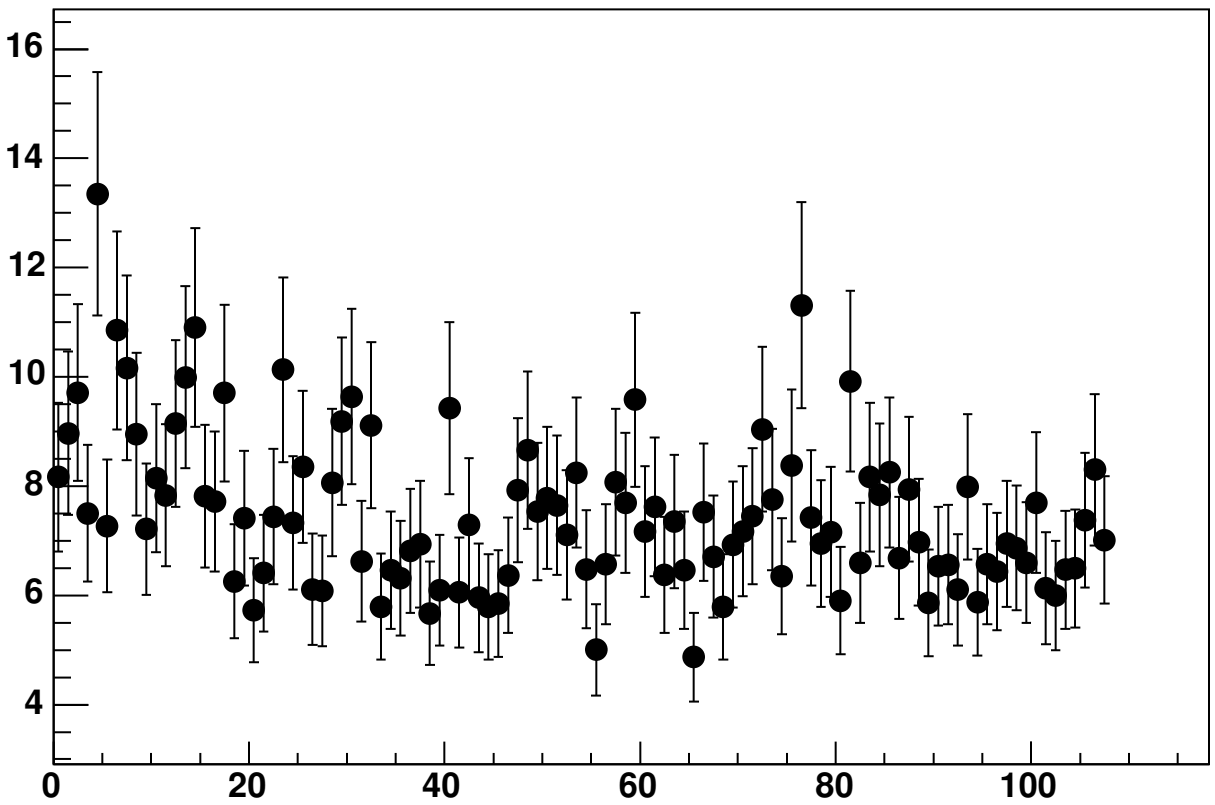
Enable 4, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



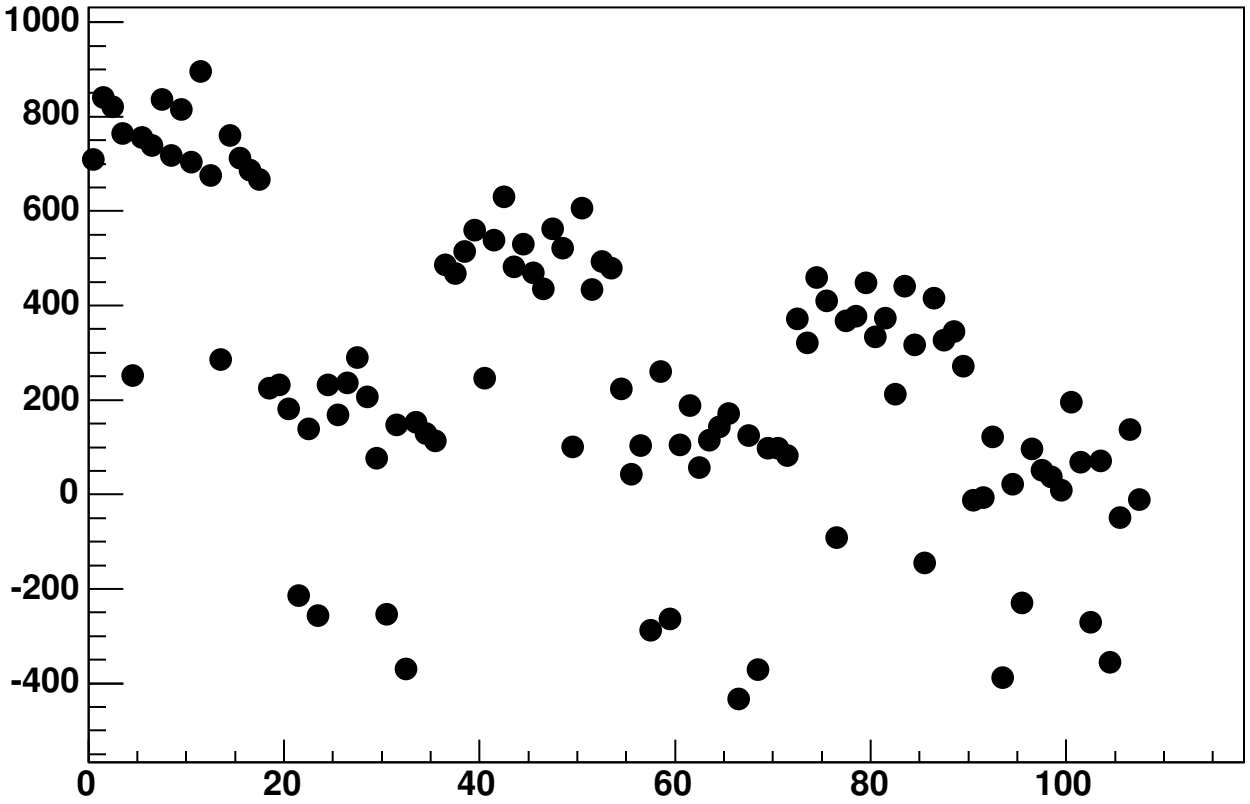
Enable 4, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



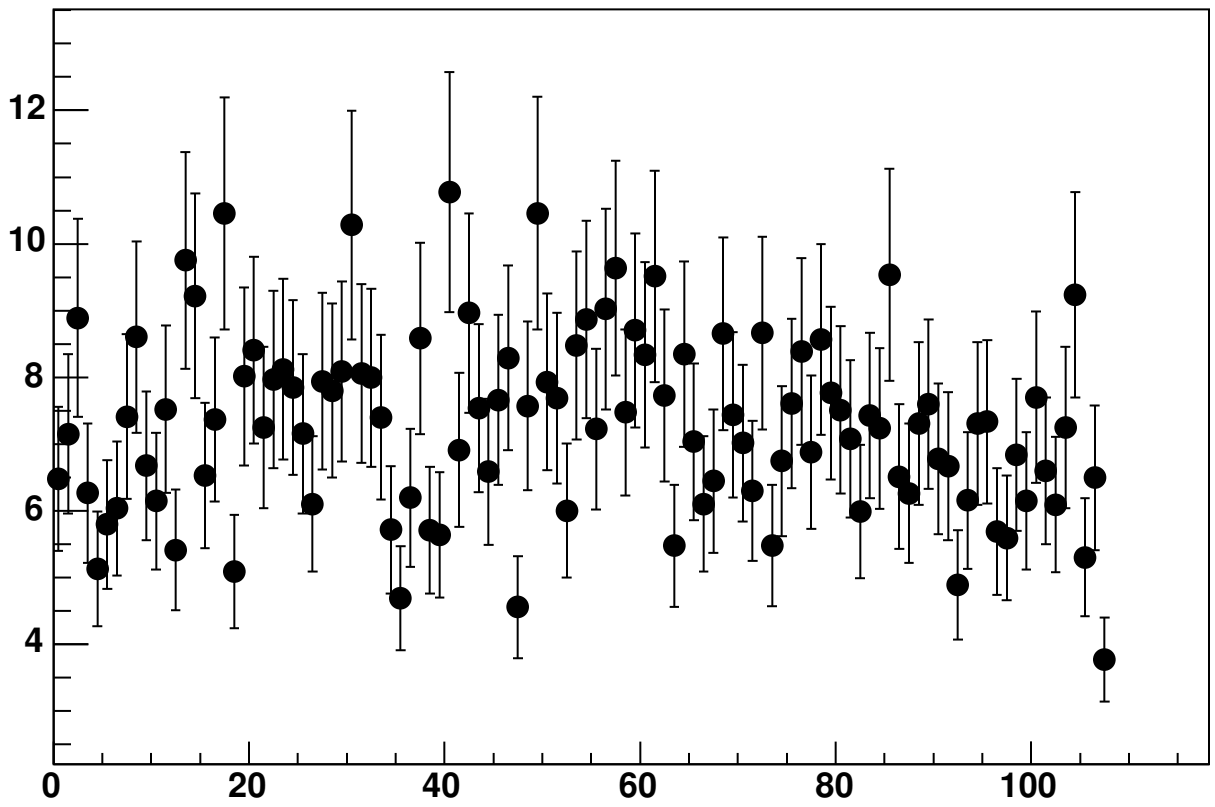
Enable 4, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



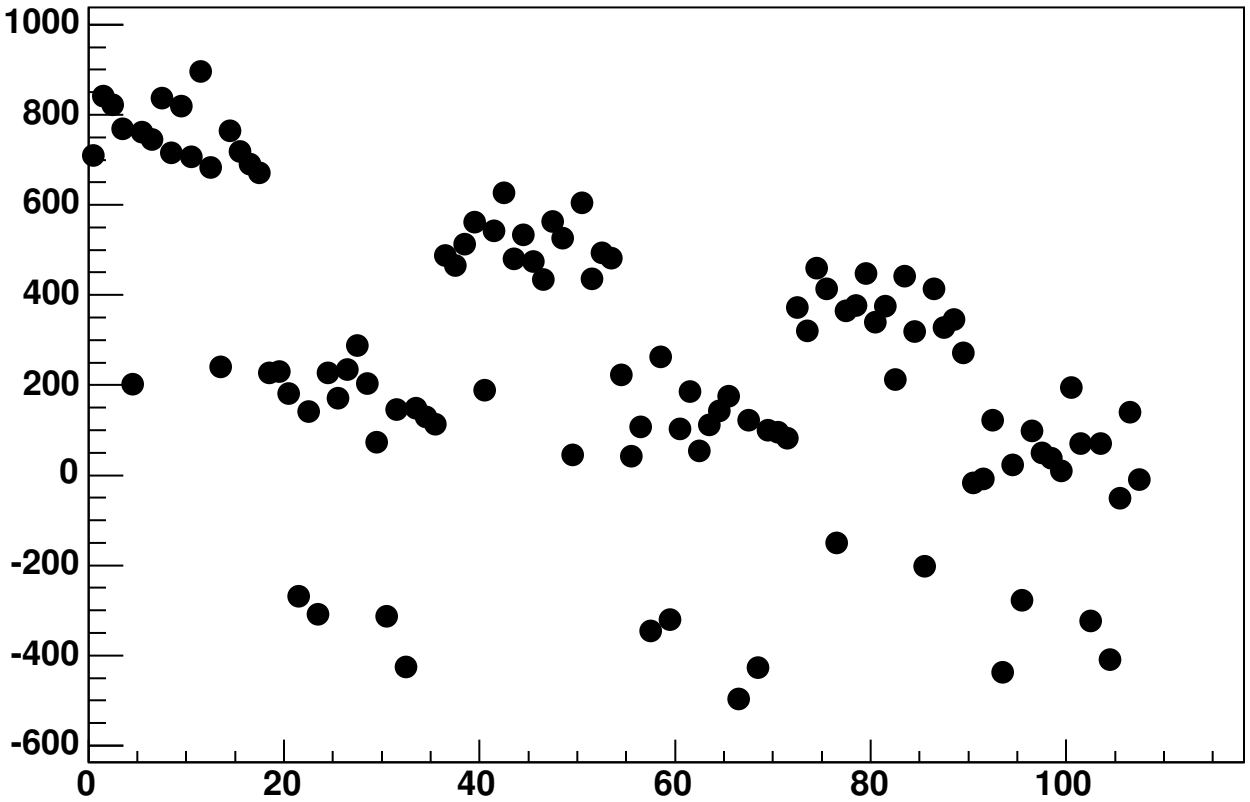
Enable 4, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



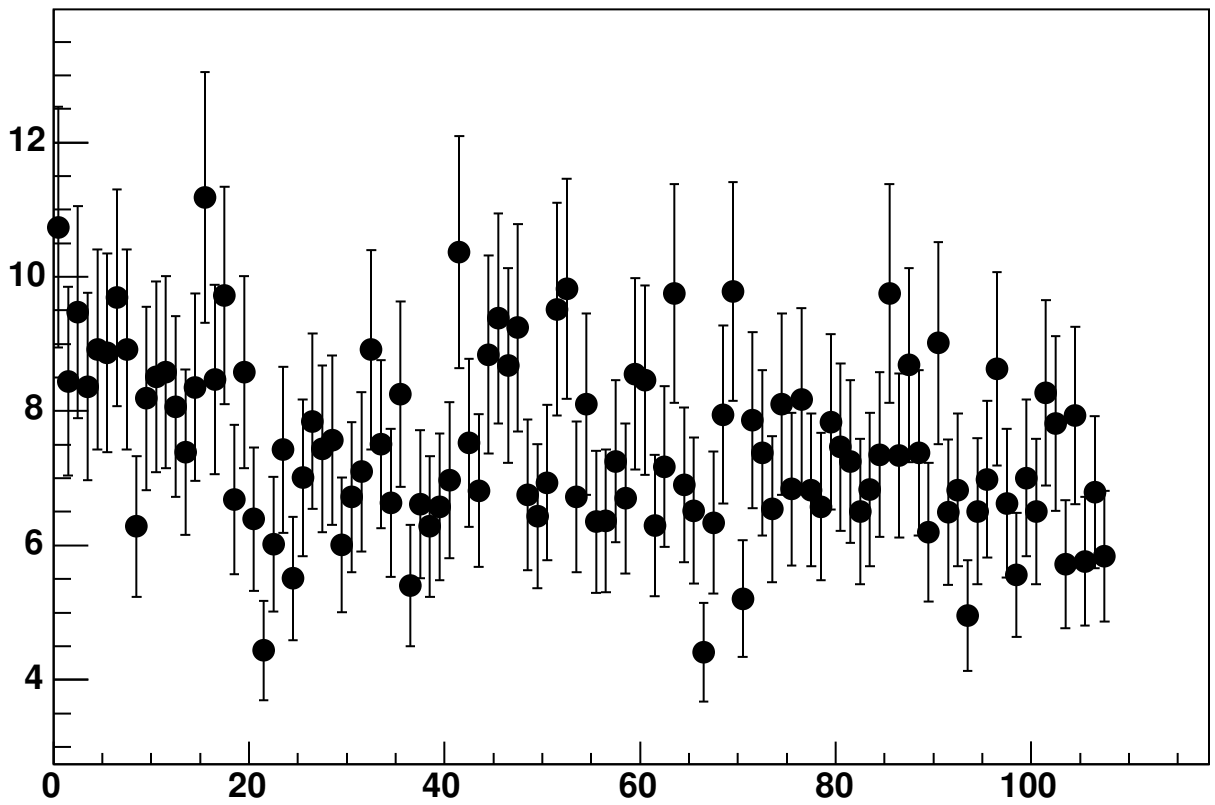
Enable 4, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



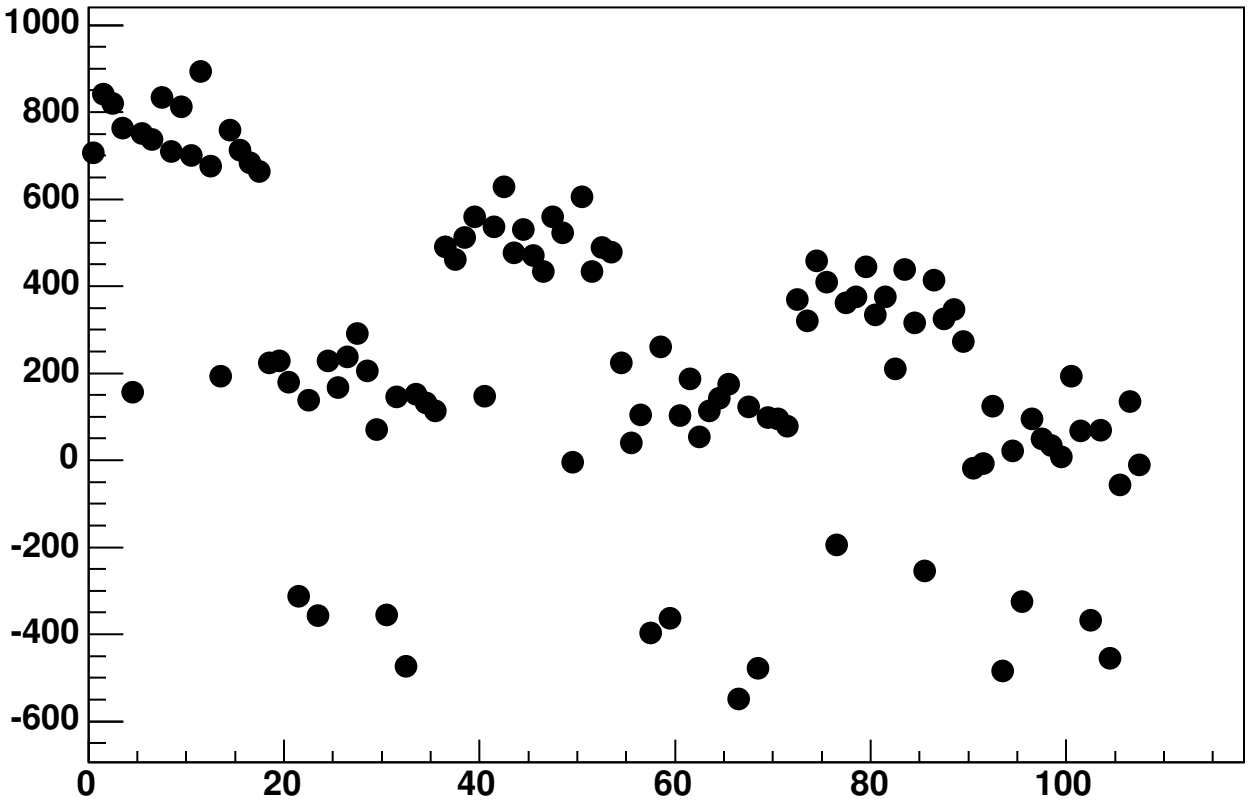
Enable 4, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



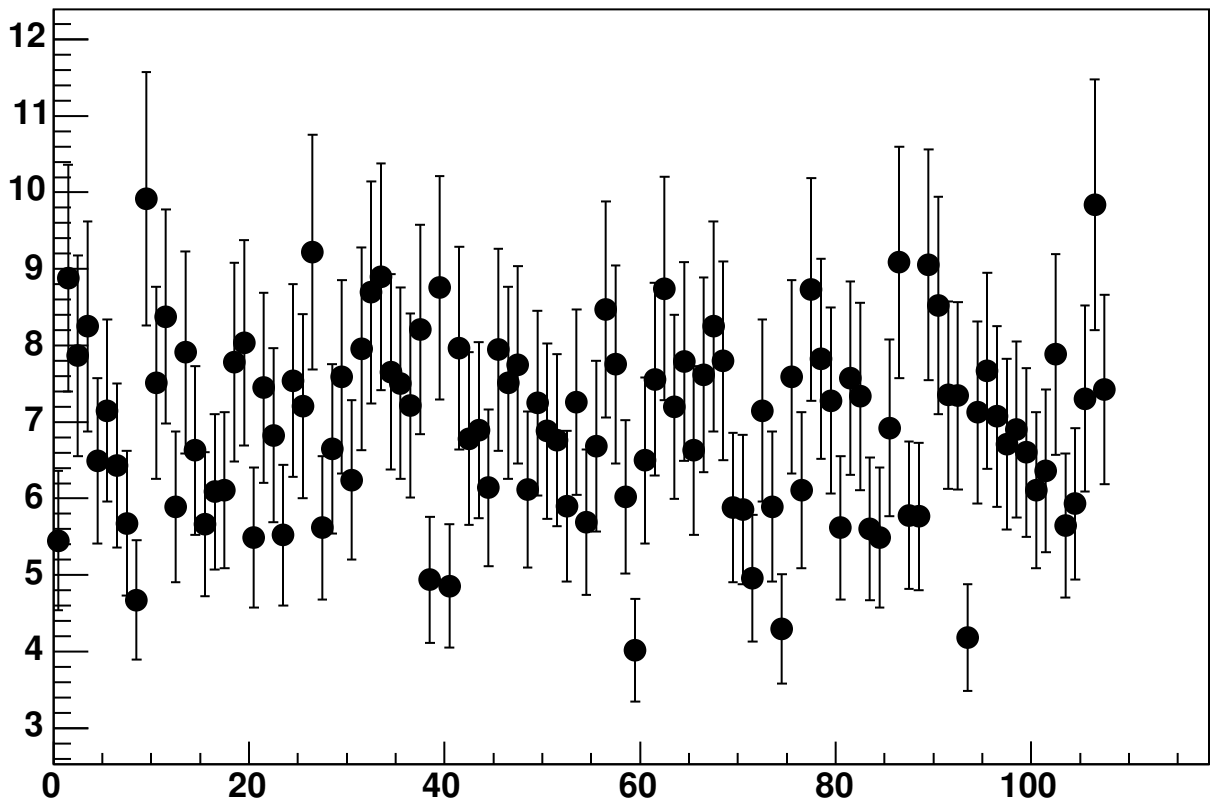
Enable 4, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan

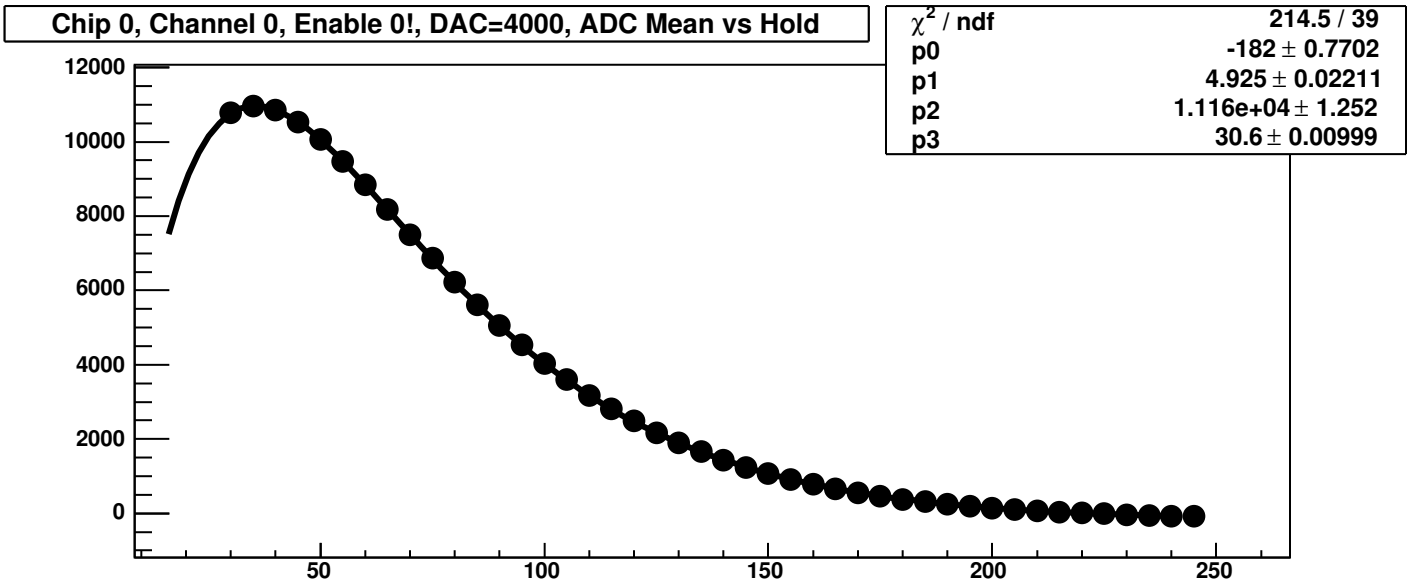


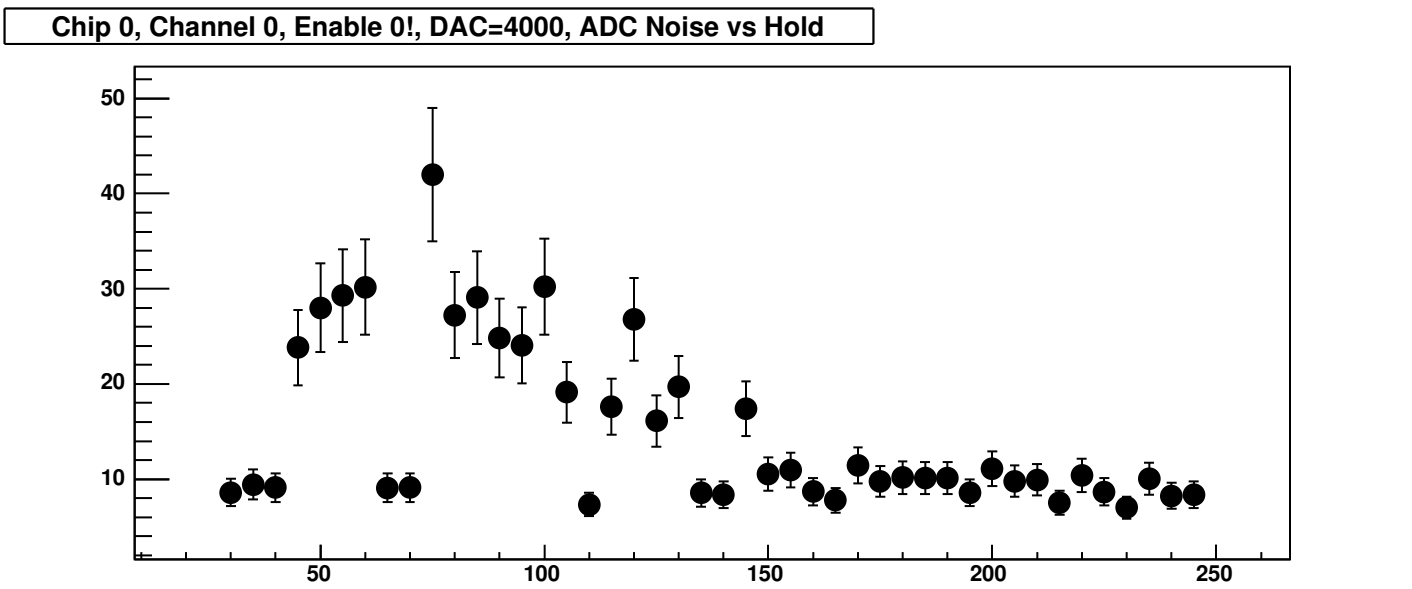
Enable 4, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan

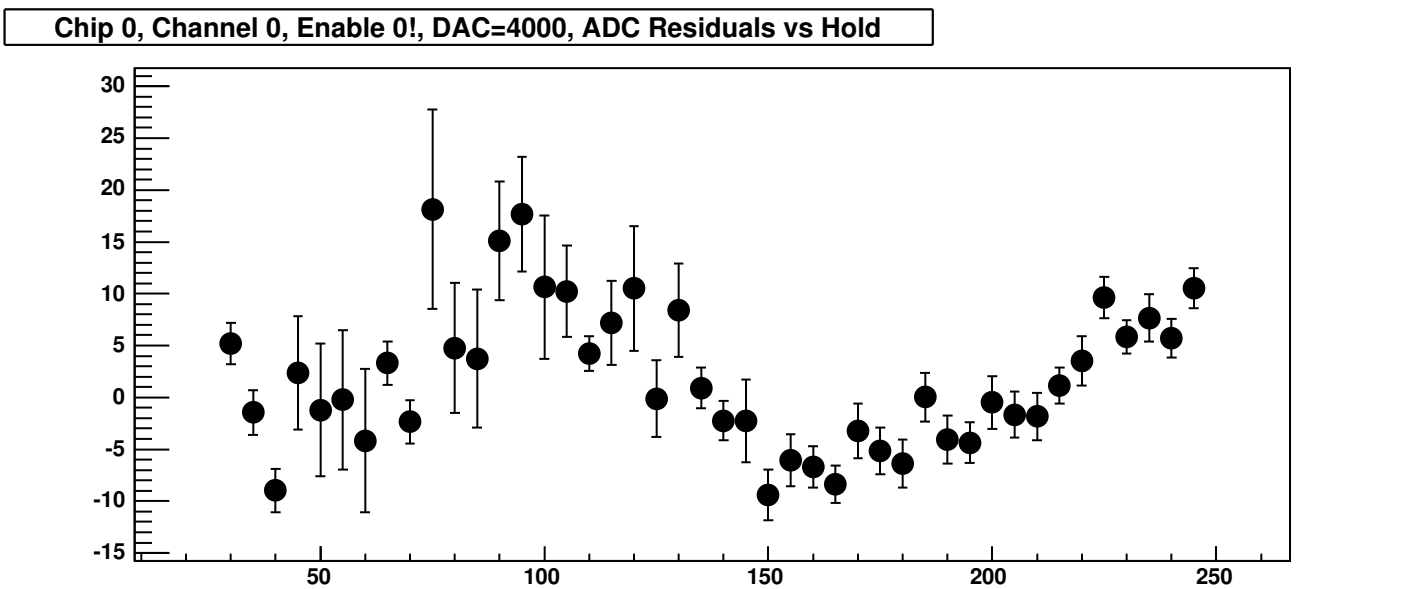


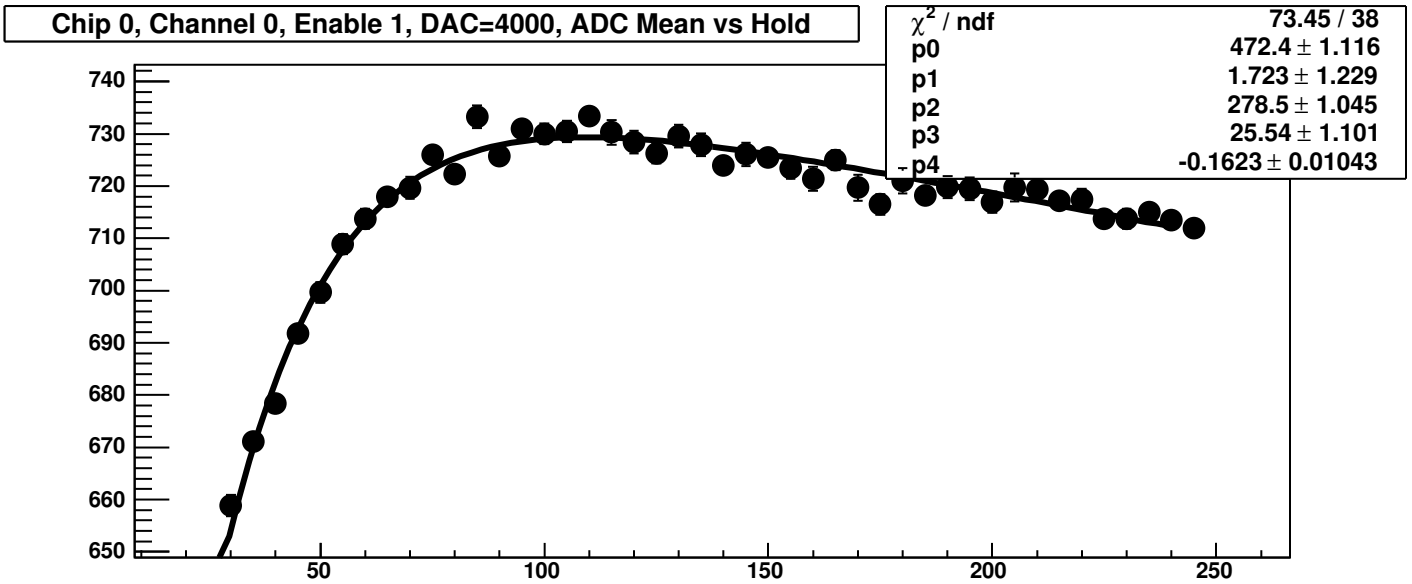
Enable 4, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan

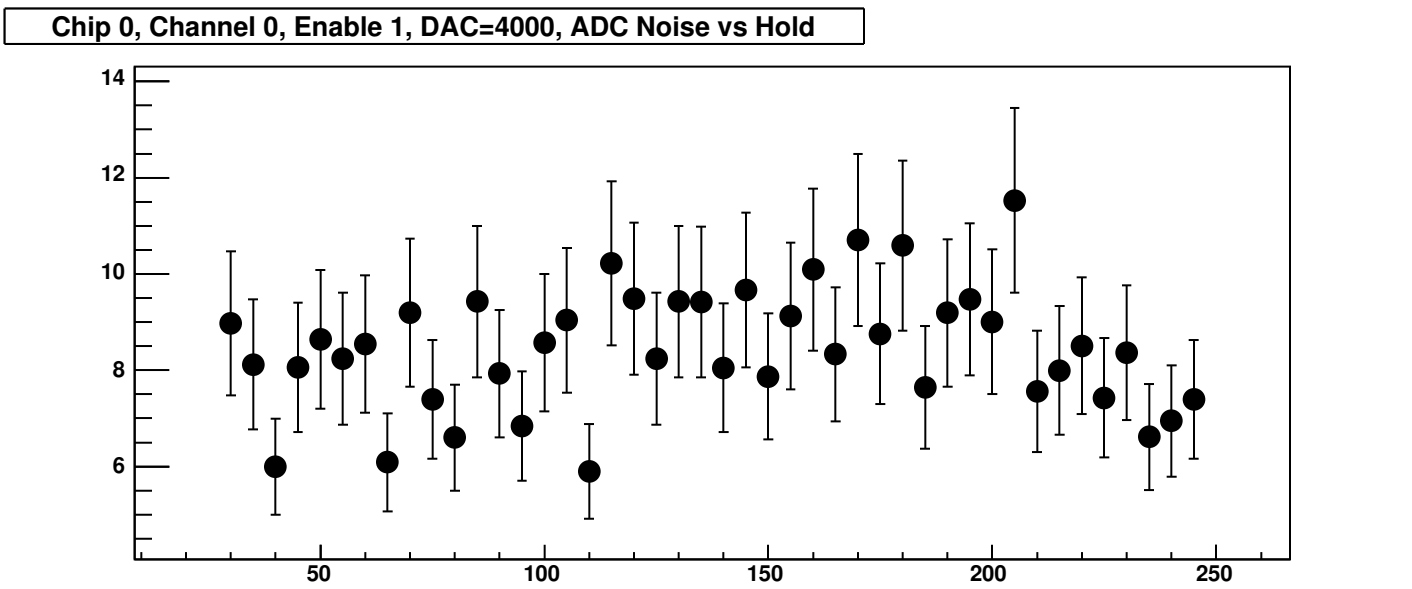


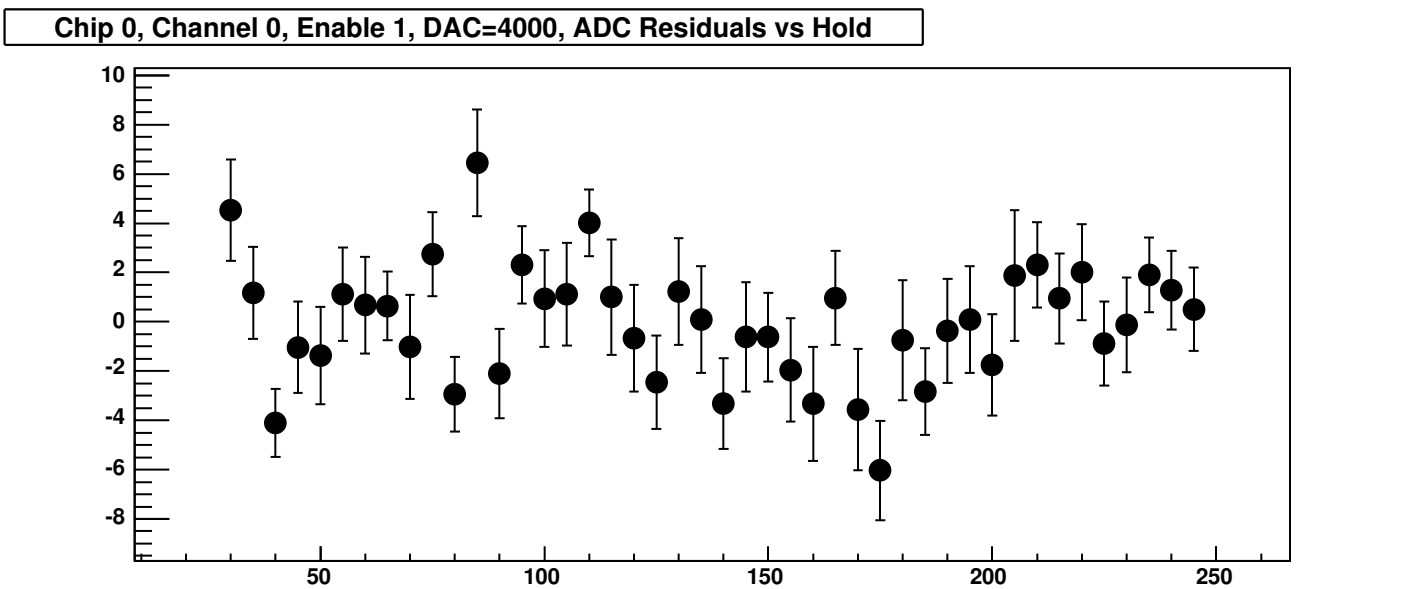

Chip 0, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 214.5 / 39
p0 -182 ± 0.7702
p1 4.925 ± 0.02211
p2 1.116e+04 ± 1.252
p3 30.6 ± 0.00999
12000
10000
8000
6000
4000
2000
0
50
100
150
200
250


Chip 0, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
50
100
150
200
250

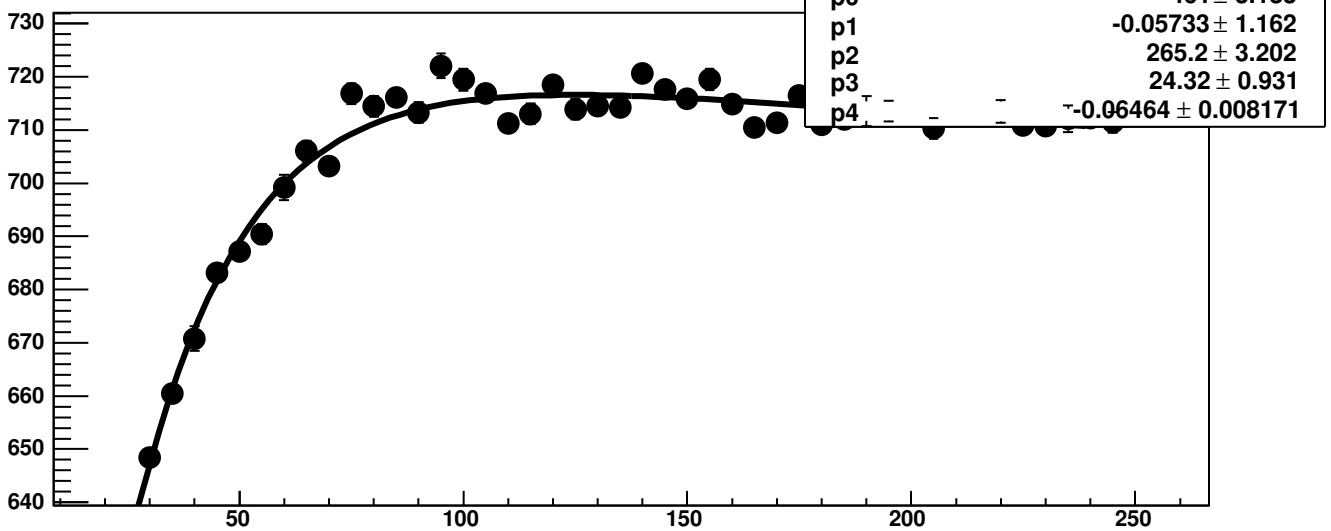

Chip 0, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
-15
50
100
150
200
250


Chip 0, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 73.45 / 38
p0 472.4 ± 1.116
p1 1.723 ± 1.229
p2 278.5 ± 1.045
p3 25.54 ± 1.101
p4 -0.1623 ± 0.01043
740
730
720
710
700
690
680
670
660
650
50
100
150
200
250

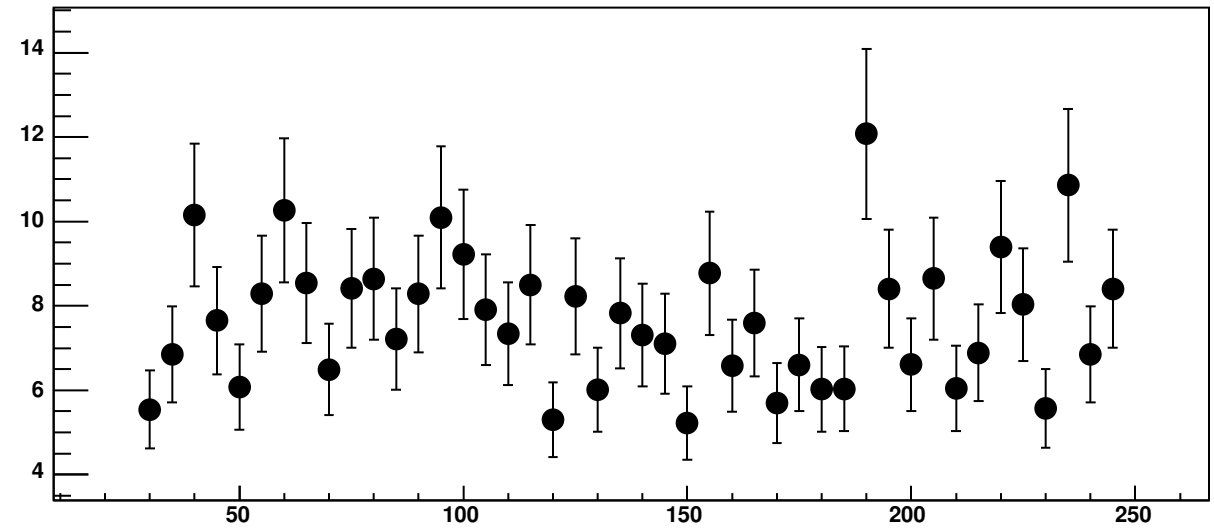

Chip 0, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
50
100
150
200
250


Chip 0, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

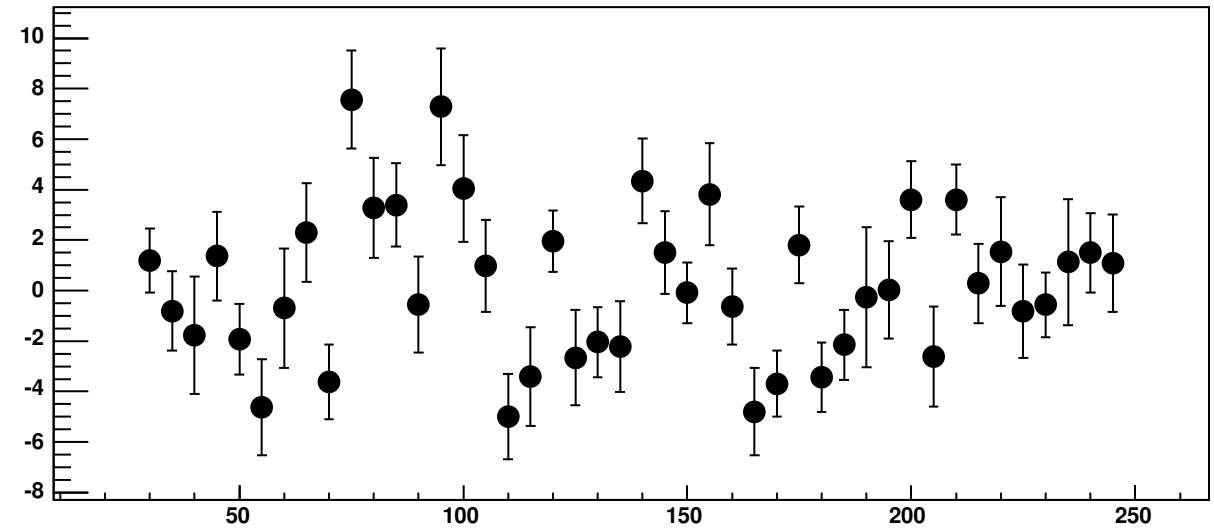
Chip 0, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold

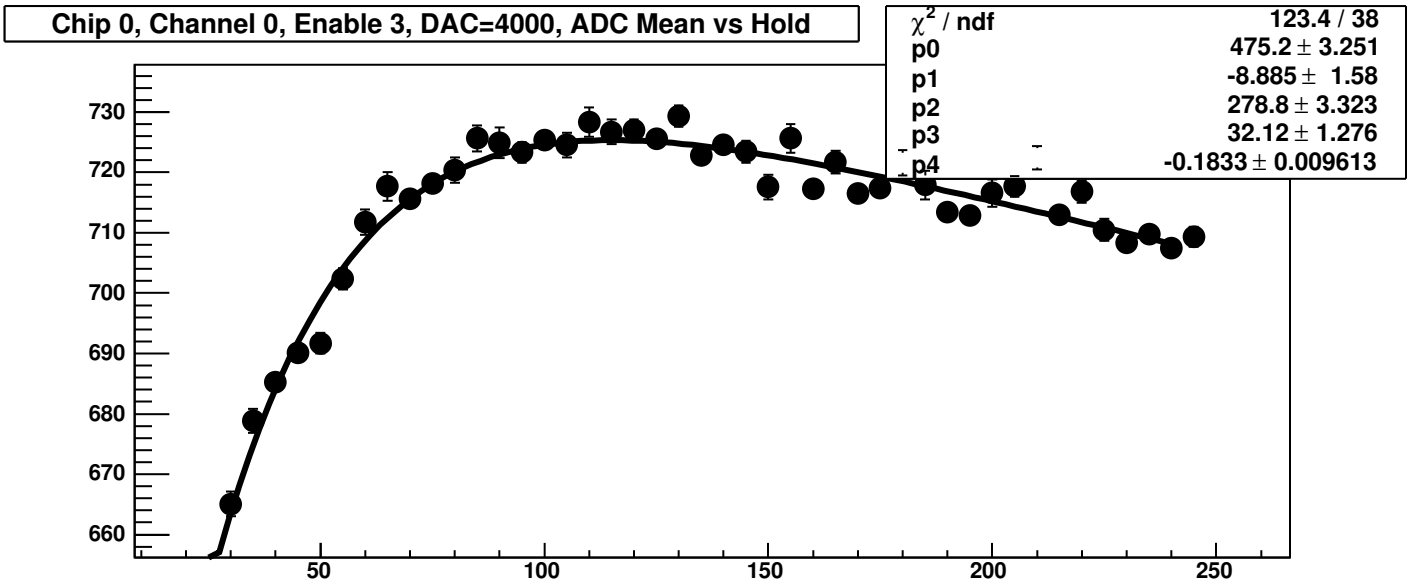


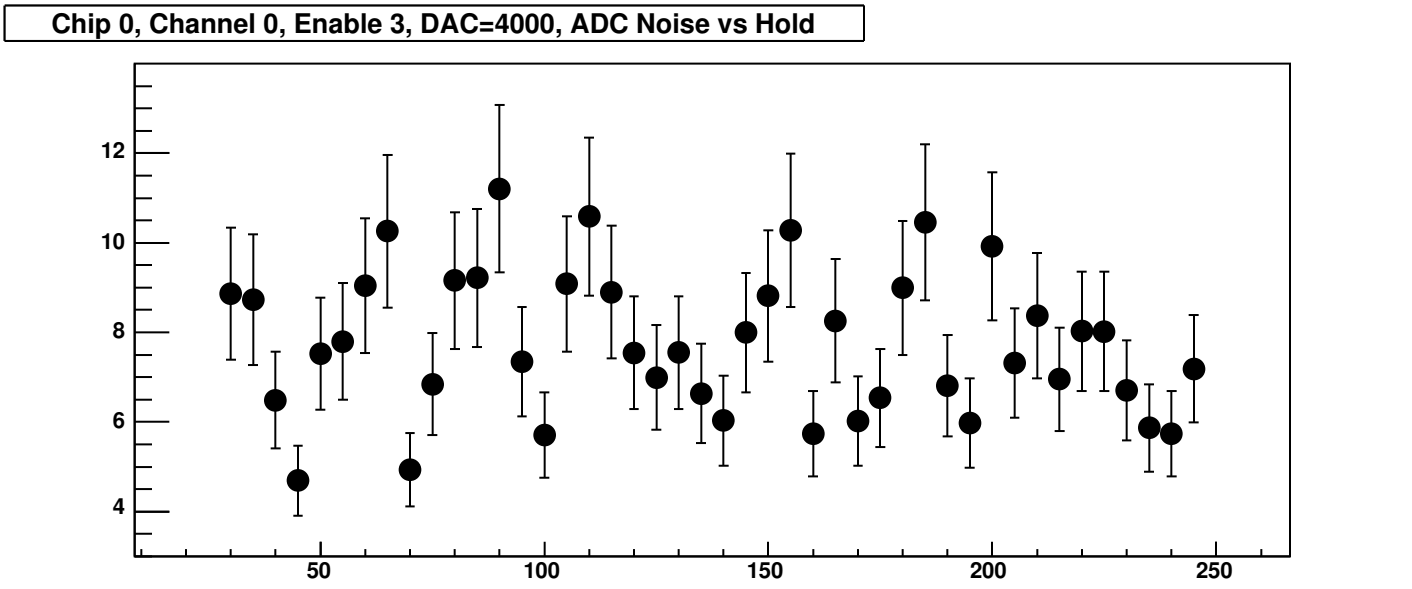
Chip 0, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold

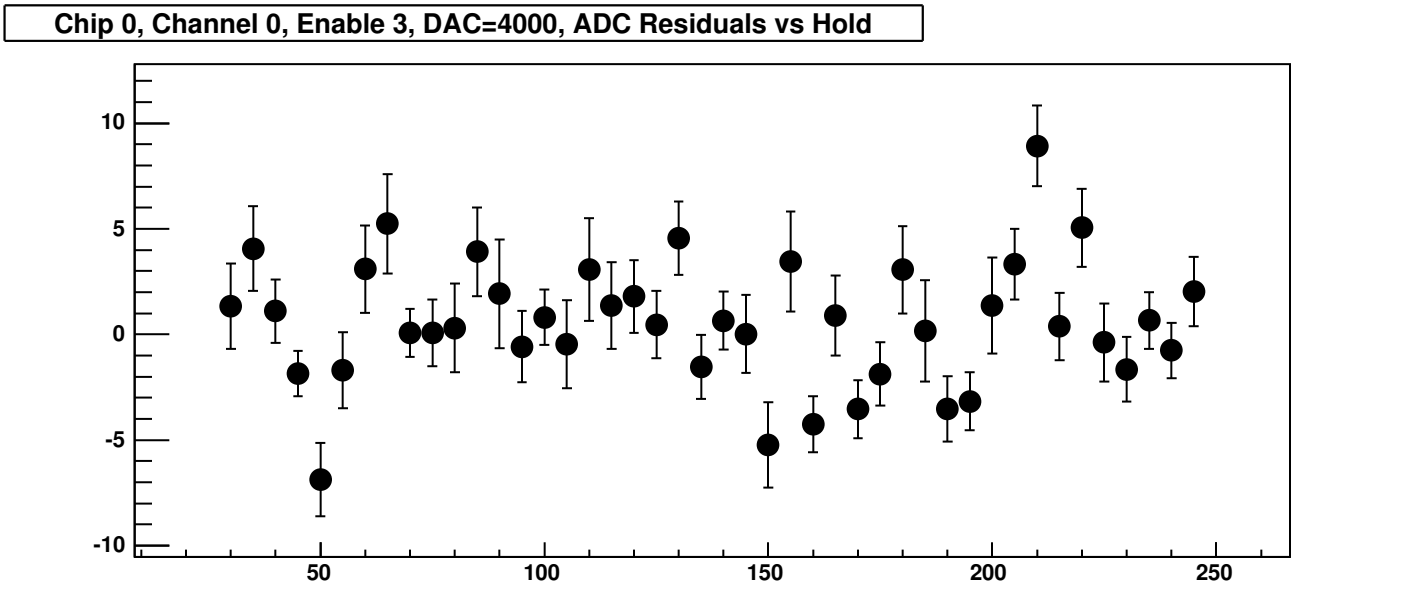


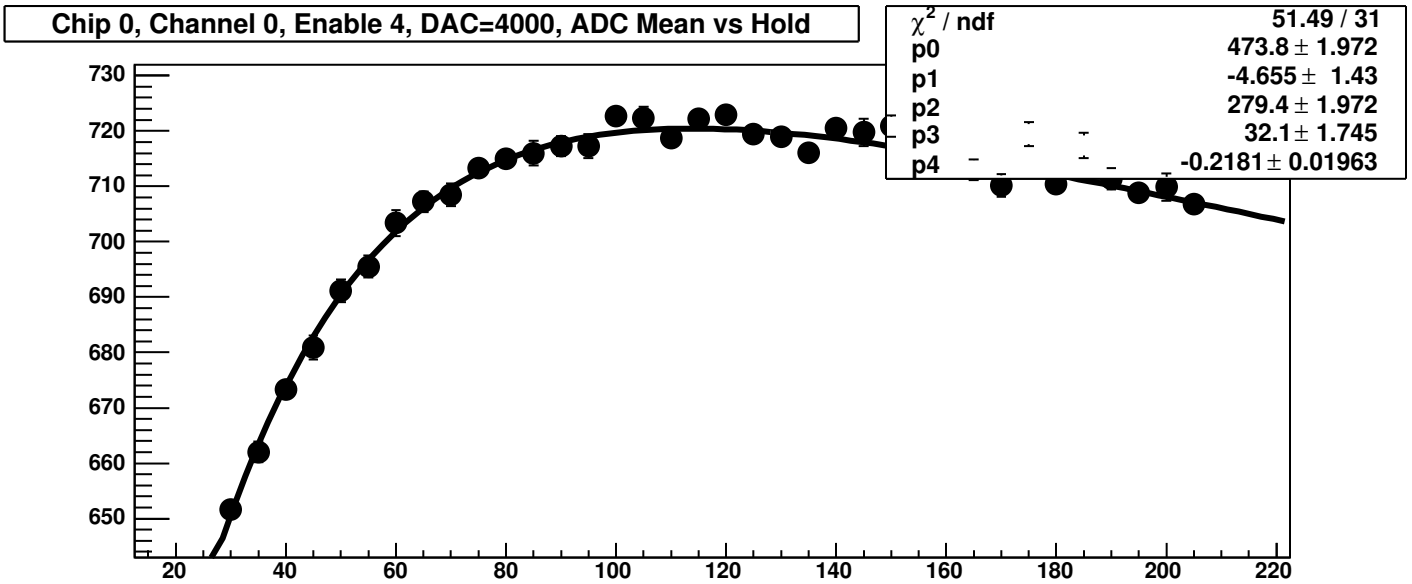
Chip 0, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold

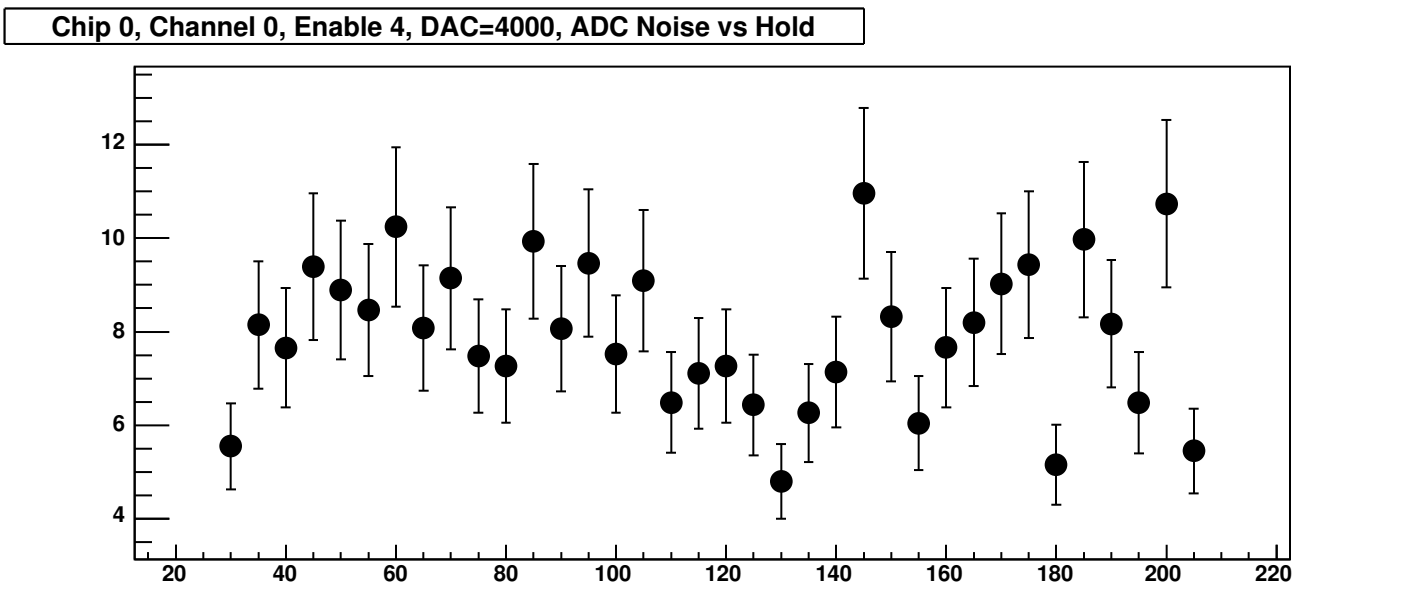


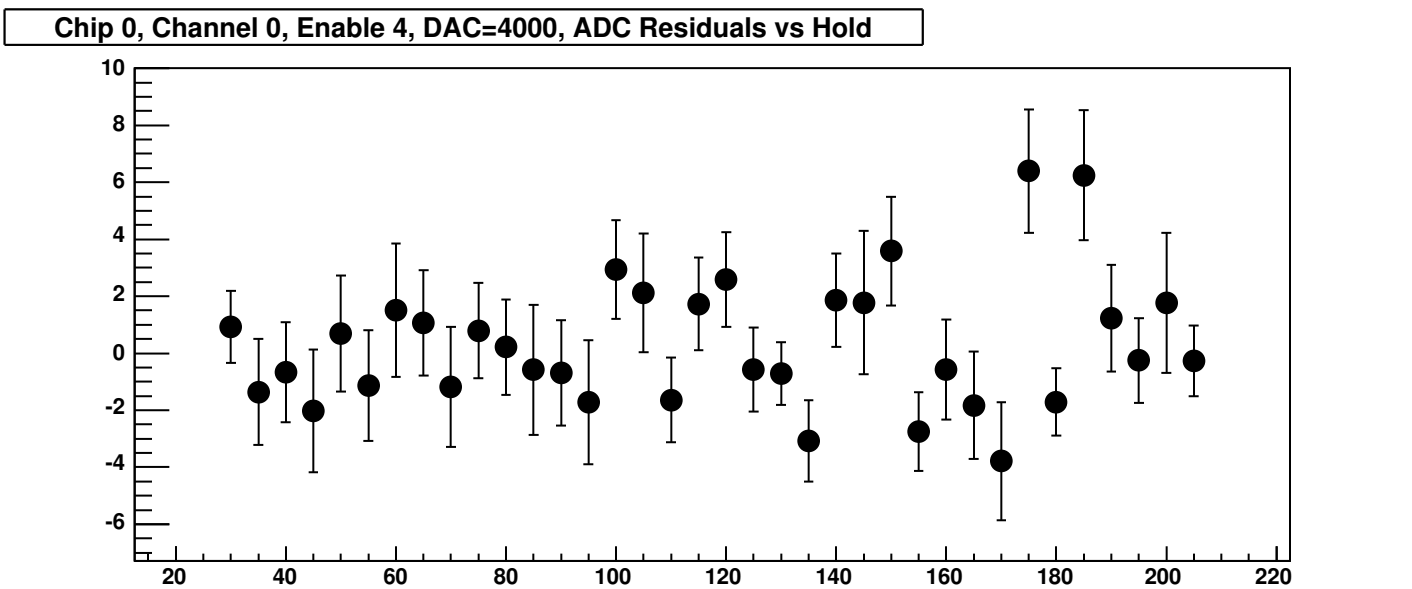

Chip 0, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 123.4 / 38
p0 475.2 ± 3.251
p1 -8.885 ± 1.58
p2 278.8 ± 3.323
p3 32.12 ± 1.276
p4 -0.1833 ± 0.009613
730
720
710
700
690
680
670
660
50
100
150
200
250


Chip 0, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250

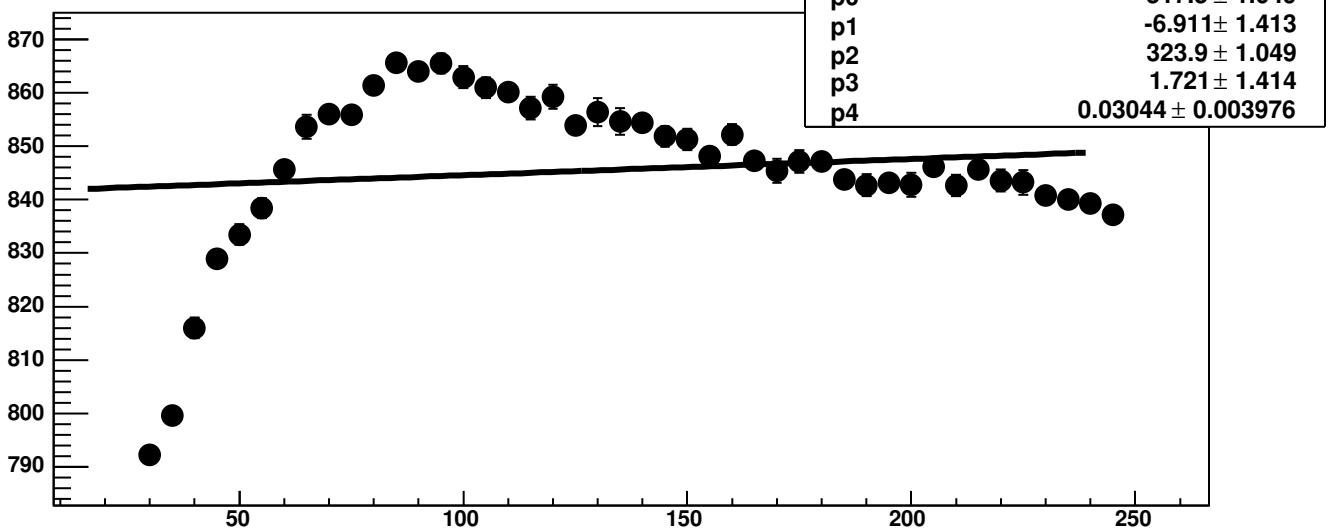

Chip 0, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250


Chip 0, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 51.49 / 31
p0 473.8 ± 1.972
p1 -4.655 ± 1.43
p2 279.4 ± 1.972
p3 32.1 ± 1.745
p4 -0.2181 ± 0.01963
730
720
710
700
690
680
670
660
650
20
40
60
80
100
120
140
160
180
200
220

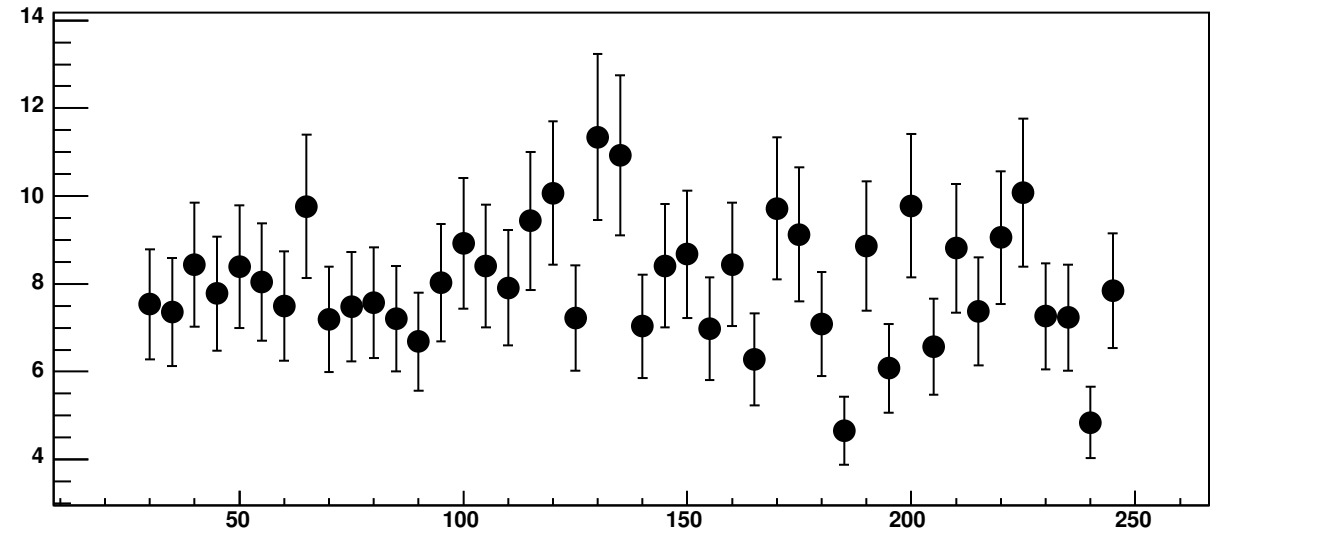

Chip 0, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
20
40
60
80
100
120
140
160
180
200
220


Chip 0, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
20
40
60
80
100
120
140
160
180
200
220

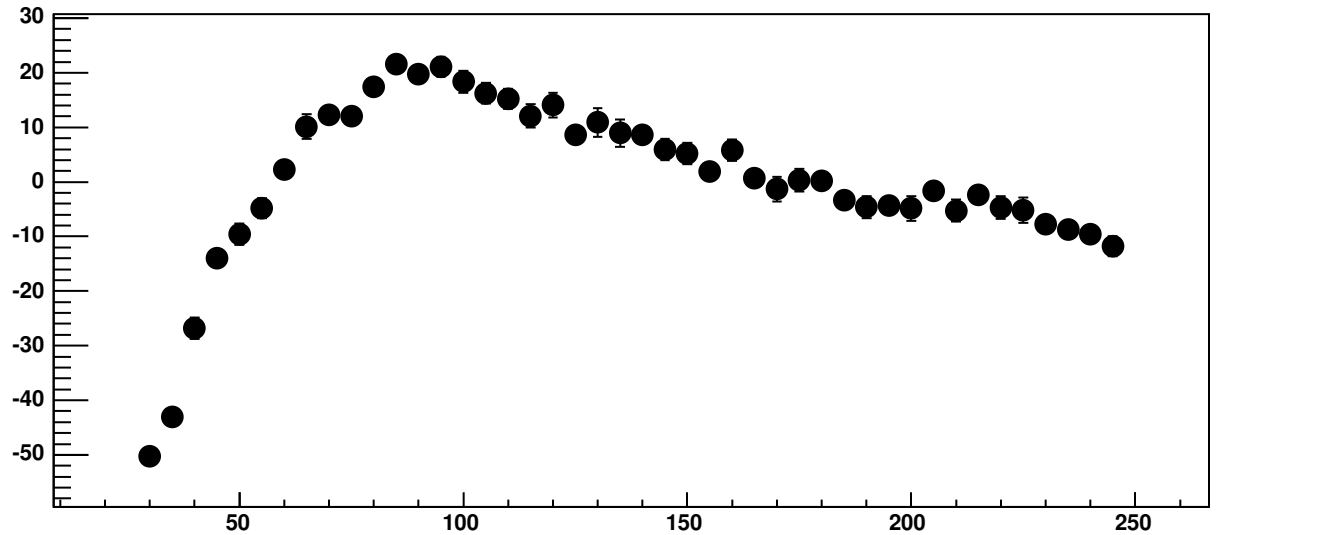
Chip 0, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold

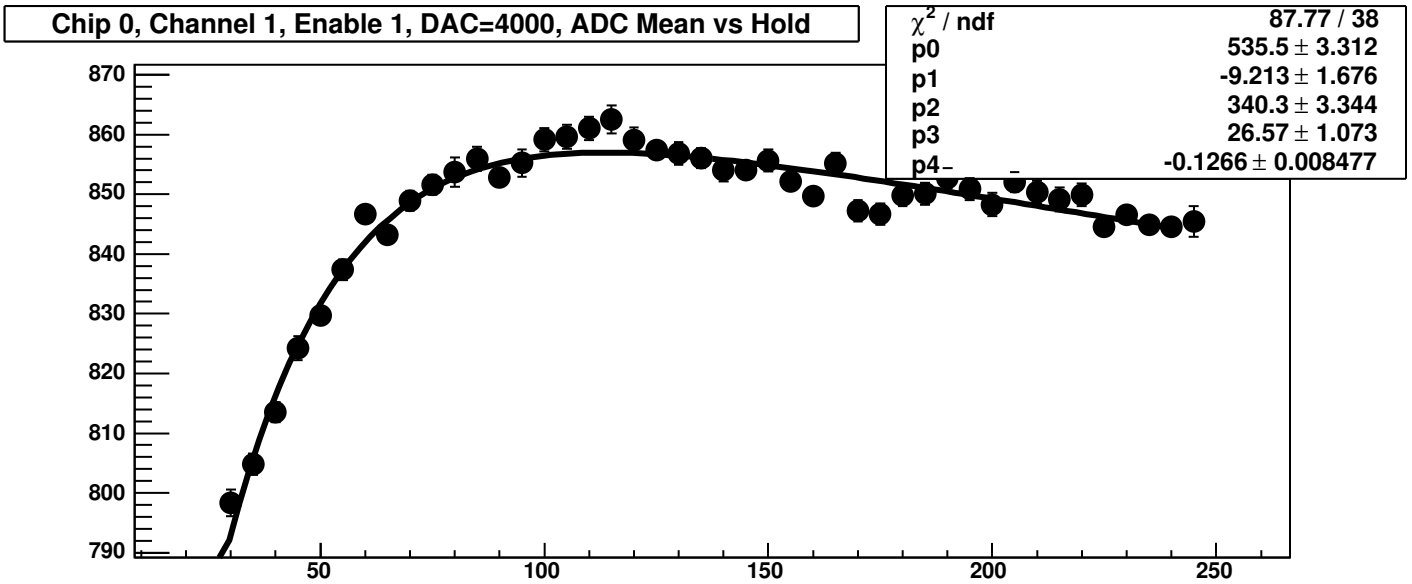


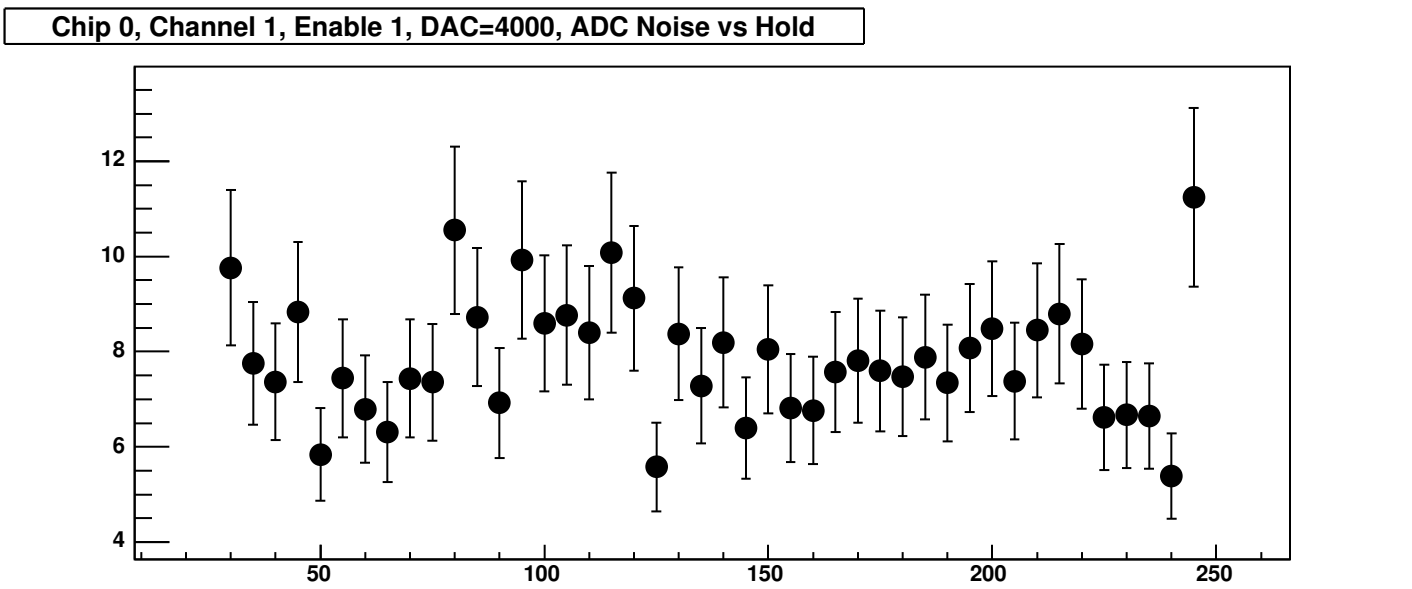
Chip 0, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold

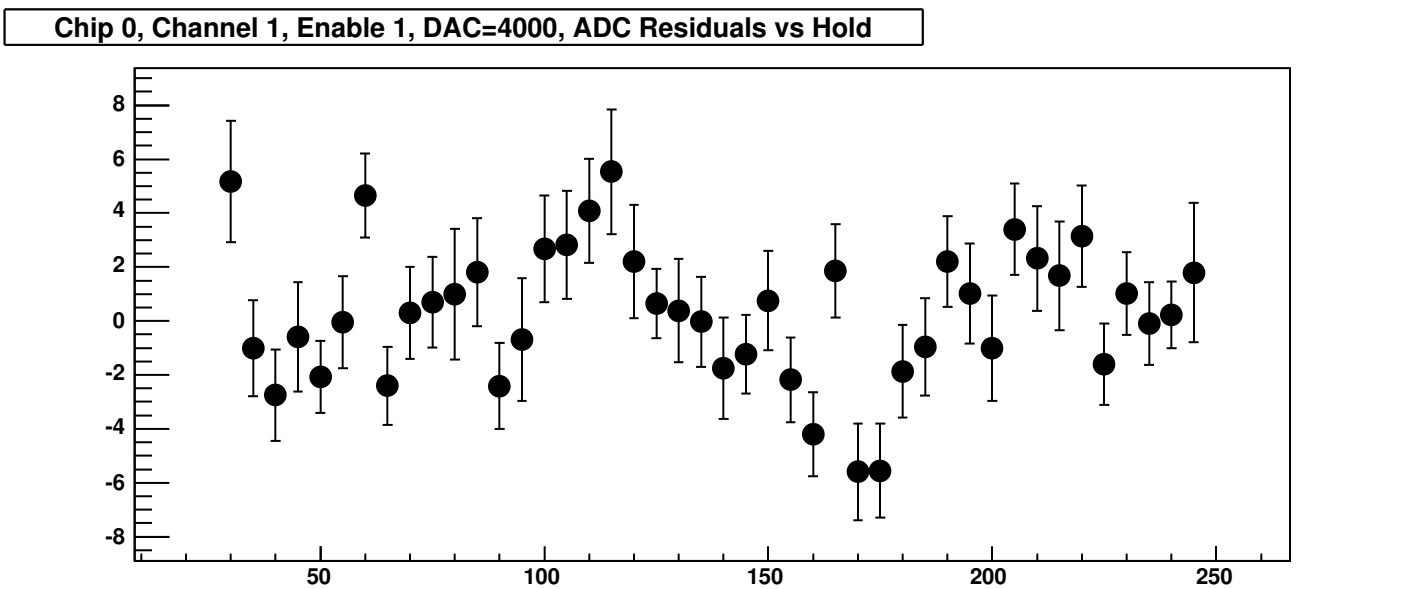


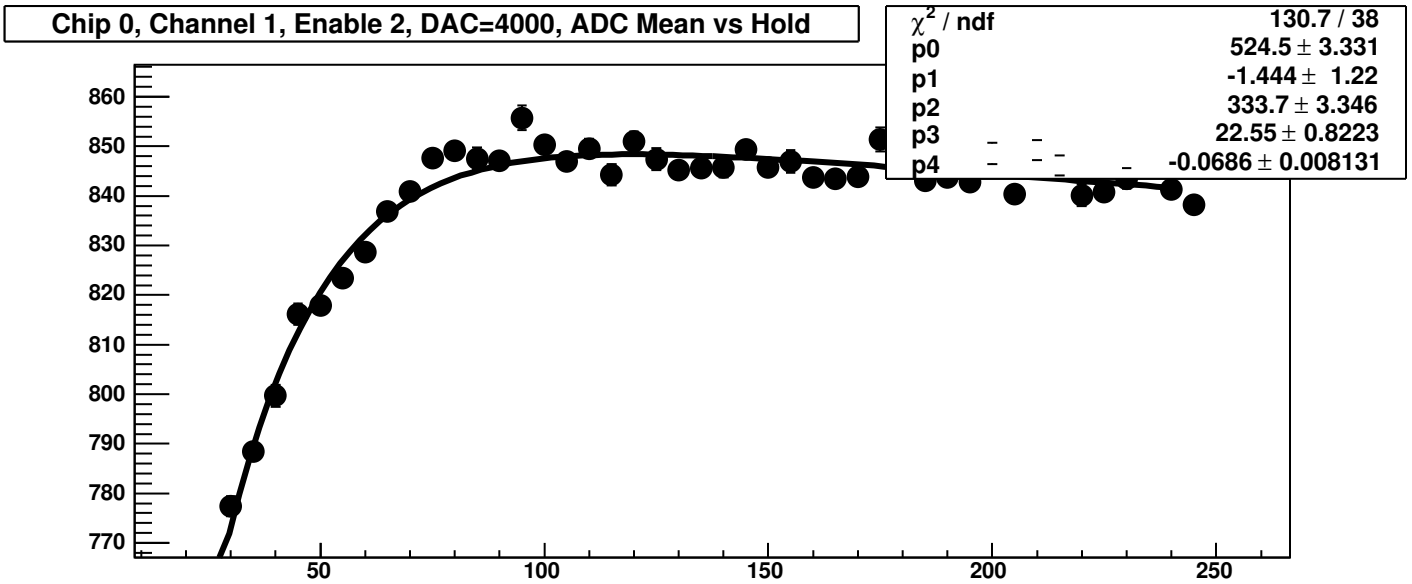
Chip 0, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold

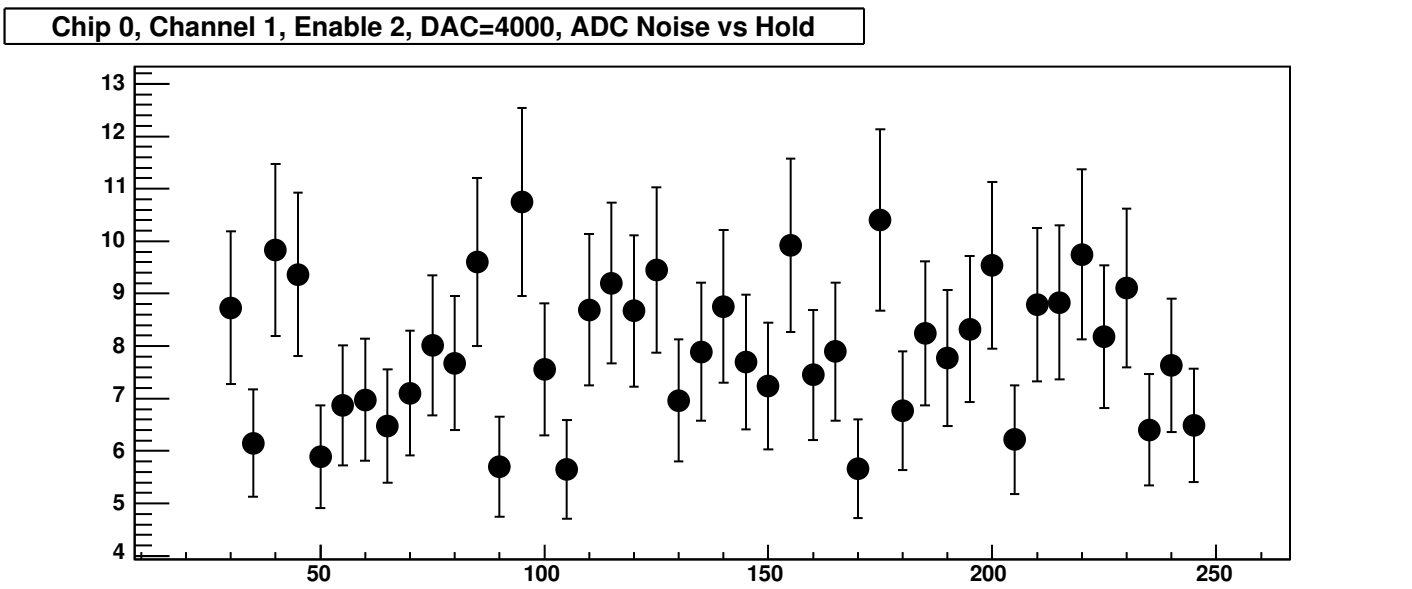


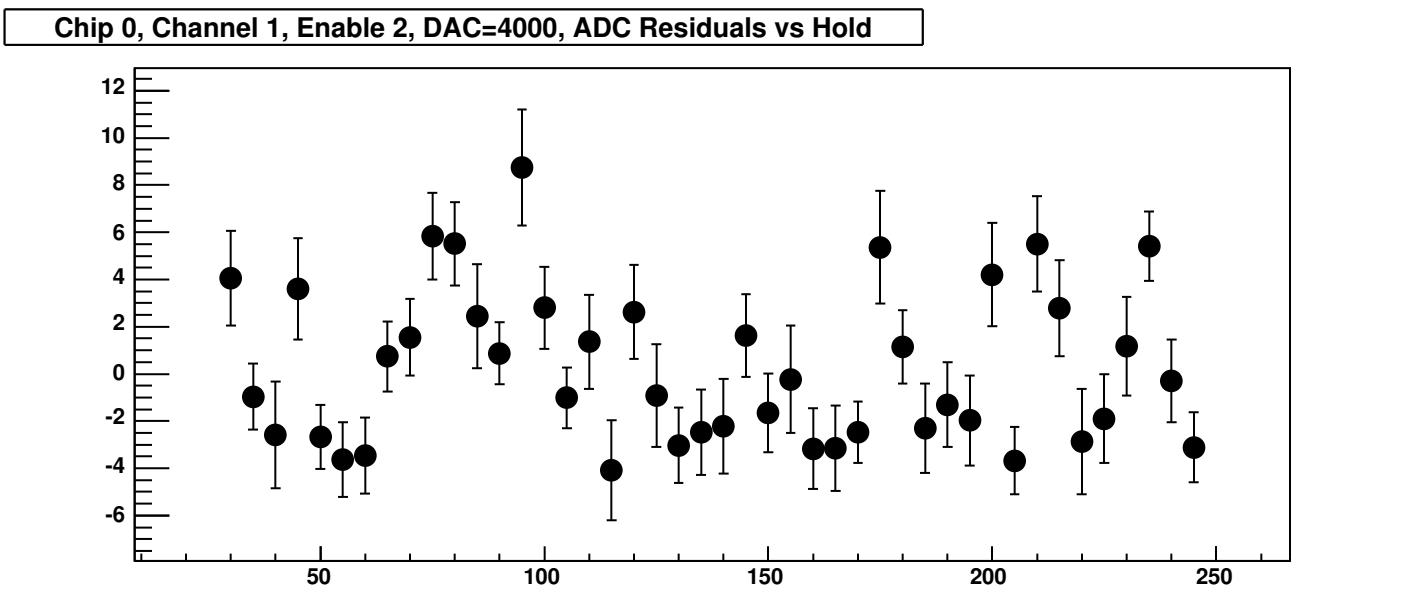

Chip 0, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 87.77 / 38
p0 535.5 ± 3.312
p1 -9.213 ± 1.676
p2 340.3 ± 3.344
p3 26.57 ± 1.073
p4 -0.1266 ± 0.008477
870
860
850
840
830
820
810
800
790
50
100
150
200
250

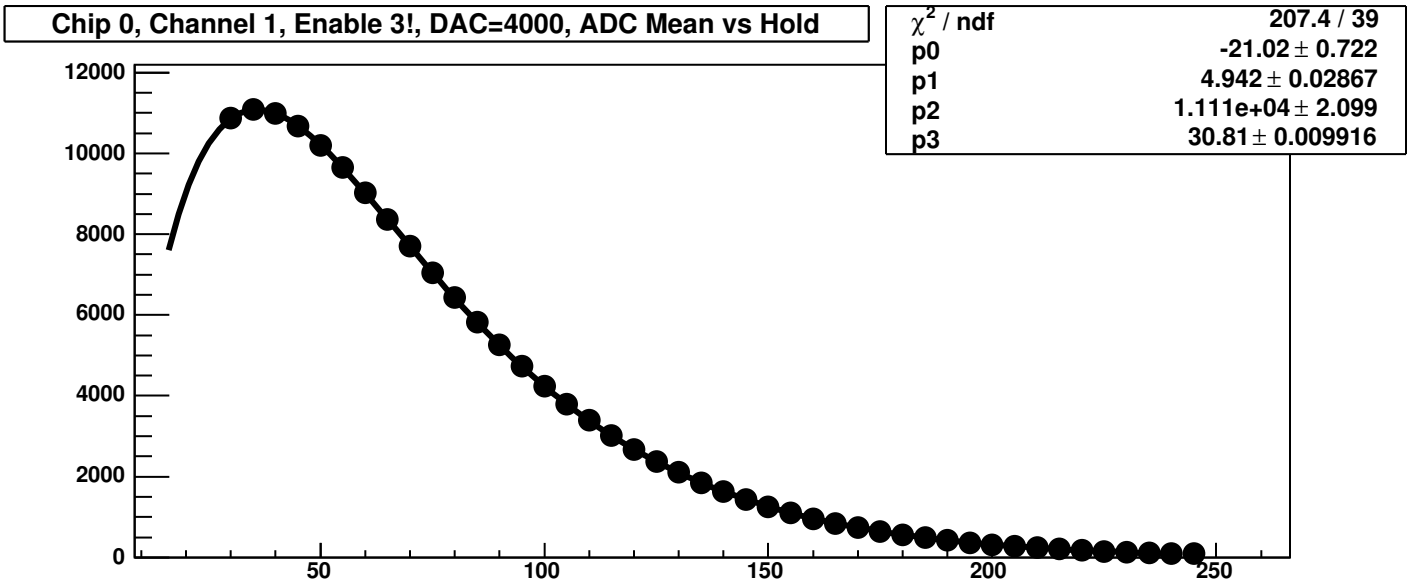

Chip 0, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250

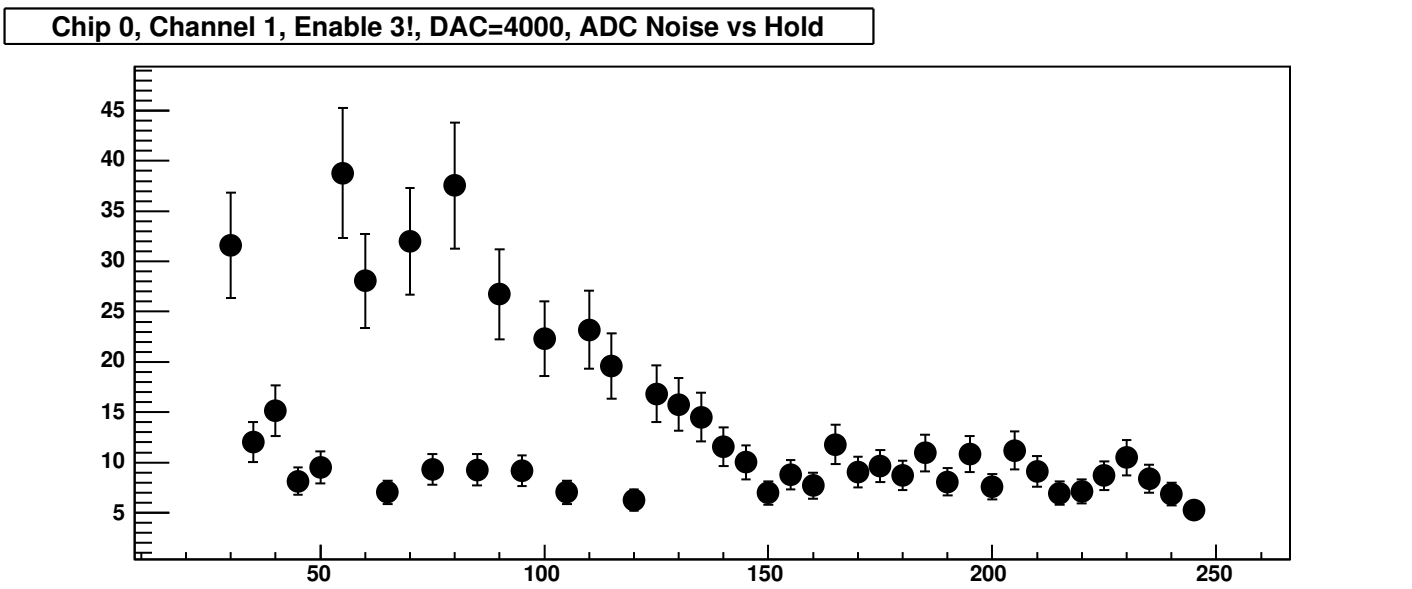

Chip 0, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

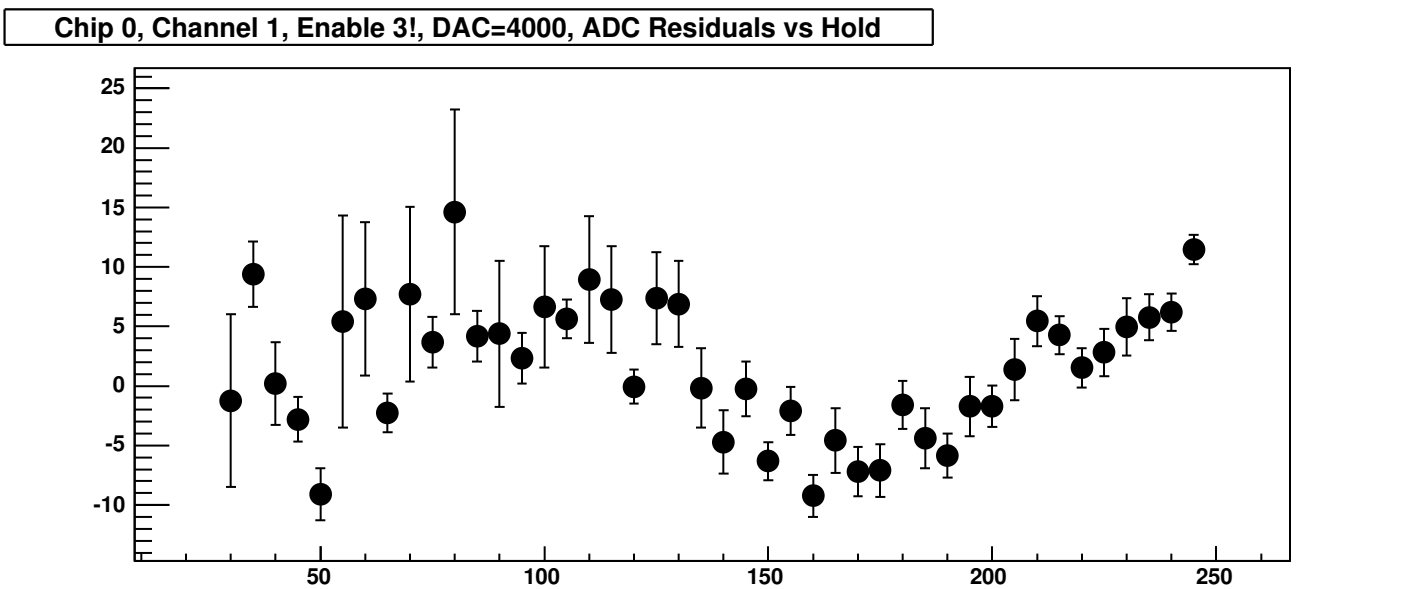

Chip 0, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 130.7 / 38
p0 524.5 ± 3.331
p1 -1.444 ± 1.22
p2 333.7 ± 3.346
p3 22.55 ± 0.8223
p4 -0.0686 ± 0.008131
860
850
840
830
820
810
800
790
780
770
50
100
150
200
250

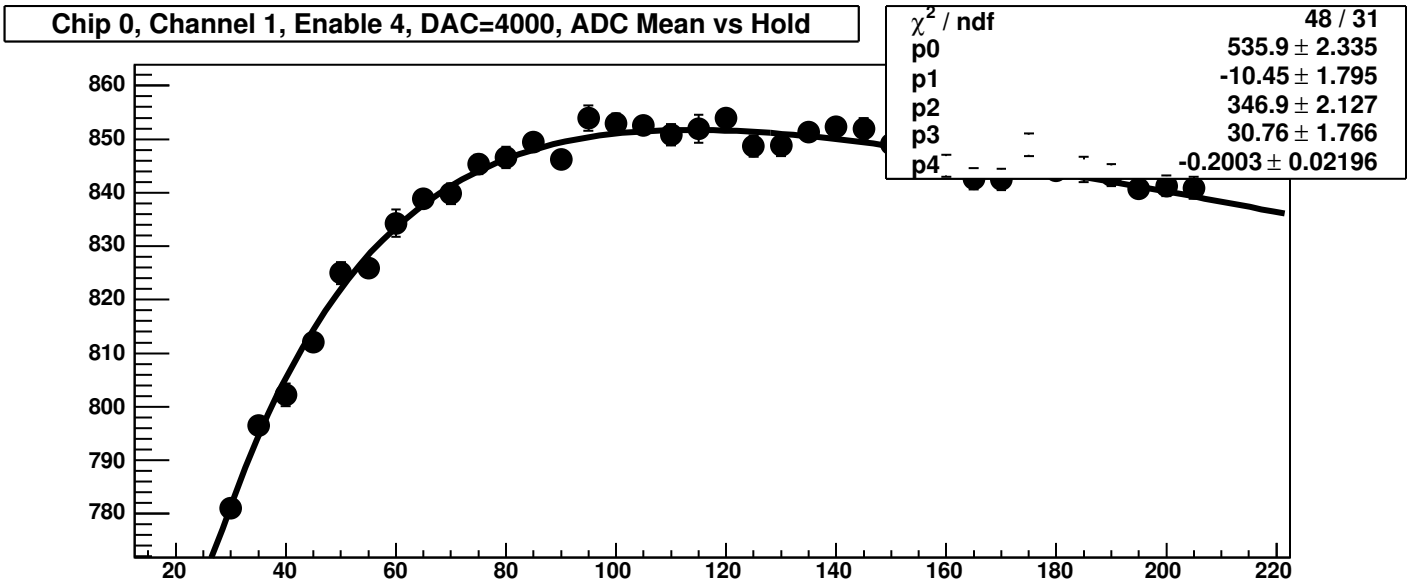

Chip 0, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
50
100
150
200
250

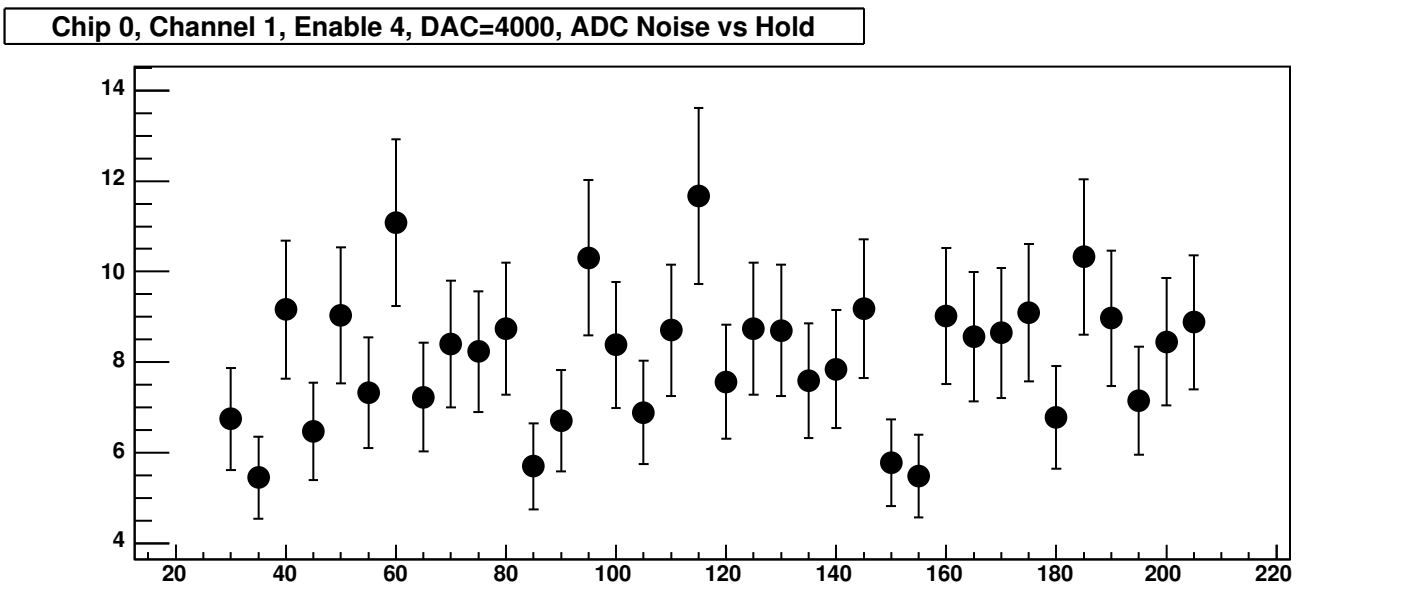

Chip 0, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold
12
10
8
6
4
2
0
-2
-4
-6
50
100
150
200
250

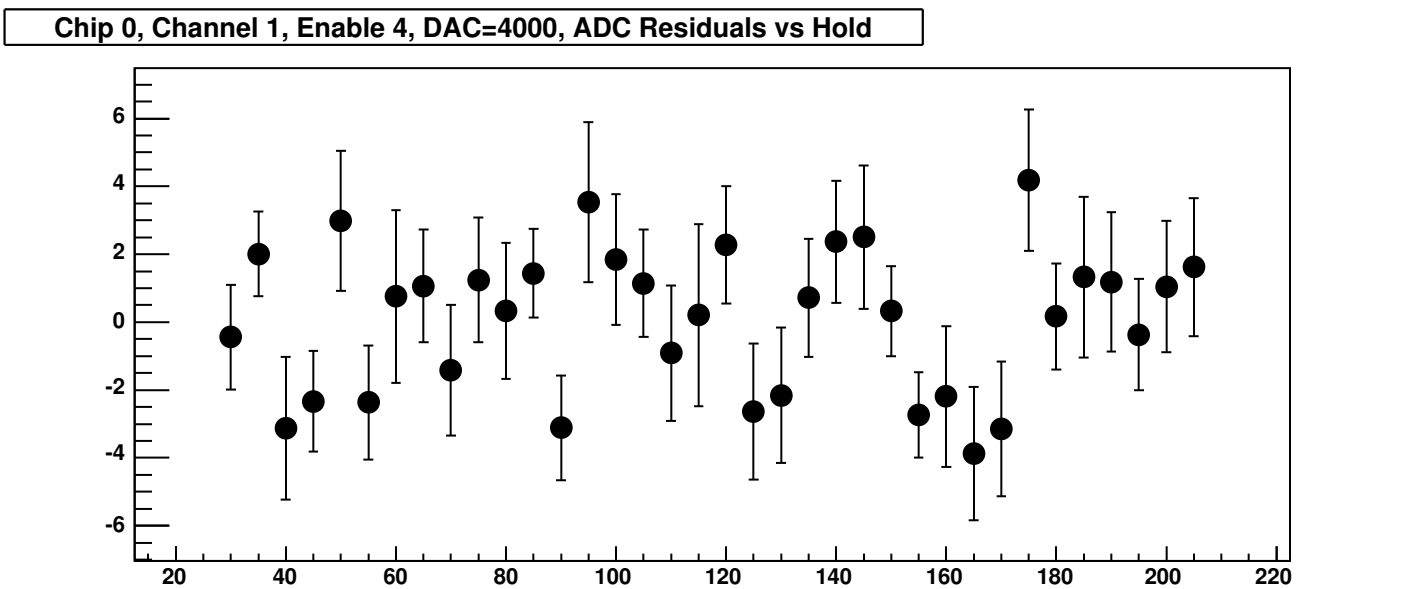

Chip 0, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 207.4 / 39
p0 -21.02 ± 0.722
p1 4.942 ± 0.02867
p2 1.111e+04 ± 2.099
p3 30.81 ± 0.009916
12000
10000
8000
6000
4000
2000
0
50
100
150
200
250

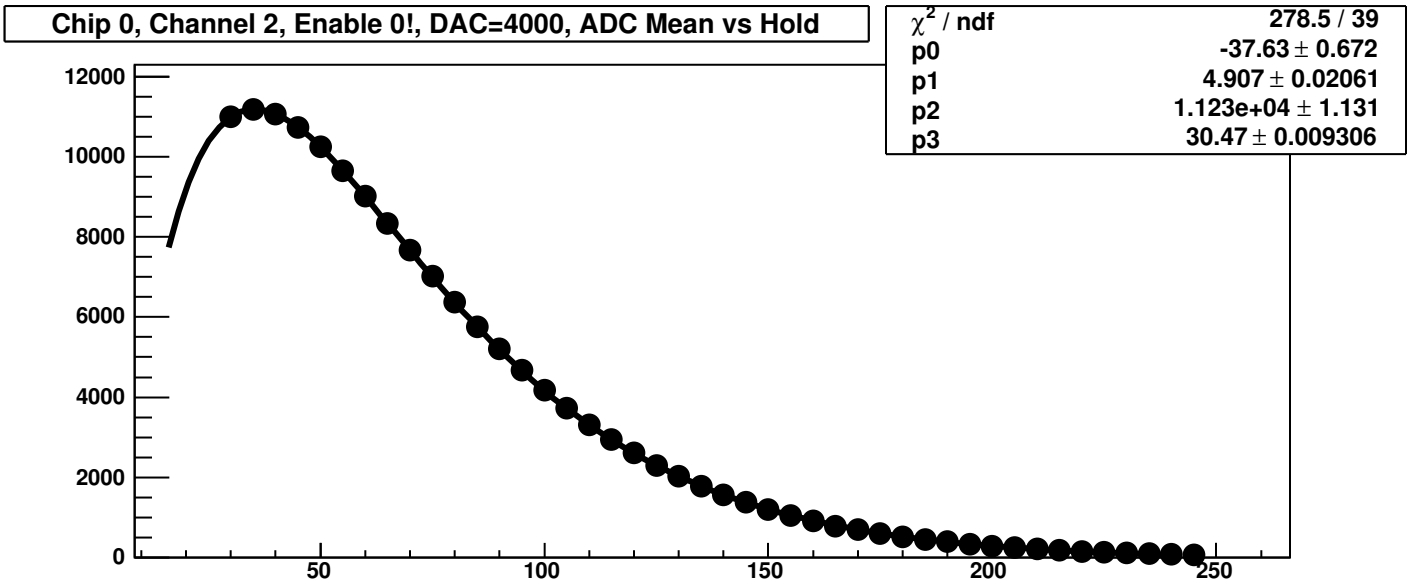

Chip 0, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold
45
40
35
30
25
20
15
10
5
50
100
150
200
250

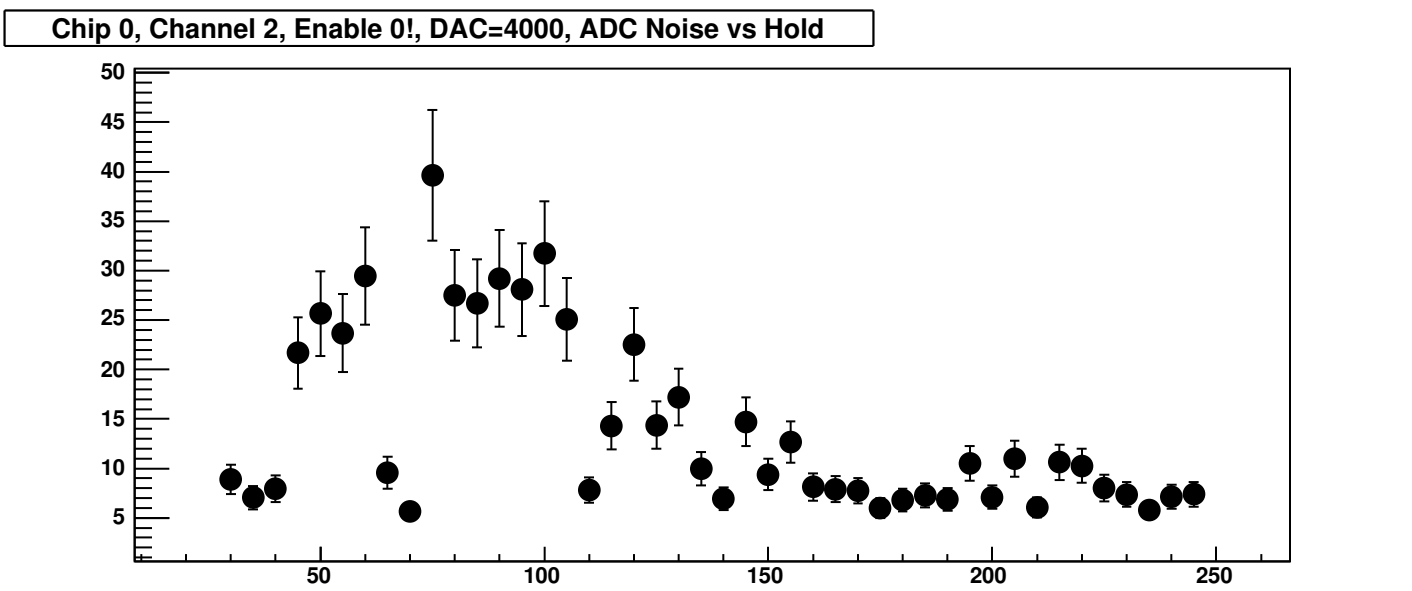

Chip 0, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
50
100
150
200
250

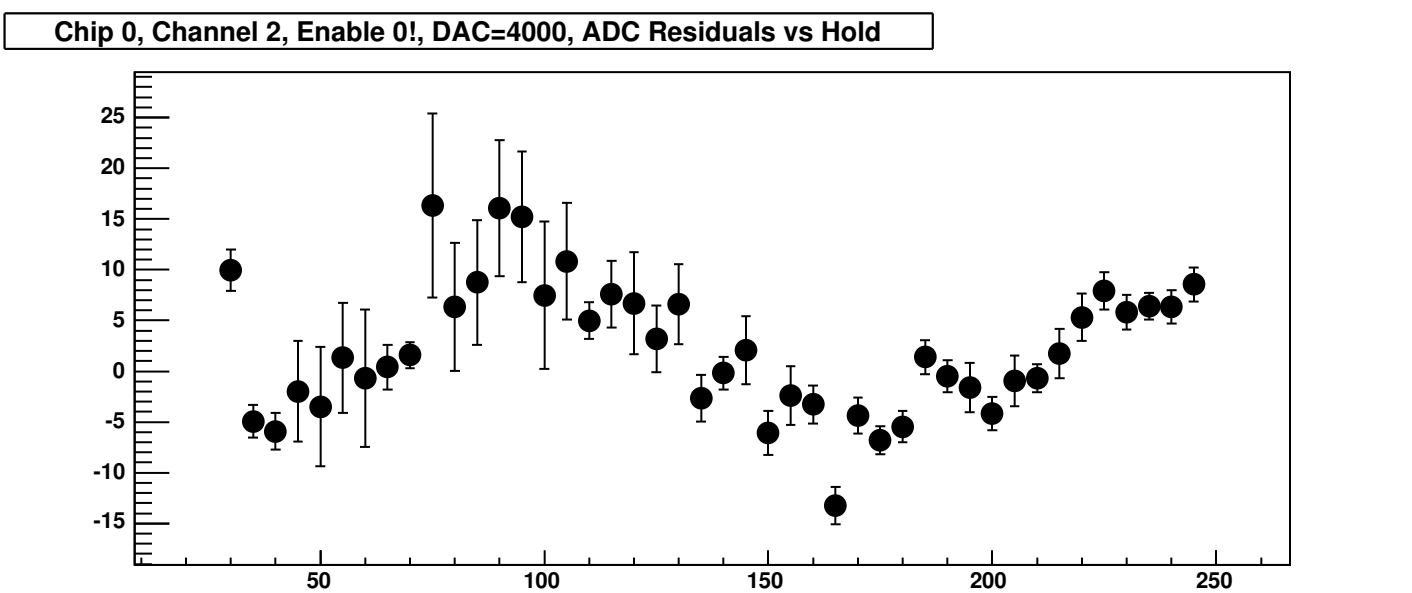

Chip 0, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 48 / 31
p0 535.9 ± 2.335
p1 -10.45 ± 1.795
p2 346.9 ± 2.127
p3 30.76 ± 1.766
p4 -0.2003 ± 0.02196
860
850
840
830
820
810
800
790
780
20
40
60
80
100
120
140
160
180
200
220


Chip 0, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
20
40
60
80
100
120
140
160
180
200
220

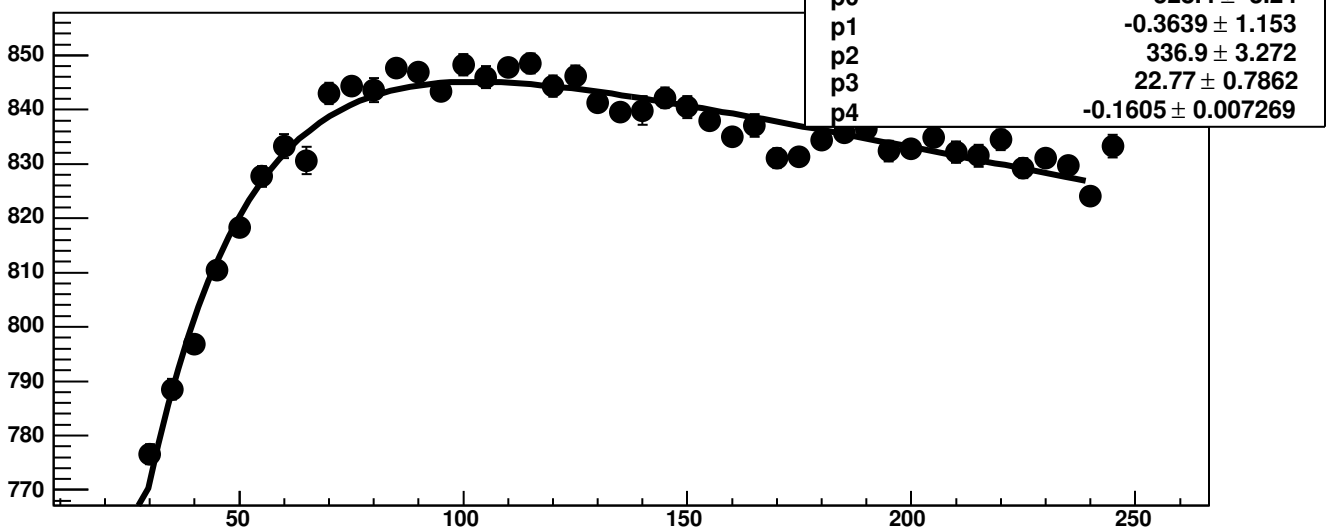

Chip 0, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
20
40
60
80
100
120
140
160
180
200
220


Chip 0, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 278.5 / 39
p0 -37.63 ± 0.672
p1 4.907 ± 0.02061
p2 1.123e+04 ± 1.131
p3 30.47 ± 0.009306
12000
10000
8000
6000
4000
2000
0
50
100
150
200
250

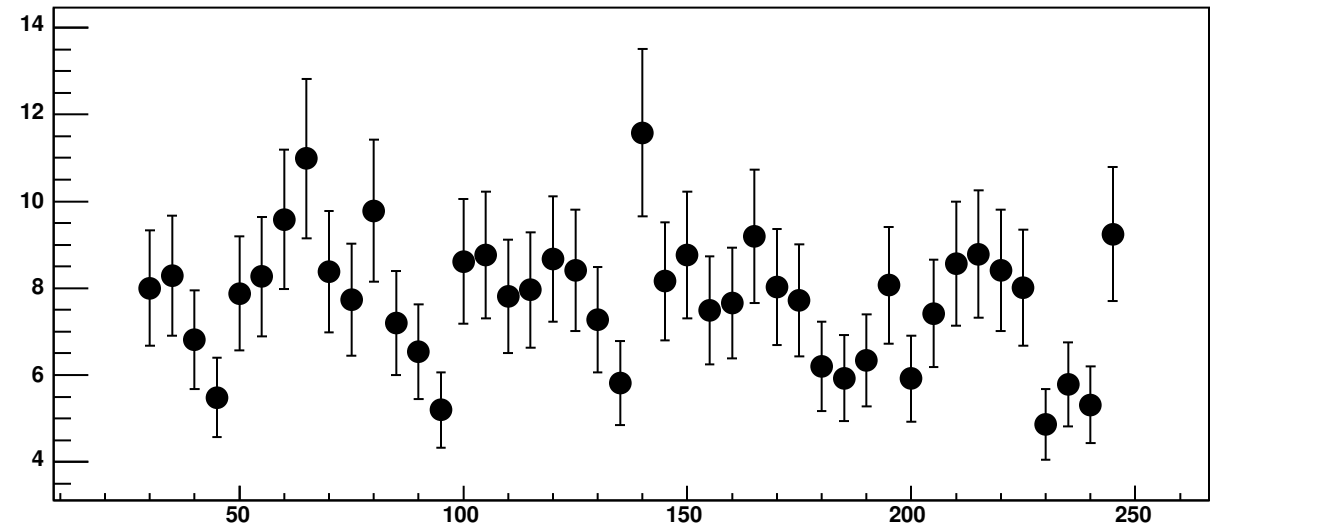

Chip 0, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold
50
45
40
35
30
25
20
15
10
5
50
100
150
200
250


Chip 0, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
-15
50
100
150
200
250

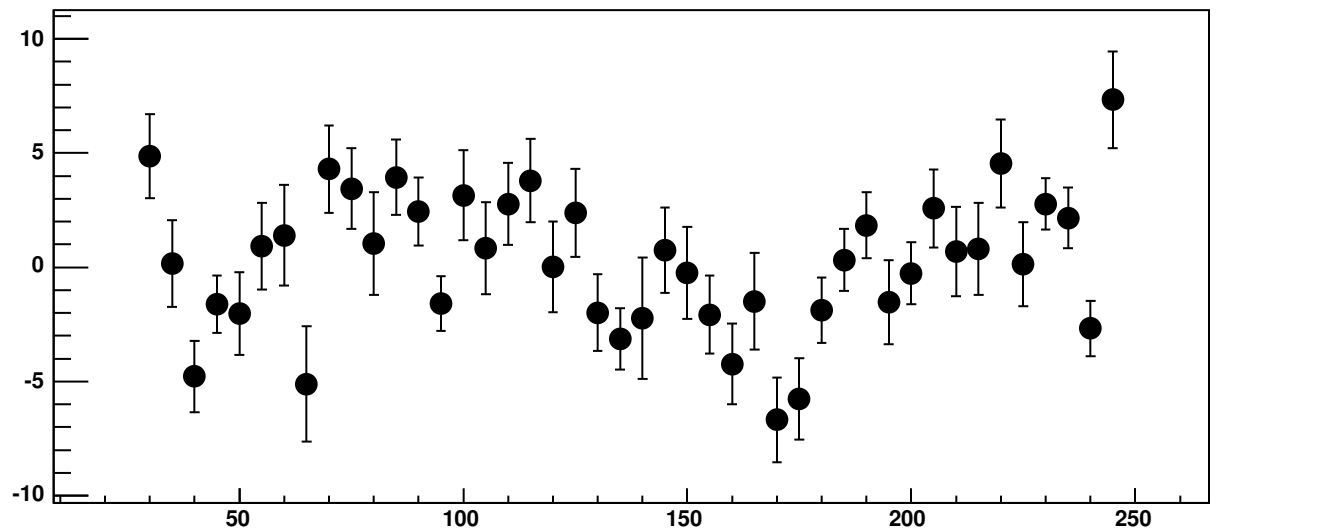
Chip 0, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

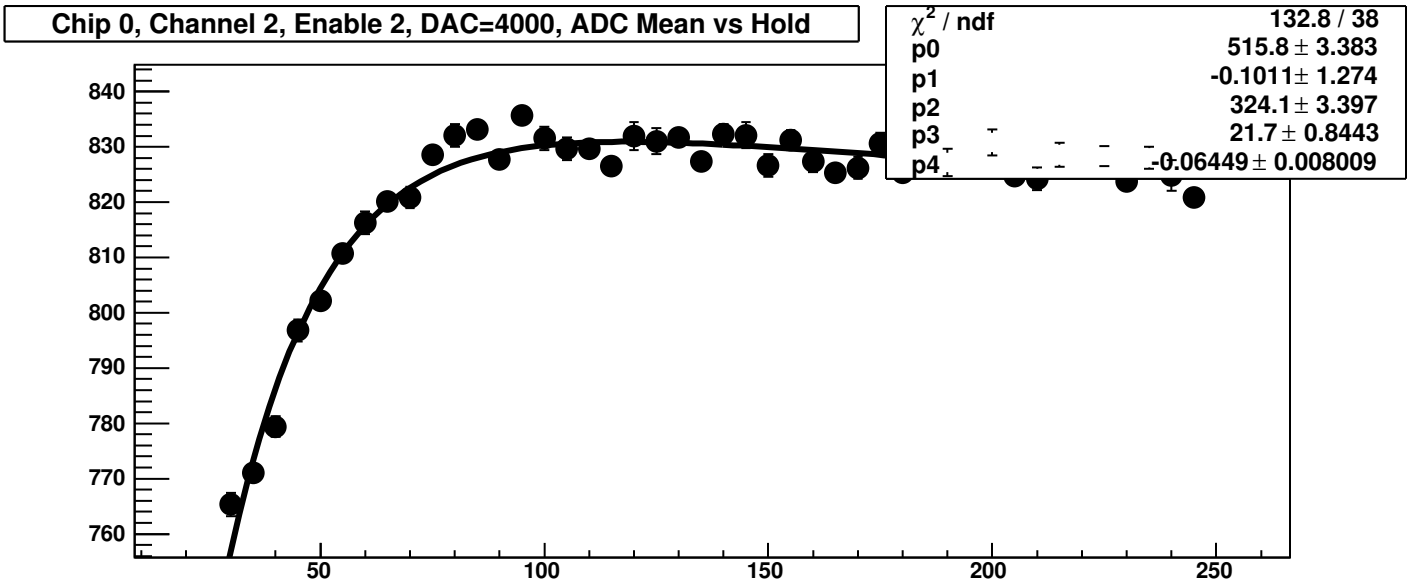


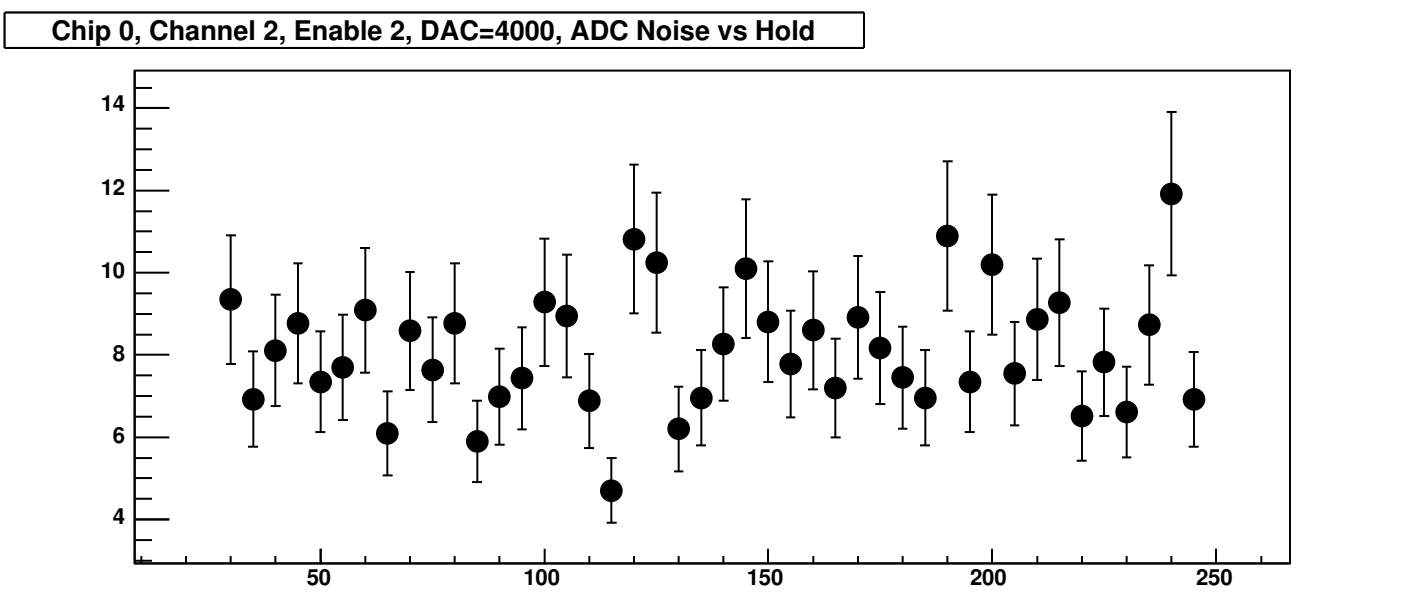
Chip 0, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold

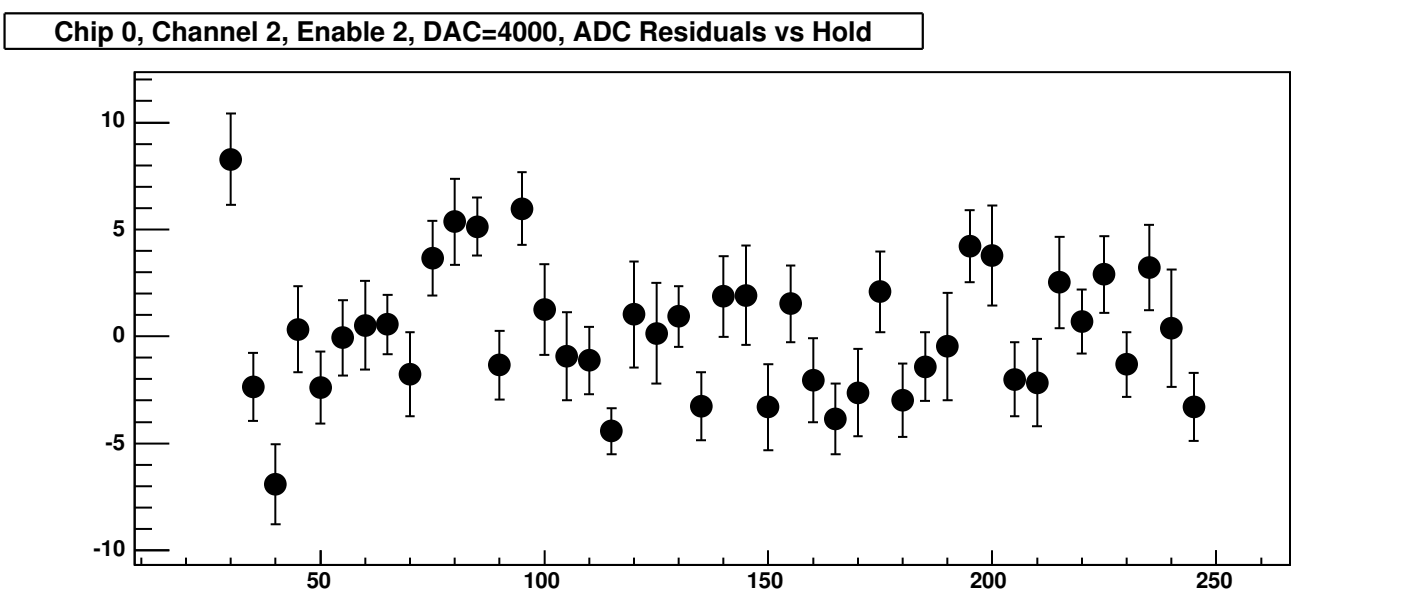


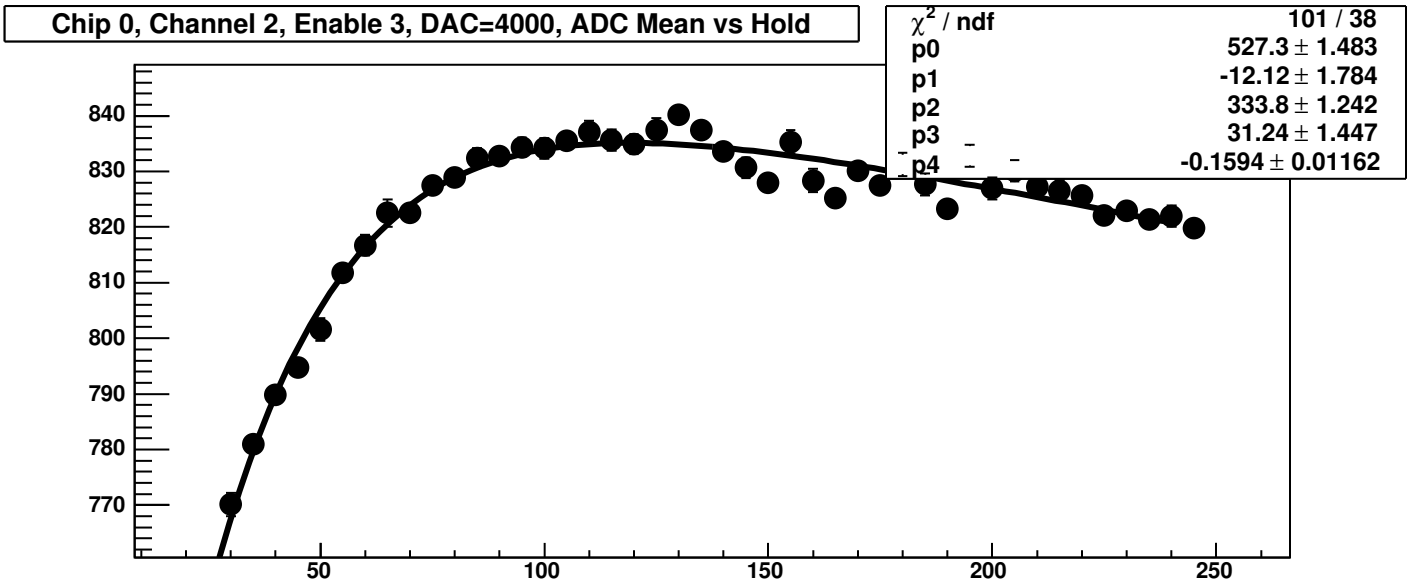
Chip 0, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold

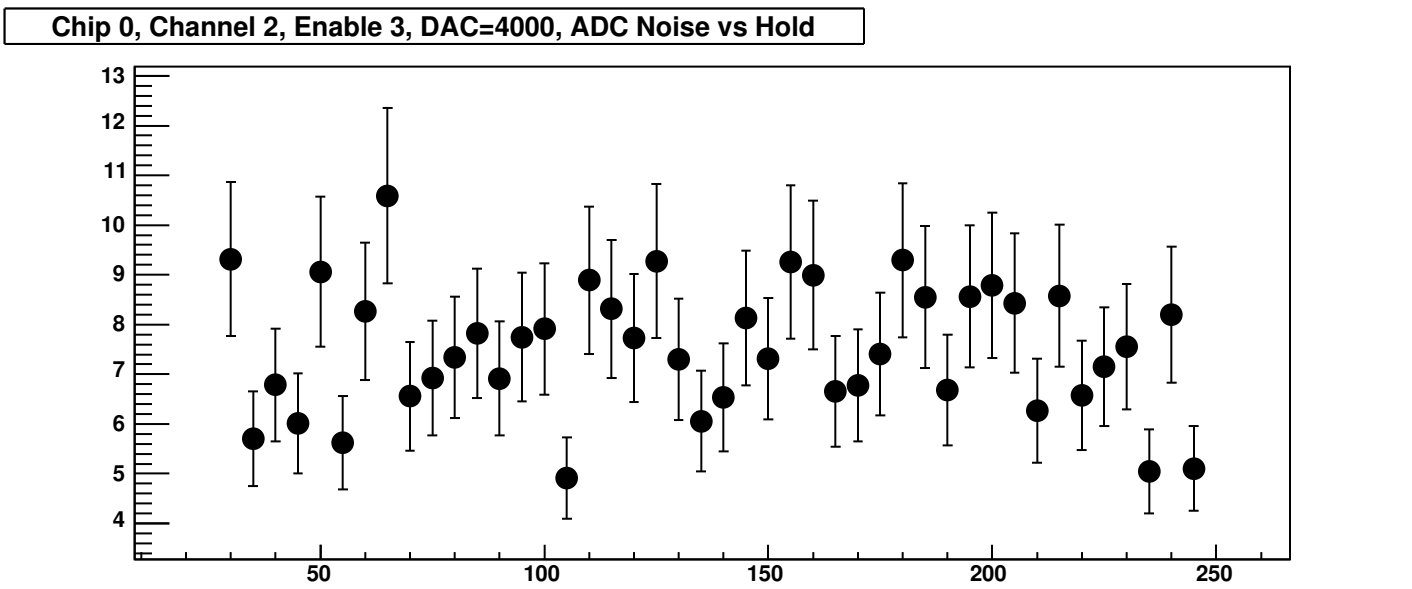


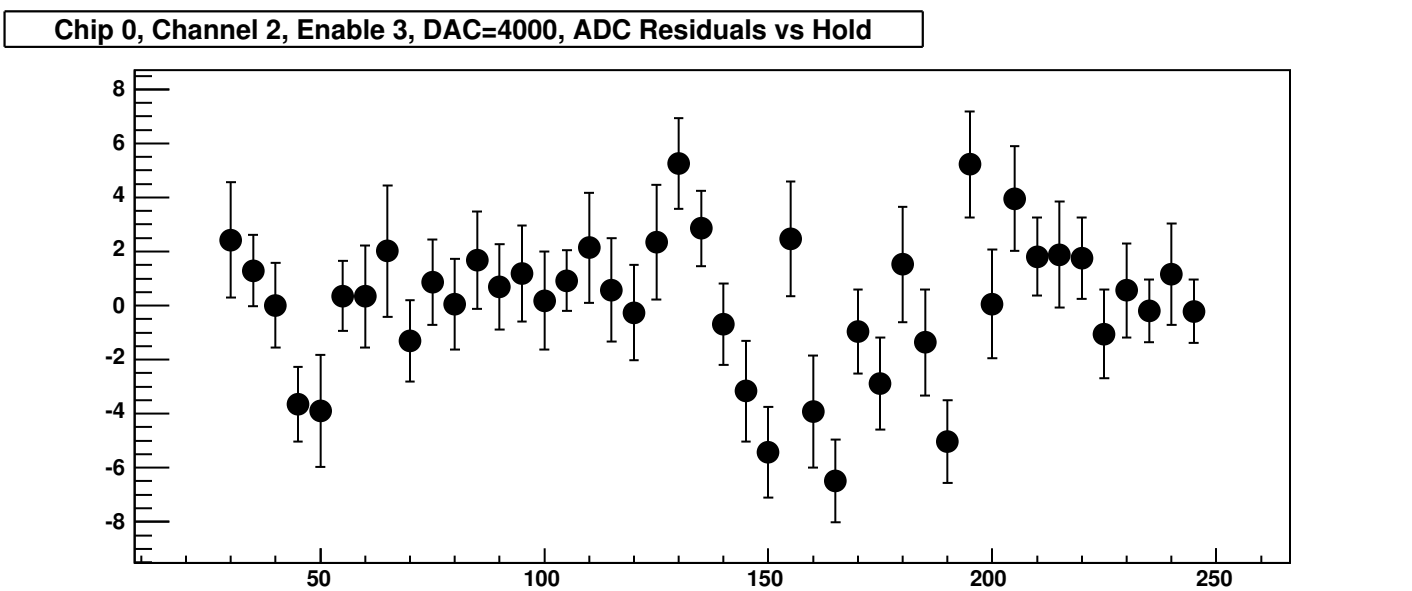

Chip 0, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 132.8 / 38
p0 515.8 ± 3.383
p1 -0.1011 ± 1.274
p2 324.1 ± 3.397
p3 21.7 ± 0.8443
p4 -0.06449 ± 0.008009
840
830
820
810
800
790
780
770
760
50
100
150
200
250

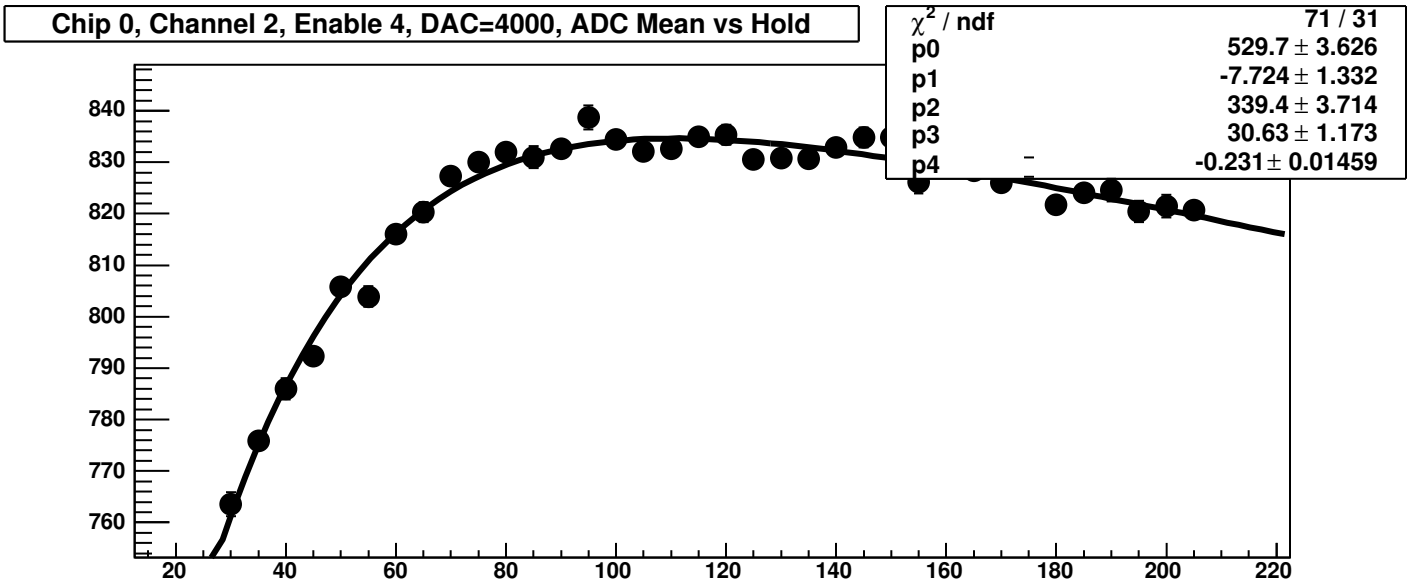

Chip 0, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250

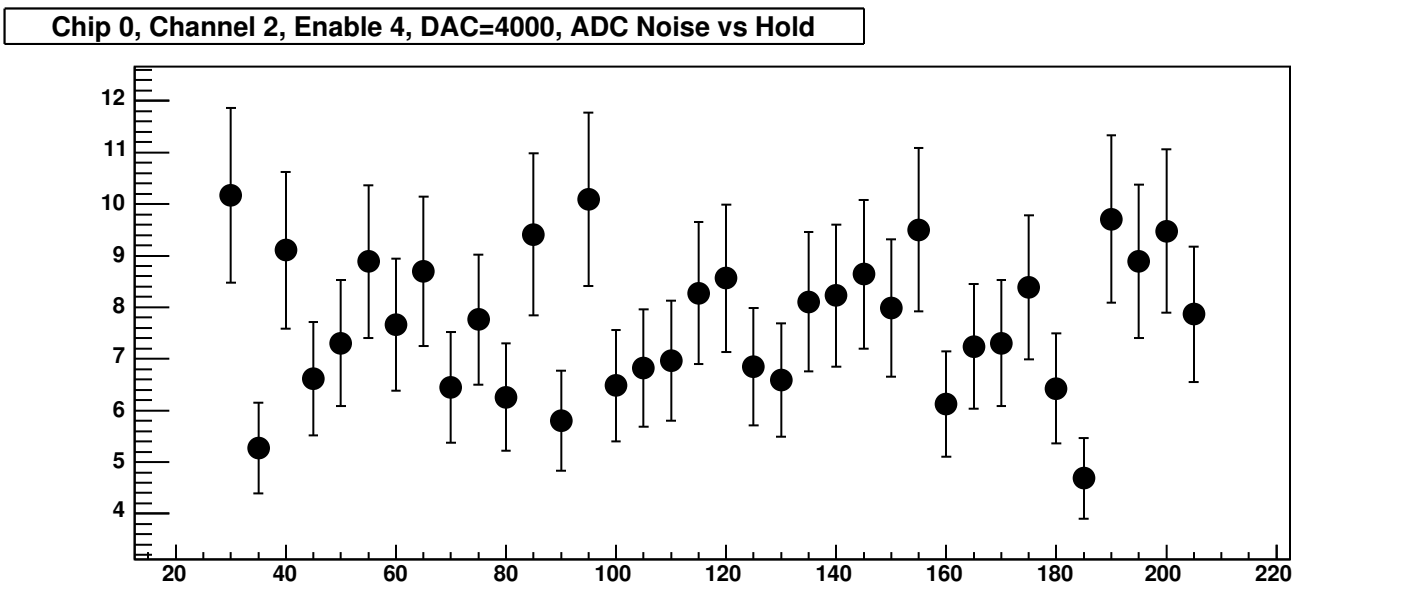

Chip 0, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

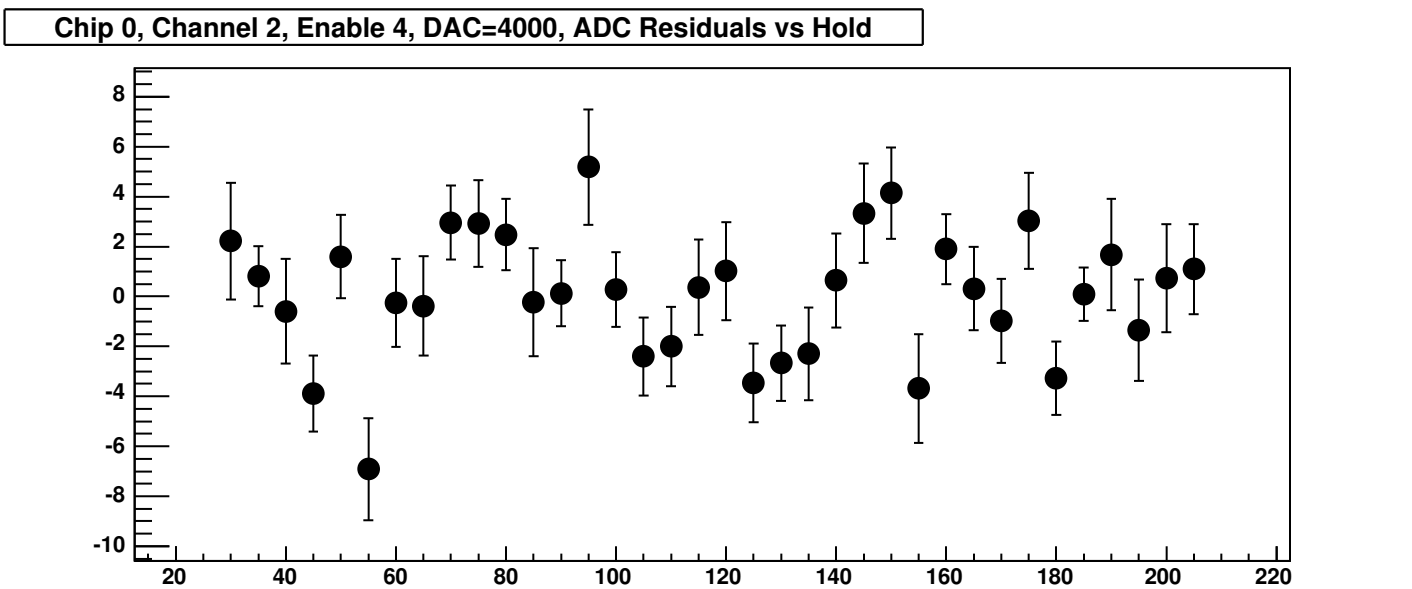

Chip 0, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 101 / 38
p0 527.3 ± 1.483
p1 -12.12 ± 1.784
p2 333.8 ± 1.242
p3 31.24 ± 1.447
p4 -0.1594 ± 0.01162
840
830
820
810
800
790
780
770
50
100
150
200
250


Chip 0, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
50
100
150
200
250

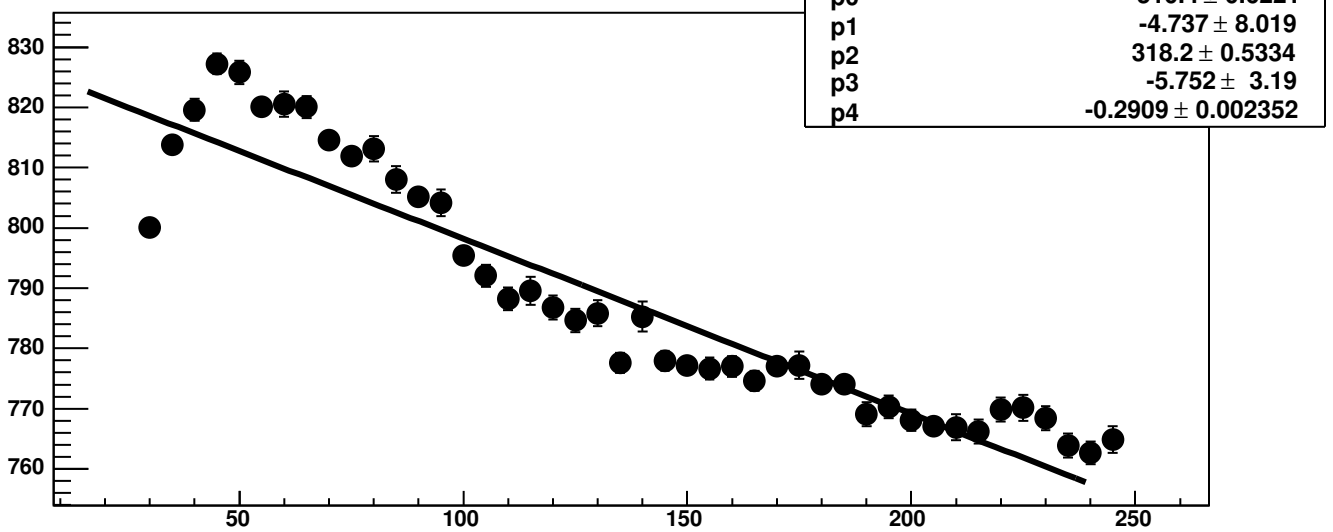

Chip 0, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250


Chip 0, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 71 / 31
p0 529.7 ± 3.626
p1 -7.724 ± 1.332
p2 339.4 ± 3.714
p3 30.63 ± 1.173
p4 -0.231 ± 0.01459
840
830
820
810
800
790
780
770
760
20
40
60
80
100
120
140
160
180
200
220

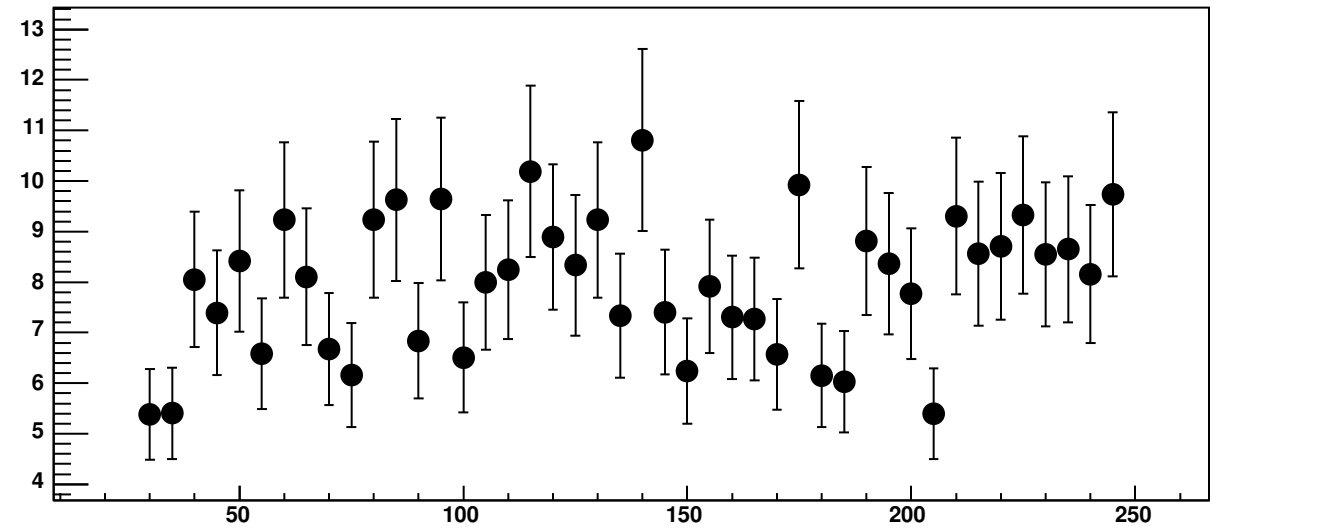

Chip 0, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
20
40
60
80
100
120
140
160
180
200
220


Chip 0, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
20
40
60
80
100
120
140
160
180
200
220

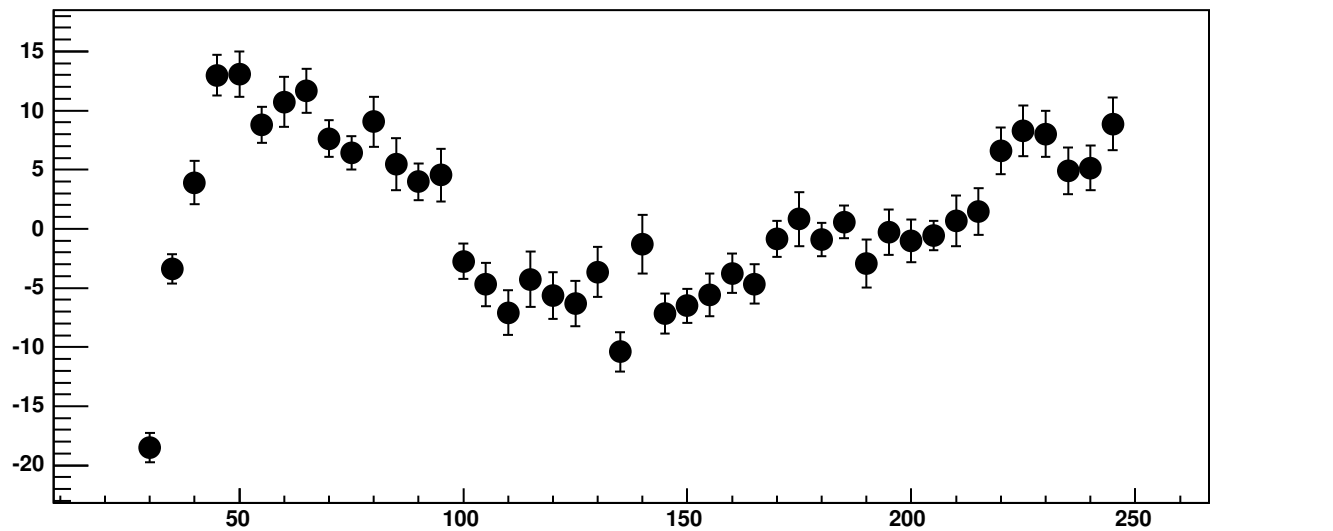
Chip 0, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold

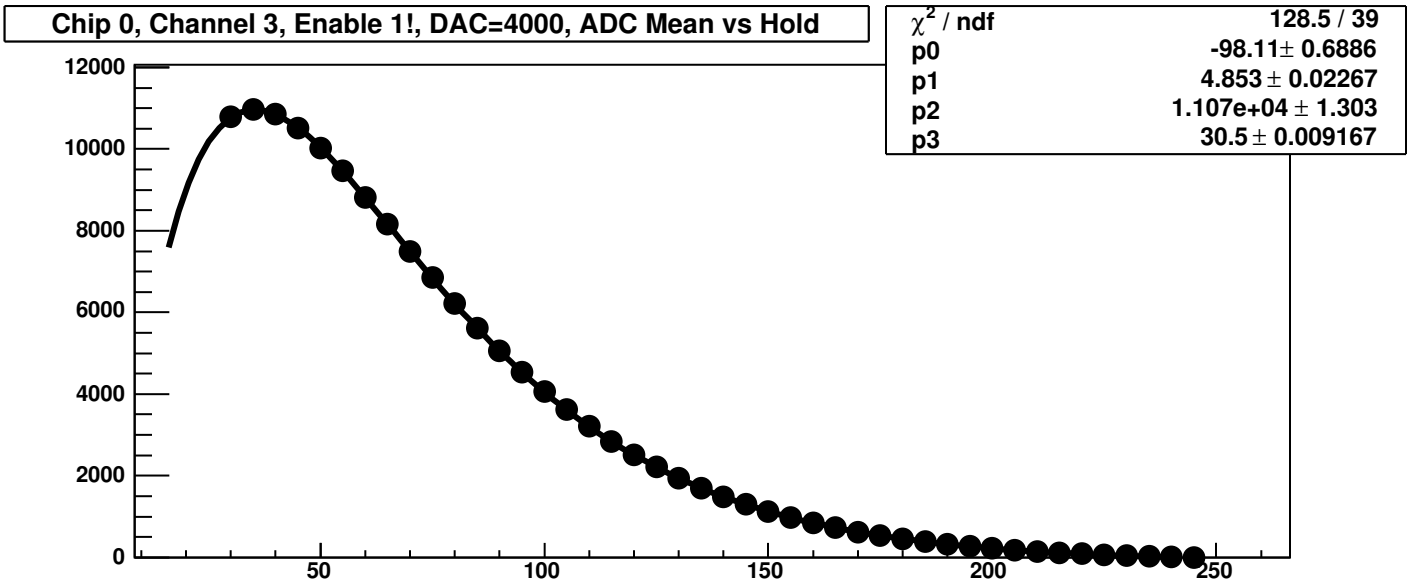


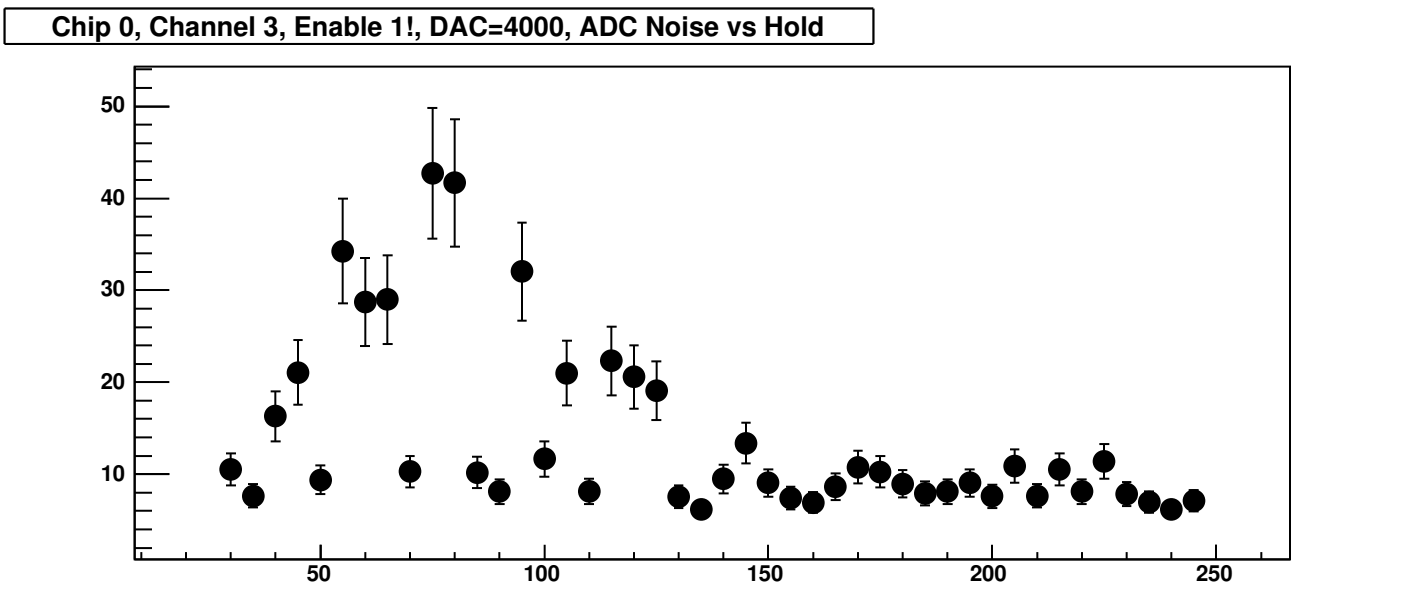
Chip 0, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold

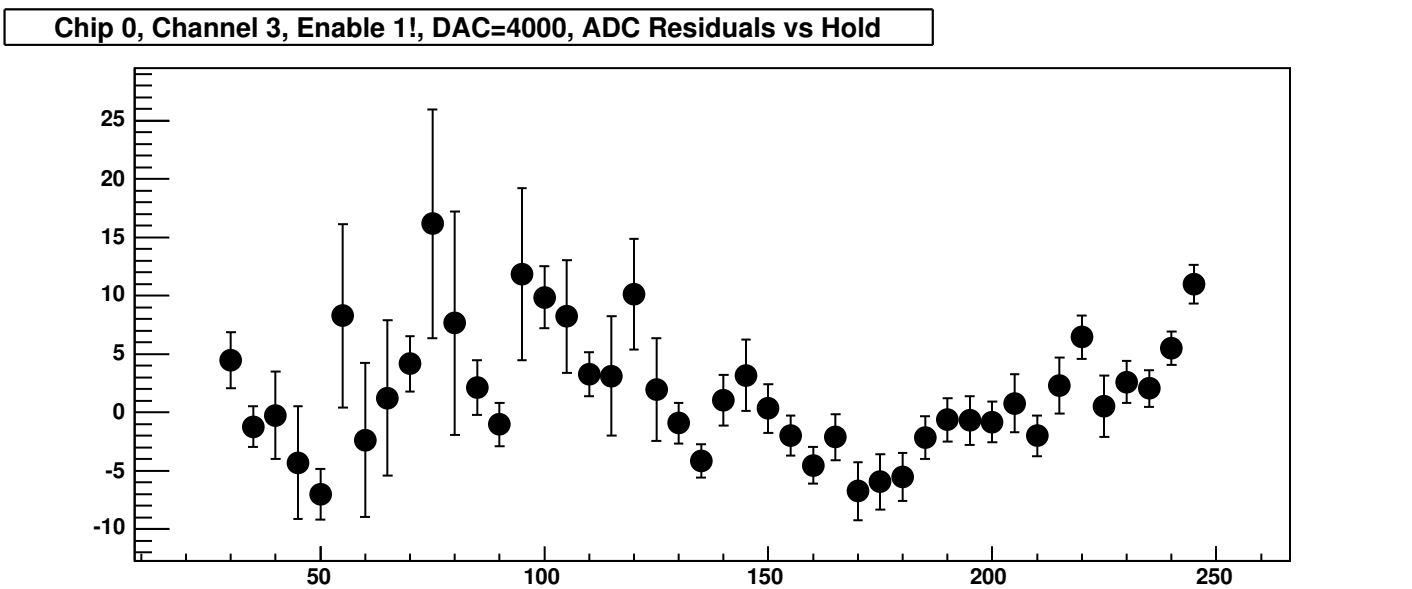


Chip 0, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

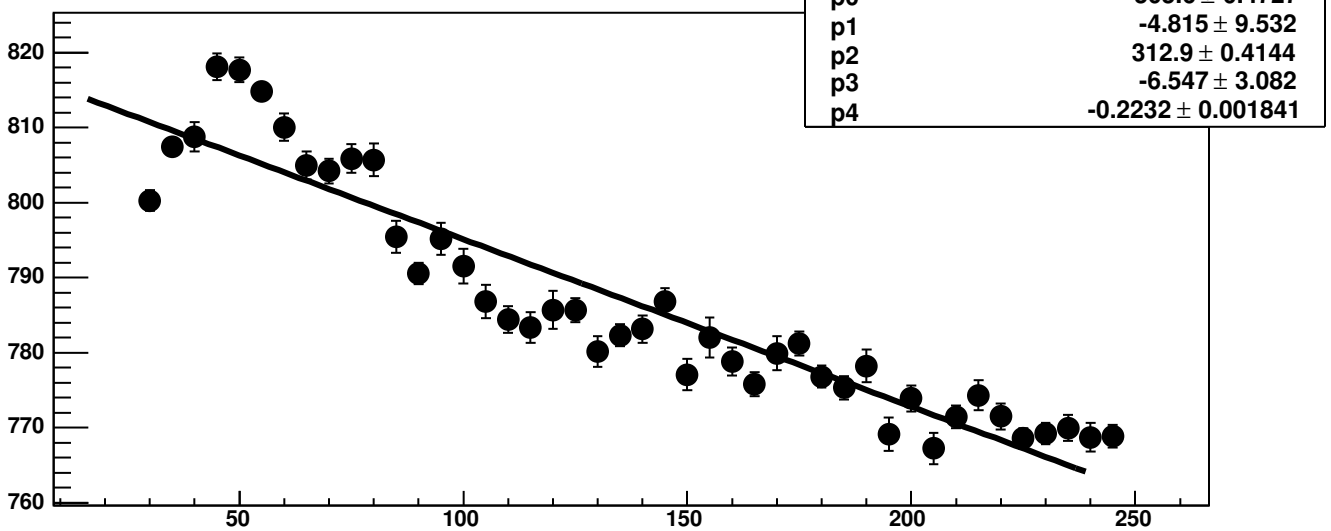



Chip 0, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 128.5 / 39
p0 -98.11 ± 0.6886
p1 4.853 ± 0.02267
p2 1.107e+04 ± 1.303
p3 30.5 ± 0.009167
12000
10000
8000
6000
4000
2000
0
50
100
150
200
250

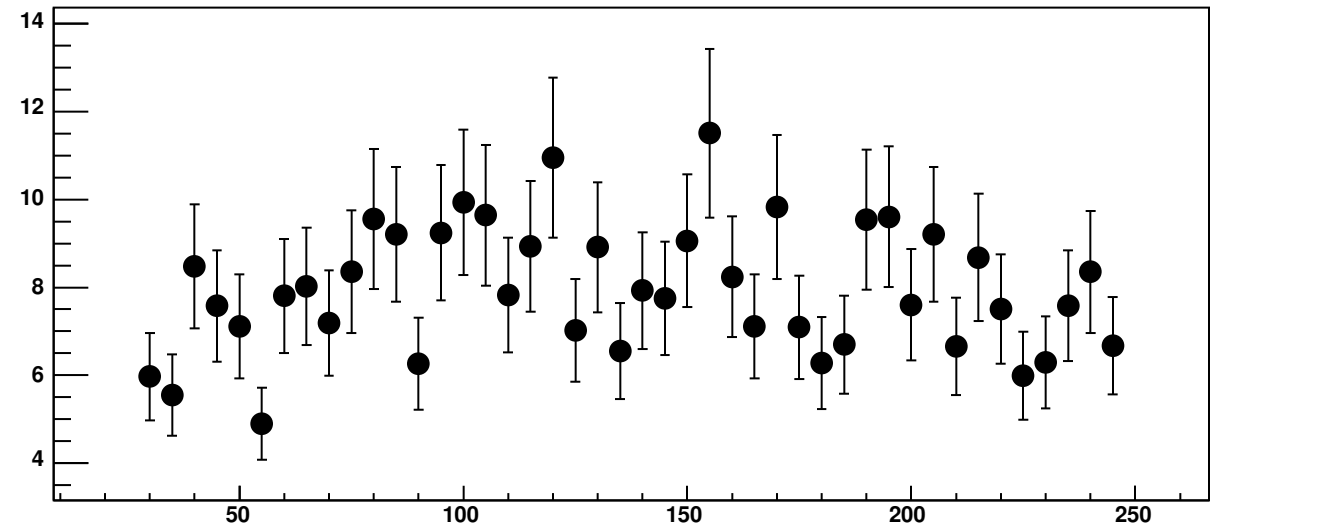

Chip 0, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
50
100
150
200
250


Chip 0, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
50
100
150
200
250

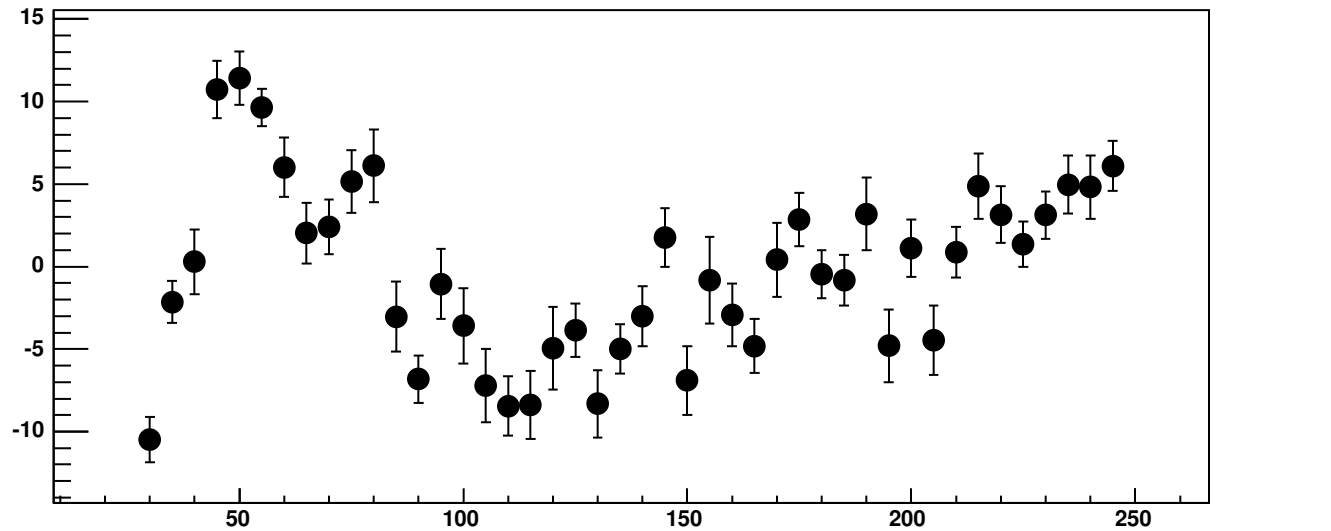
Chip 0, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold



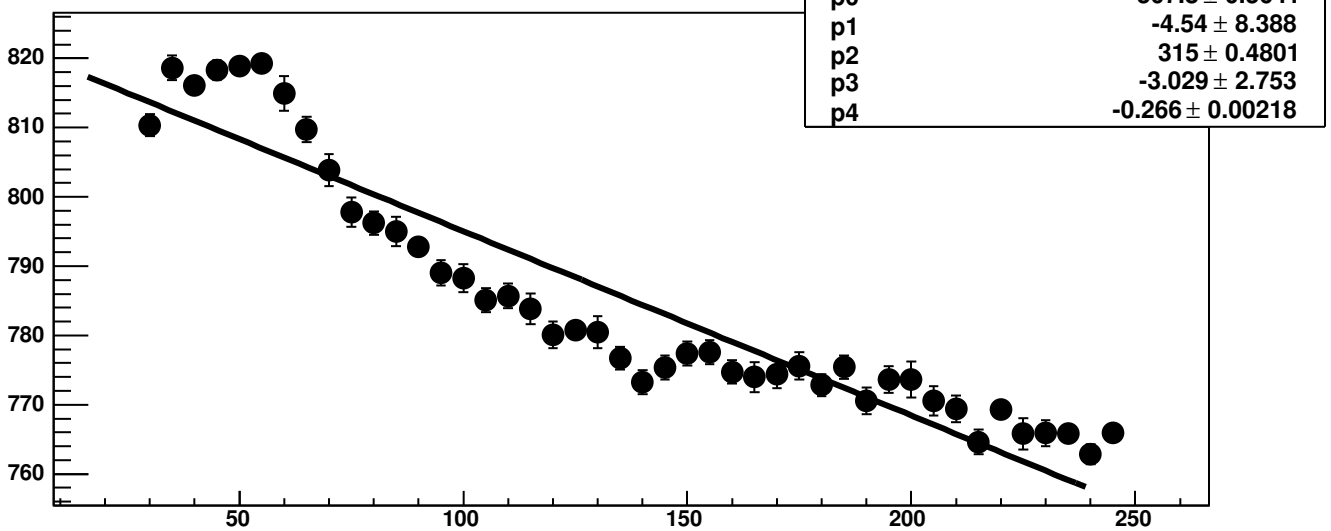
Chip 0, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



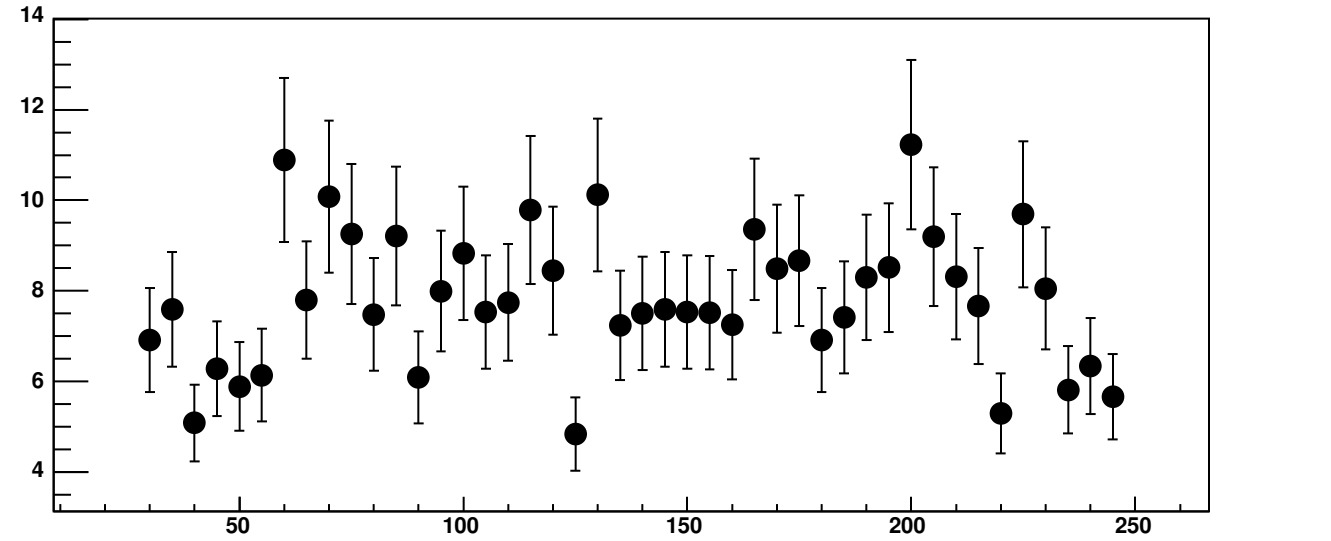
Chip 0, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold



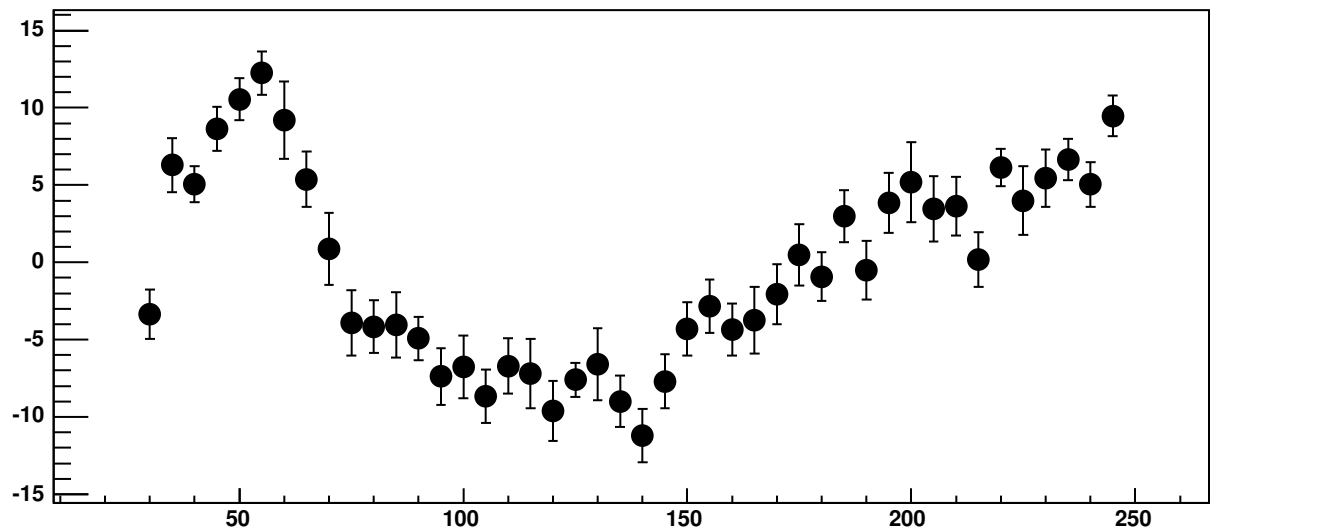
Chip 0, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

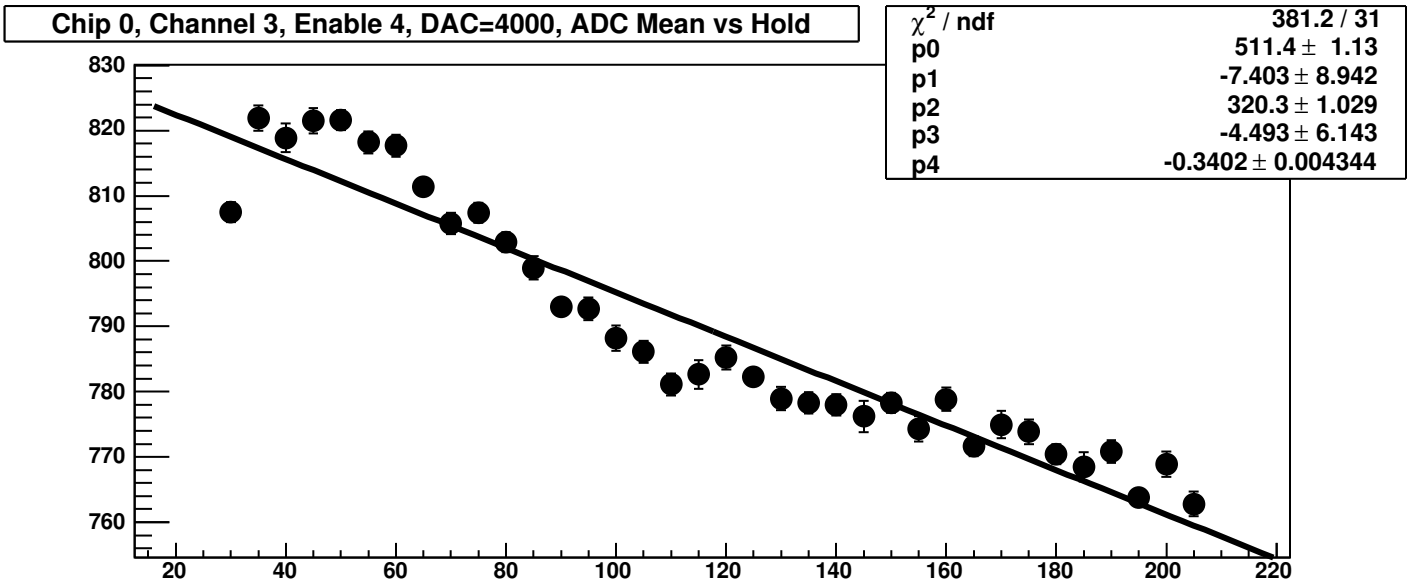


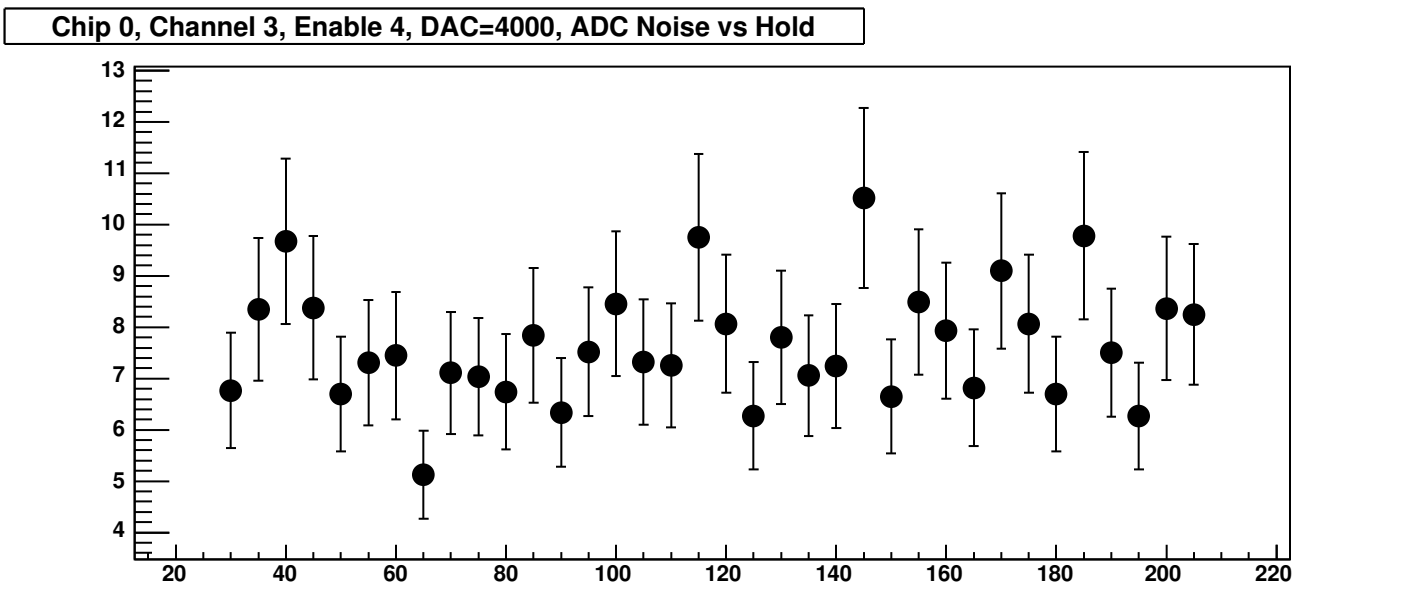
Chip 0, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold

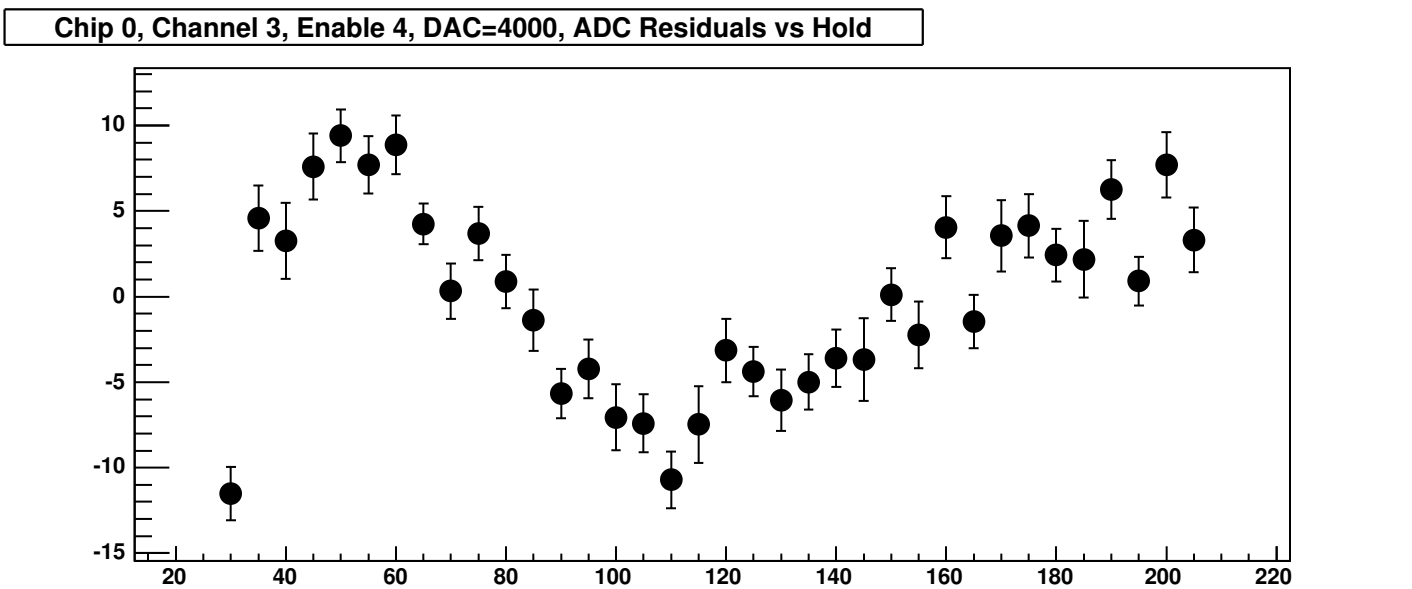


Chip 0, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

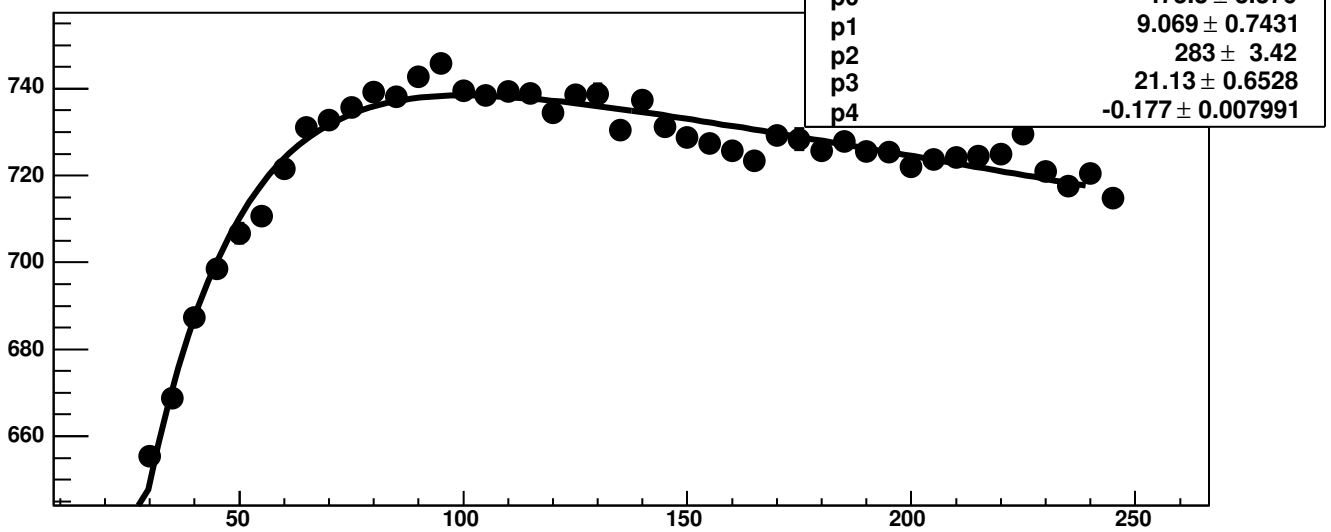



Chip 0, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 381.2 / 31
p0 511.4 ± 1.13
p1 -7.403 ± 8.942
p2 320.3 ± 1.029
p3 -4.493 ± 6.143
p4 -0.3402 ± 0.004344
830
820
810
800
790
780
770
760
20
40
60
80
100
120
140
160
180
200
220

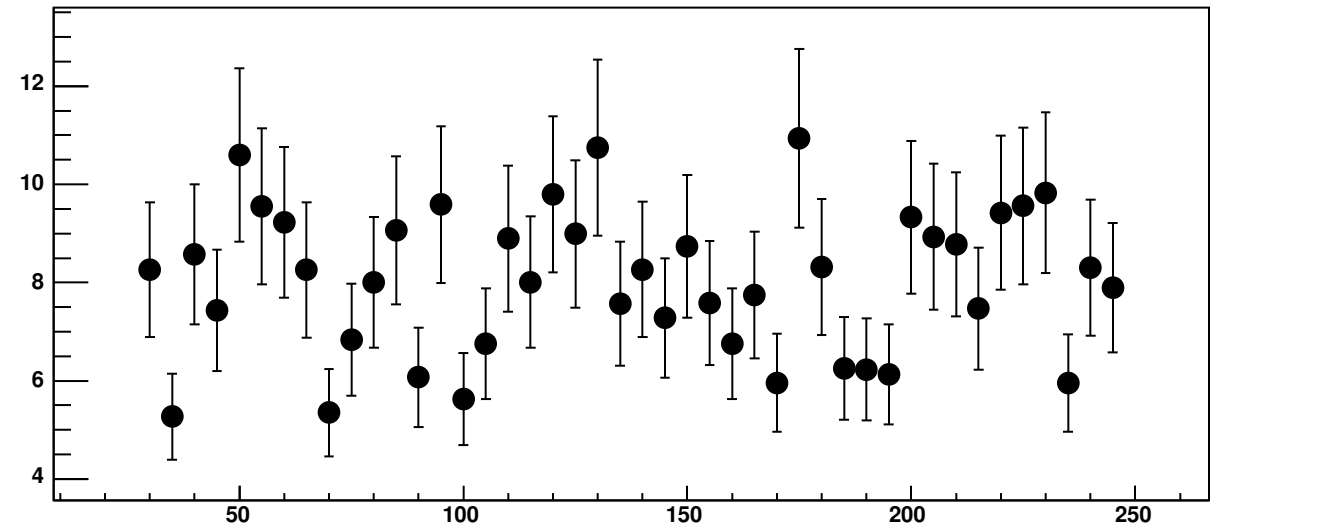

Chip 0, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
20
40
60
80
100
120
140
160
180
200
220


Chip 0, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
20
40
60
80
100
120
140
160
180
200
220

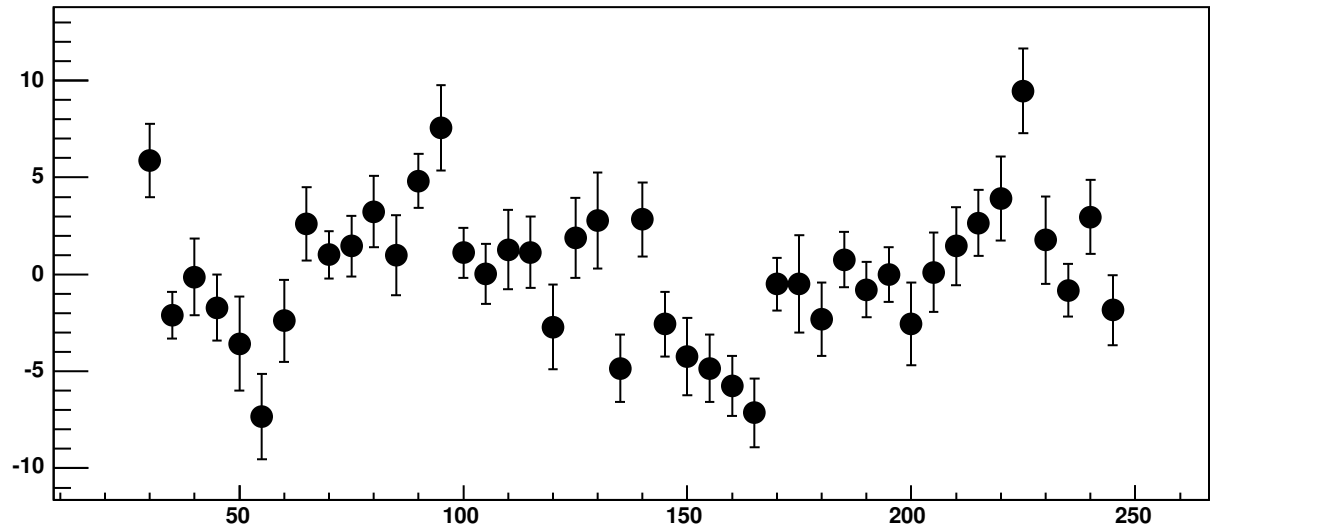
Chip 0, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

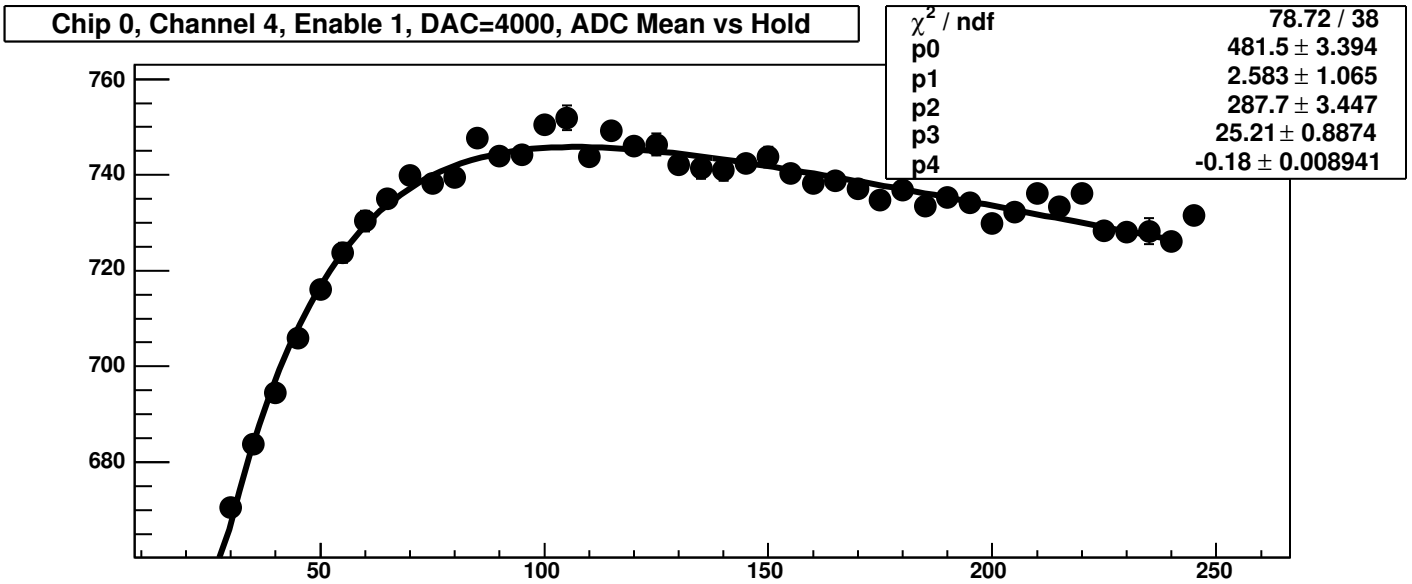


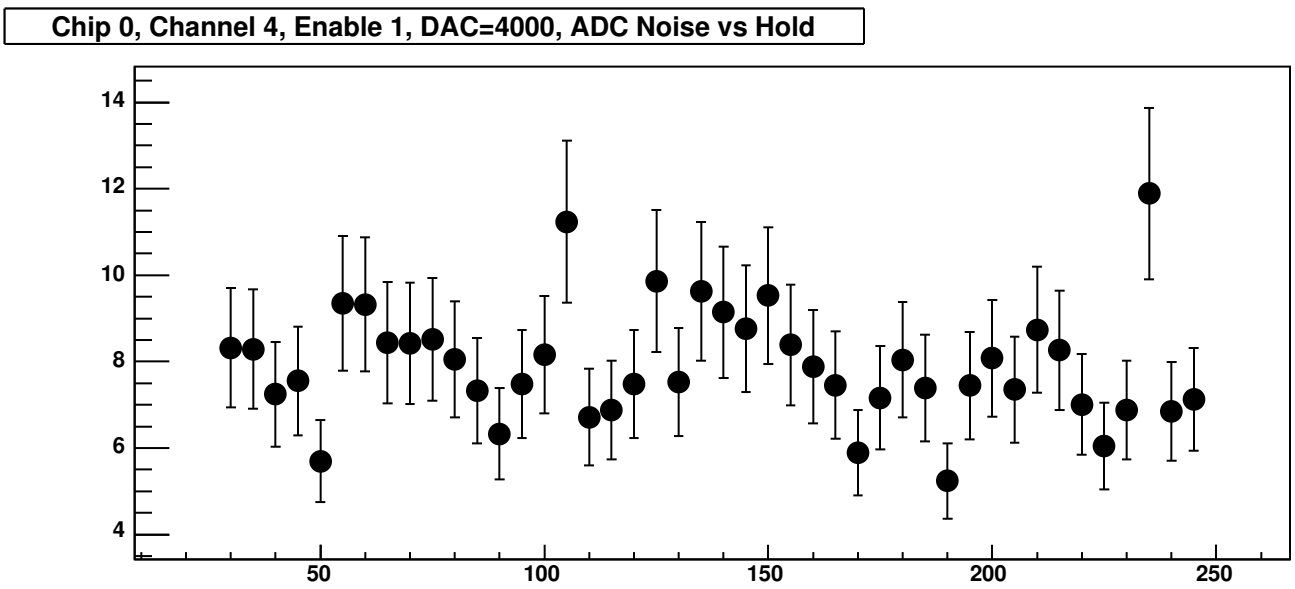
Chip 0, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold

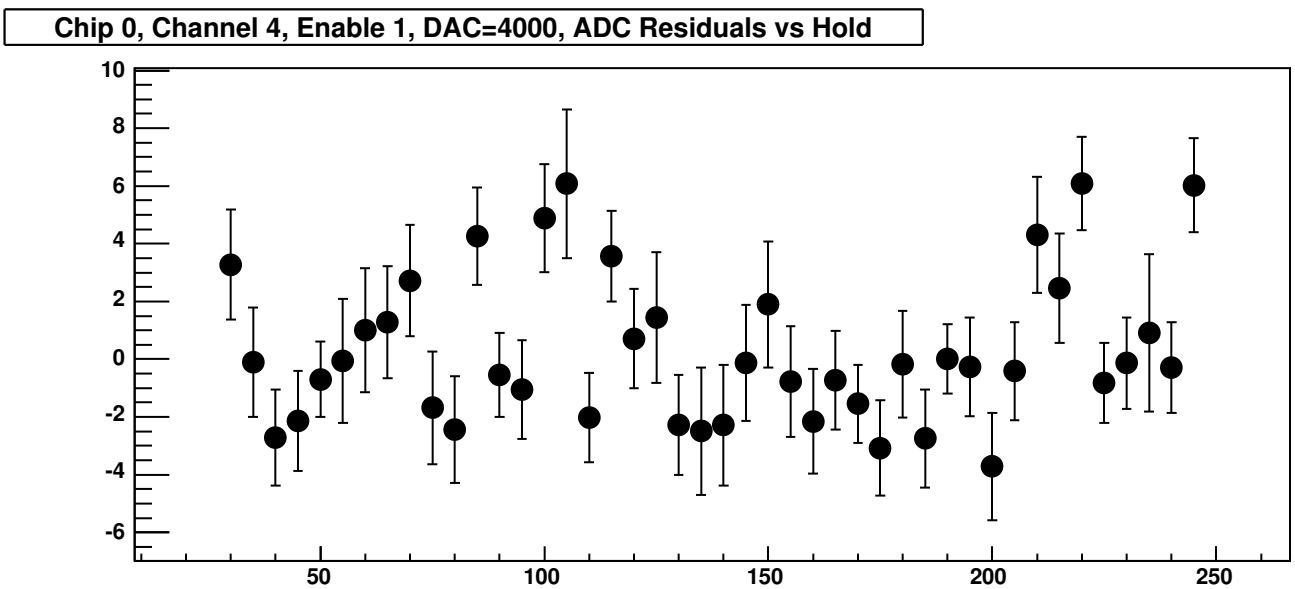


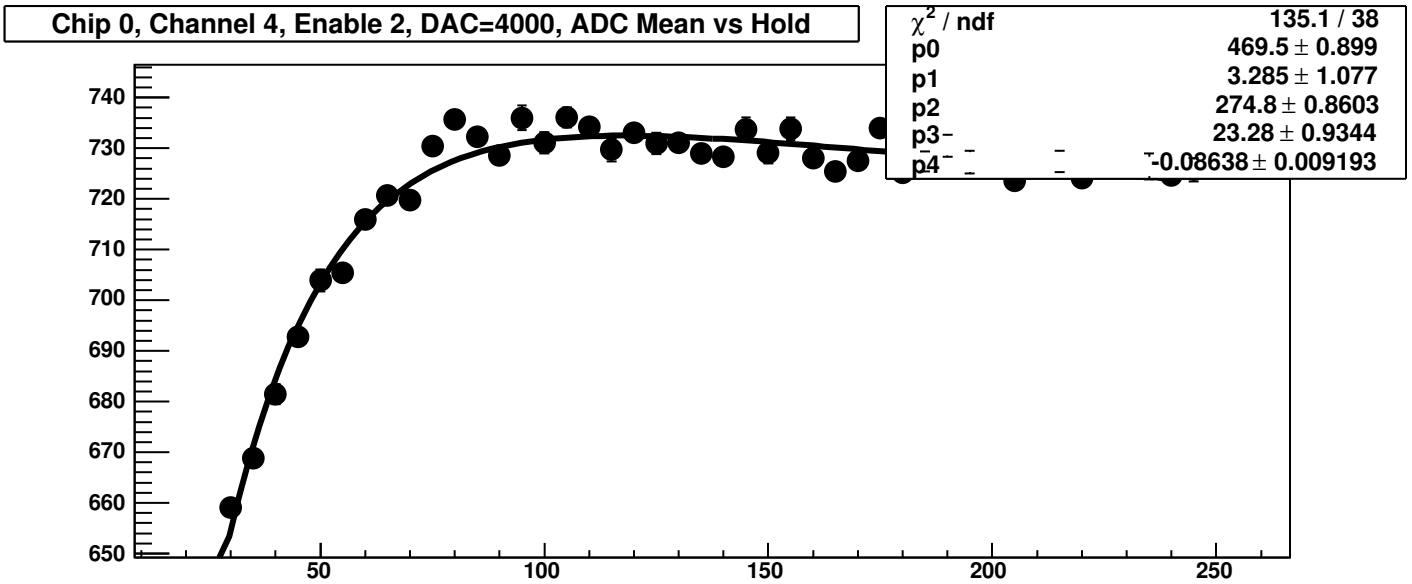
Chip 0, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

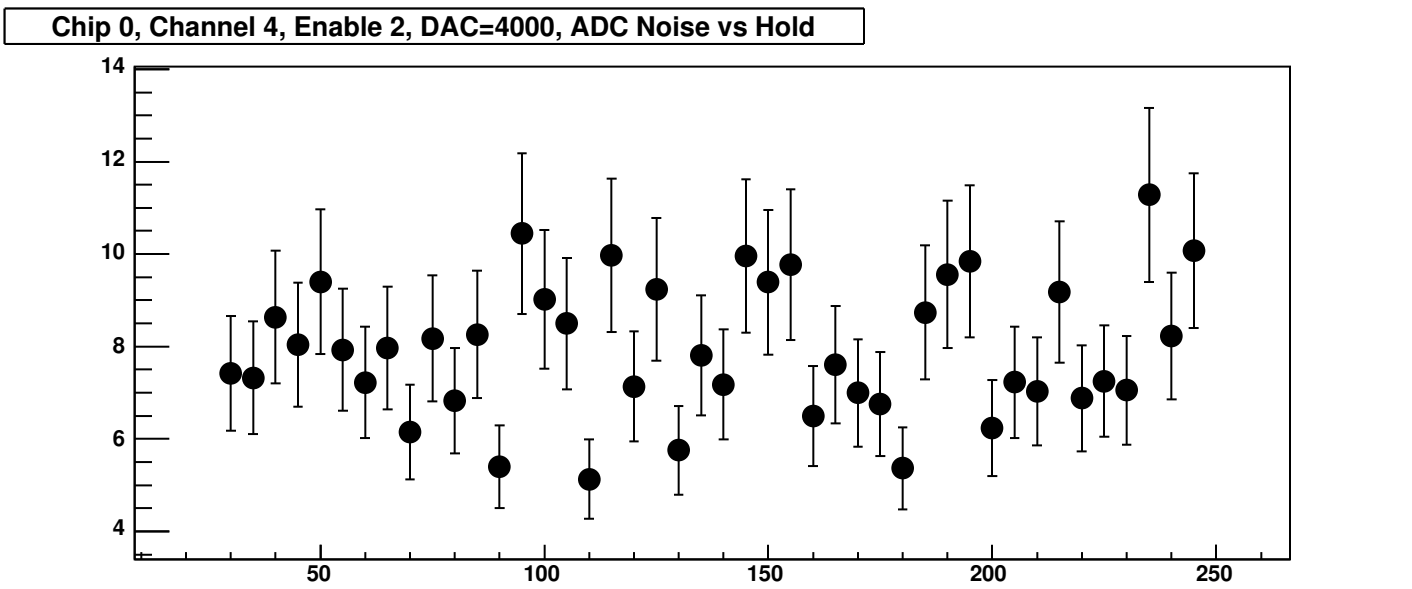


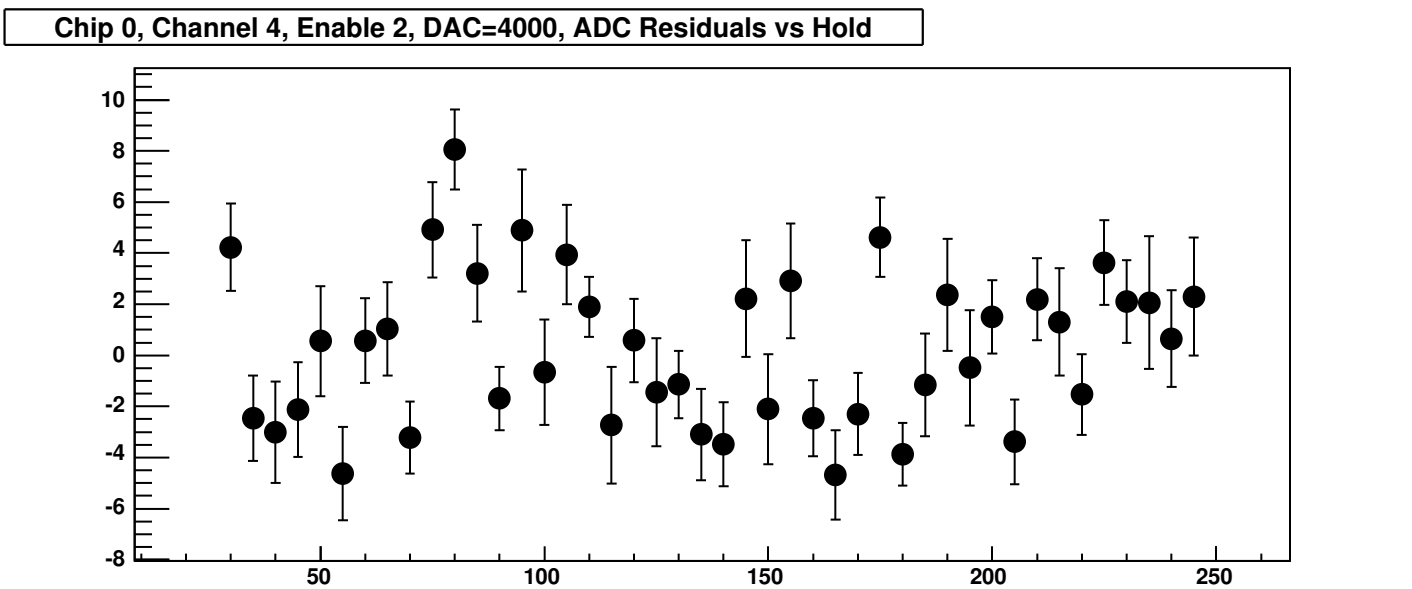

Chip 0, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 78.72 / 38
p0 481.5 ± 3.394
p1 2.583 ± 1.065
p2 287.7 ± 3.447
p3 25.21 ± 0.8874
p4 -0.18 ± 0.008941
760
740
720
700
680
50
100
150
200
250

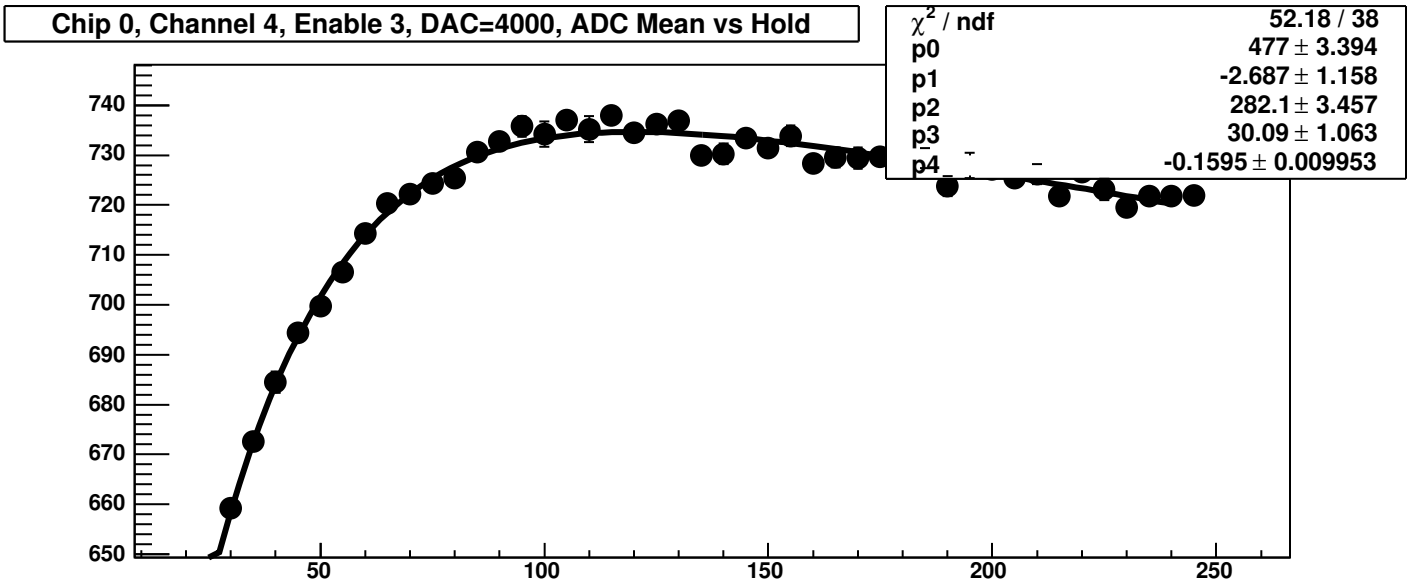

Chip 0, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250

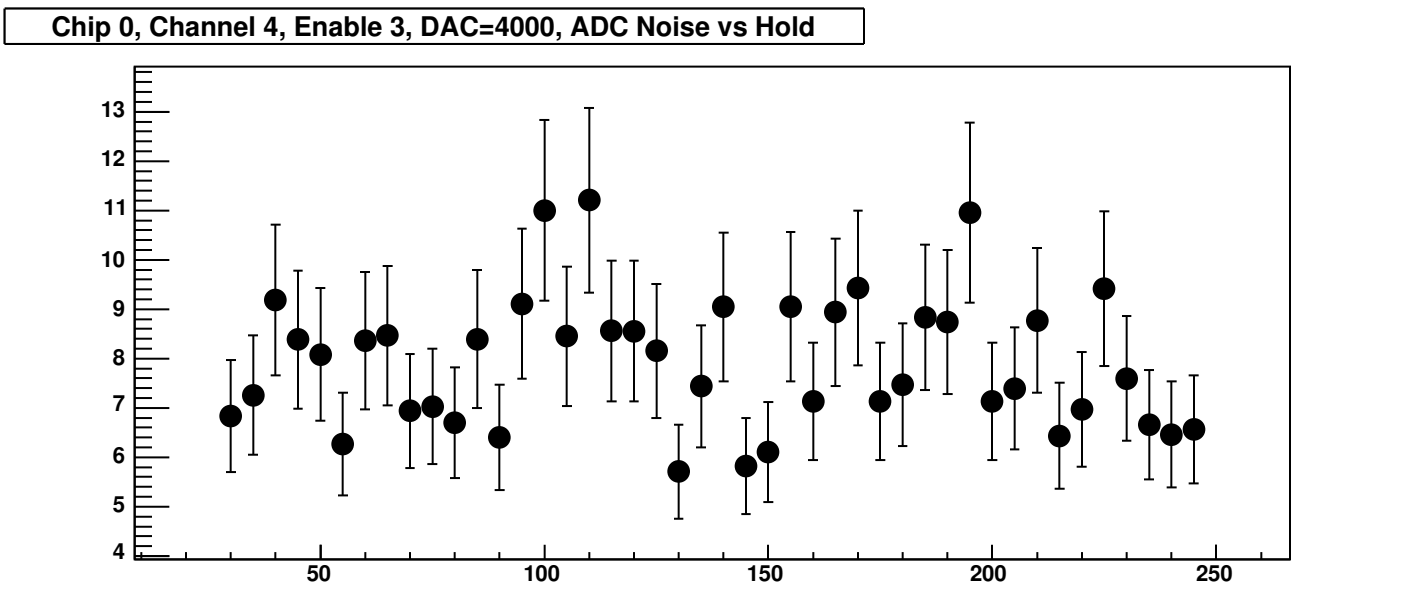

Chip 0, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
50
100
150
200
250

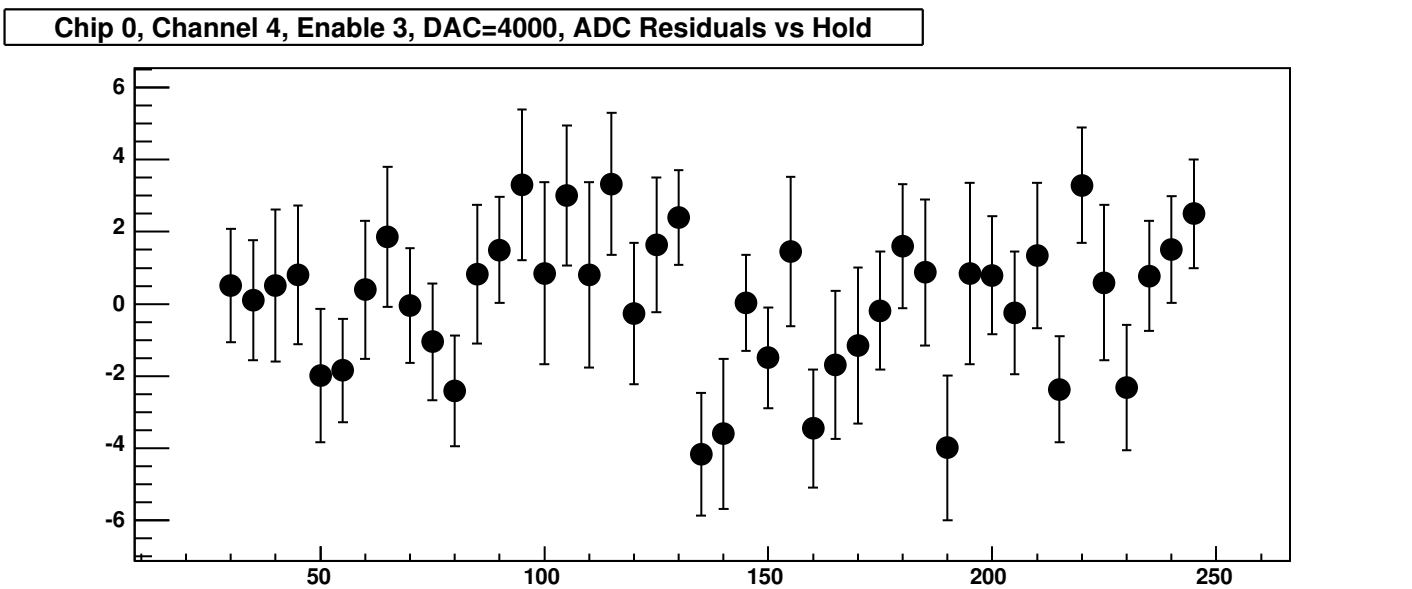

Chip 0, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 135.1 / 38
p0 469.5 ± 0.899
p1 3.285 ± 1.077
p2 274.8 ± 0.8603
p3 23.28 ± 0.9344
p4 -0.08638 ± 0.009193
740
730
720
710
700
690
680
670
660
650
50
100
150
200
250

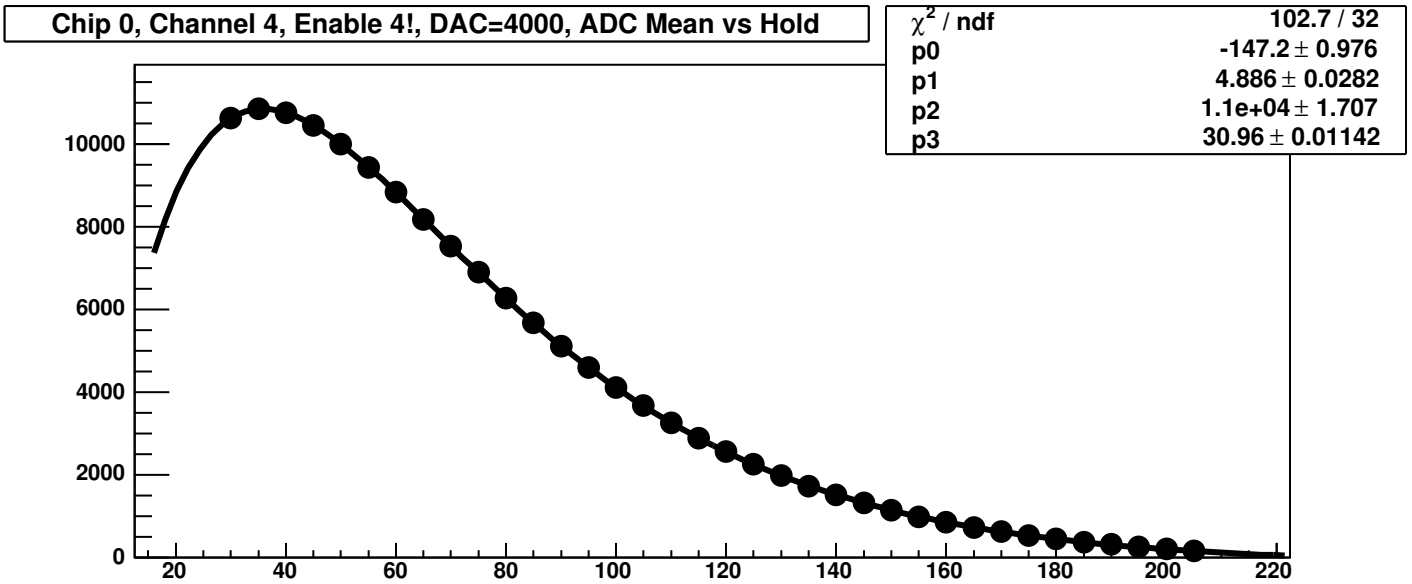

Chip 0, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250

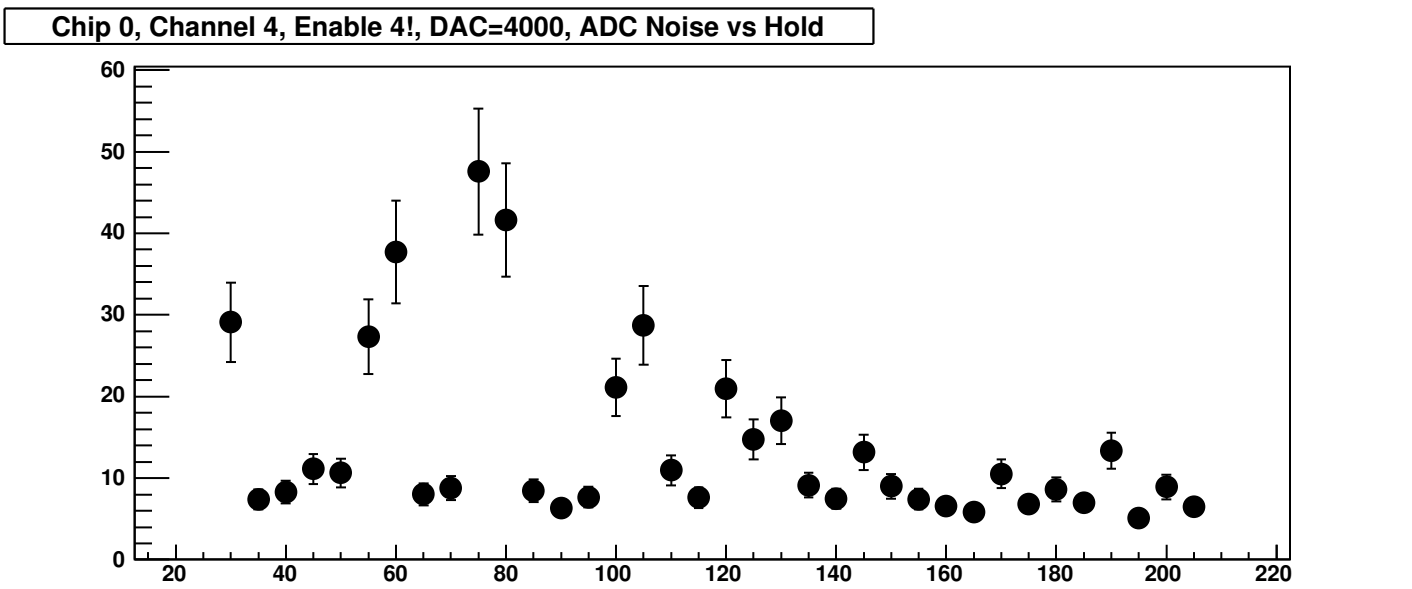

Chip 0, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

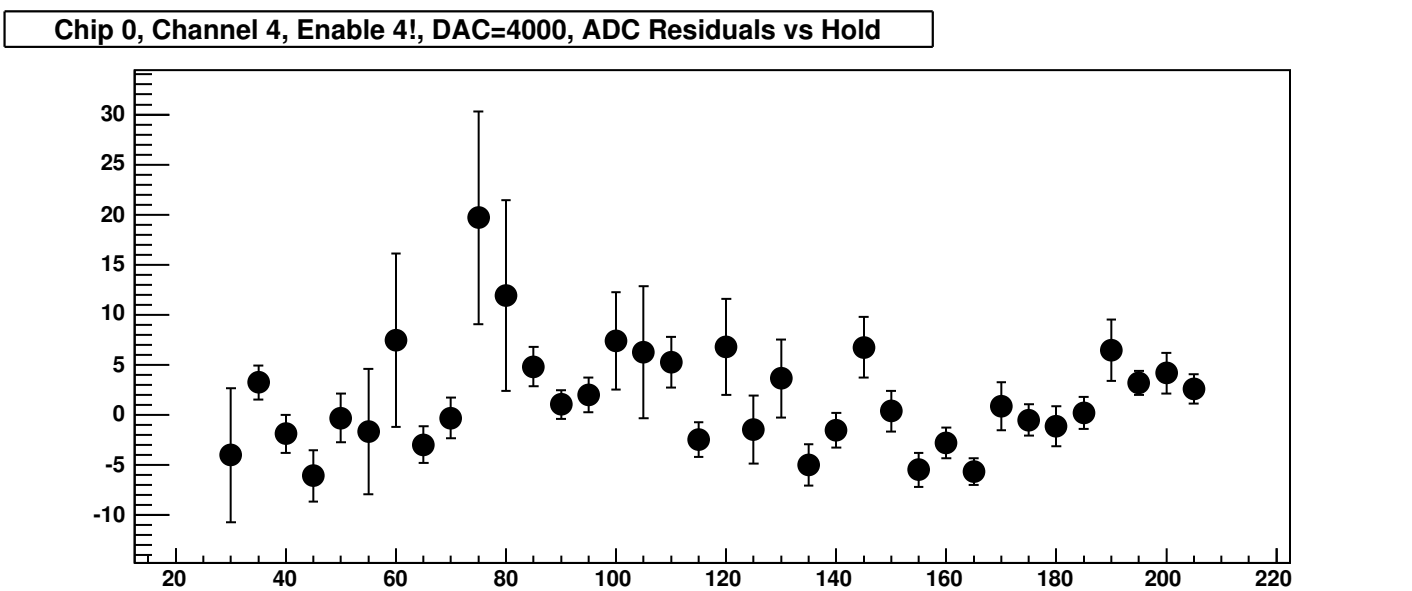

Chip 0, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 52.18 / 38
p0 477 ± 3.394
p1 -2.687 ± 1.158
p2 282.1 ± 3.457
p3 30.09 ± 1.063
p4 -0.1595 ± 0.009953
740
730
720
710
700
690
680
670
660
650
50
100
150
200
250

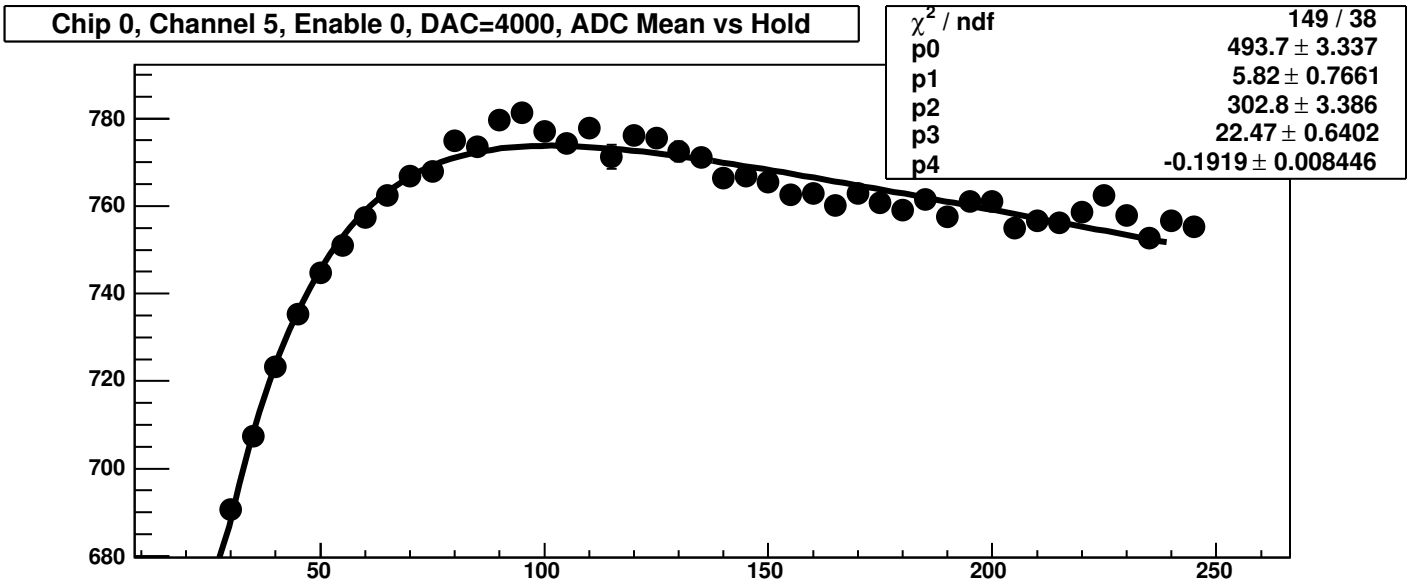

Chip 0, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
50
100
150
200
250

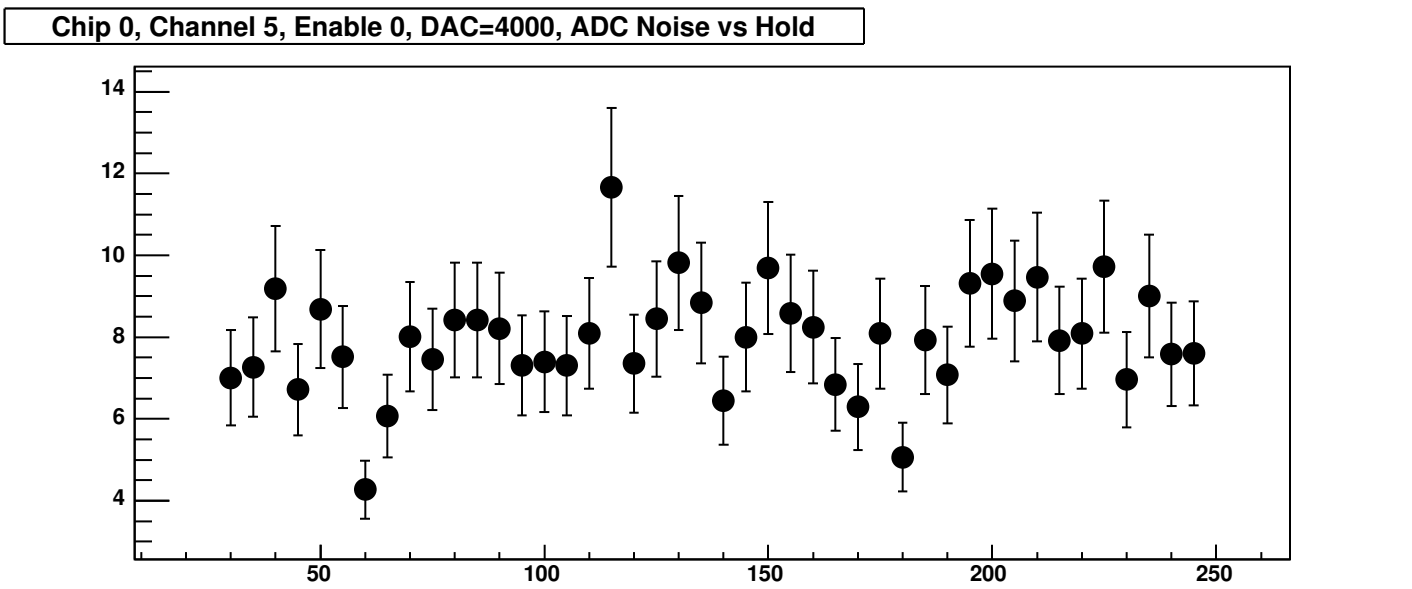

Chip 0, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
50
100
150
200
250

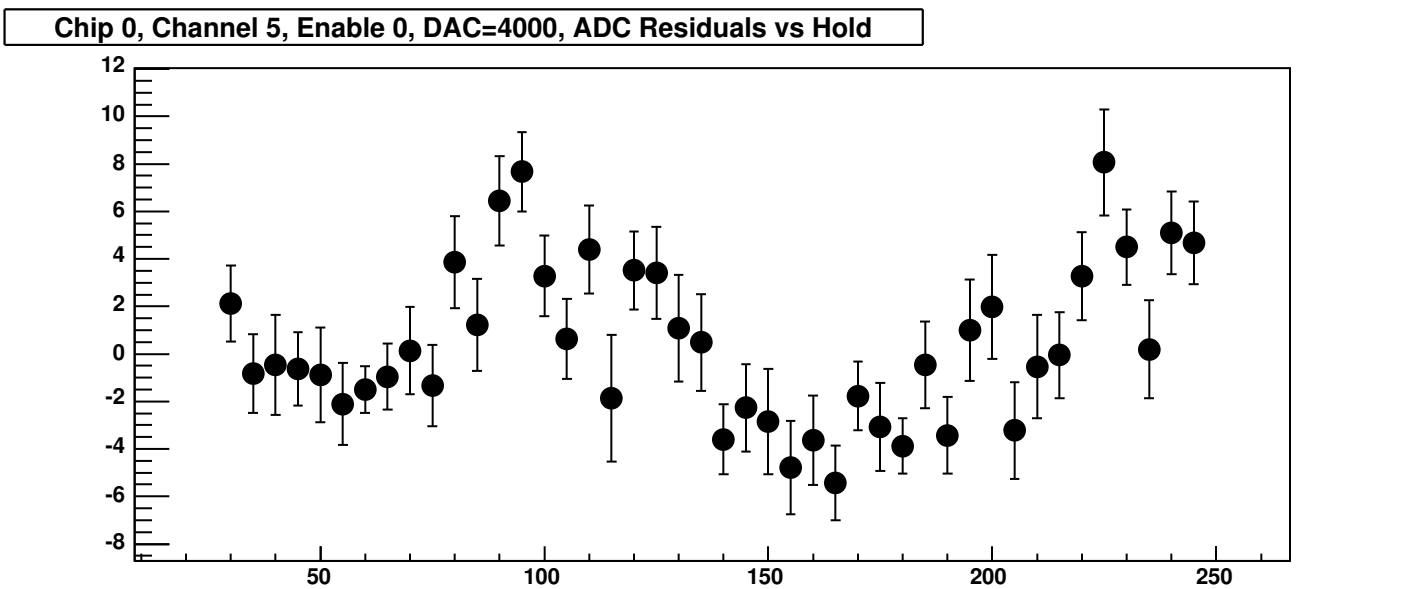

Chip 0, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 102.7 / 32
p0 -147.2 ± 0.976
p1 4.886 ± 0.0282
p2 1.1e+04 ± 1.707
p3 30.96 ± 0.01142
0 2000 4000 6000 8000 10000
20 40 60 80 100 120 140 160 180 200 220

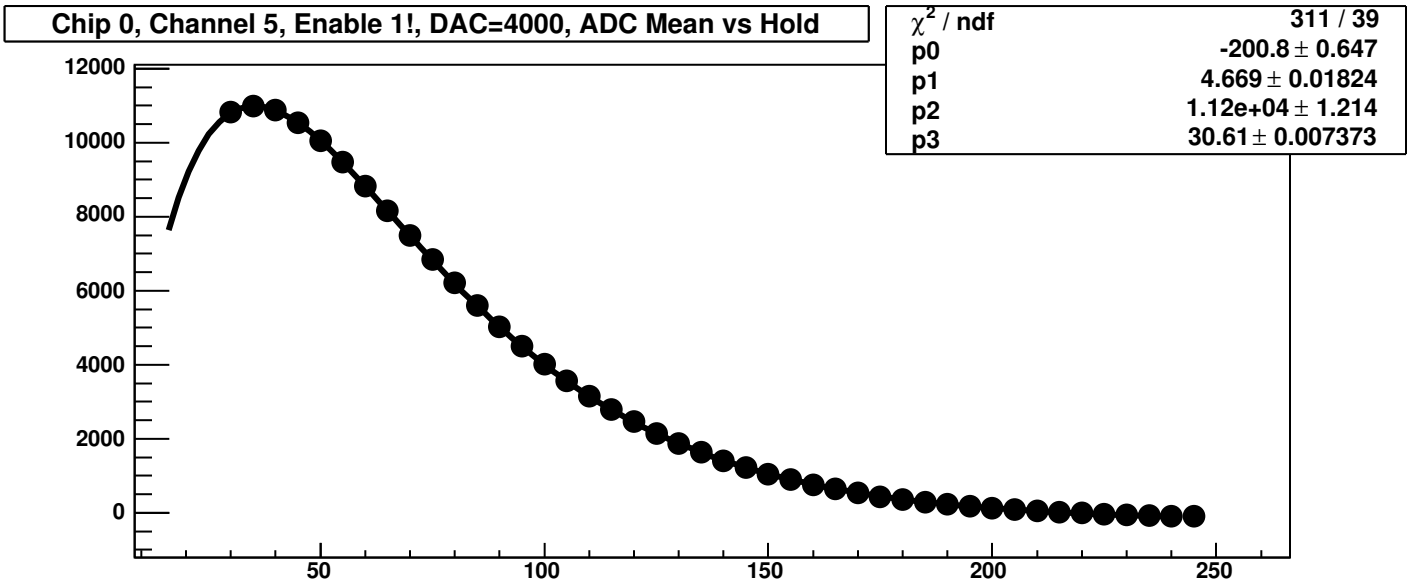

Chip 0, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold
0 10 20 30 40 50 60
20 40 60 80 100 120 140 160 180 200 220

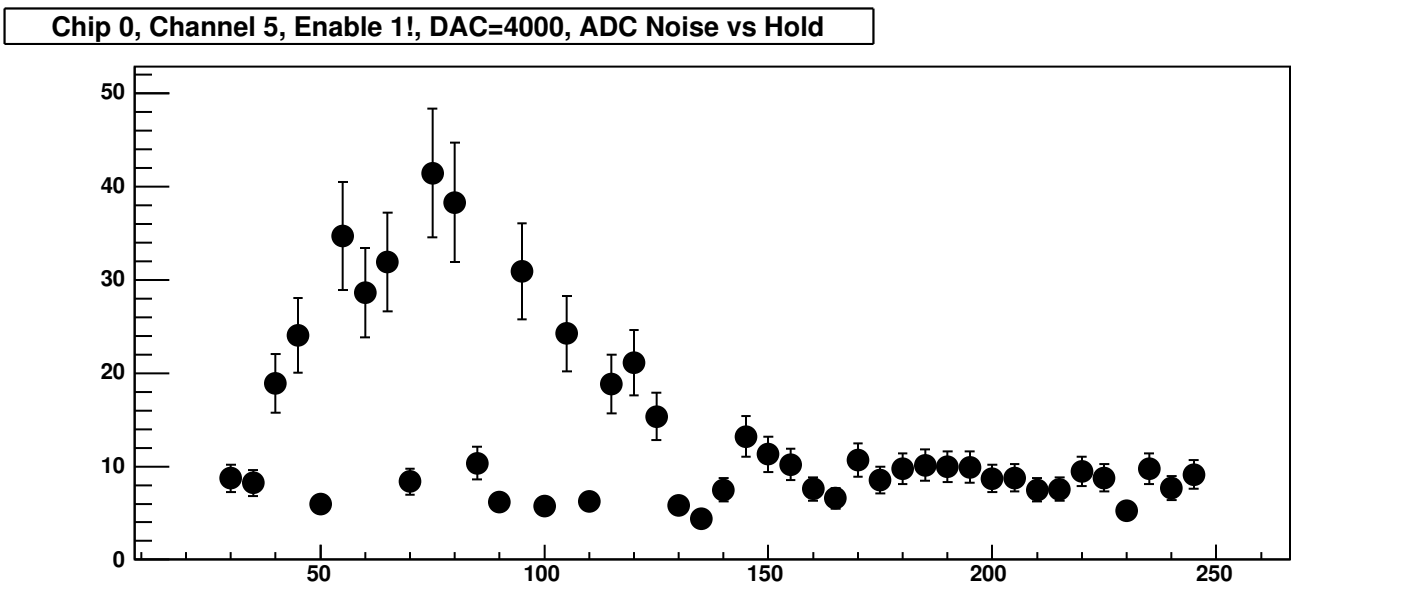

Chip 0, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold
-10 -5 0 5 10 15 20 25 30
20 40 60 80 100 120 140 160 180 200 220

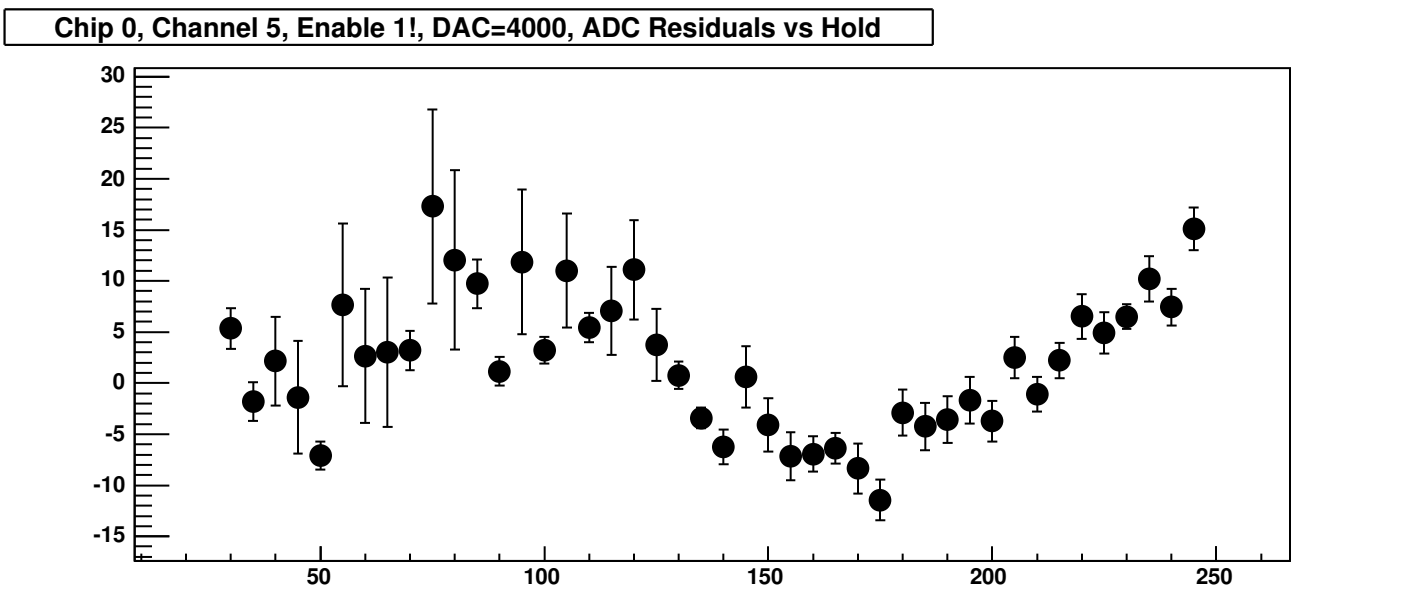

Chip 0, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 149 / 38
p0 493.7 ± 3.337
p1 5.82 ± 0.7661
p2 302.8 ± 3.386
p3 22.47 ± 0.6402
p4 -0.1919 ± 0.008446
780
760
740
720
700
680
50
100
150
200
250


Chip 0, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250

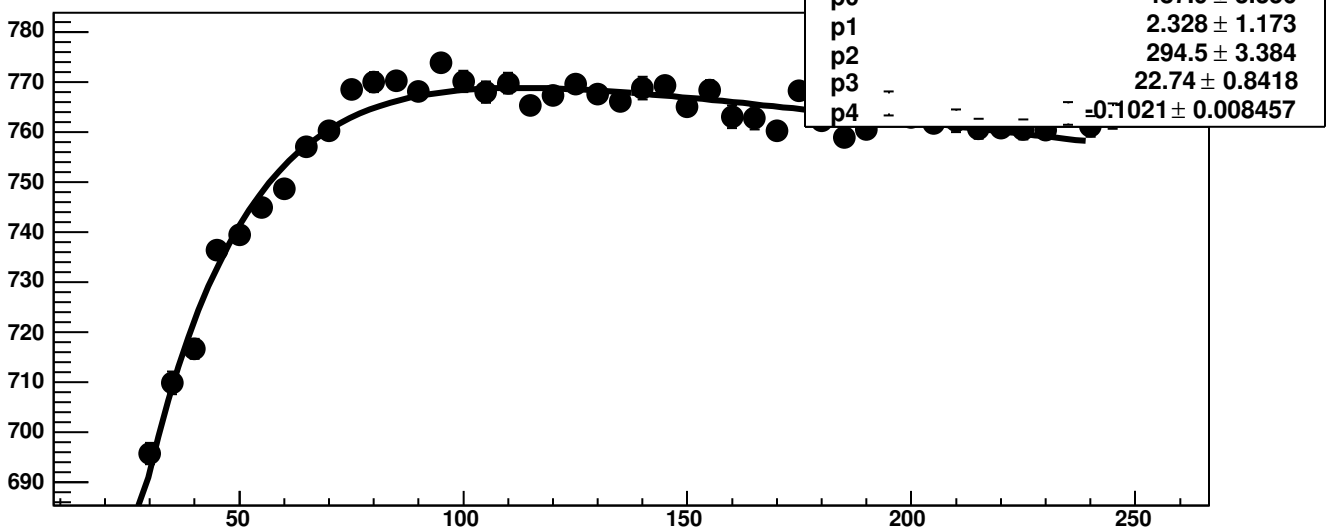

Chip 0, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold
12
10
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250


Chip 0, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 311 / 39
p0 -200.8 ± 0.647
p1 4.669 ± 0.01824
p2 1.12e+04 ± 1.214
p3 30.61 ± 0.007373
12000
10000
8000
6000
4000
2000
0
50
100
150
200
250

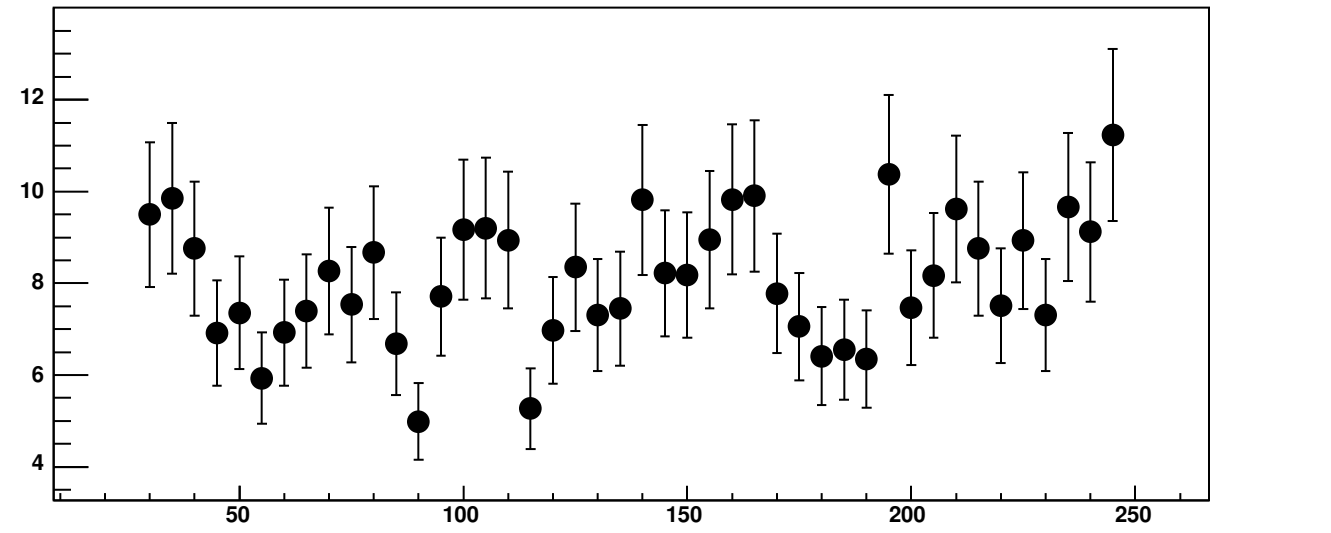

Chip 0, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
0
50
100
150
200
250


Chip 0, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
-15
50
100
150
200
250

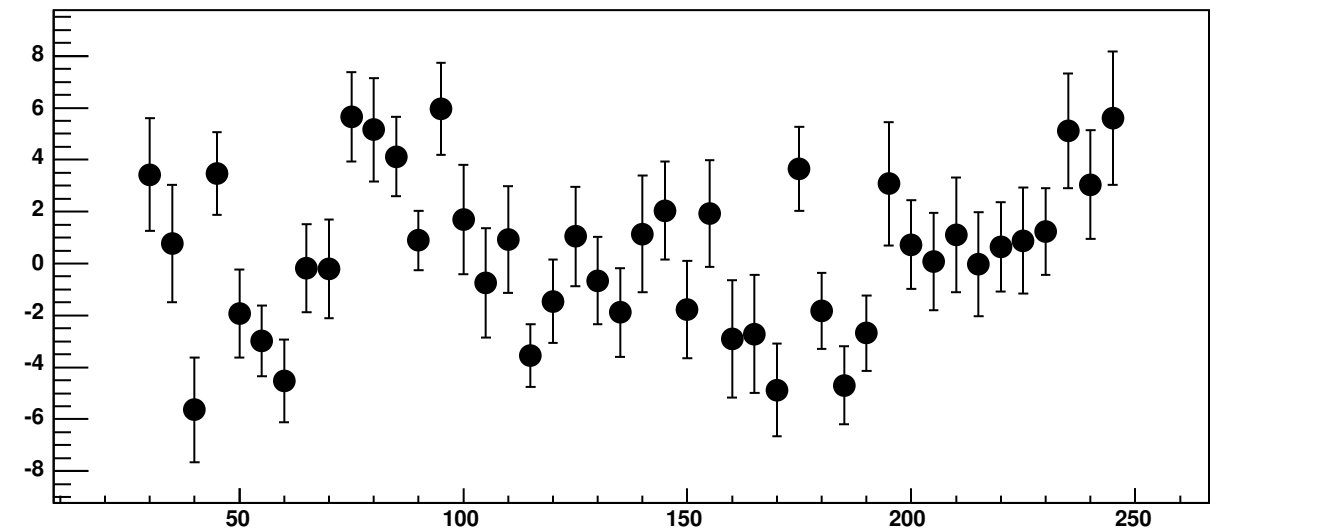
Chip 0, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold



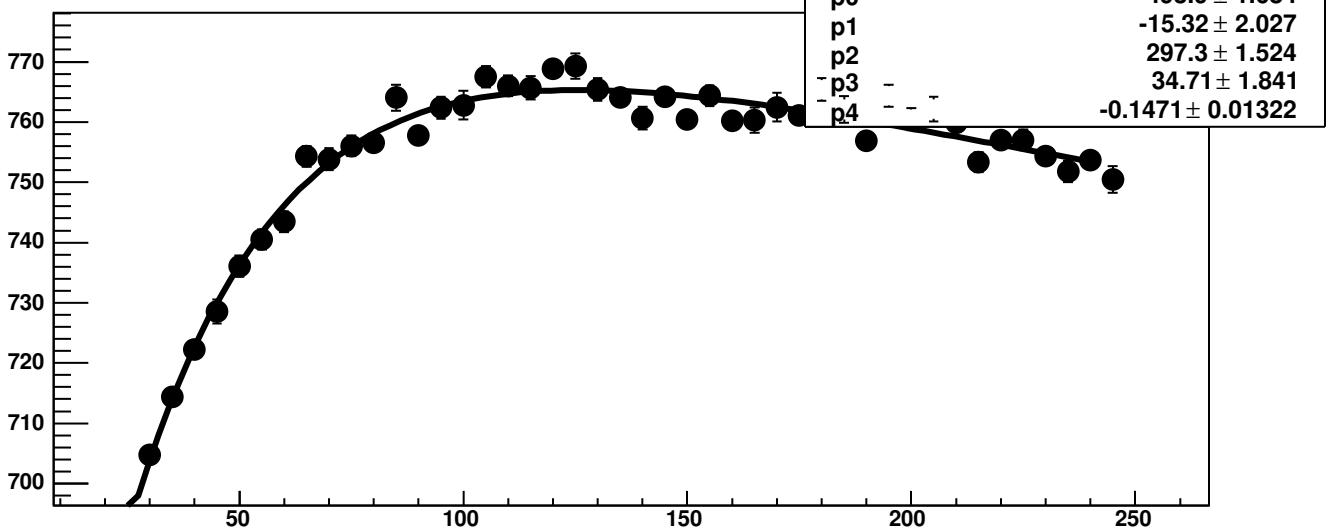
Chip 0, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



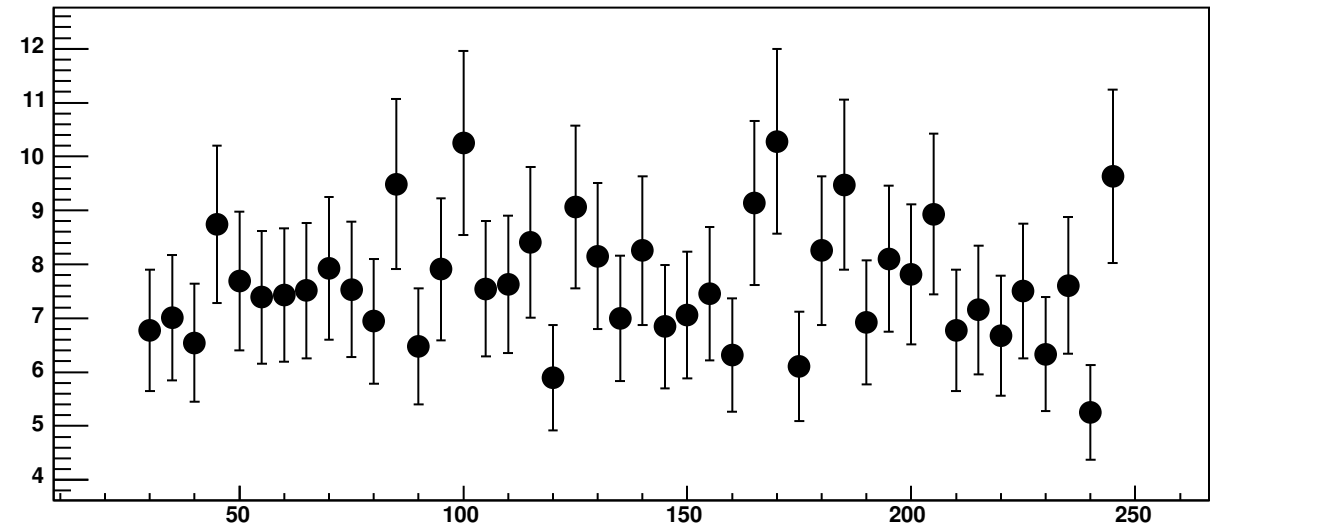
Chip 0, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold



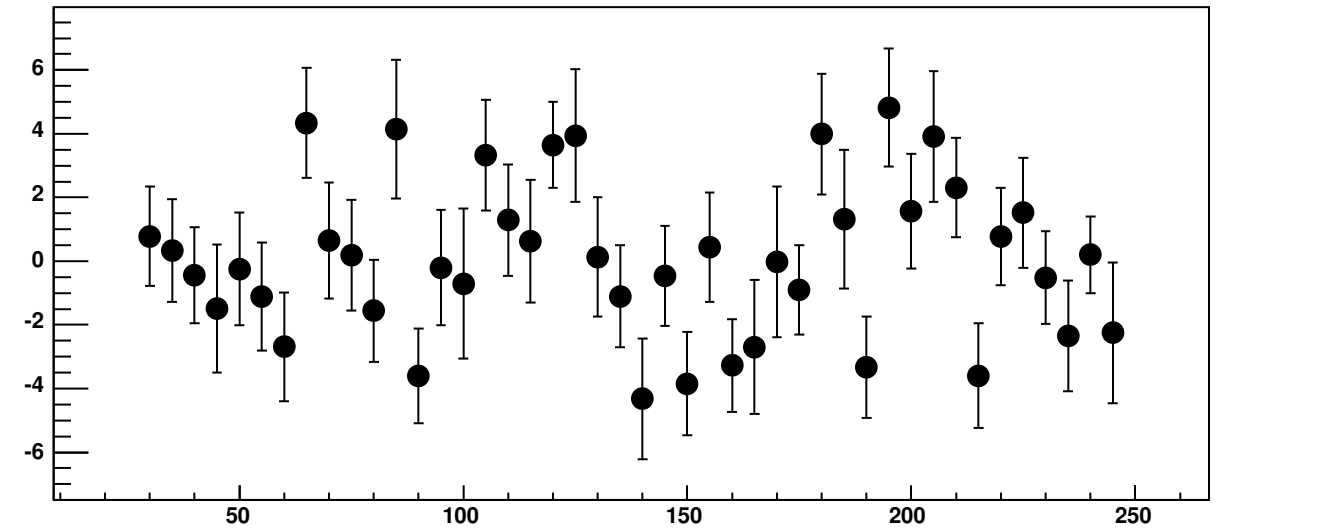
Chip 0, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

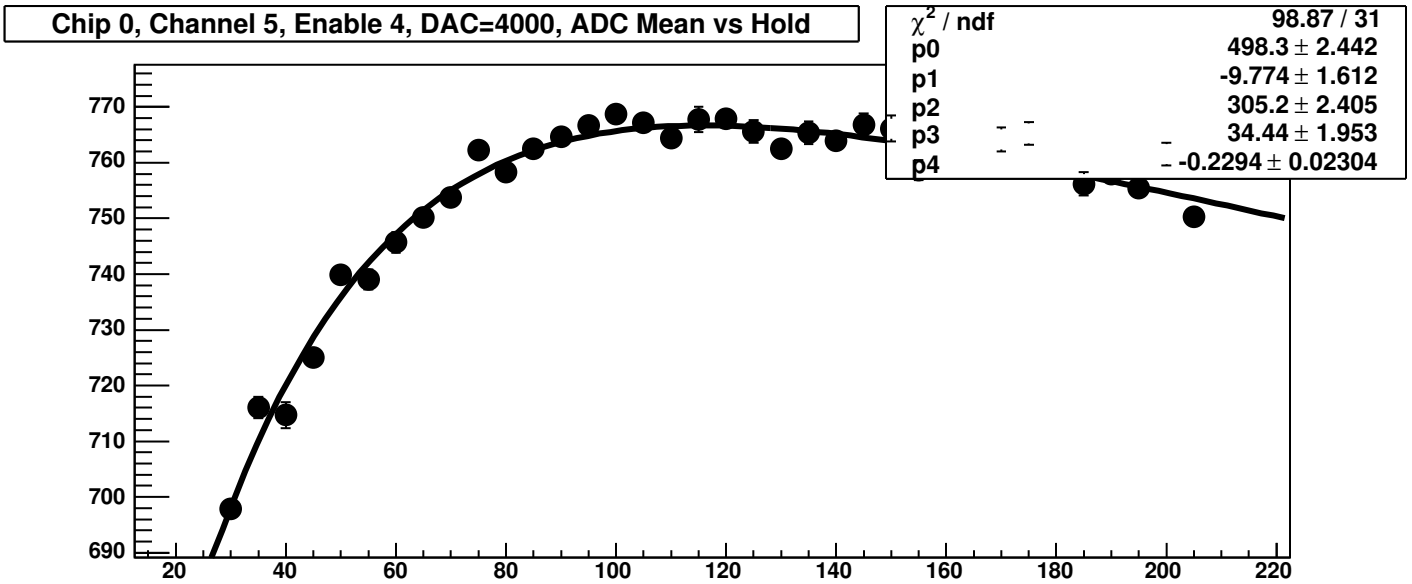


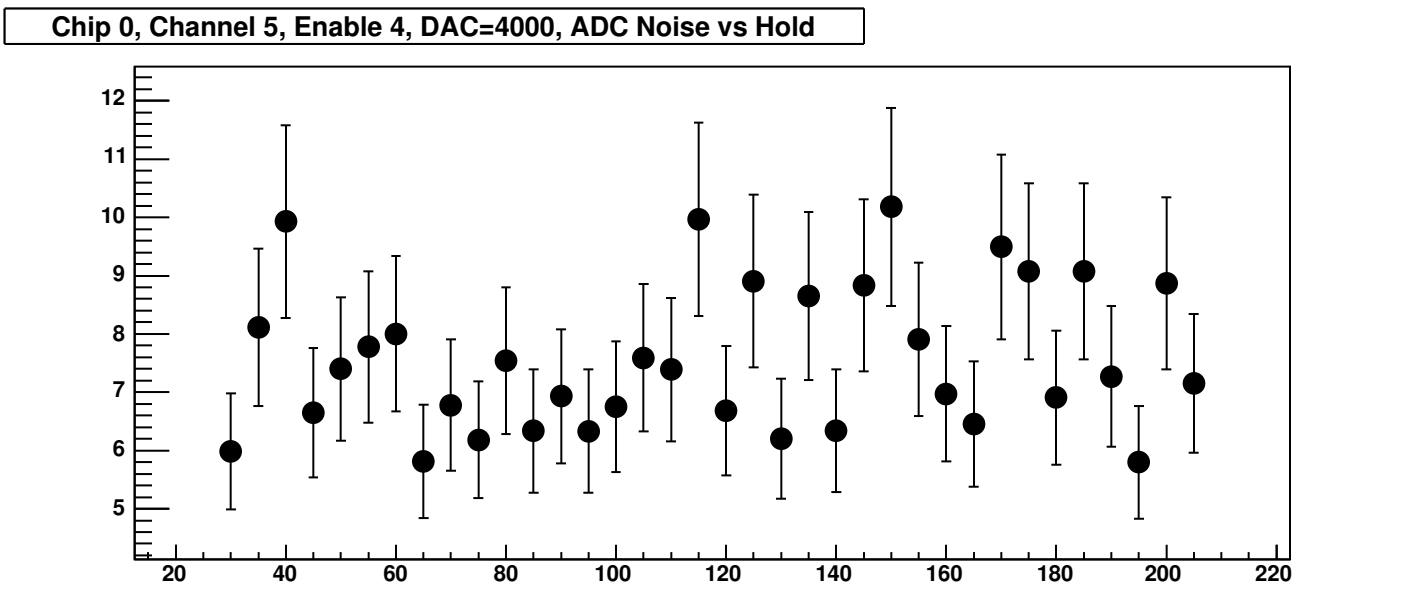
Chip 0, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold

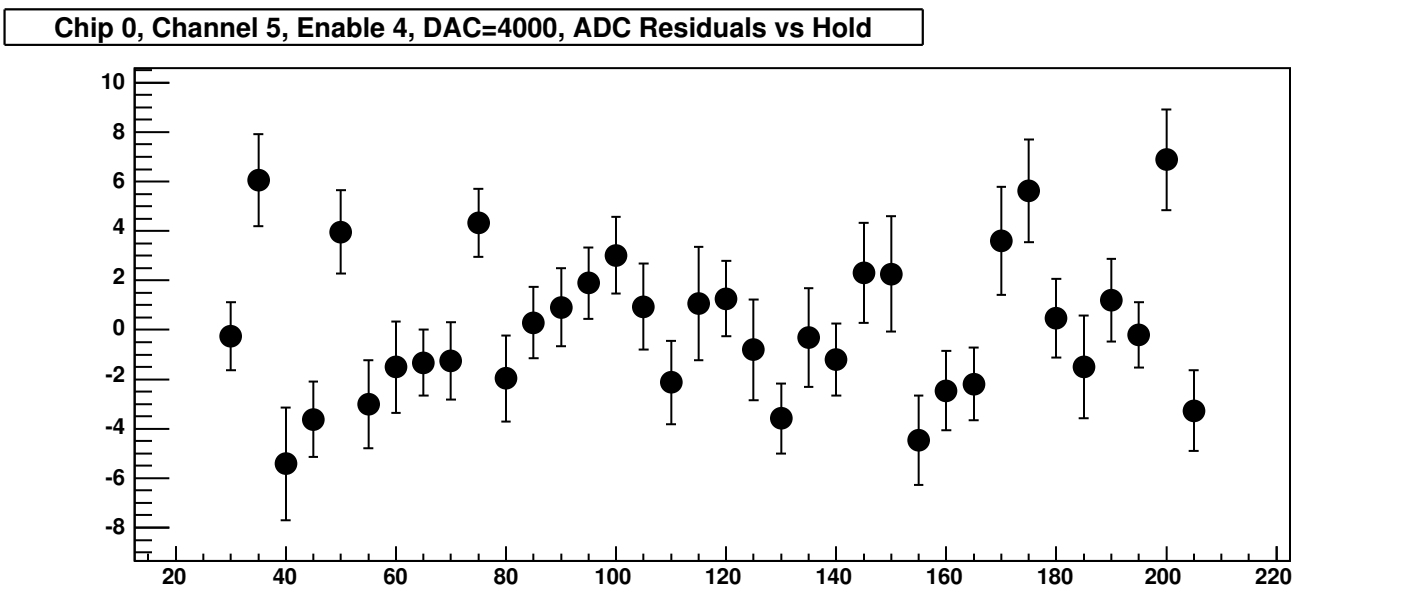


Chip 0, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

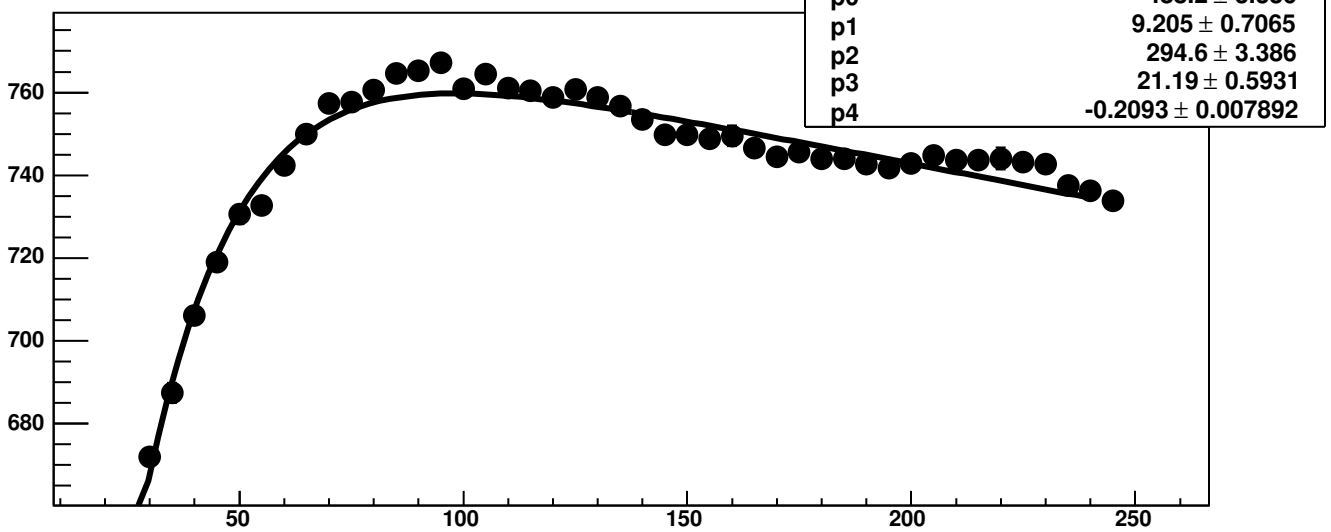



Chip 0, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 98.87 / 31
p0 498.3 ± 2.442
p1 -9.774 ± 1.612
p2 305.2 ± 2.405
p3 34.44 ± 1.953
p4 -0.2294 ± 0.02304
690 700 710 720 730 740 750 760 770
20 40 60 80 100 120 140 160 180 200 220

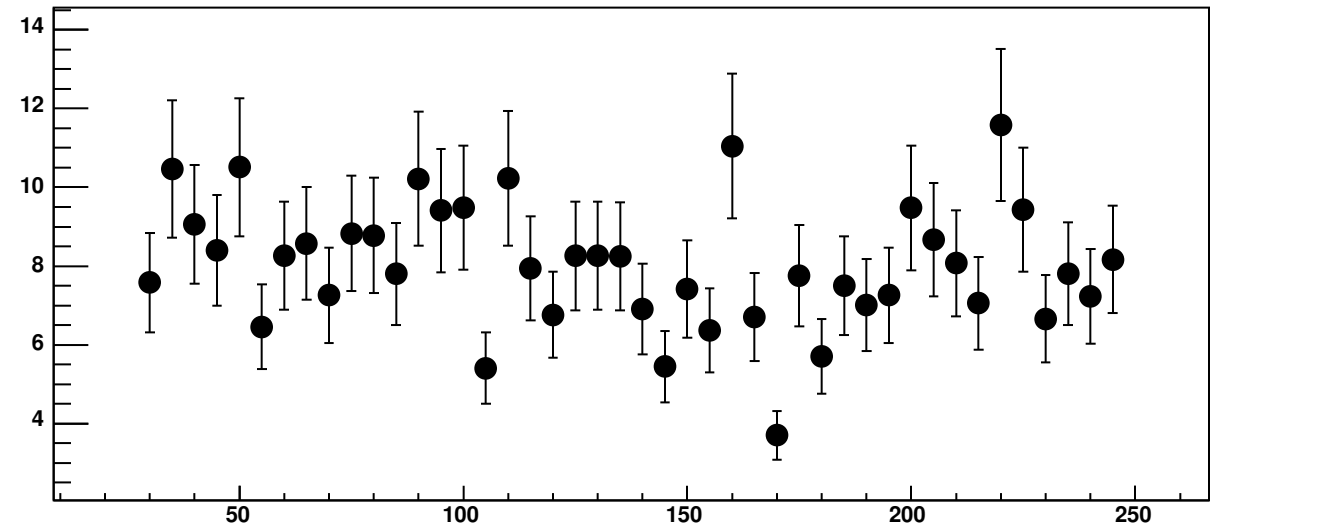

Chip 0, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold
5 6 7 8 9 10 11 12
20 40 60 80 100 120 140 160 180 200 220


Chip 0, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold
-8 -6 -4 -2 0 2 4 6 8 10
20 40 60 80 100 120 140 160 180 200 220

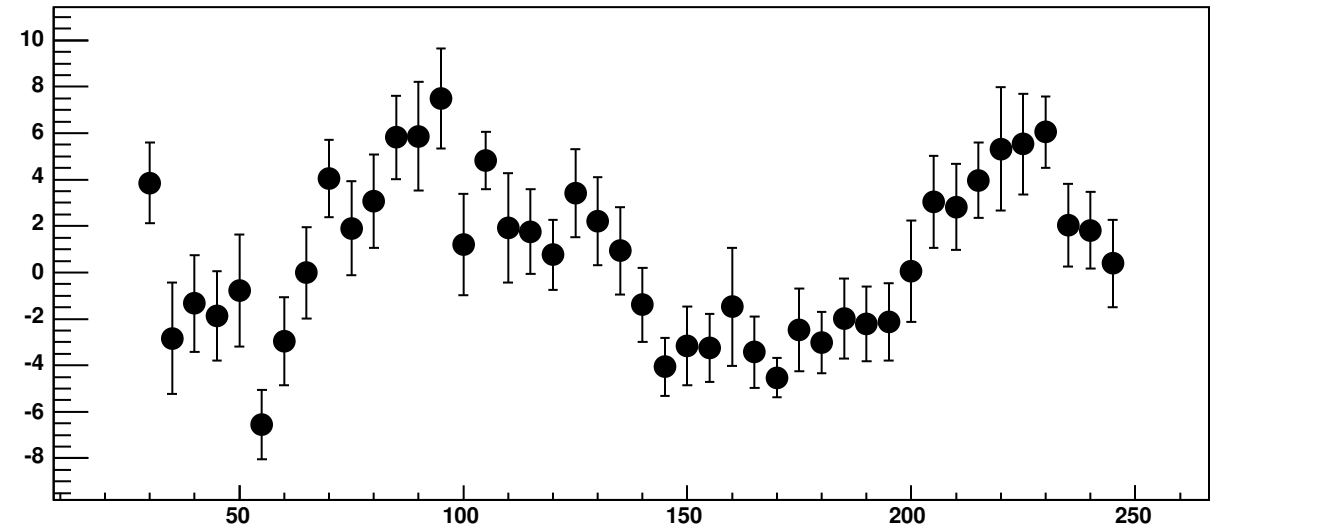
Chip 0, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold

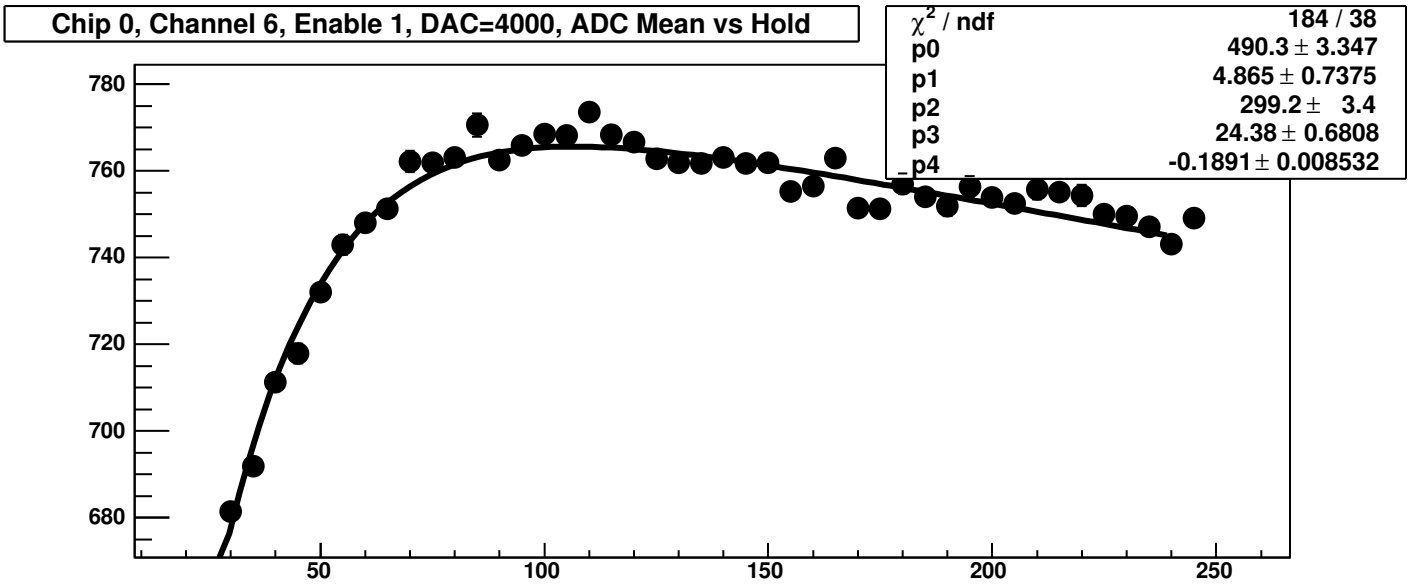


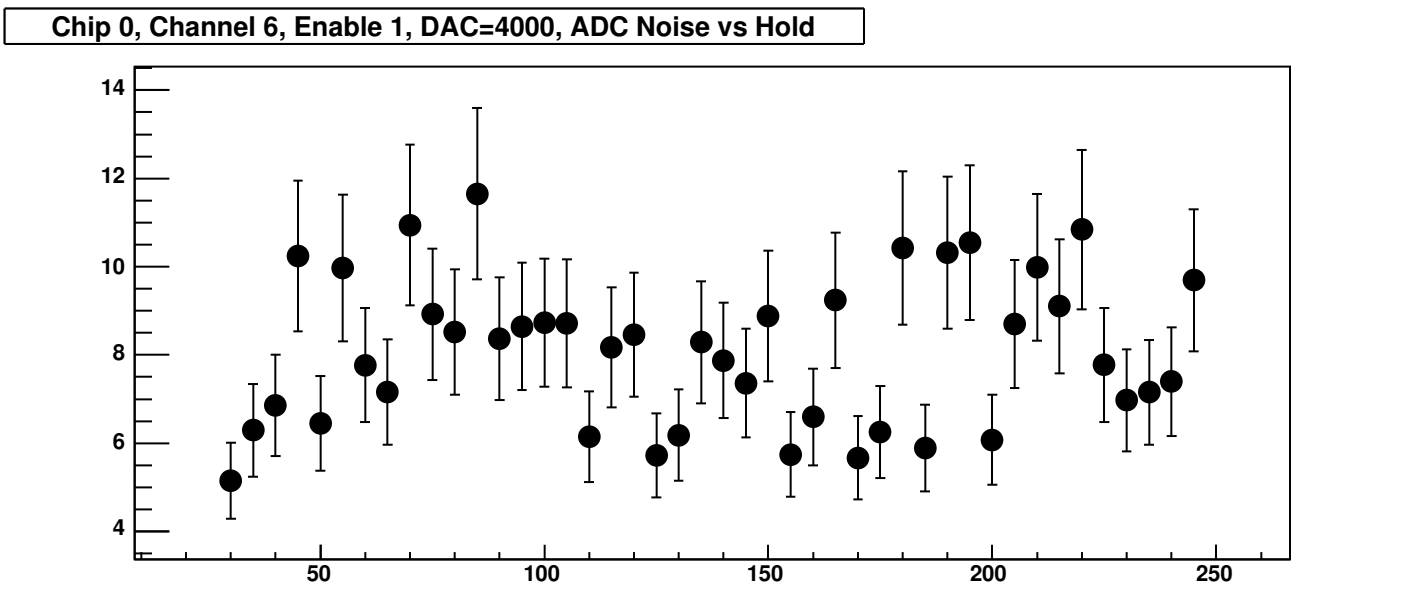
Chip 0, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold

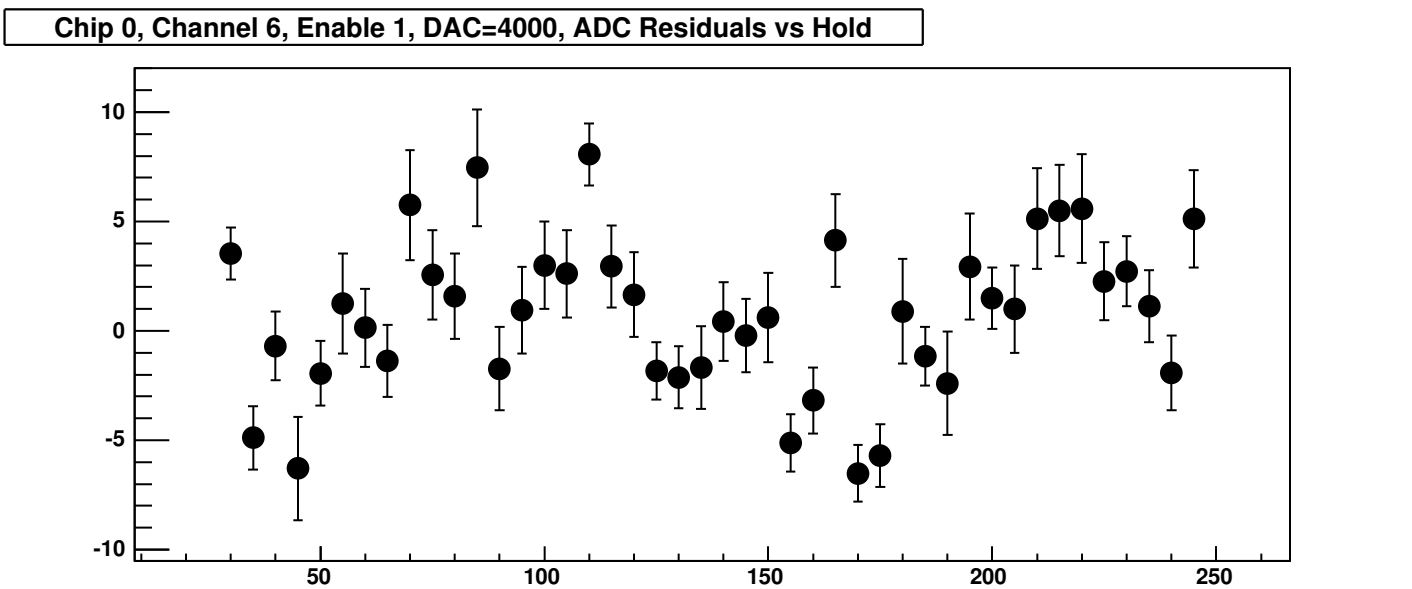


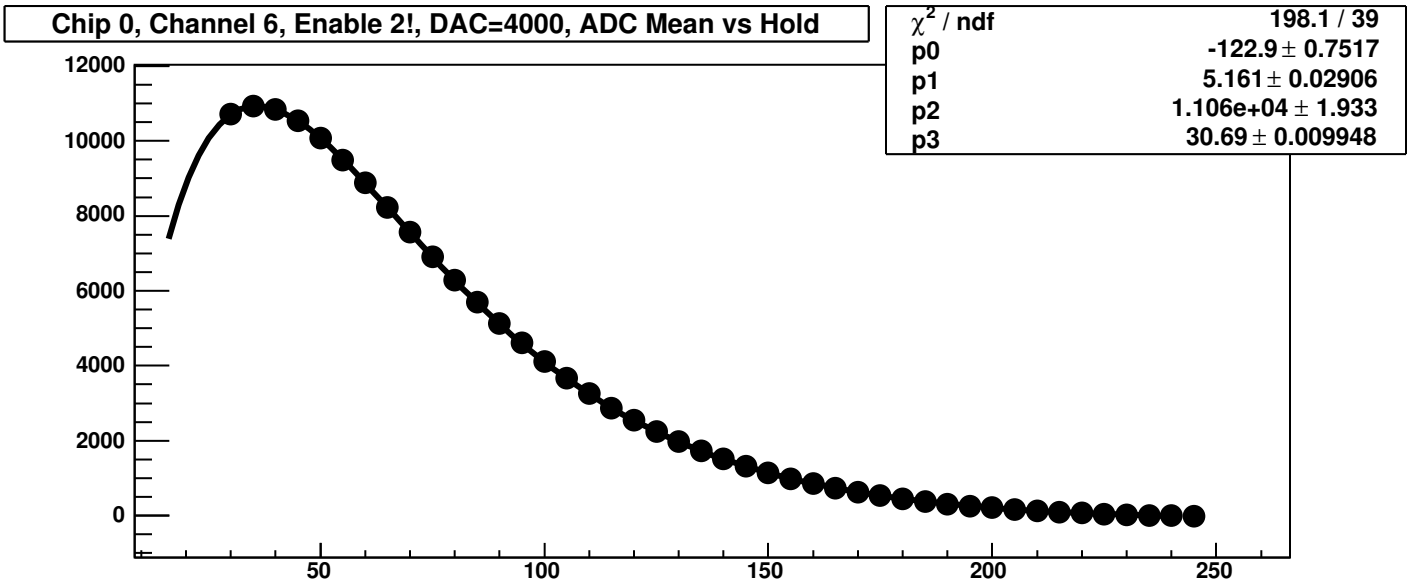
Chip 0, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold

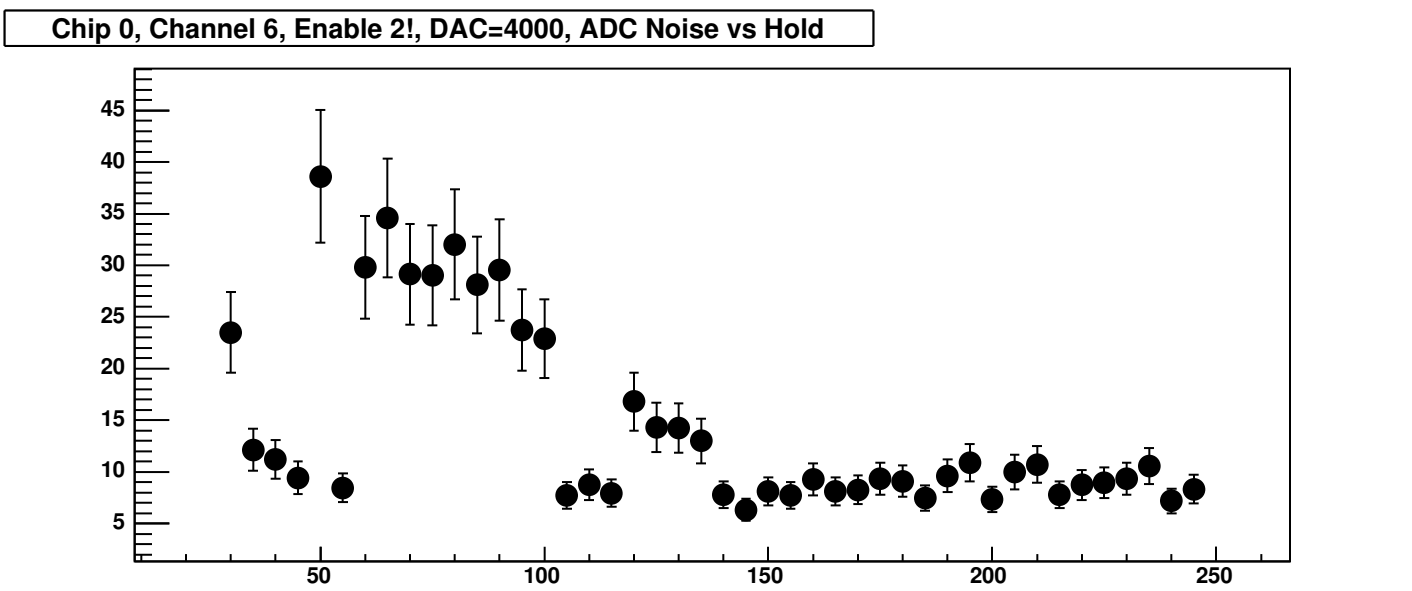


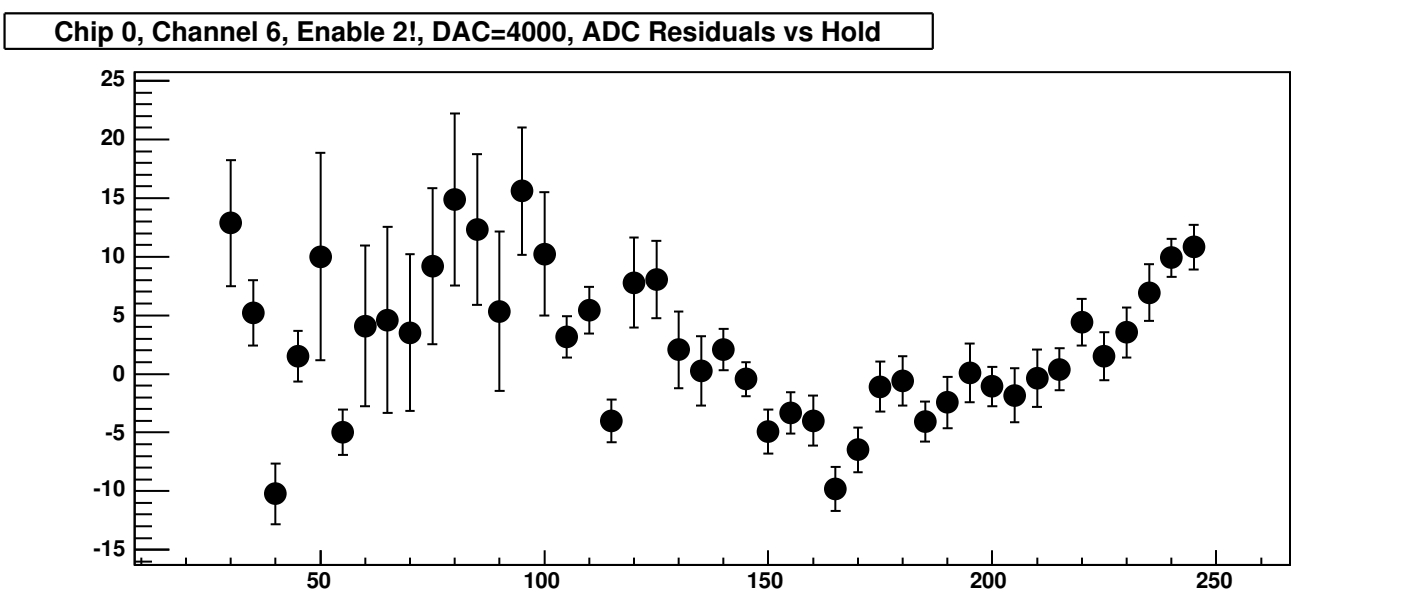

Chip 0, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 184 / 38
p0 490.3 ± 3.347
p1 4.865 ± 0.7375
p2 299.2 ± 3.4
p3 24.38 ± 0.6808
p4 -0.1891 ± 0.008532
780
760
740
720
700
680
50
100
150
200
250


Chip 0, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250

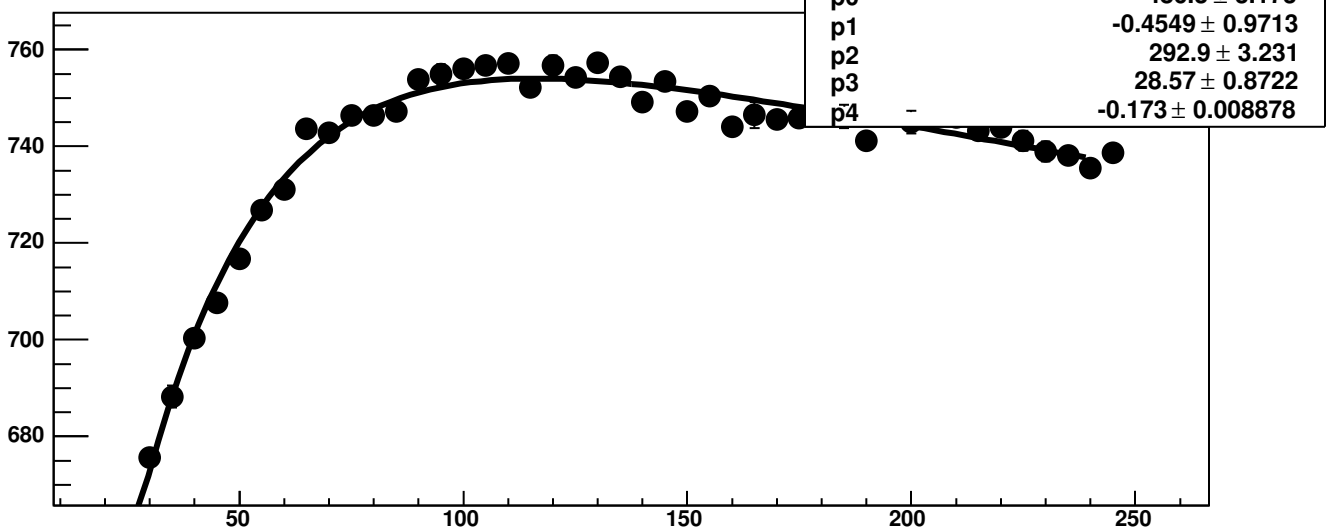

Chip 0, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250


Chip 0, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 198.1 / 39
p0 -122.9 ± 0.7517
p1 5.161 ± 0.02906
p2 1.106e+04 ± 1.933
p3 30.69 ± 0.009948
12000
10000
8000
6000
4000
2000
0
50
100
150
200
250

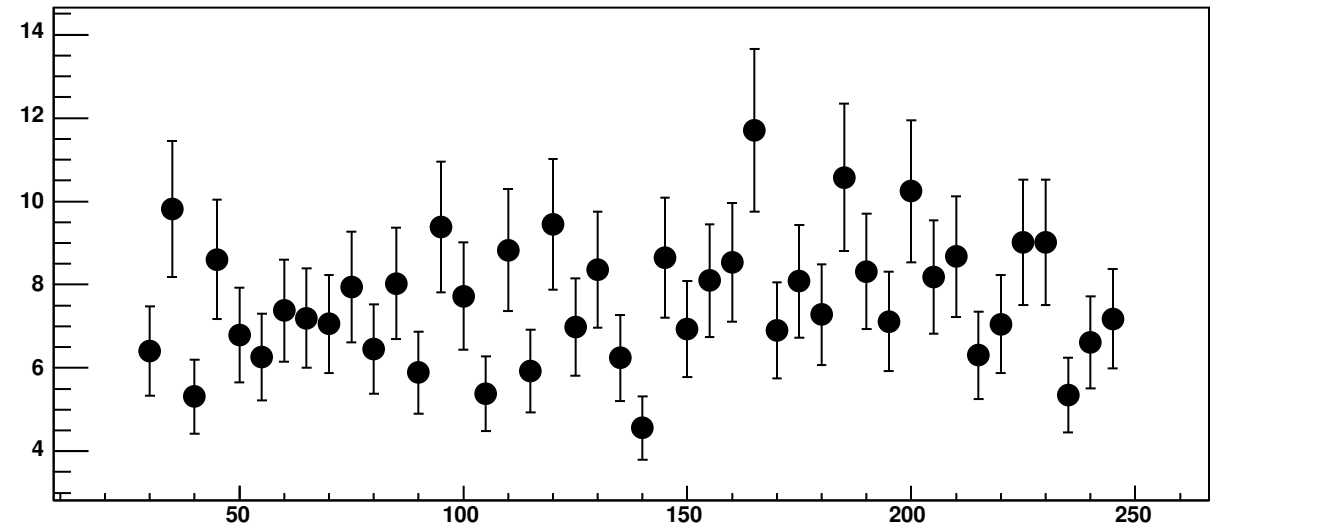

Chip 0, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold
45
40
35
30
25
20
15
10
5
50
100
150
200
250


Chip 0, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
-15
50
100
150
200
250

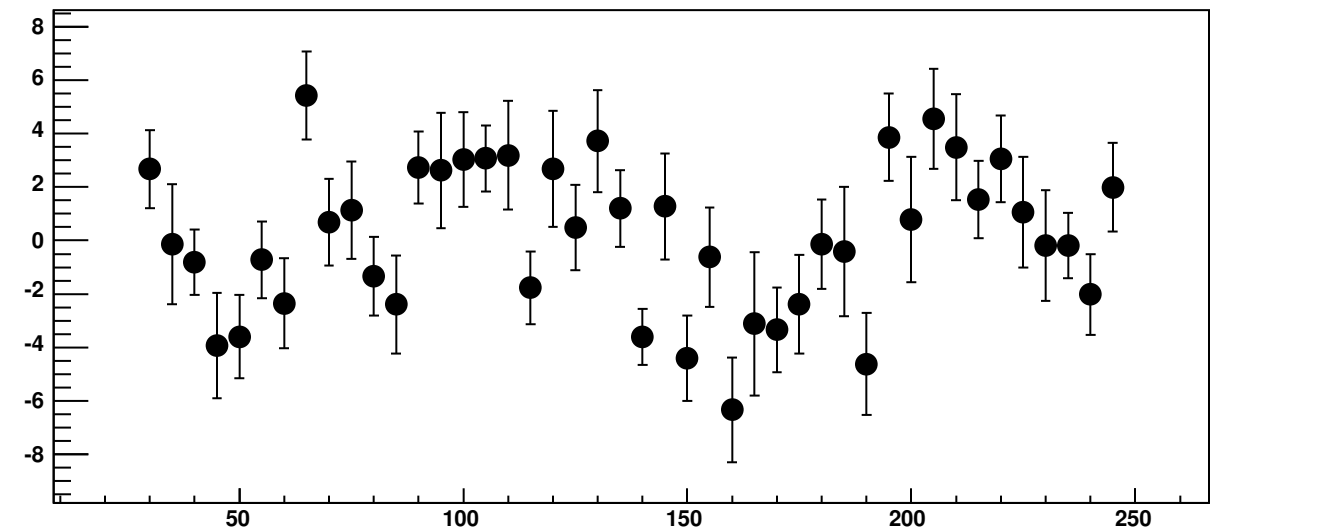
Chip 0, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

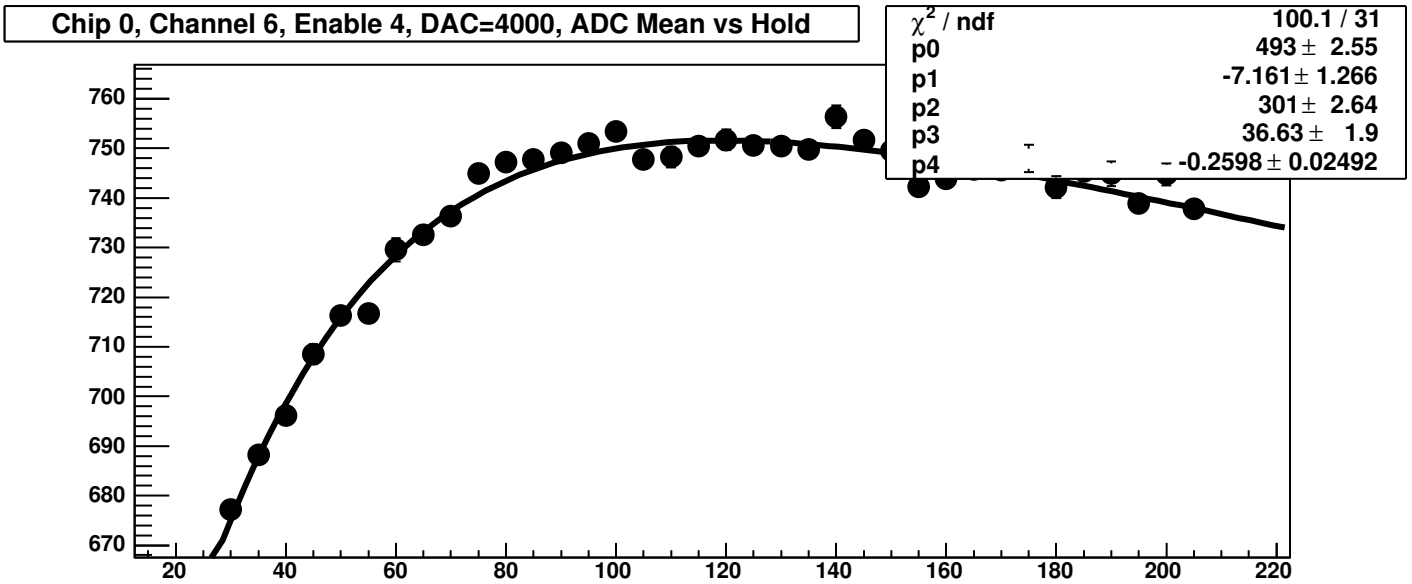


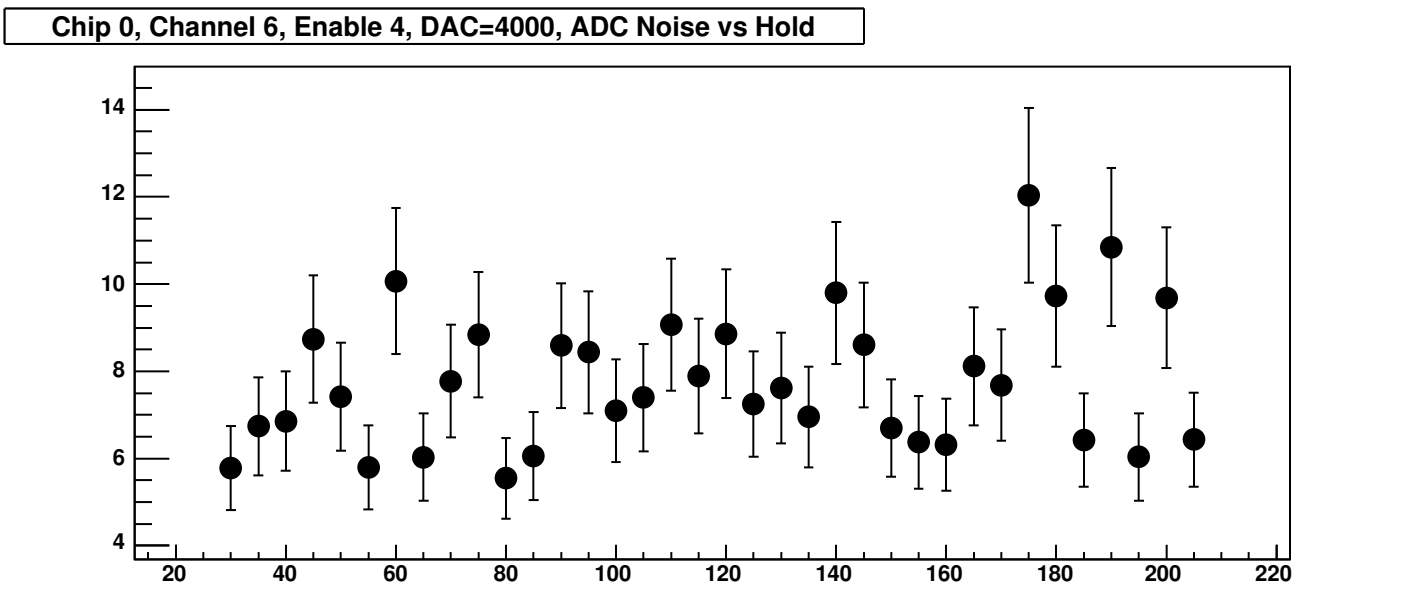
Chip 0, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold

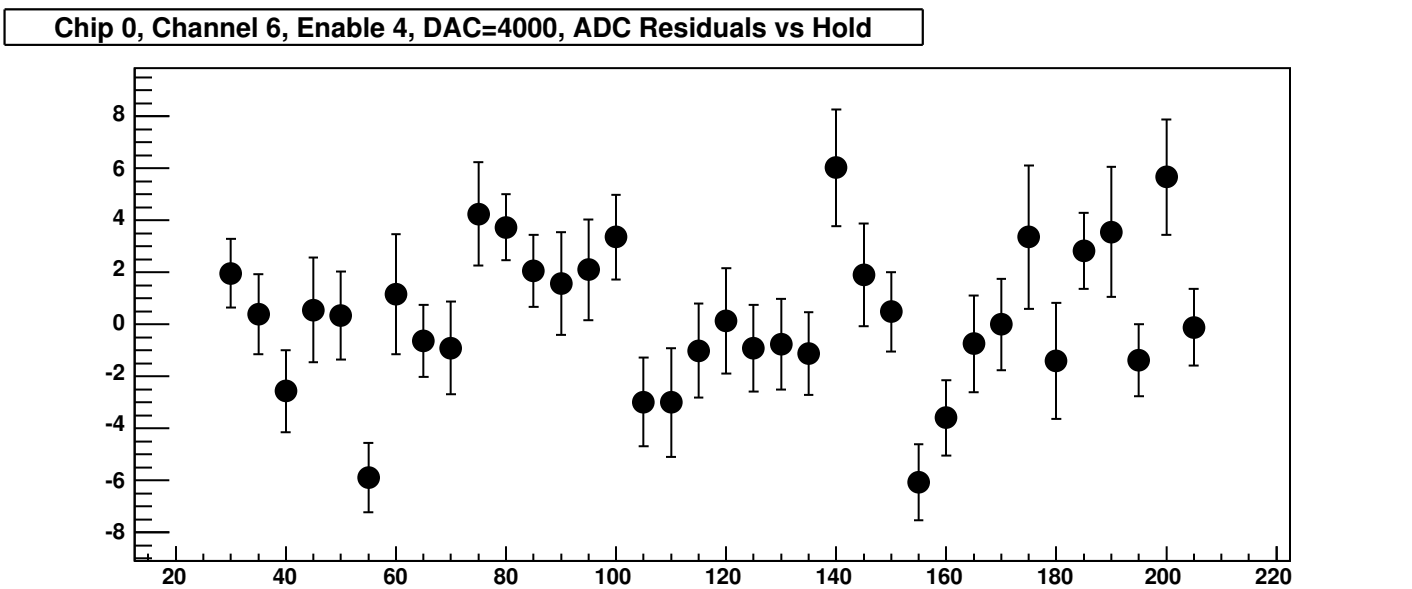


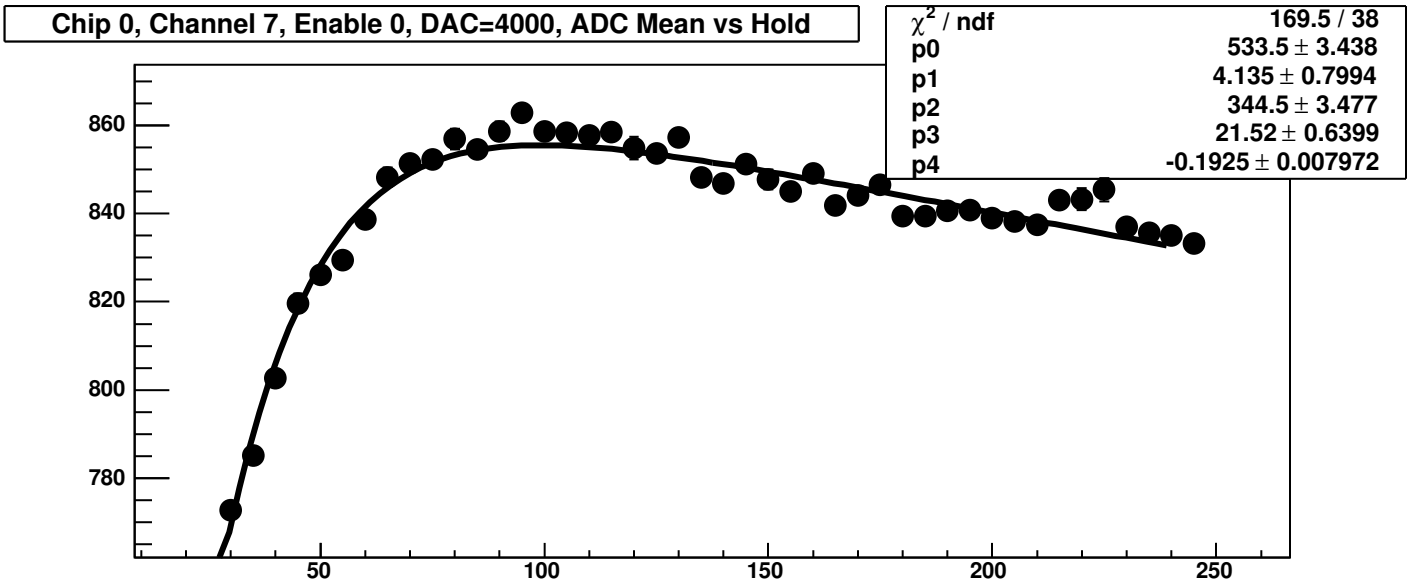
Chip 0, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

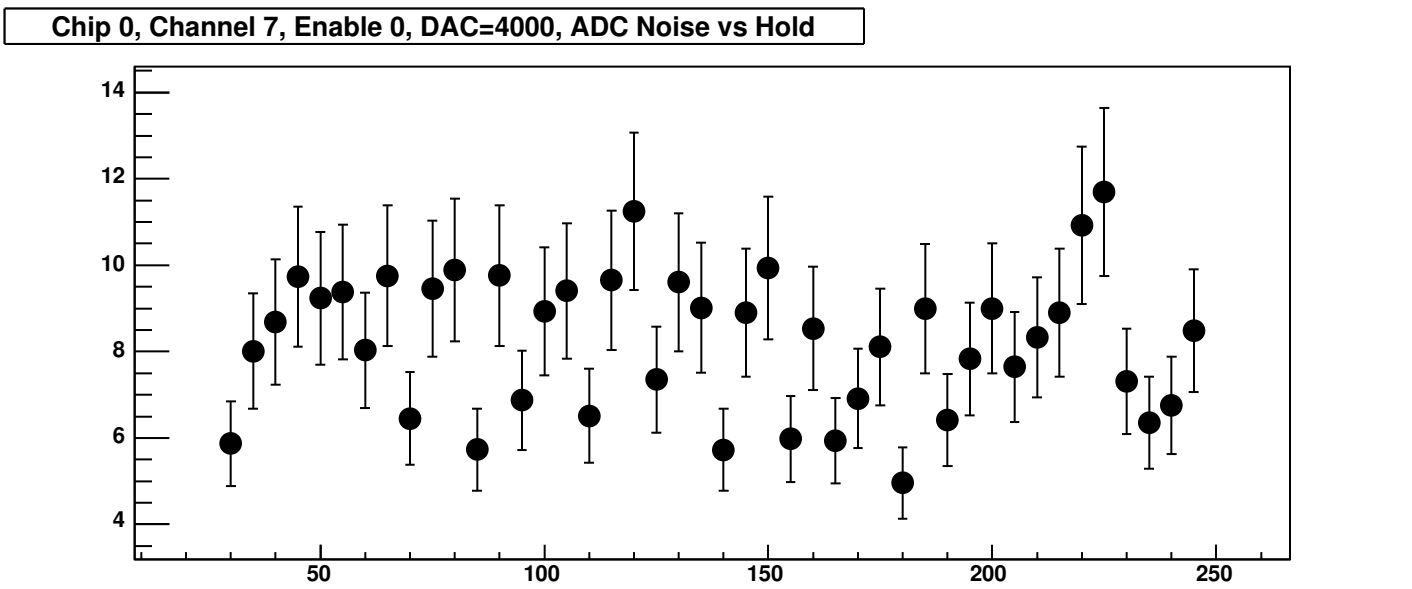


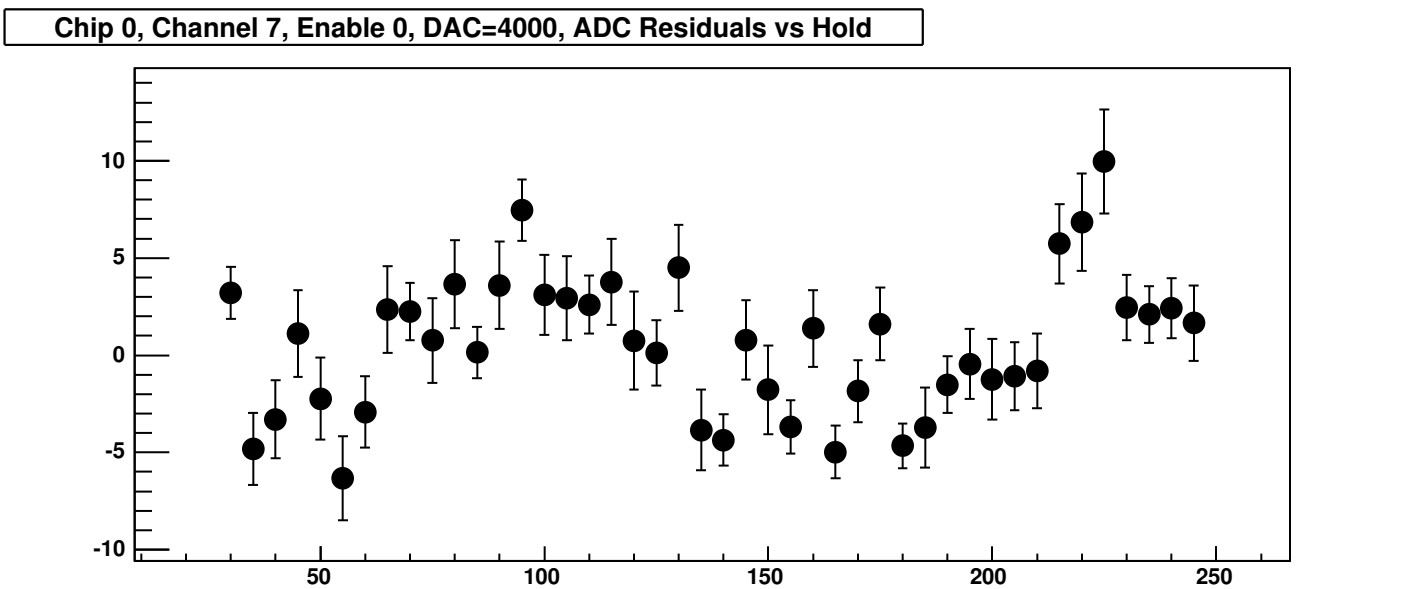

Chip 0, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 100.1 / 31
p0 493 ± 2.55
p1 -7.161 ± 1.266
p2 301 ± 2.64
p3 36.63 ± 1.9
p4 -0.2598 ± 0.02492
760
750
740
730
720
710
700
690
680
670
20
40
60
80
100
120
140
160
180
200
220

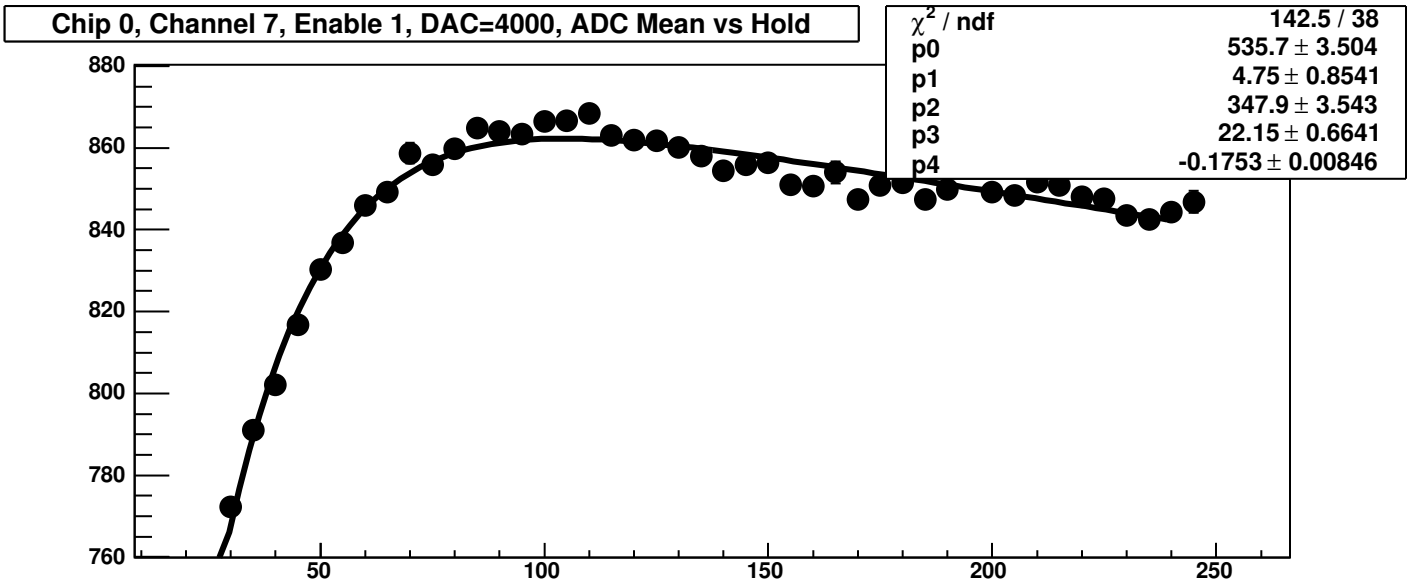

Chip 0, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
20
40
60
80
100
120
140
160
180
200
220

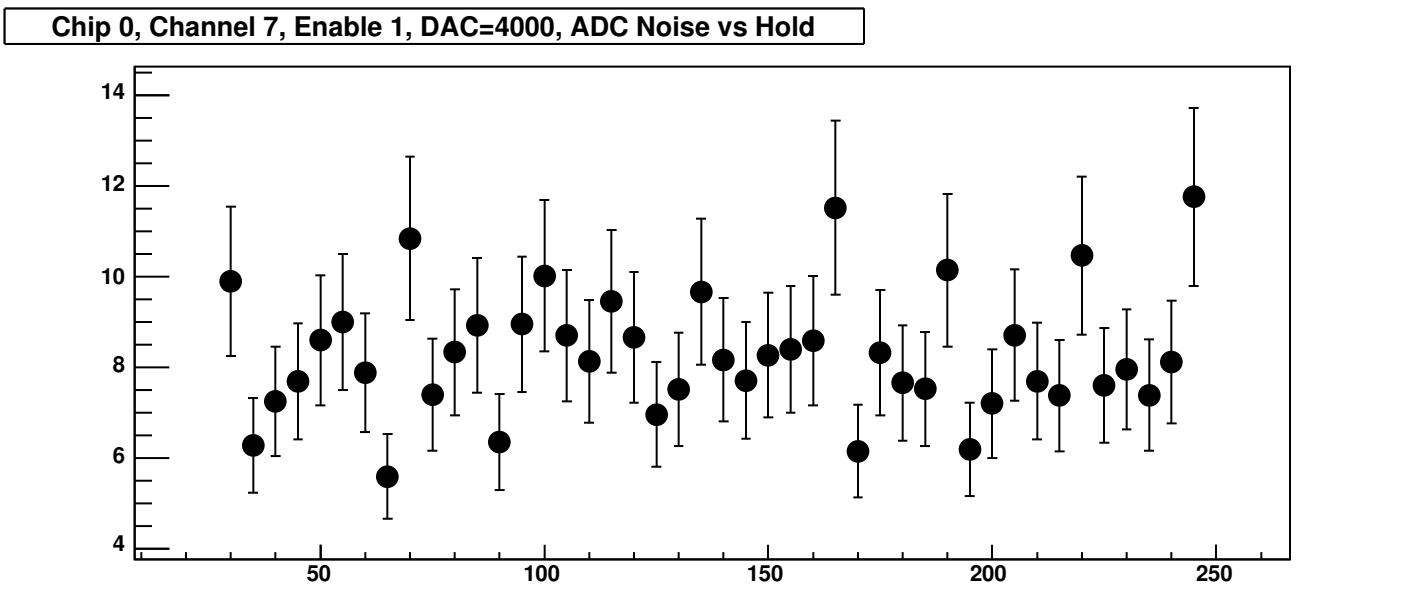

Chip 0, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
20
40
60
80
100
120
140
160
180
200
220

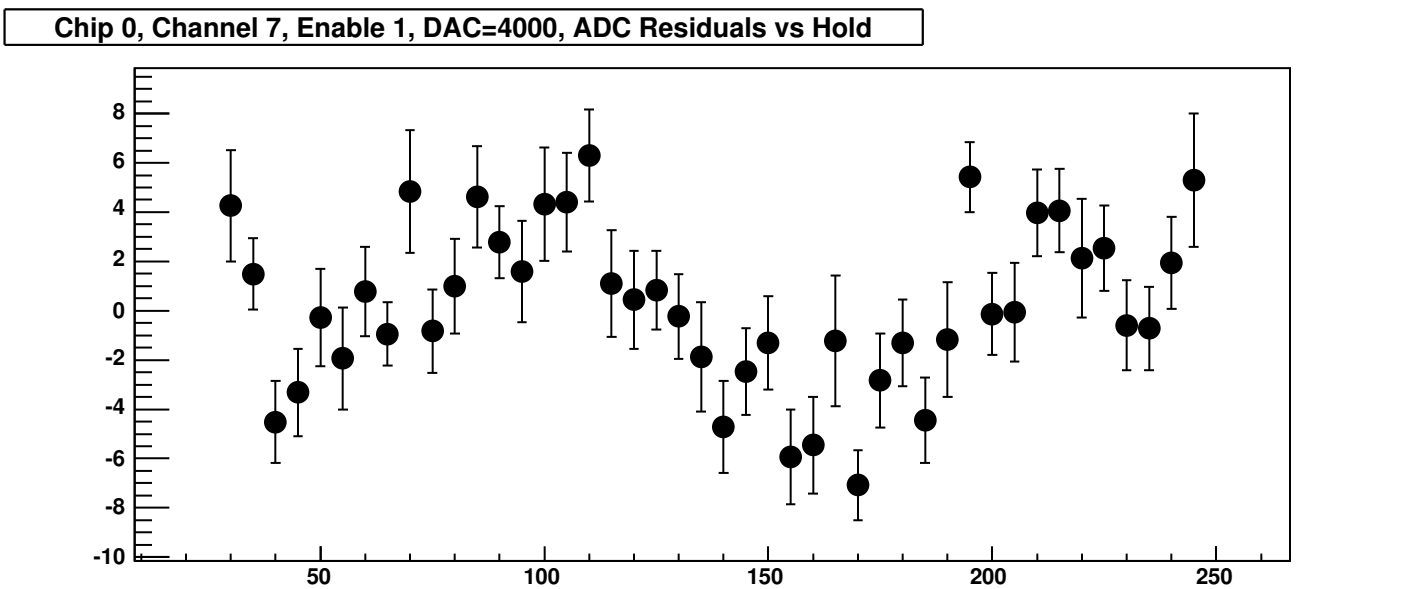

Chip 0, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 169.5 / 38
p0 533.5 ± 3.438
p1 4.135 ± 0.7994
p2 344.5 ± 3.477
p3 21.52 ± 0.6399
p4 -0.1925 ± 0.007972
860
840
820
800
780
50
100
150
200
250

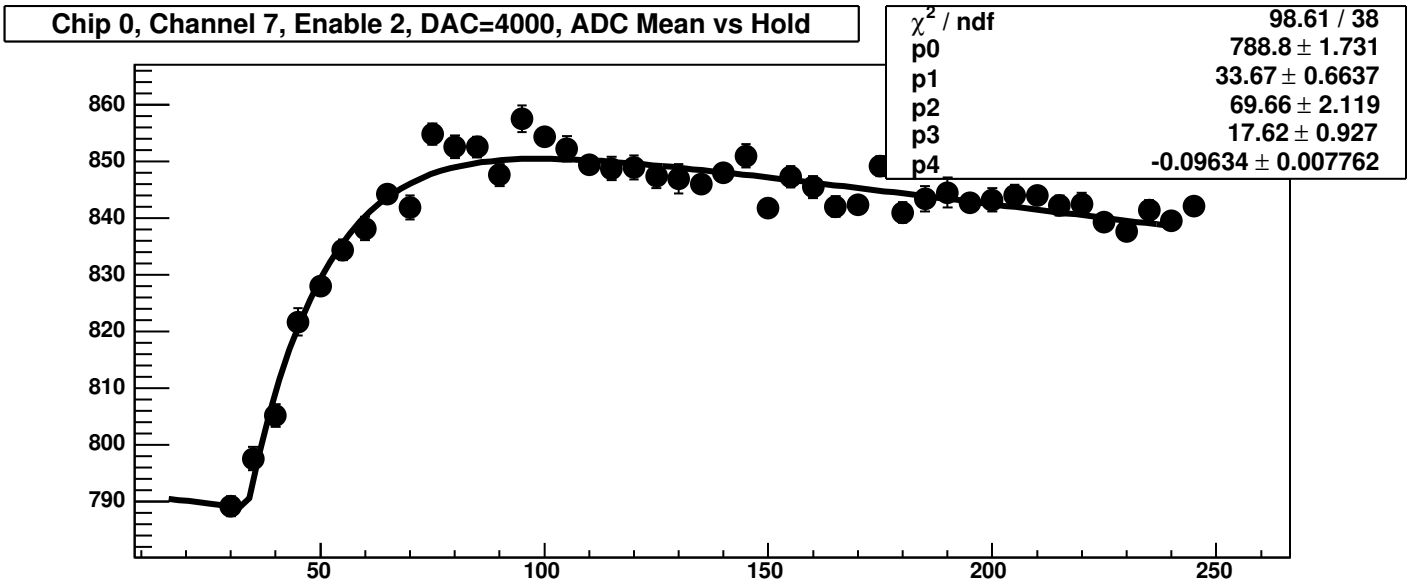

Chip 0, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250

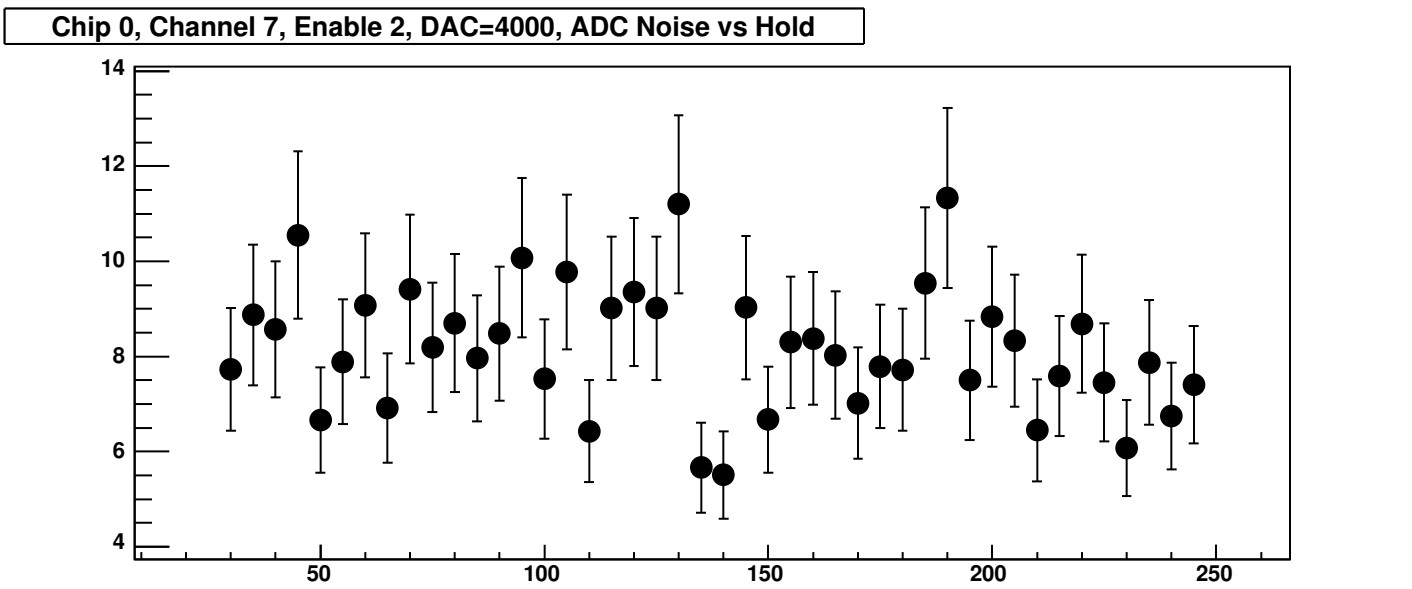

Chip 0, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

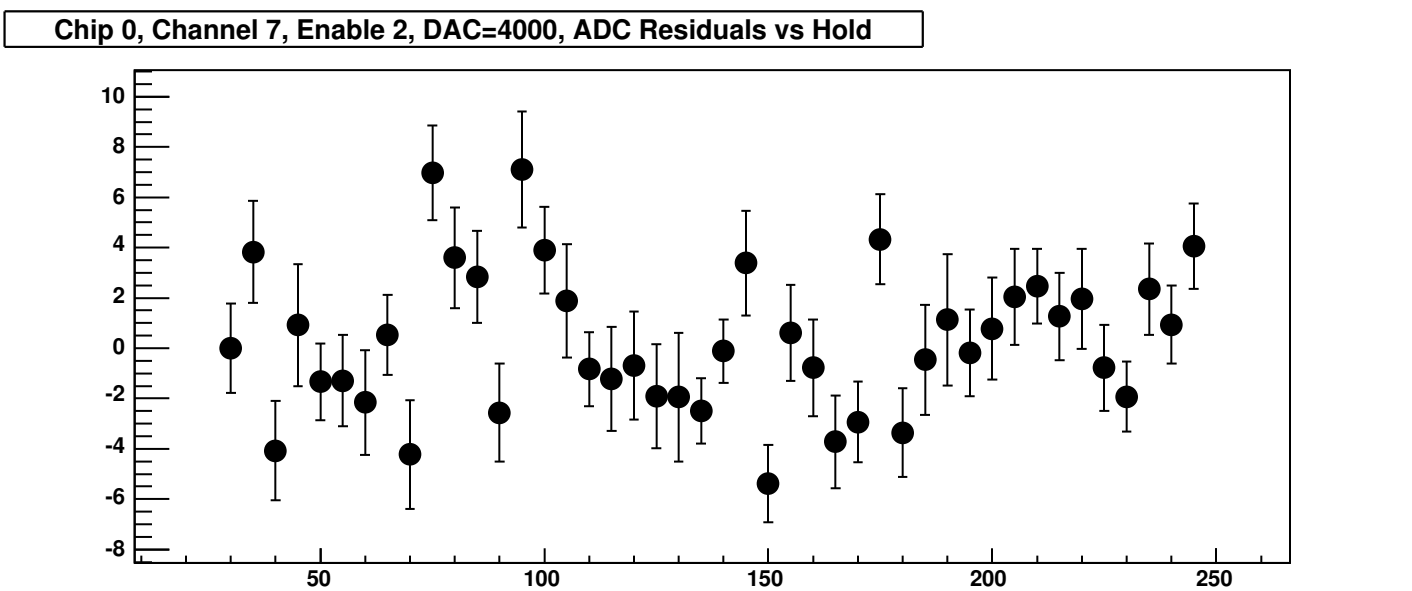

Chip 0, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 142.5 / 38
p0 535.7 ± 3.504
p1 4.75 ± 0.8541
p2 347.9 ± 3.543
p3 22.15 ± 0.6641
p4 -0.1753 ± 0.00846
880
860
840
820
800
780
760
50
100
150
200
250


Chip 0, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250

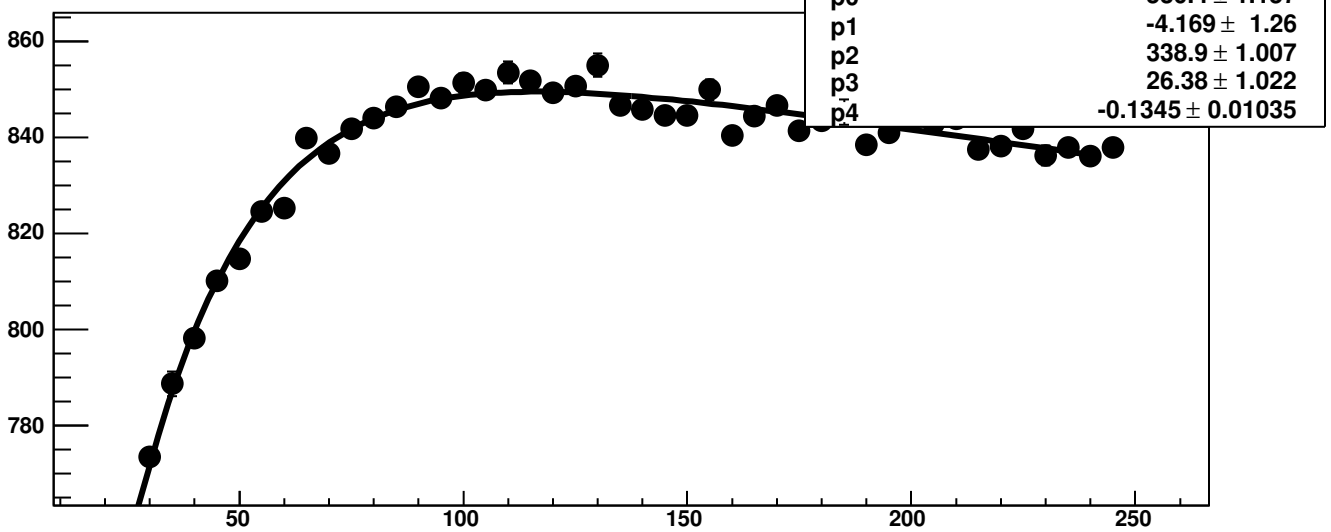

Chip 0, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
50
100
150
200
250


Chip 0, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 98.61 / 38
p0 788.8 ± 1.731
p1 33.67 ± 0.6637
p2 69.66 ± 2.119
p3 17.62 ± 0.927
p4 -0.09634 ± 0.007762
860
850
840
830
820
810
800
790
50
100
150
200
250

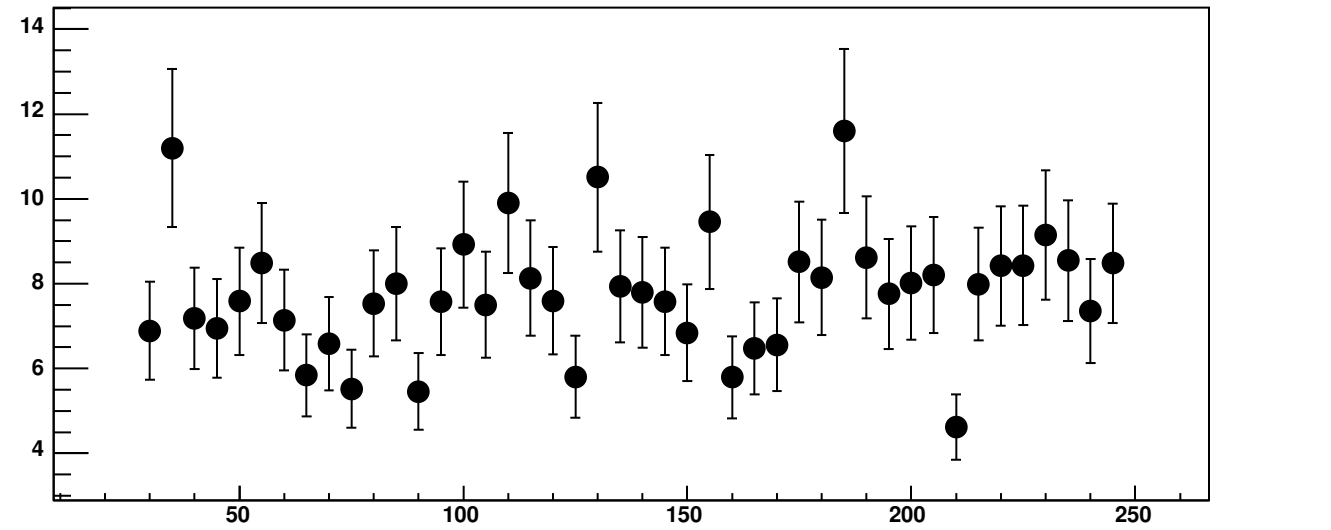

Chip 0, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250


Chip 0, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

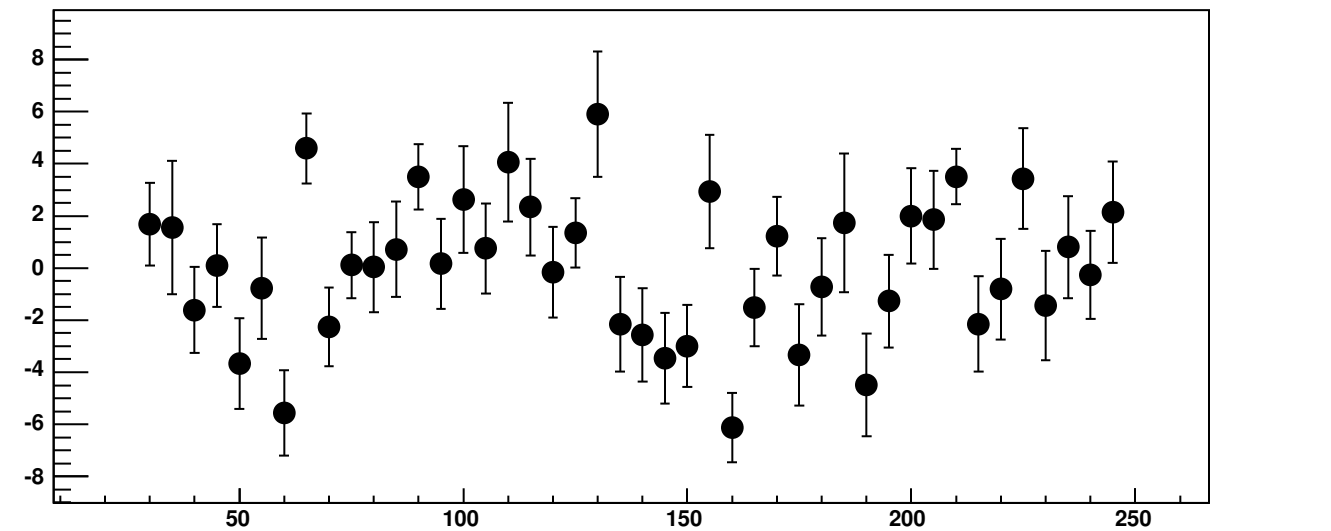
Chip 0, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

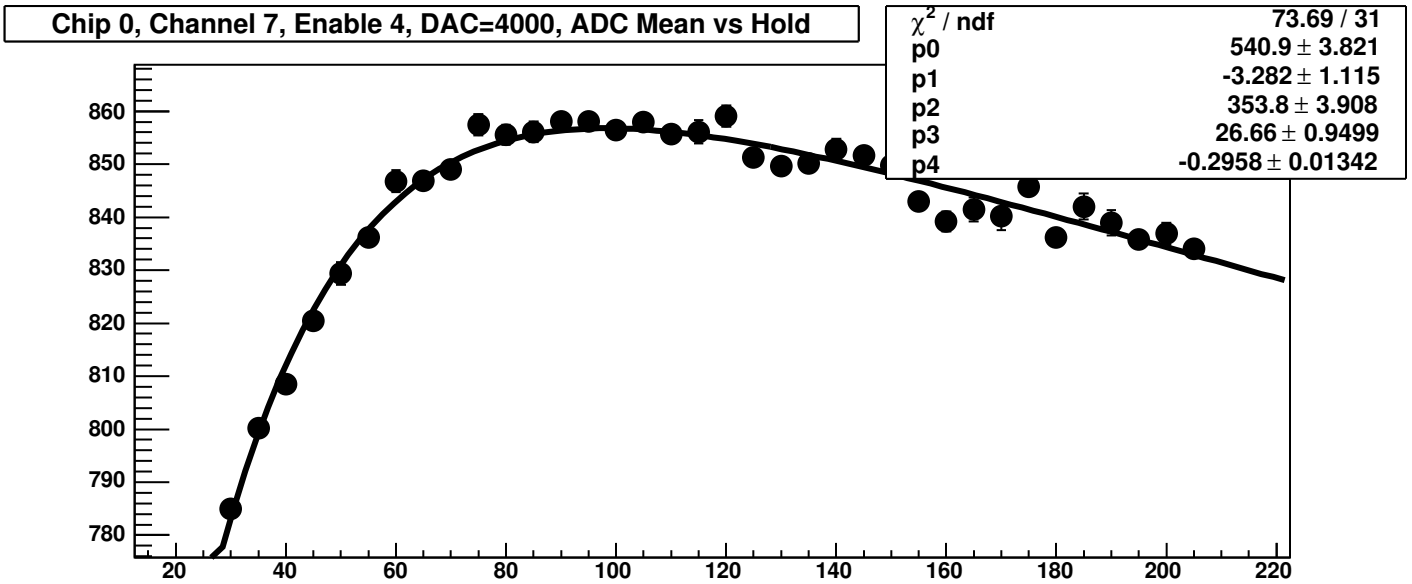


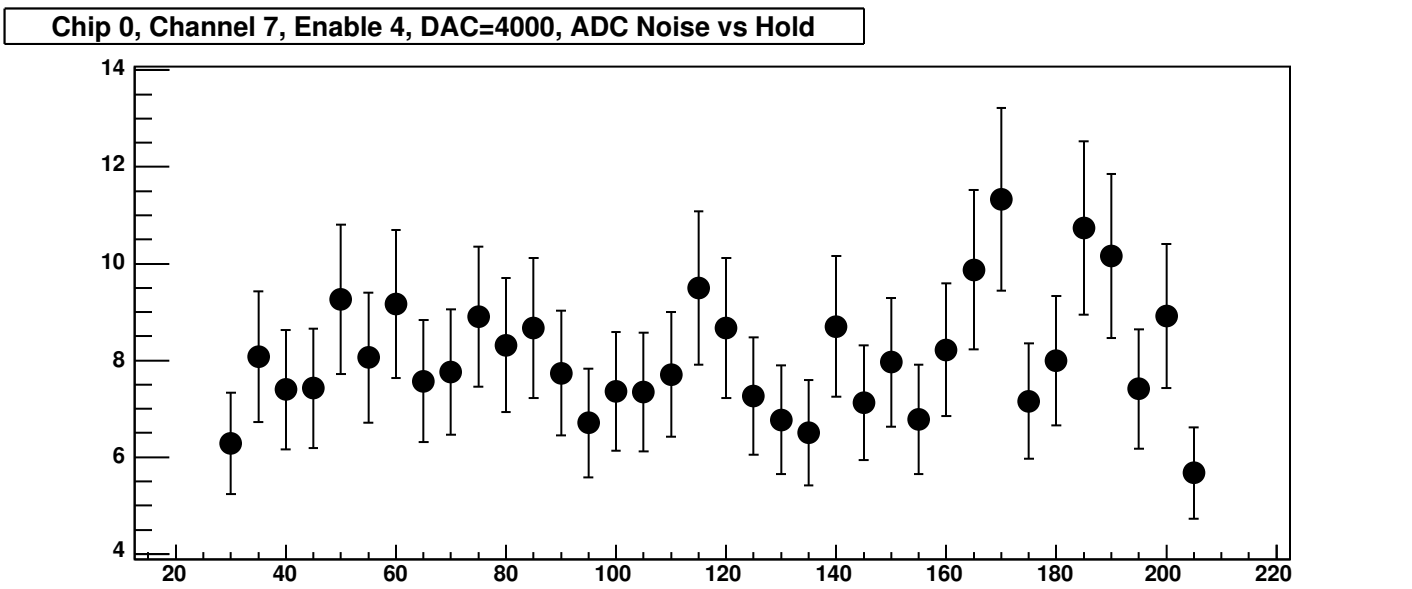
Chip 0, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold

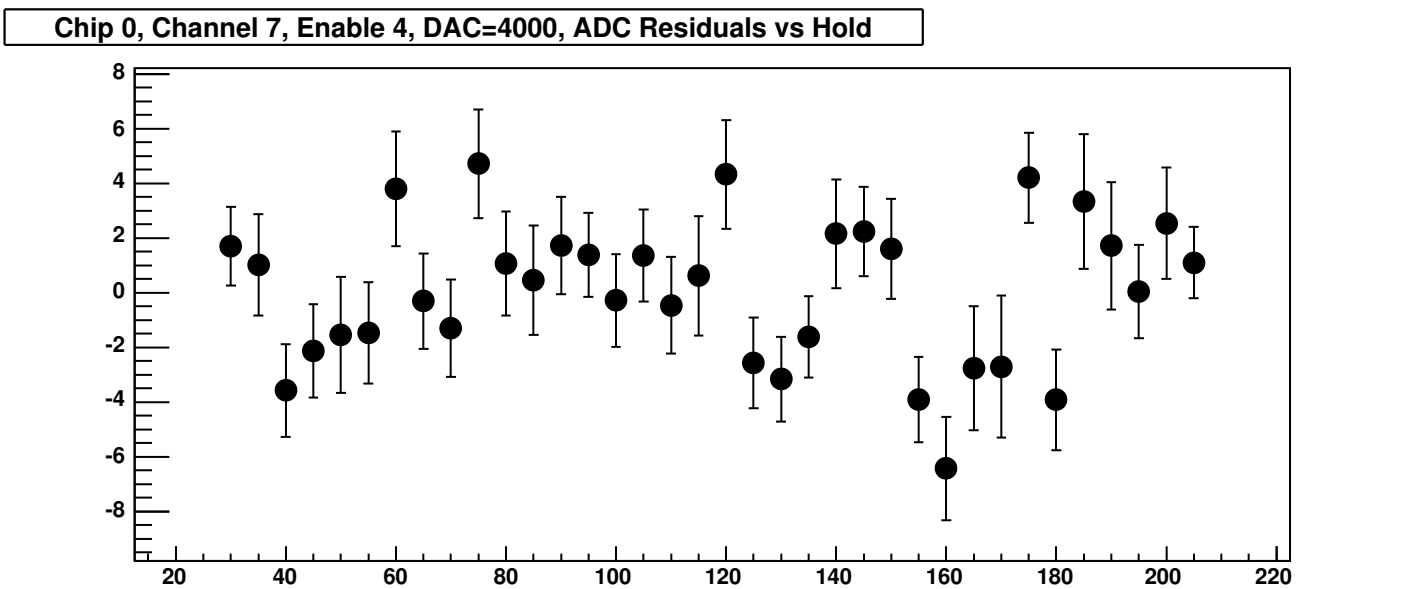


Chip 0, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

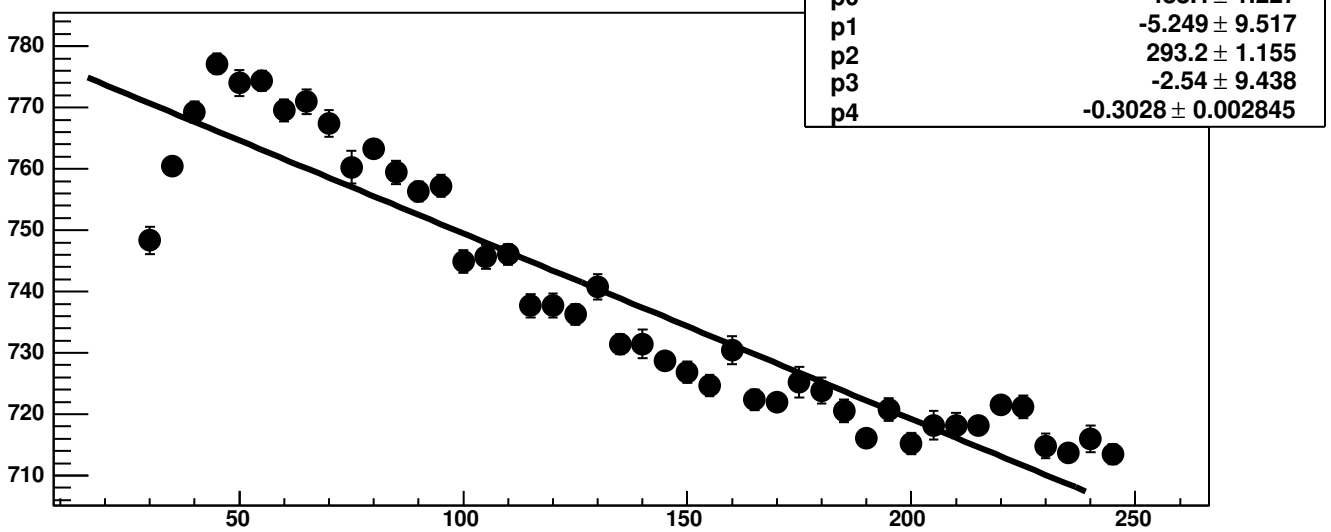



Chip 0, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 73.69 / 31
p0 540.9 ± 3.821
p1 -3.282 ± 1.115
p2 353.8 ± 3.908
p3 26.66 ± 0.9499
p4 -0.2958 ± 0.01342
860
850
840
830
820
810
800
790
780
20
40
60
80
100
120
140
160
180
200
220

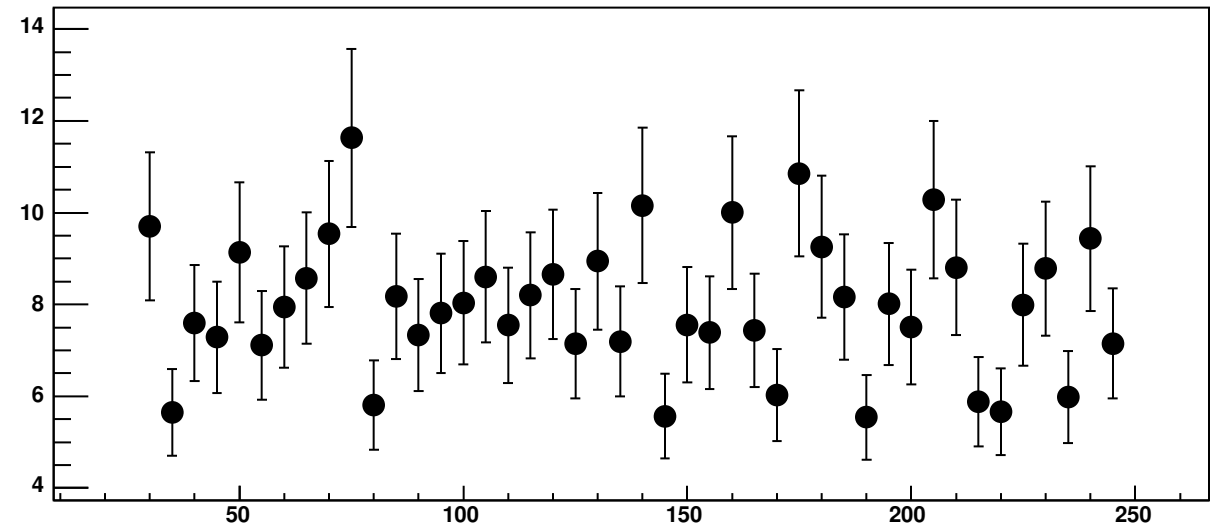

Chip 0, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
20
40
60
80
100
120
140
160
180
200
220


Chip 0, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
20
40
60
80
100
120
140
160
180
200
220

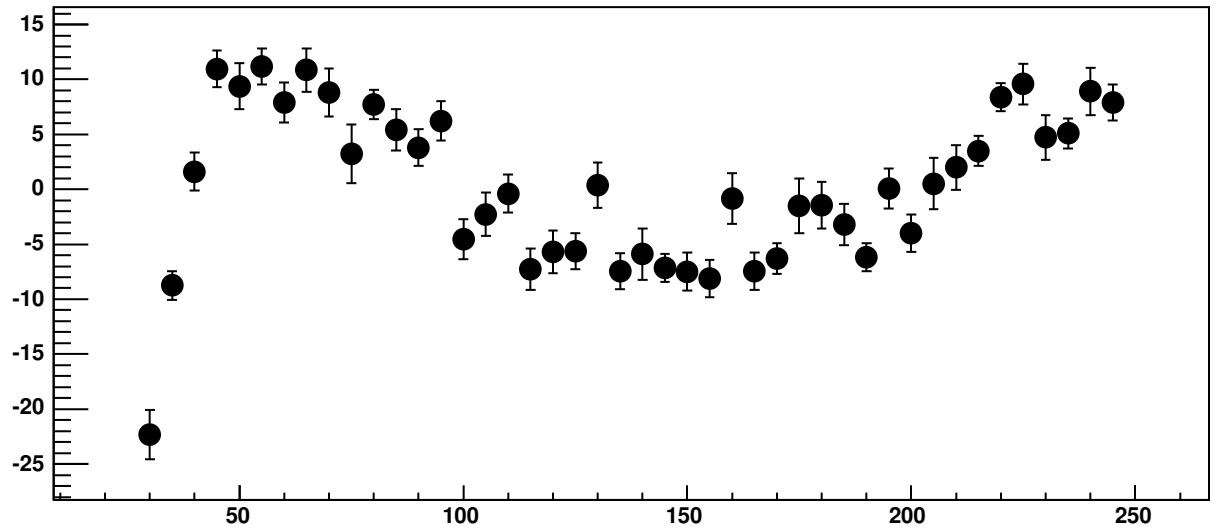
Chip 0, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold



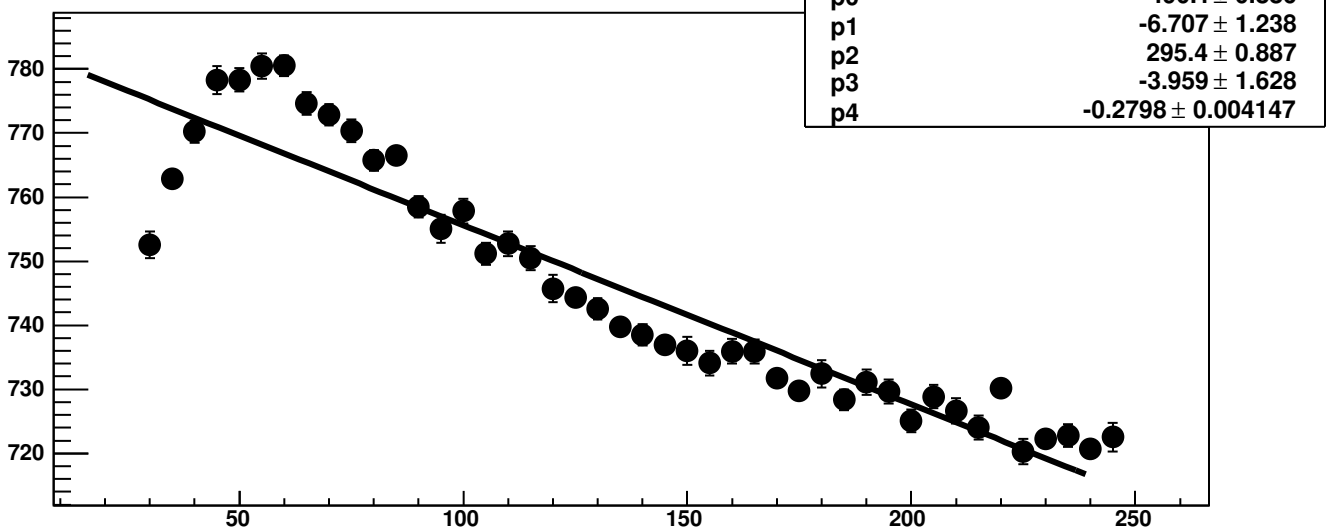
Chip 0, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



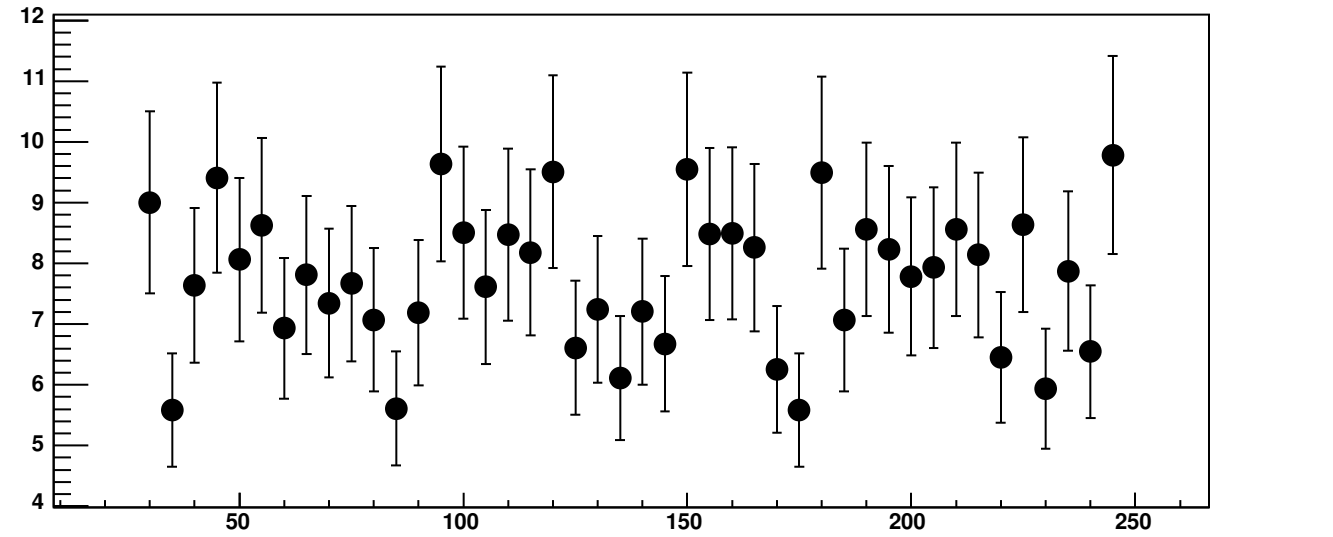
Chip 0, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold



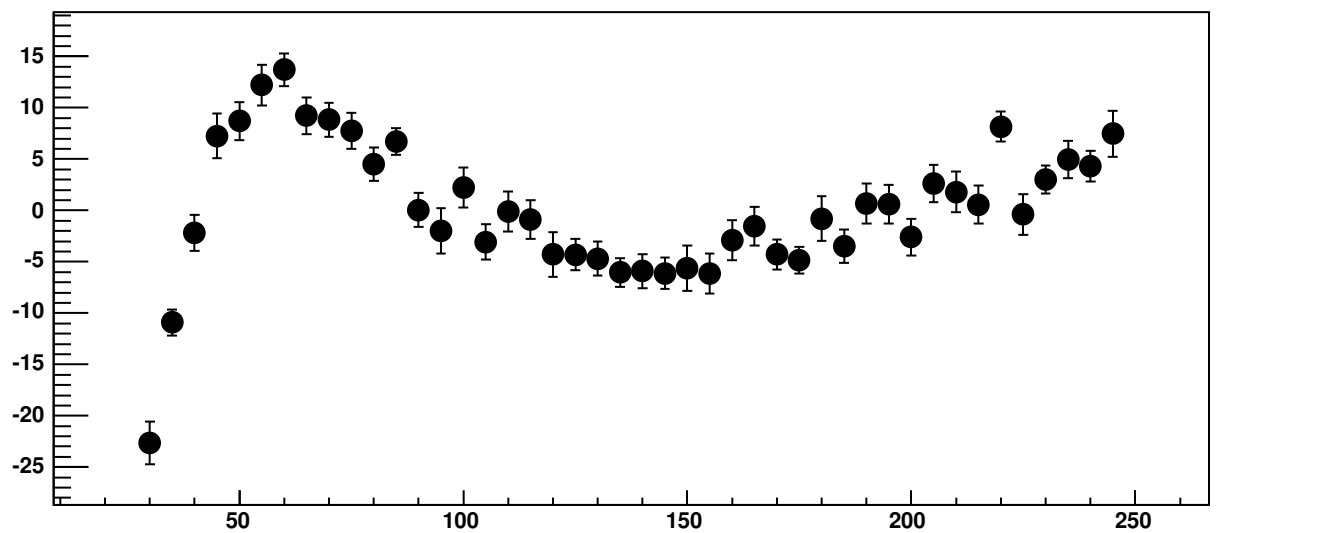
Chip 0, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

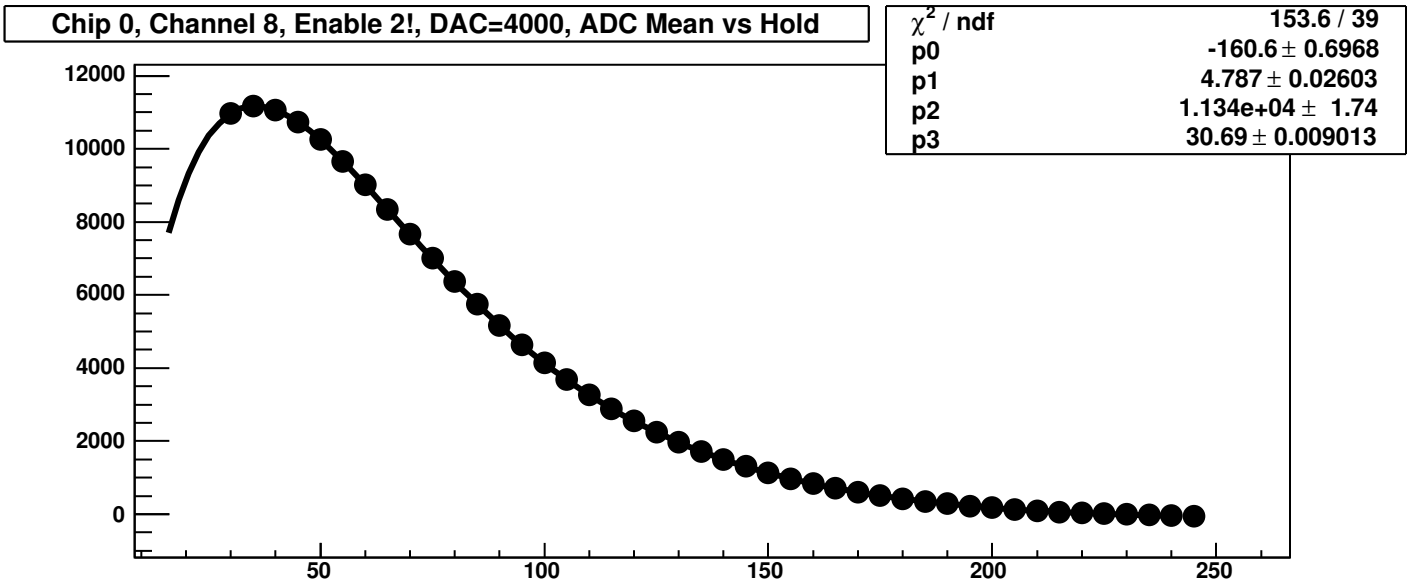


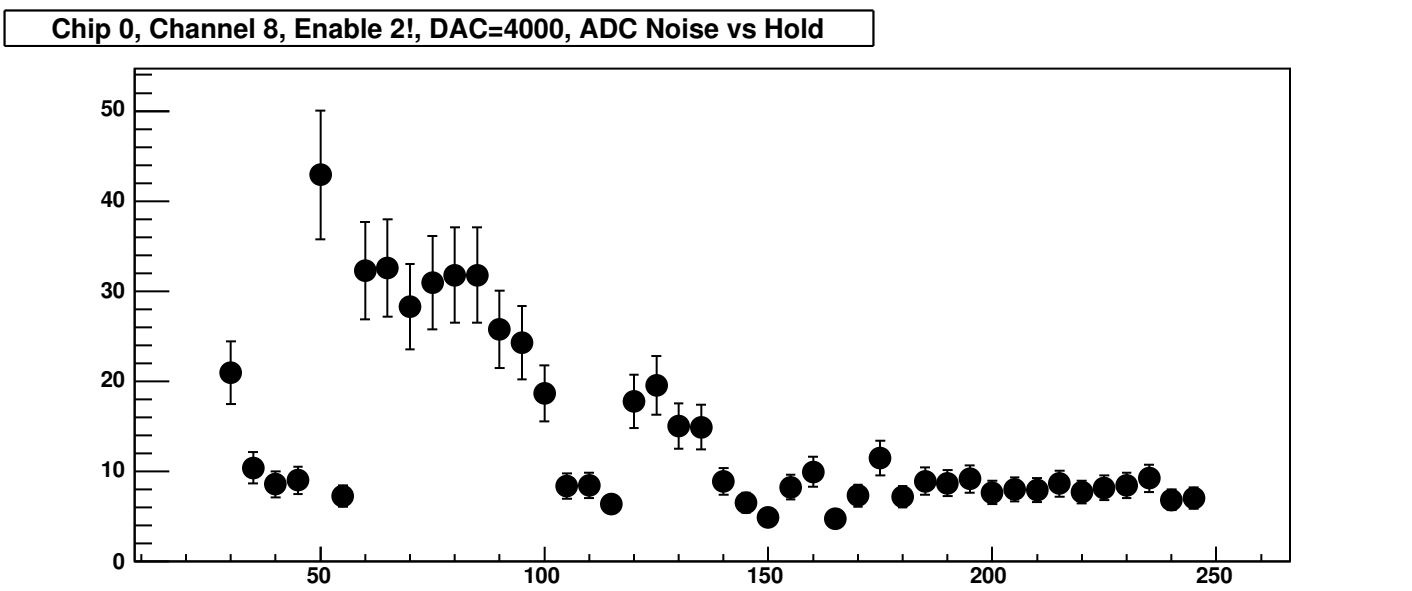
Chip 0, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold

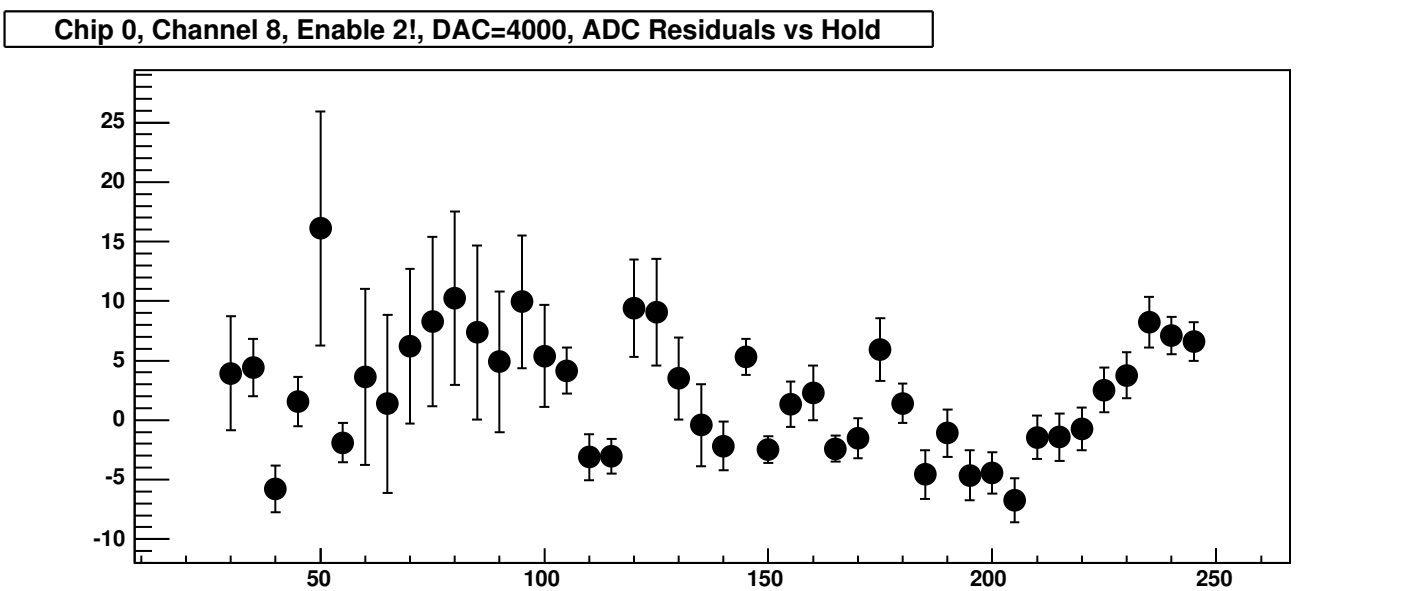


Chip 0, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

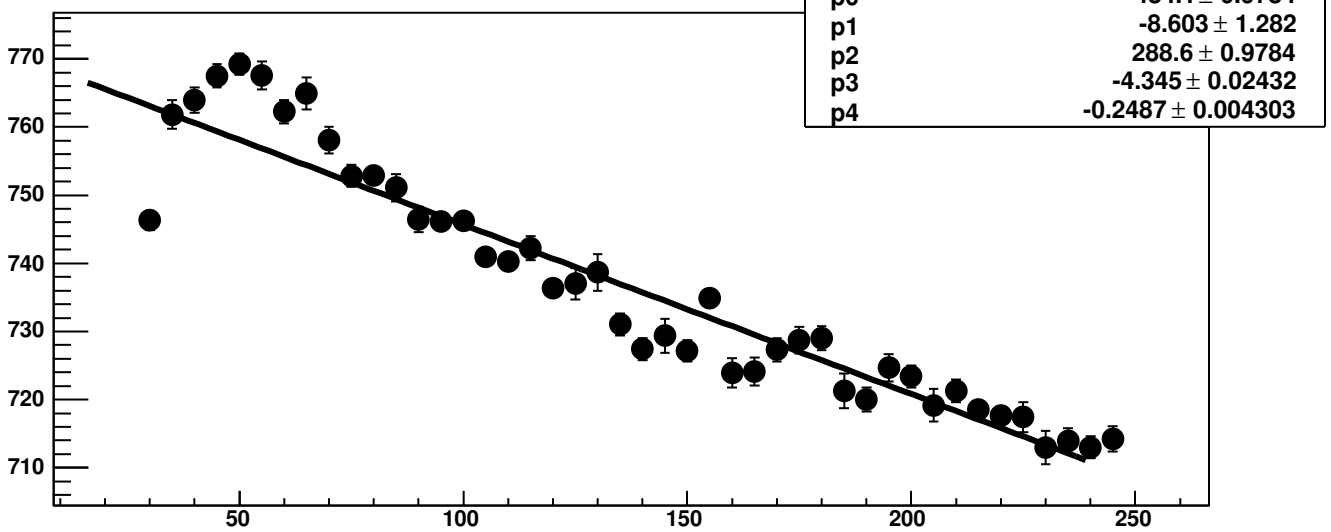



Chip 0, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 153.6 / 39
p0 -160.6 ± 0.6968
p1 4.787 ± 0.02603
p2 1.134e+04 ± 1.74
p3 30.69 ± 0.009013
12000
10000
8000
6000
4000
2000
0
50
100
150
200
250

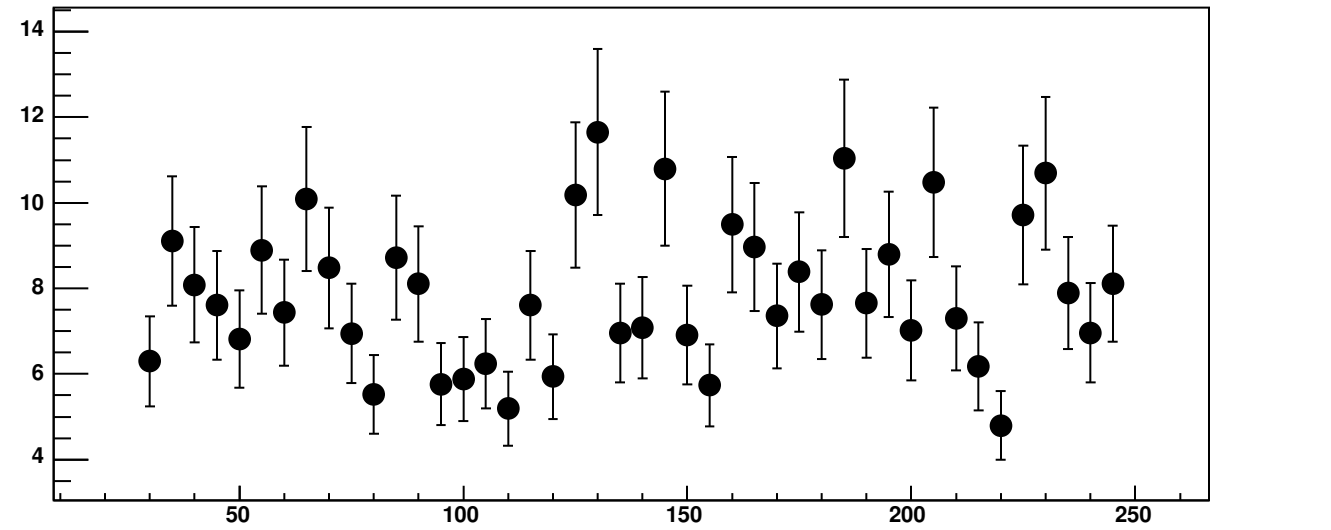

Chip 0, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
0
50
100
150
200
250


Chip 0, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
50
100
150
200
250

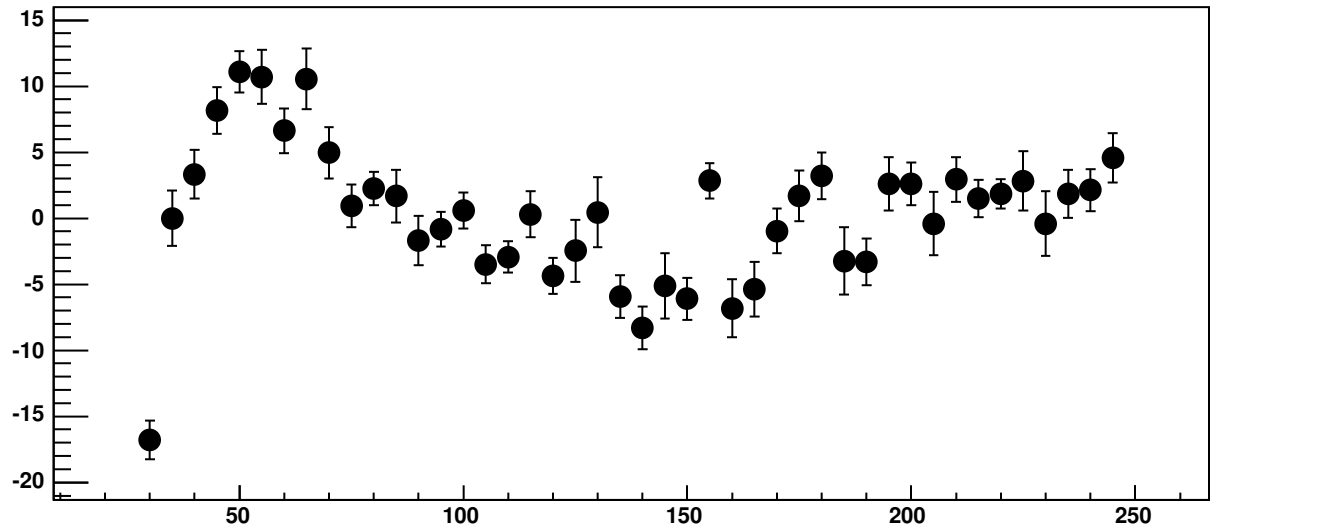
Chip 0, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold

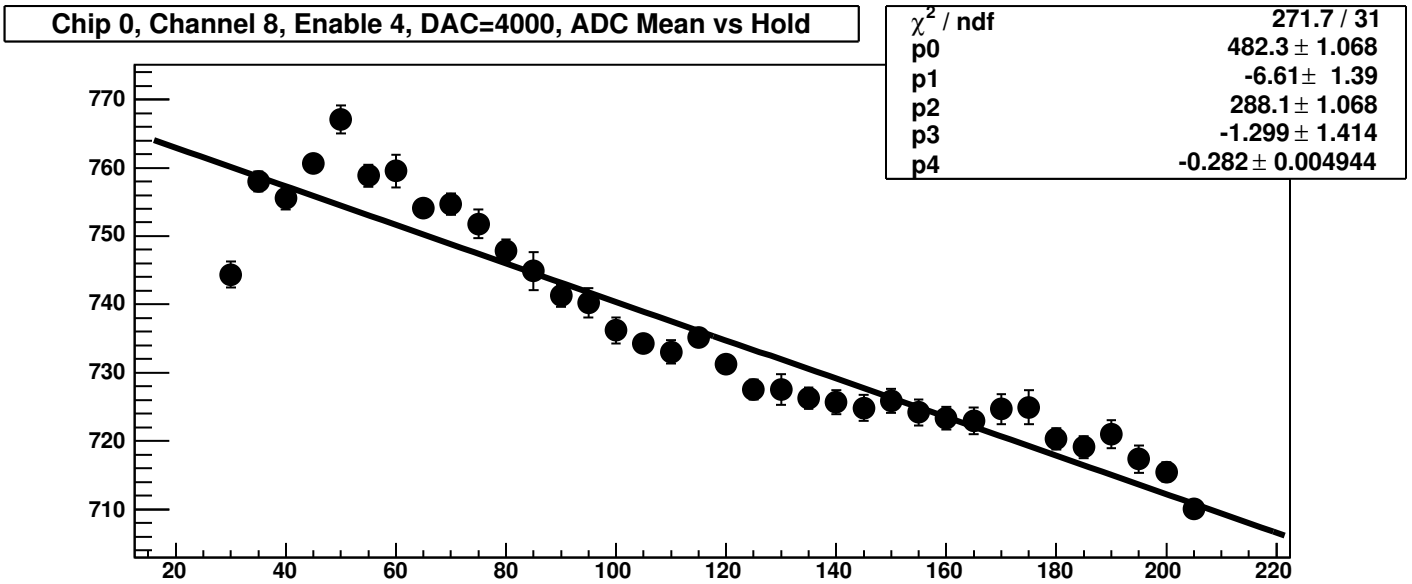


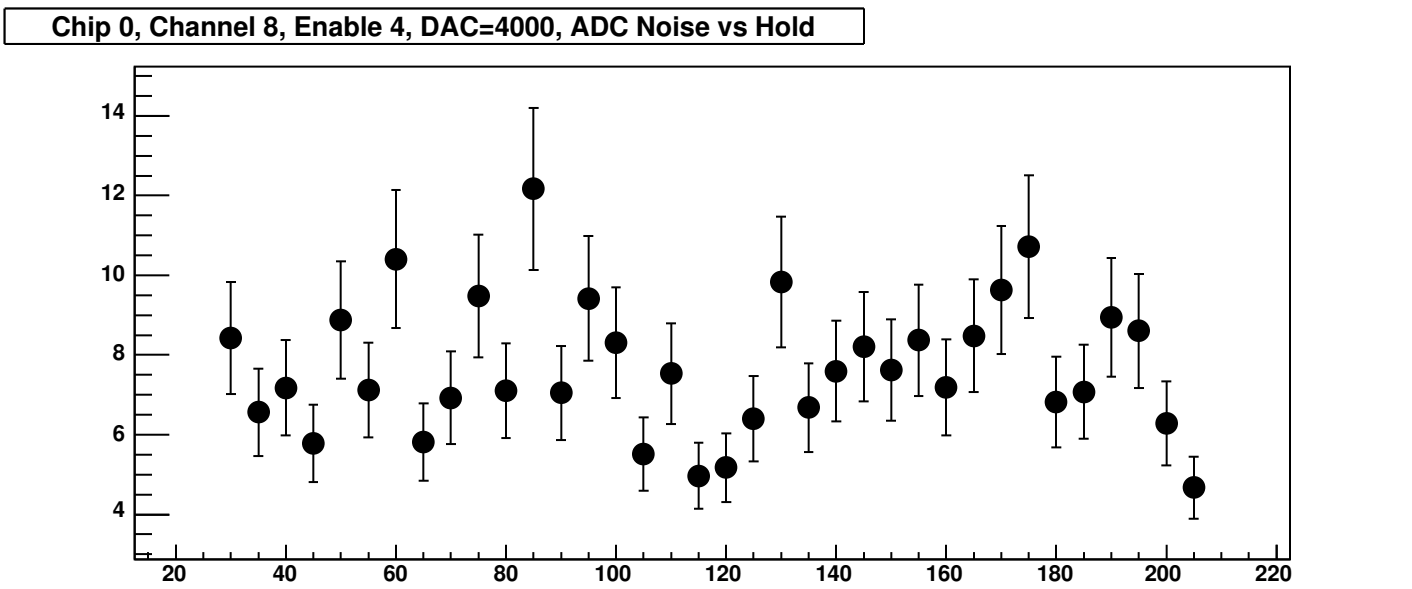
Chip 0, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold

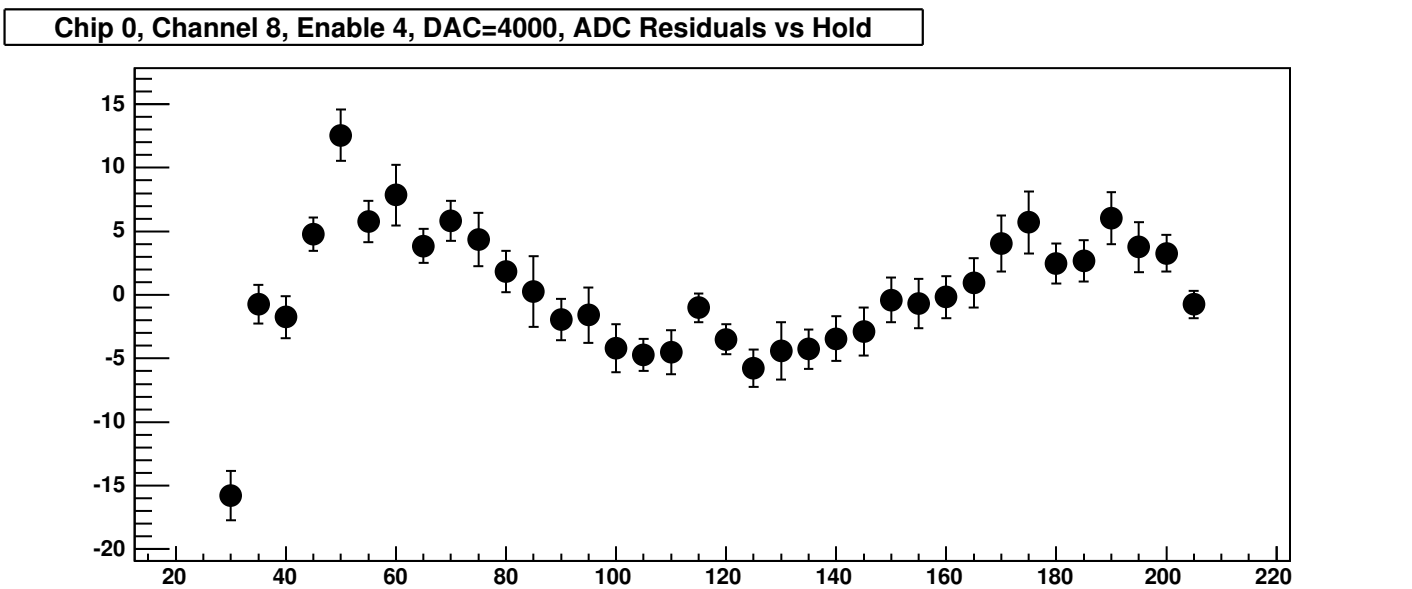


Chip 0, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold

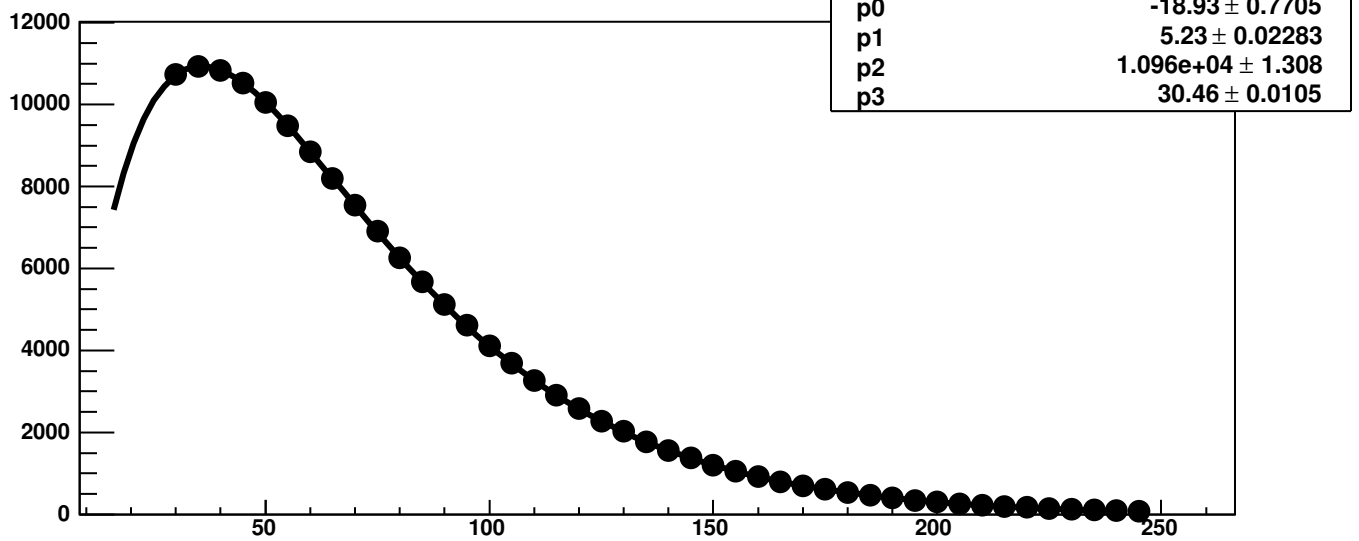



Chip 0, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 271.7 / 31
p0 482.3 ± 1.068
p1 -6.61 ± 1.39
p2 288.1 ± 1.068
p3 -1.299 ± 1.414
p4 -0.282 ± 0.004944
770
760
750
740
730
720
710
20
40
60
80
100
120
140
160
180
200
220

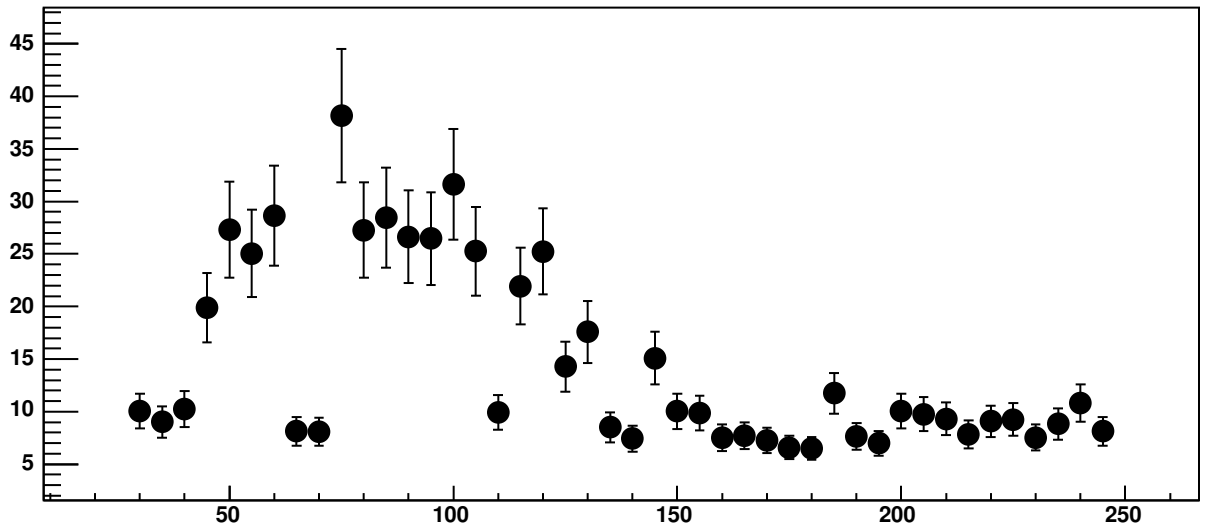

Chip 0, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
20
40
60
80
100
120
140
160
180
200
220


Chip 0, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
-15
-20
20
40
60
80
100
120
140
160
180
200
220

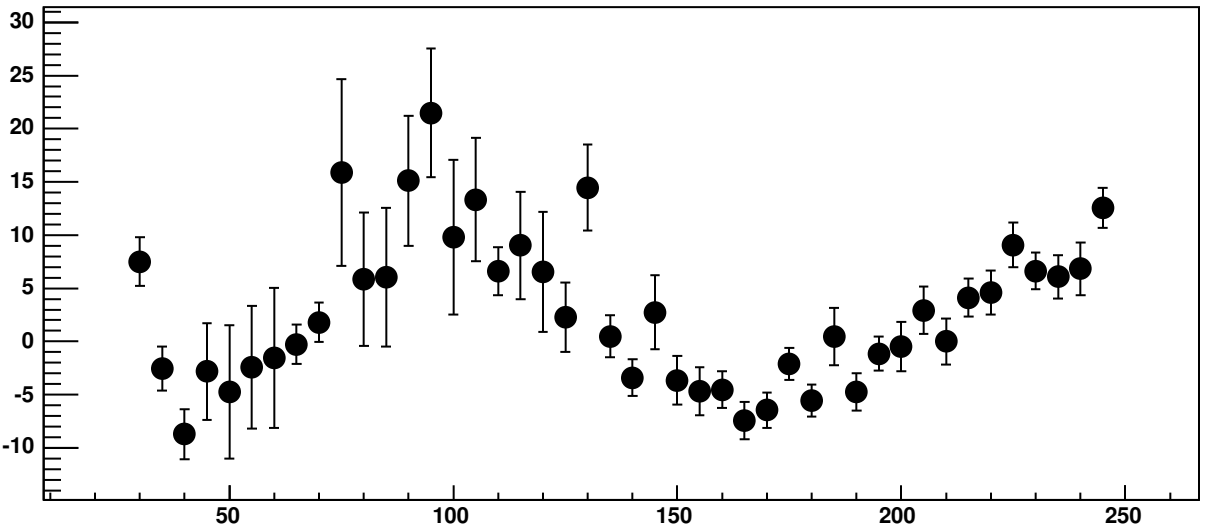
Chip 0, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold



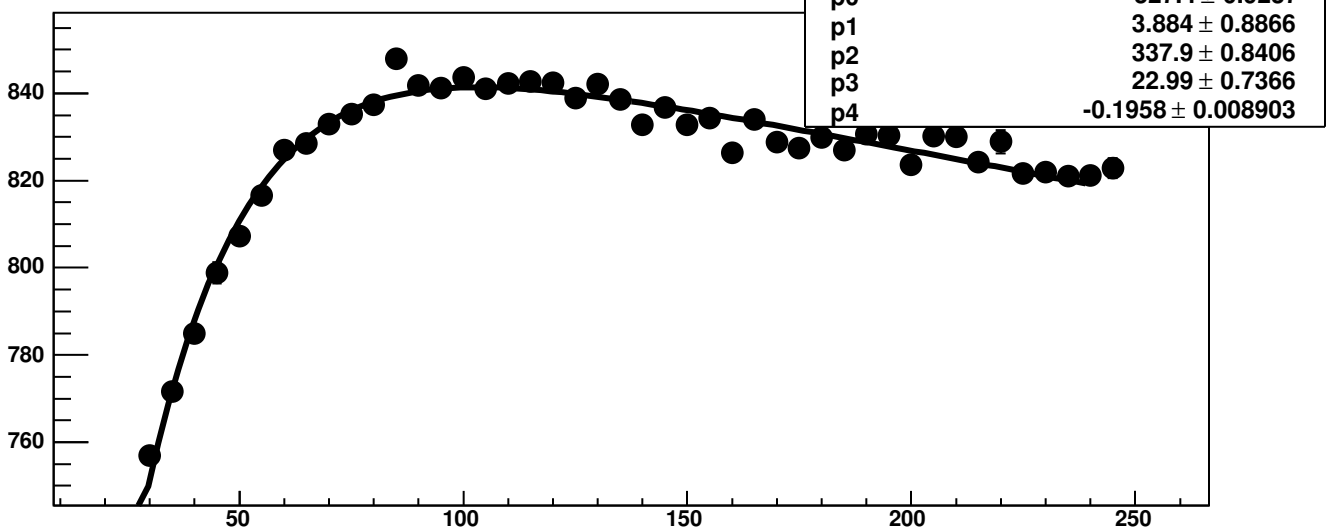
Chip 0, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold



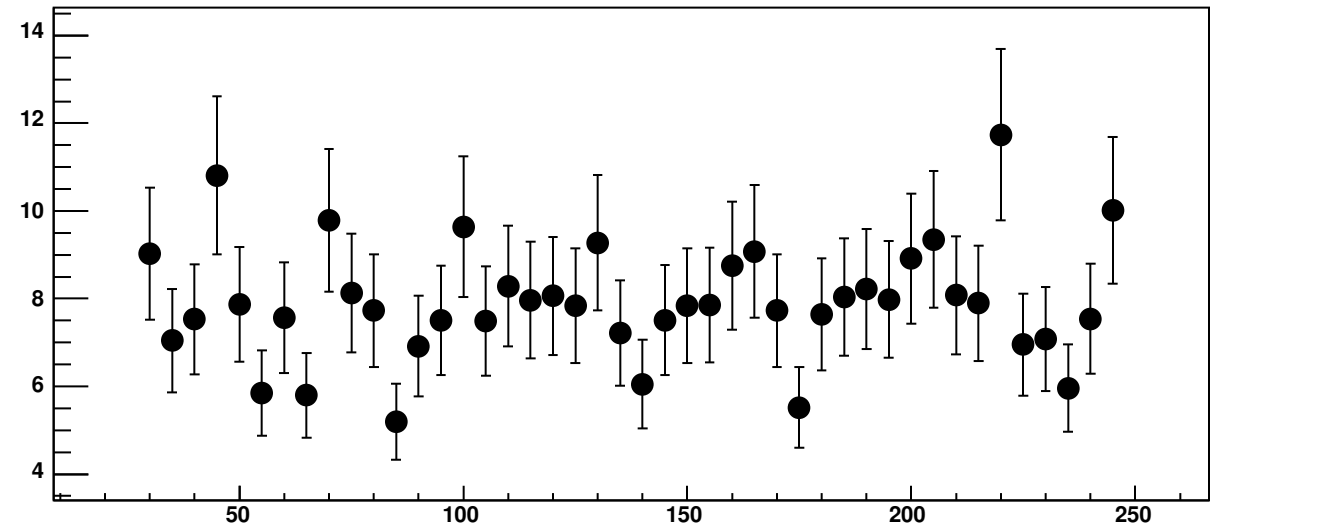
Chip 0, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold



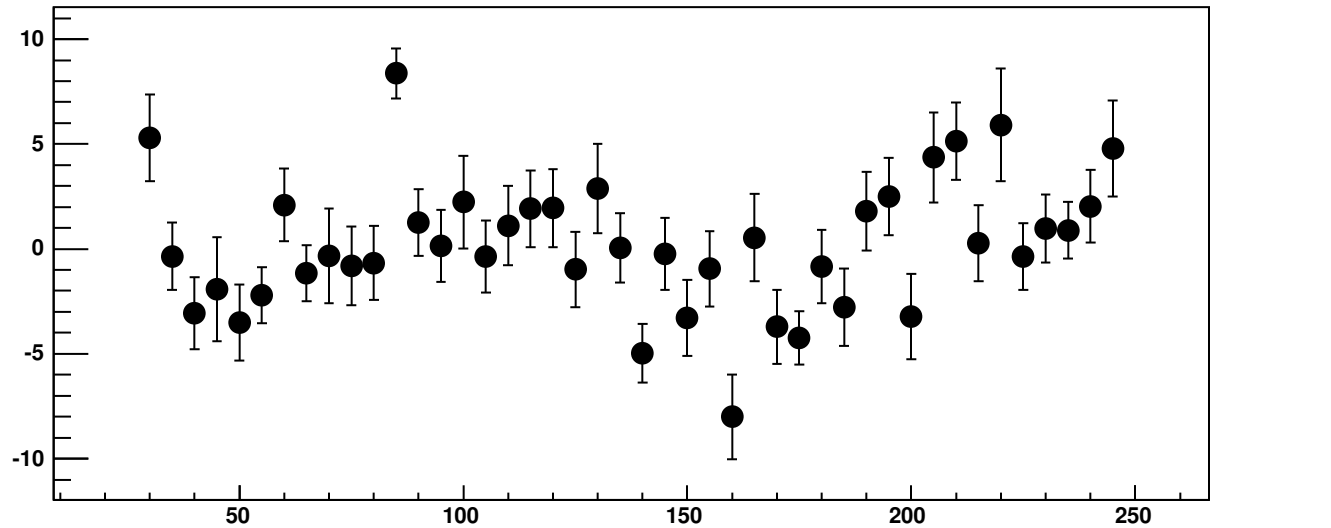
Chip 0, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

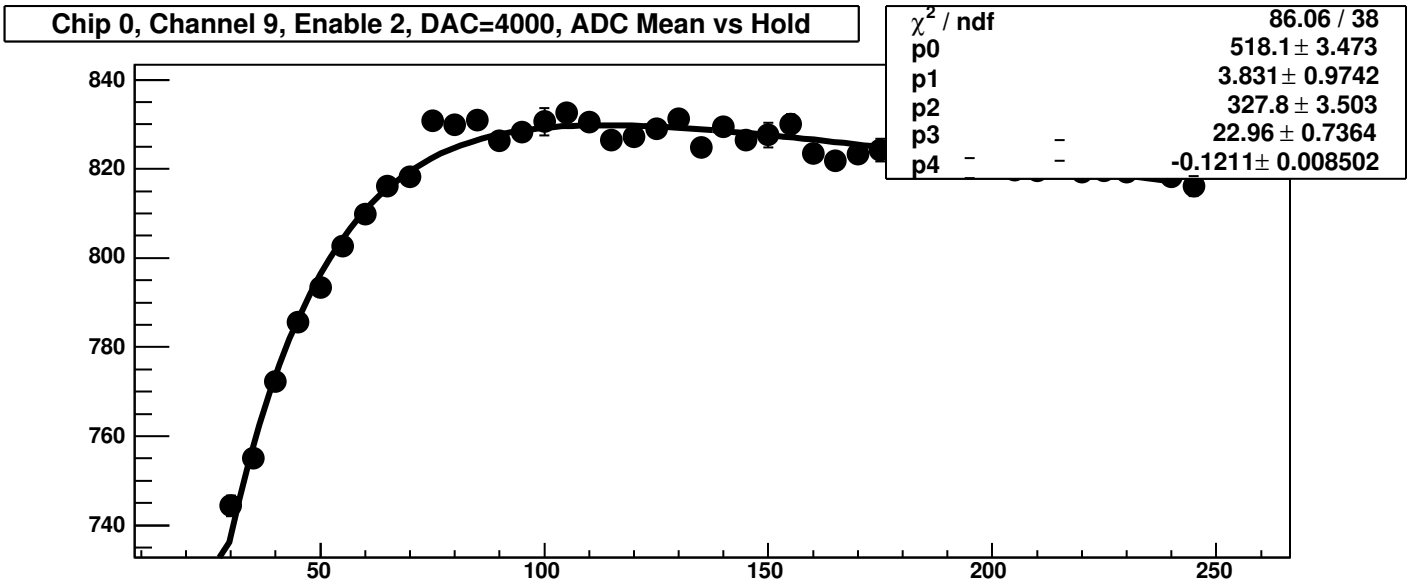


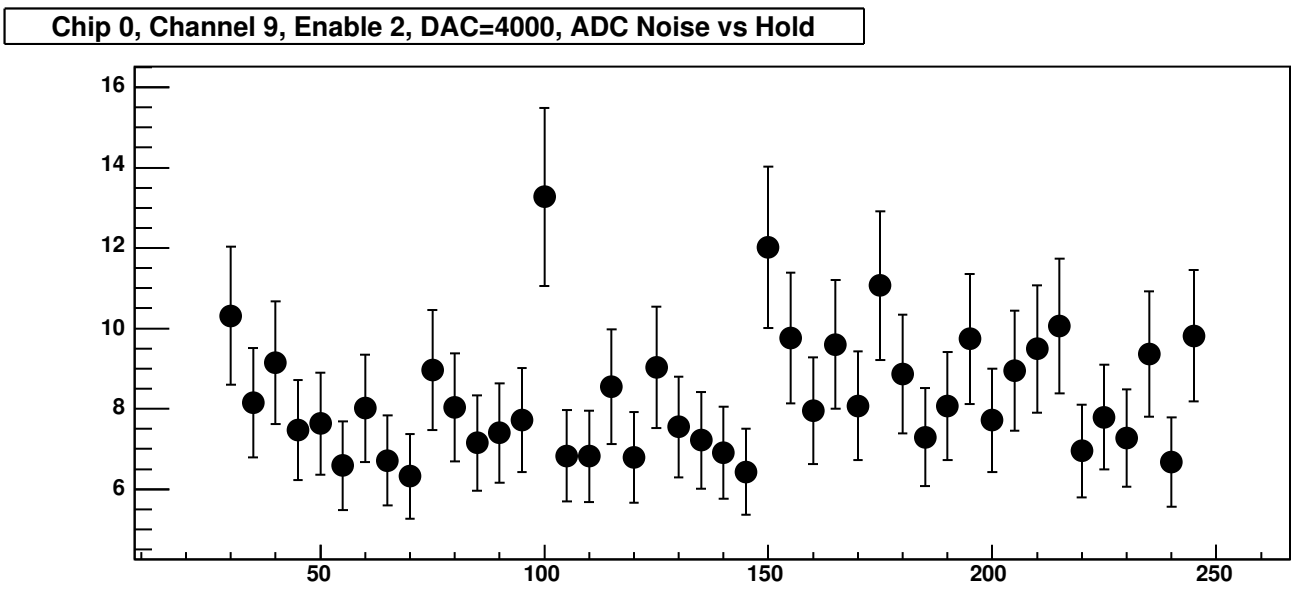
Chip 0, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold

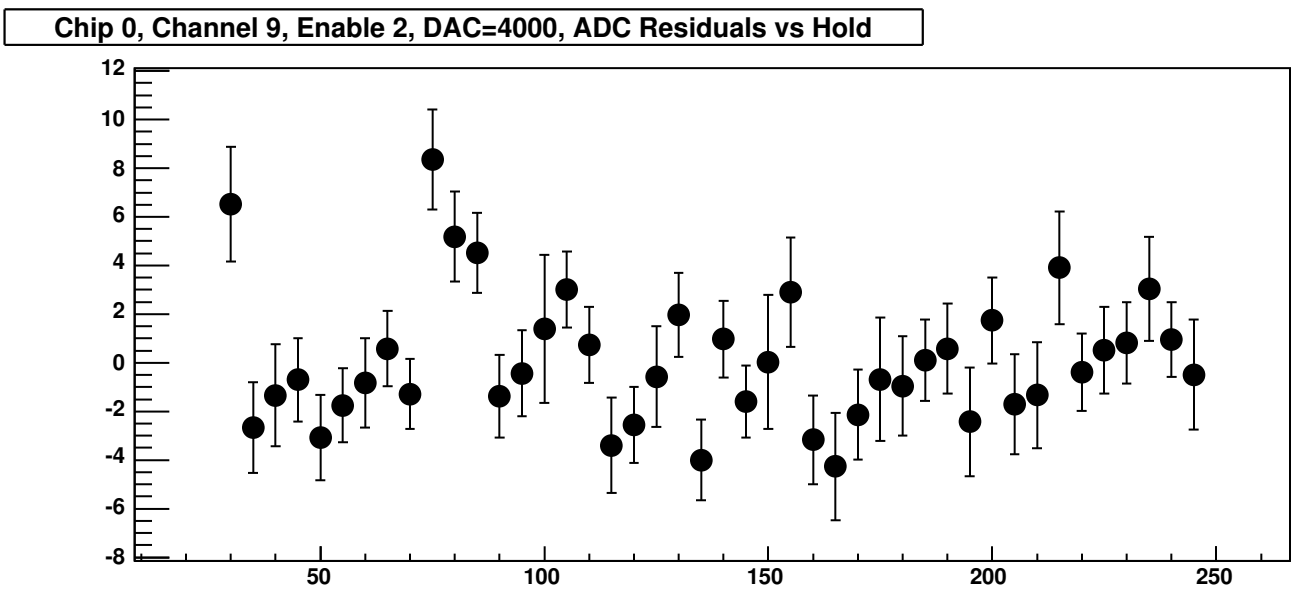


Chip 0, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

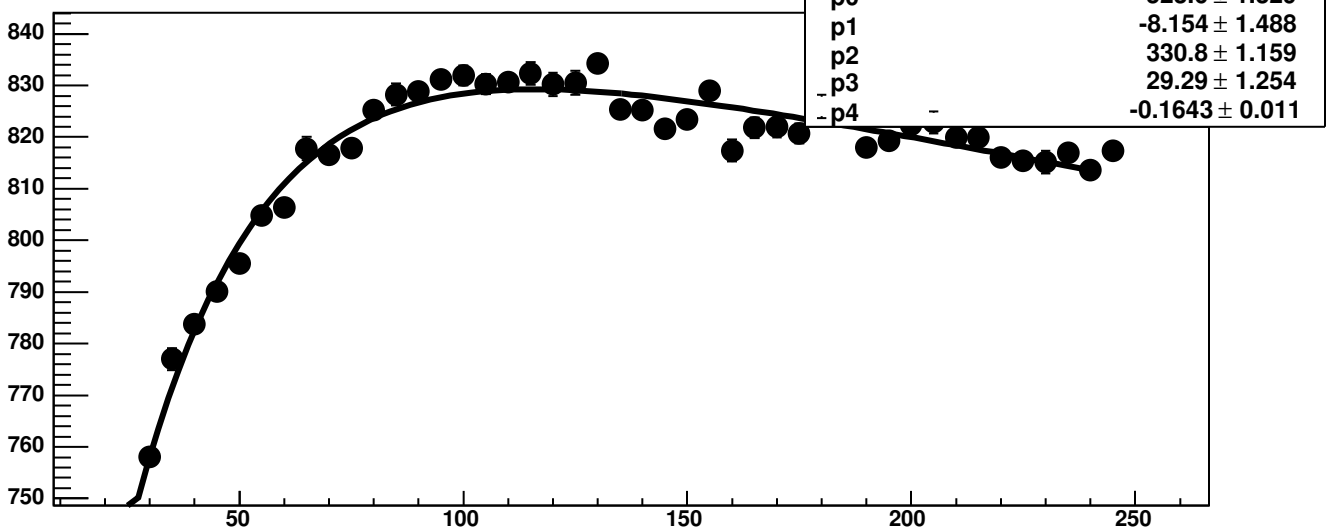



Chip 0, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 86.06 / 38
p0 518.1 ± 3.473
p1 3.831 ± 0.9742
p2 327.8 ± 3.503
p3 22.96 ± 0.7364
p4 -0.1211 ± 0.008502
840
820
800
780
760
740
50
100
150
200
250

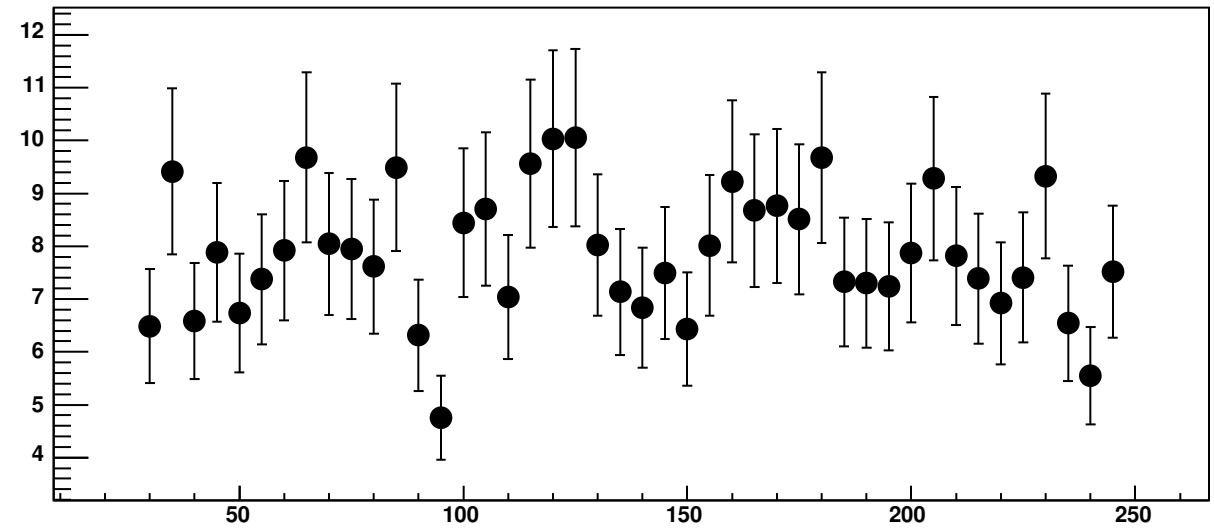

Chip 0, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold
16
14
12
10
8
6
50
100
150
200
250


Chip 0, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold
12
10
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

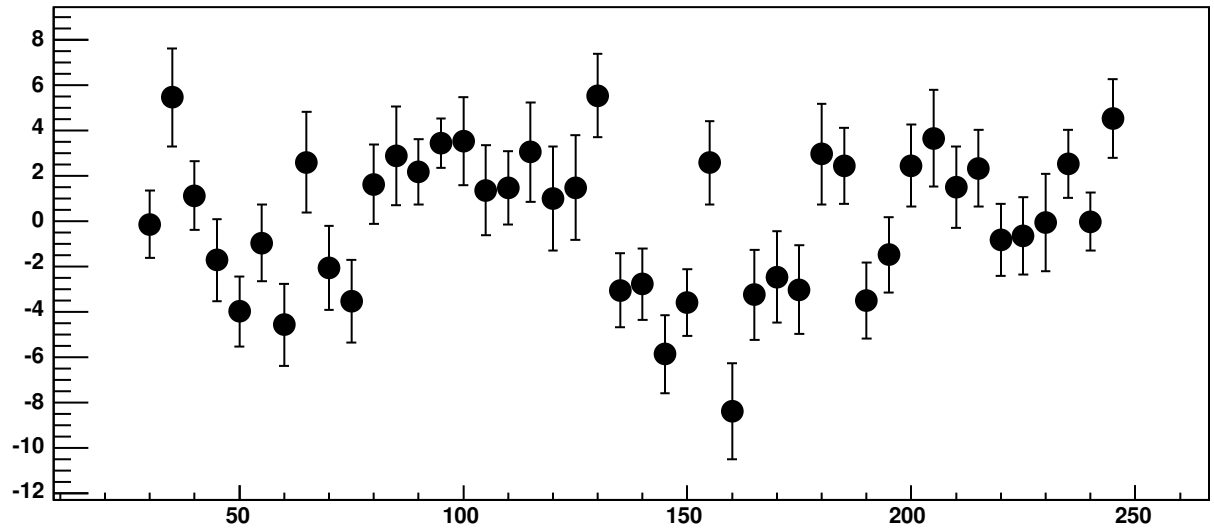
Chip 0, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold



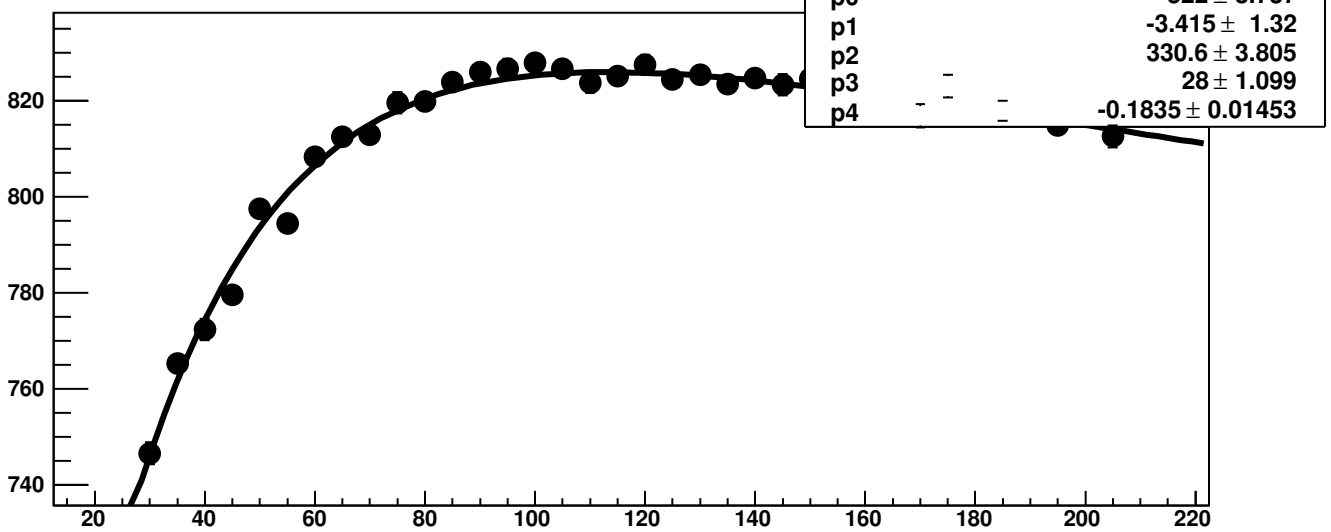
Chip 0, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold



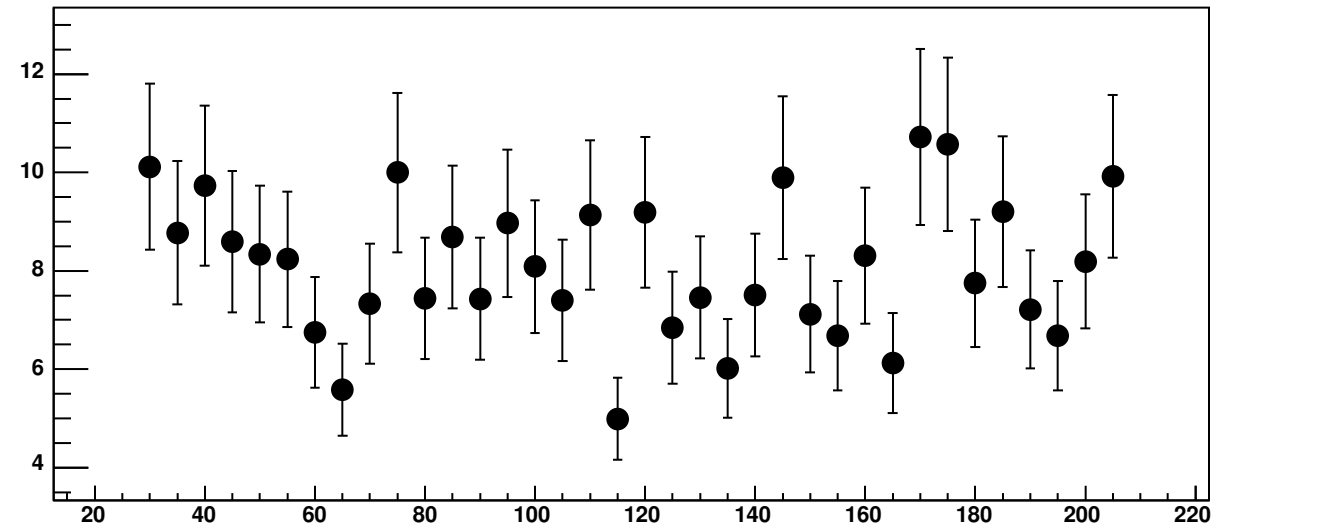
Chip 0, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold



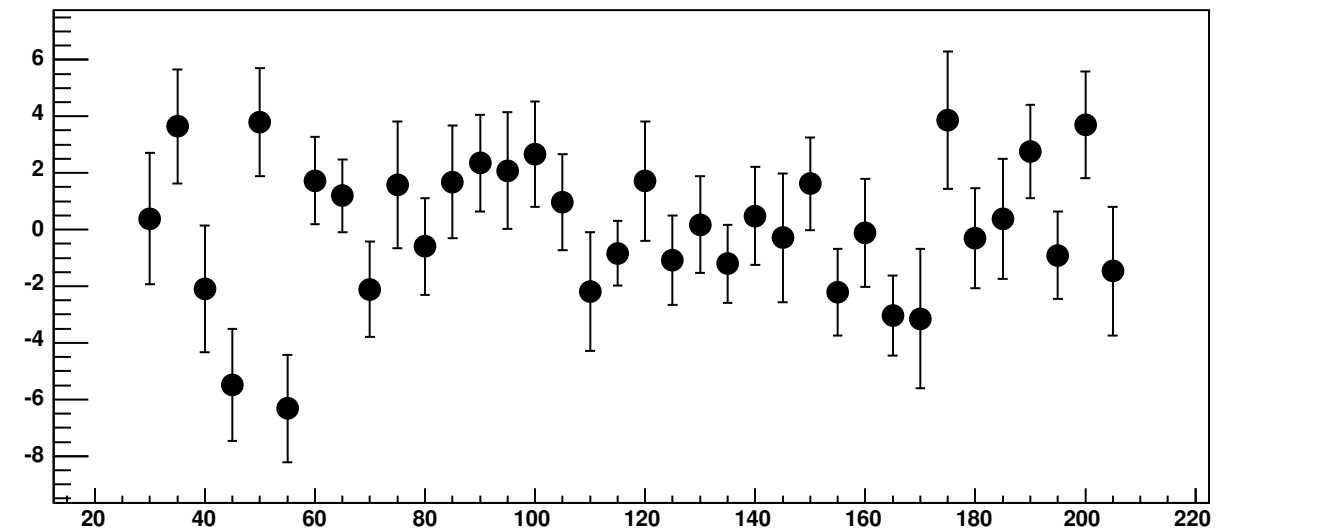
Chip 0, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold



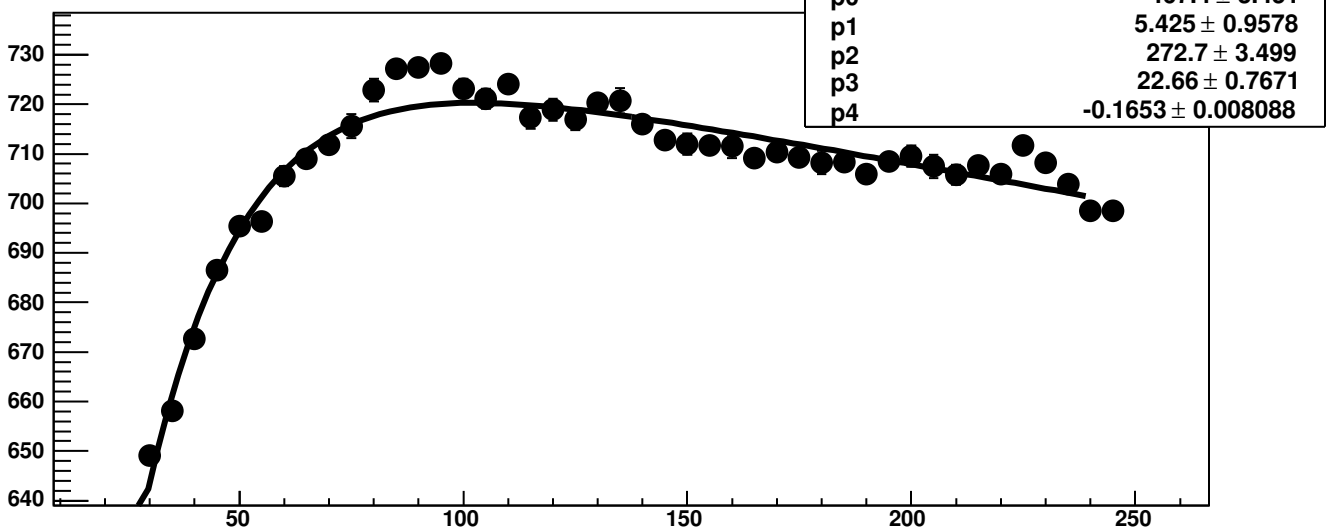
Chip 0, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



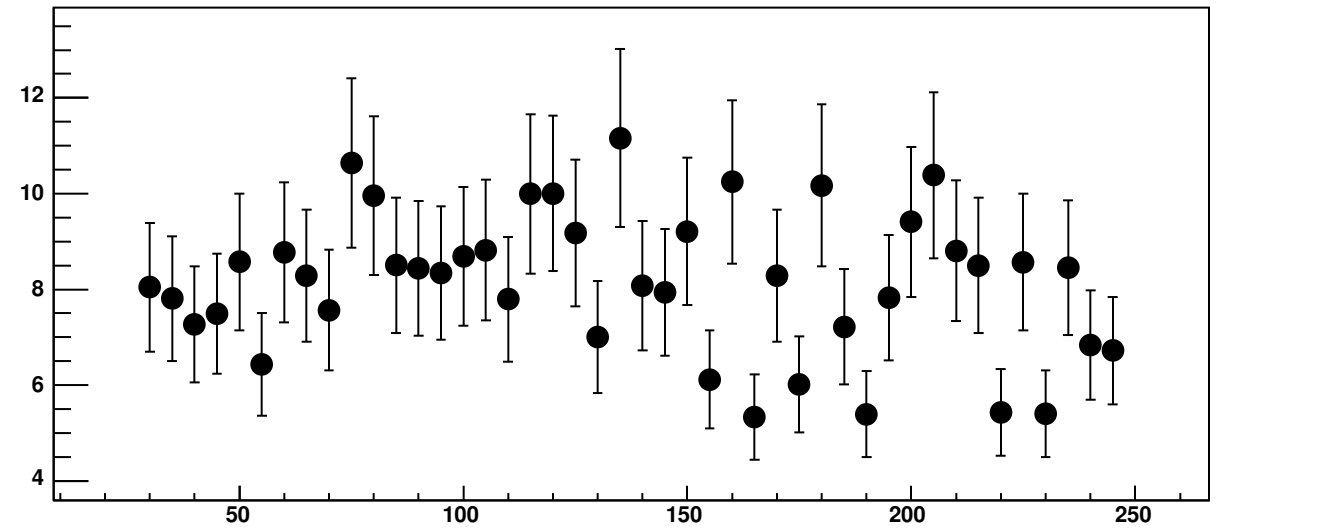
Chip 0, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold



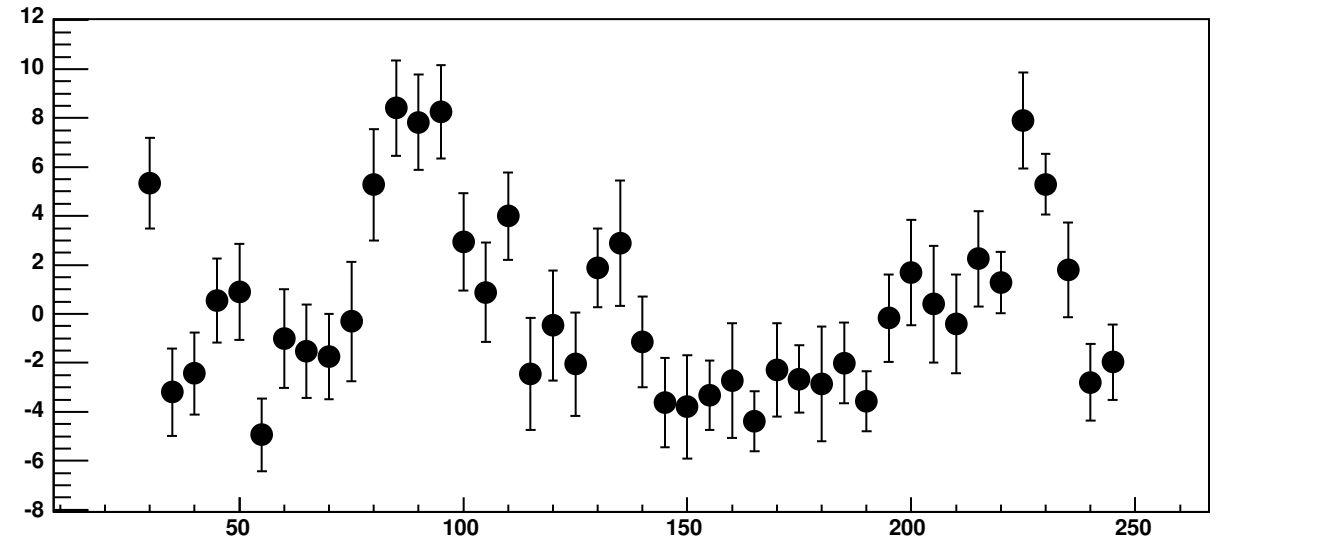
Chip 0, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold

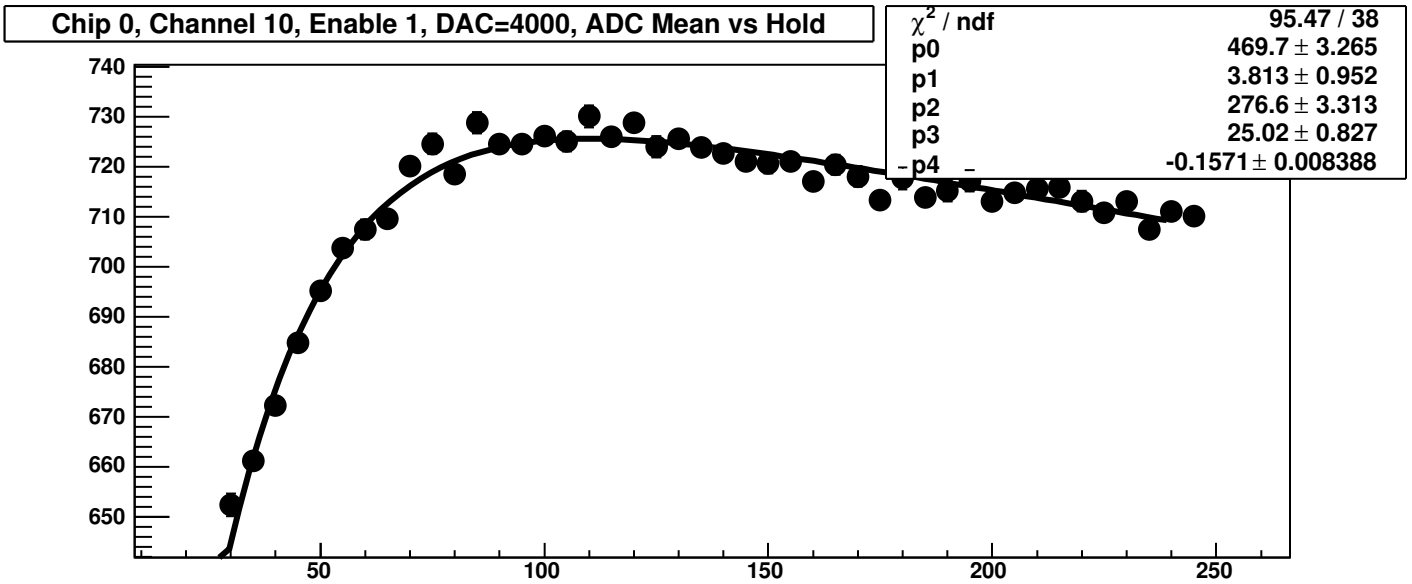


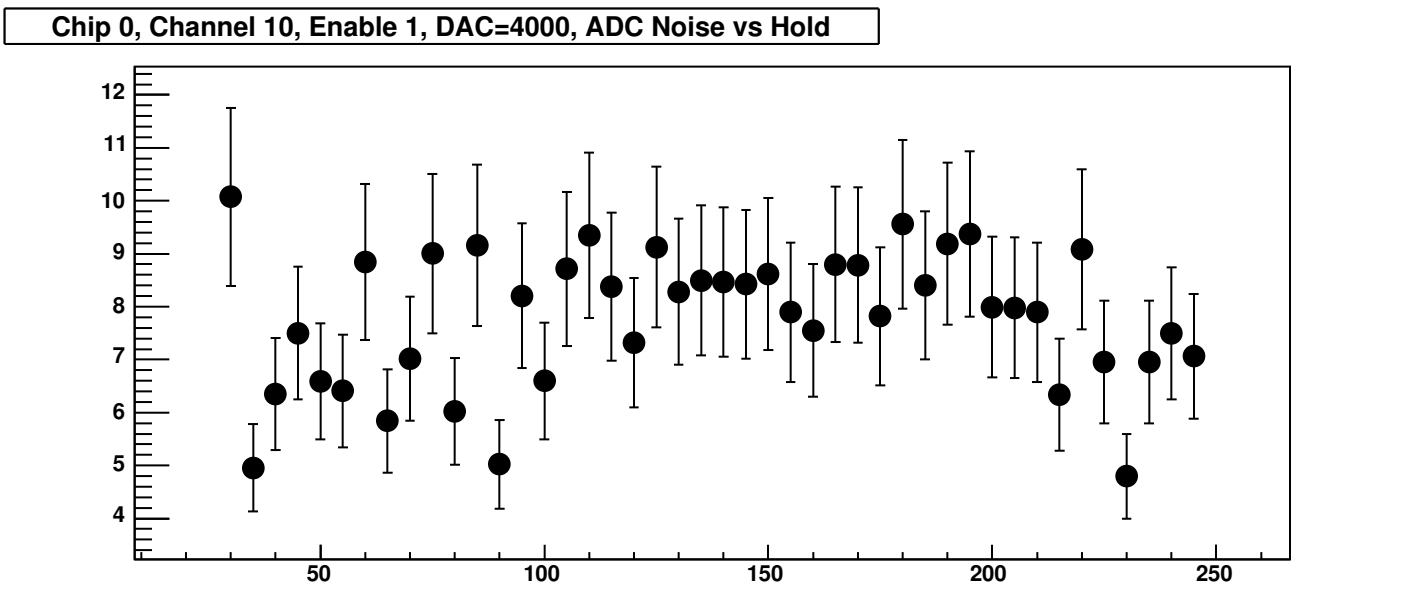
Chip 0, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold

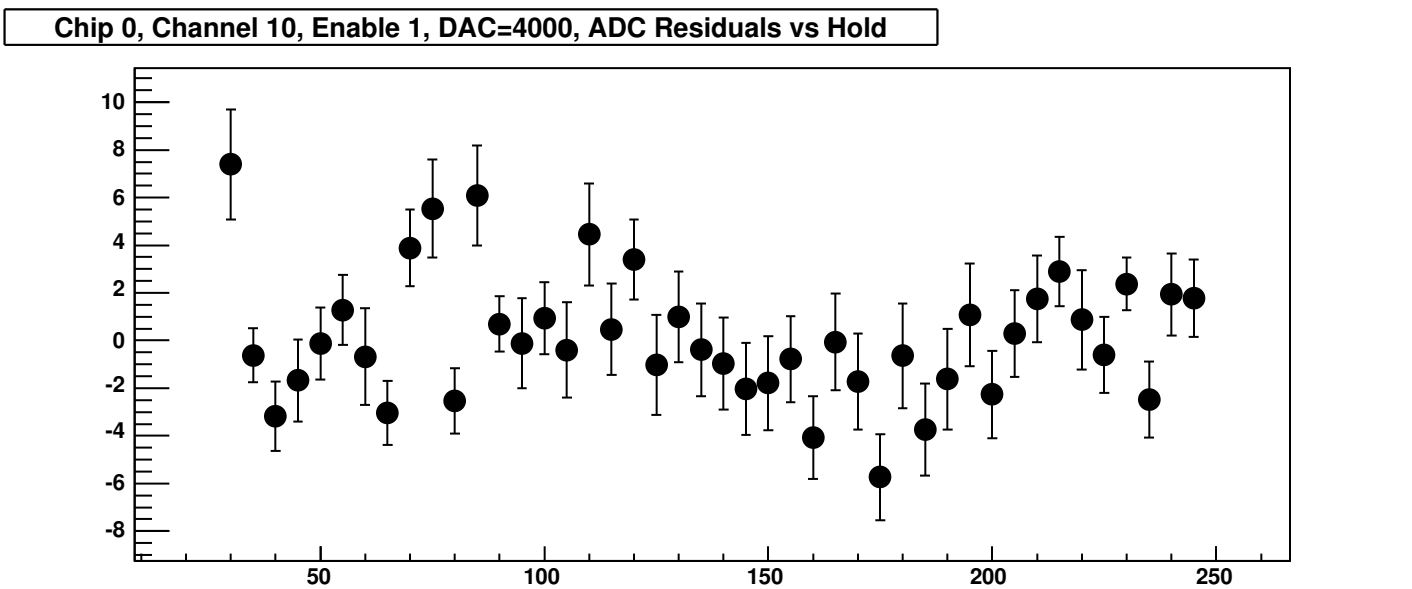


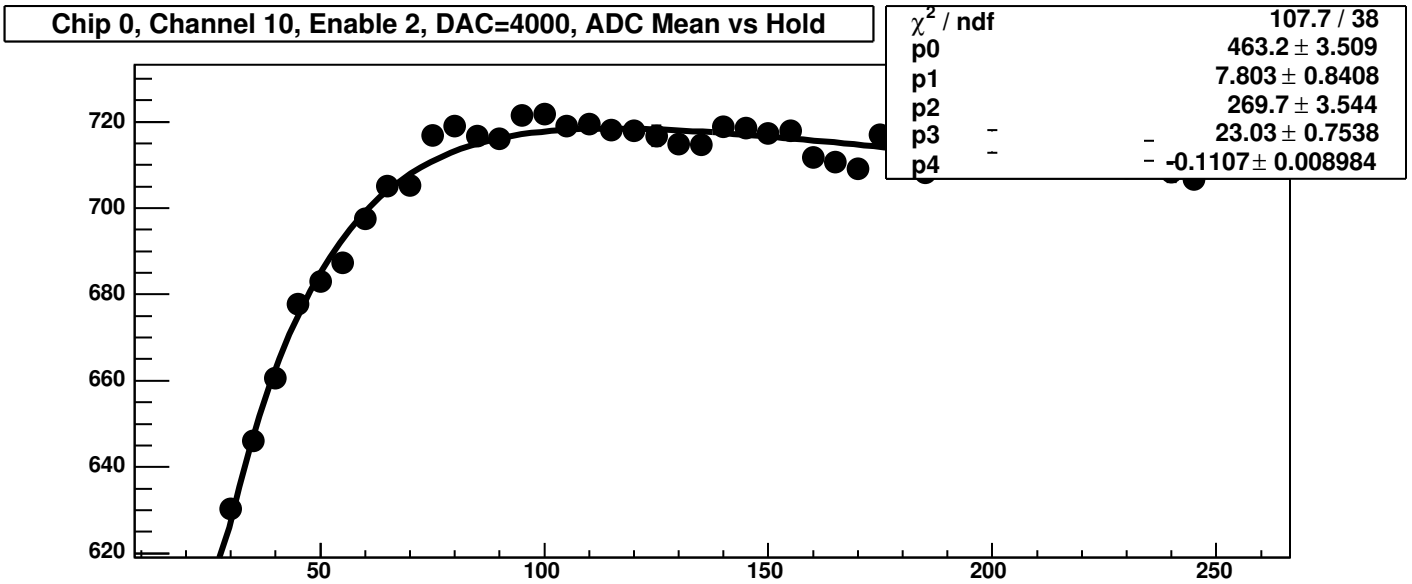
Chip 0, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold

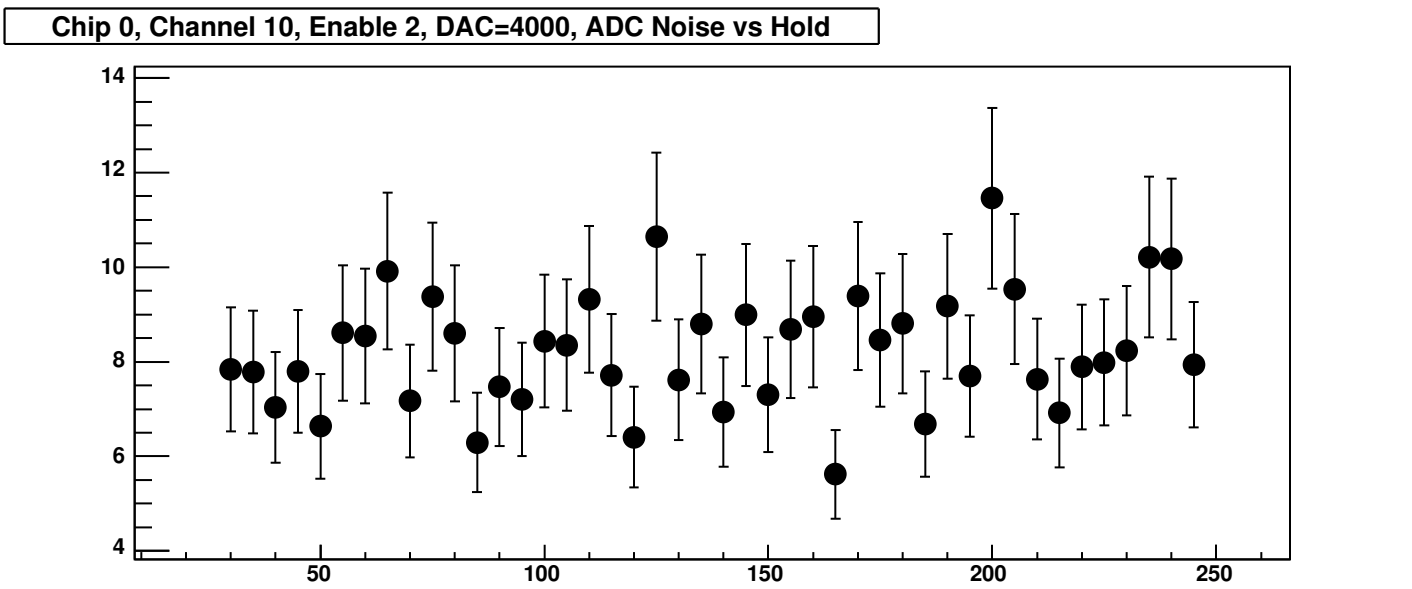


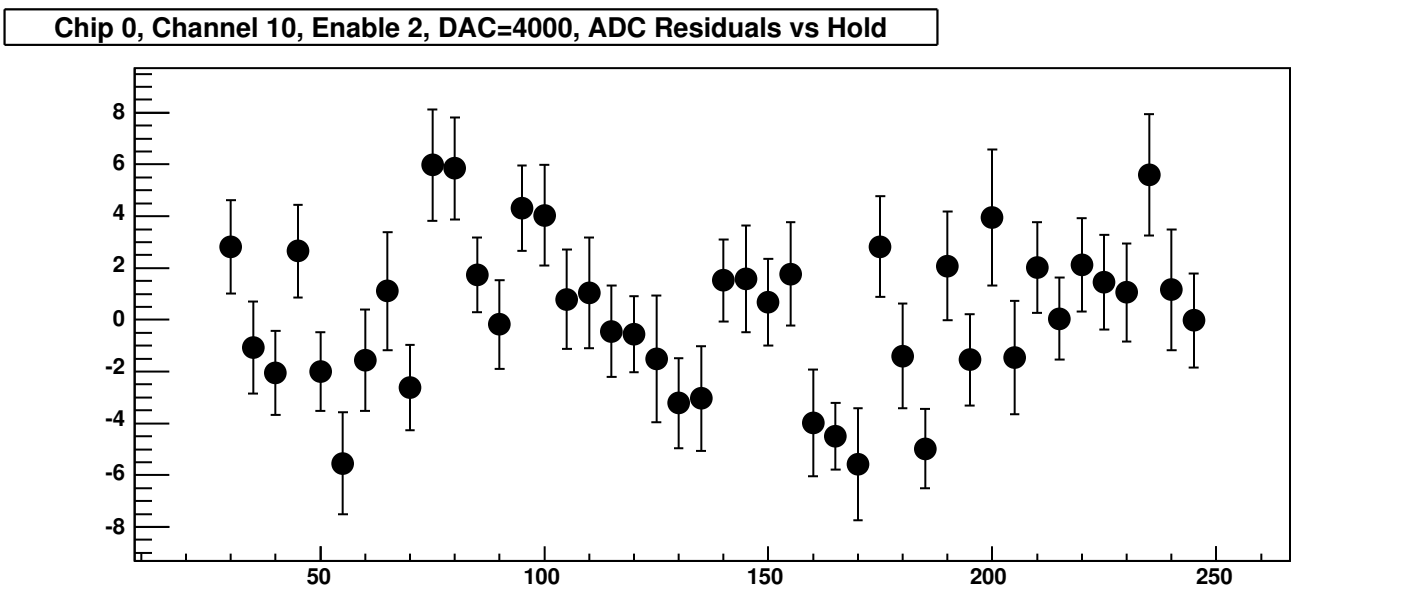

Chip 0, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 95.47 / 38
p0 469.7 ± 3.265
p1 3.813 ± 0.952
p2 276.6 ± 3.313
p3 25.02 ± 0.827
p4 -0.1571 ± 0.008388
740
730
720
710
700
690
680
670
660
650
50
100
150
200
250

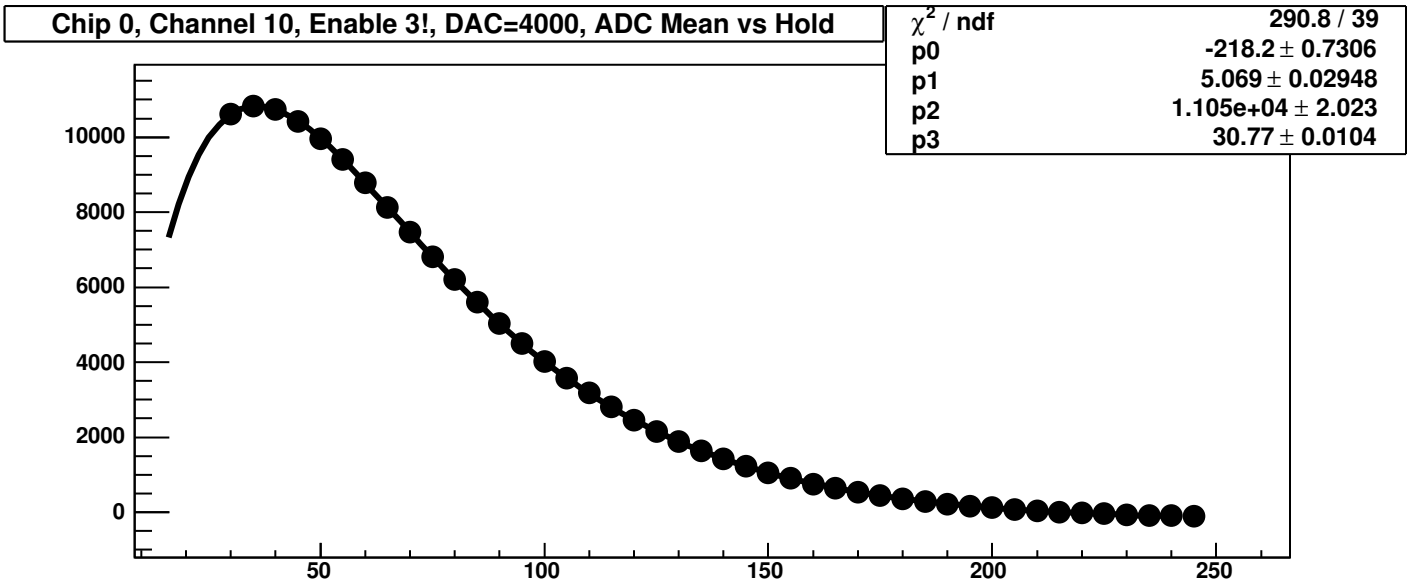

Chip 0, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

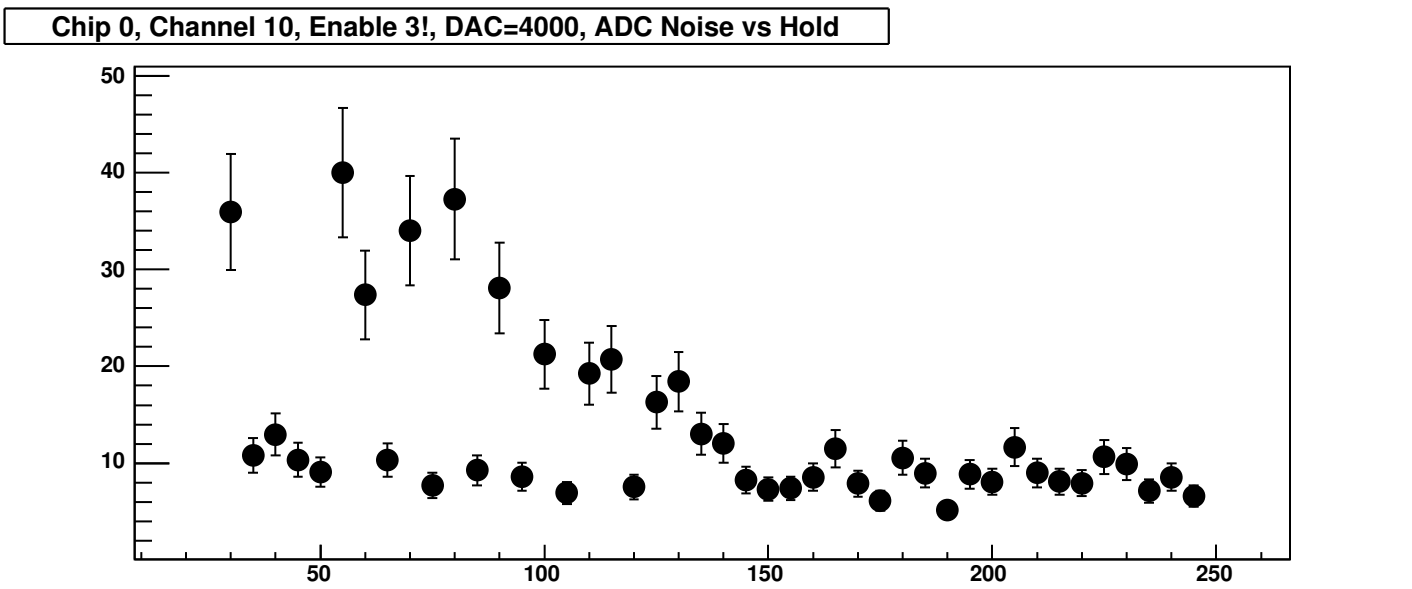

Chip 0, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

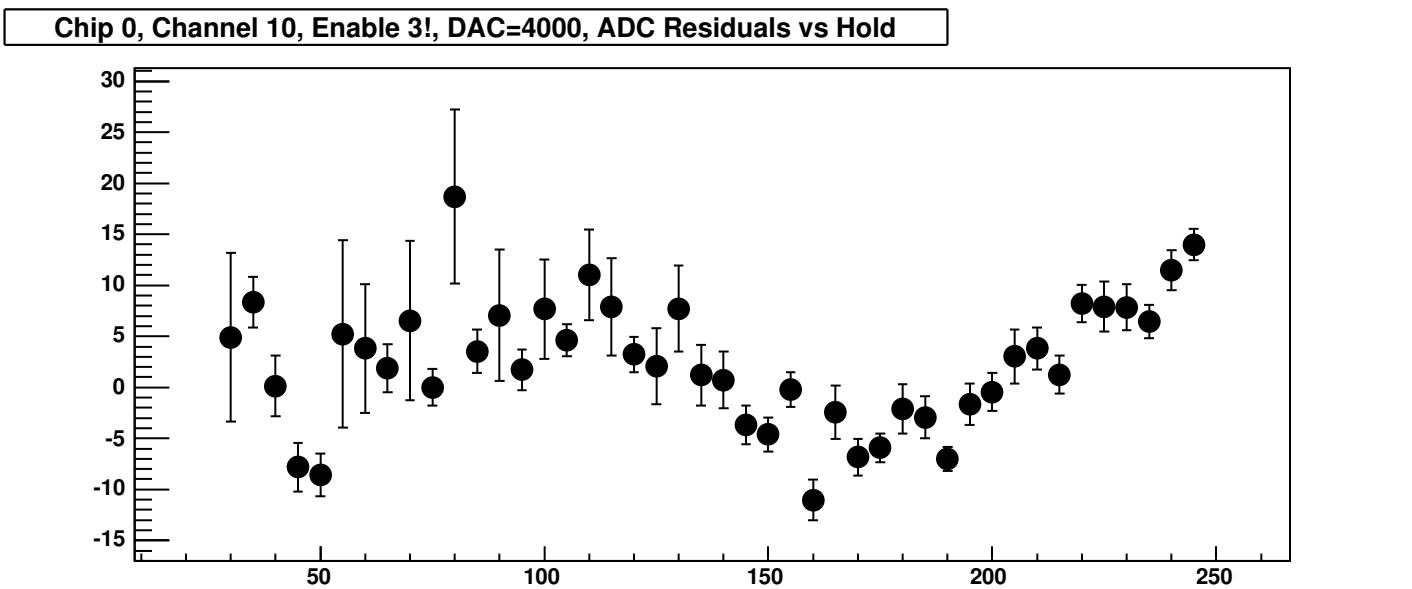

Chip 0, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 107.7 / 38
p0 463.2 ± 3.509
p1 7.803 ± 0.8408
p2 269.7 ± 3.544
p3 23.03 ± 0.7538
p4 -0.1107 ± 0.008984
720
700
680
660
640
620
50
100
150
200
250


Chip 0, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250

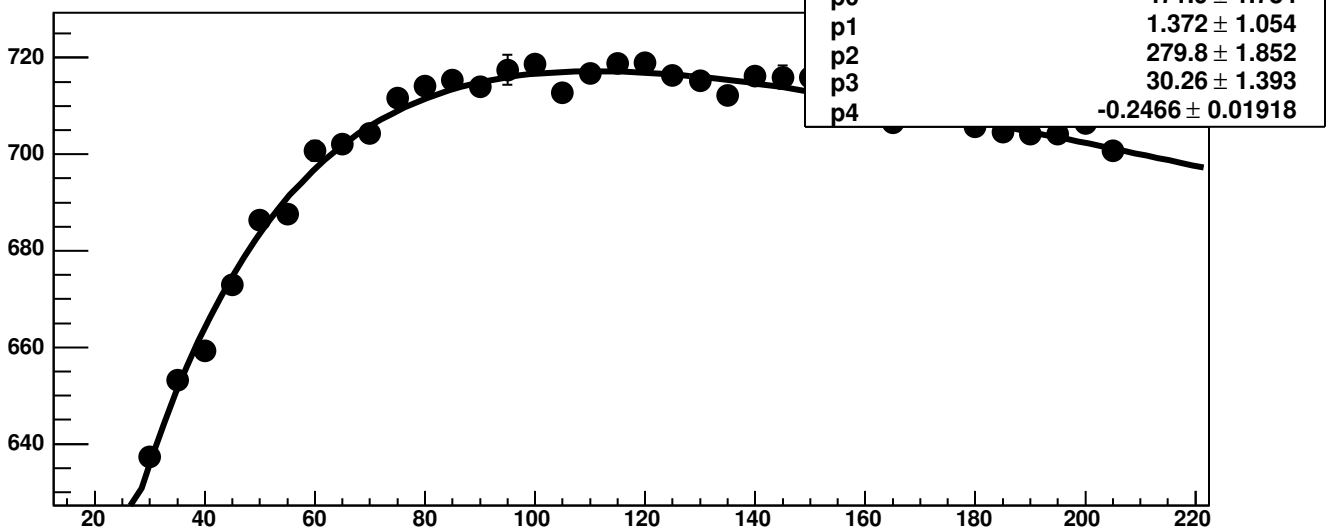

Chip 0, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250


Chip 0, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 290.8 / 39
p0 -218.2 ± 0.7306
p1 5.069 ± 0.02948
p2 1.105e+04 ± 2.023
p3 30.77 ± 0.0104
10000
8000
6000
4000
2000
0
50
100
150
200
250

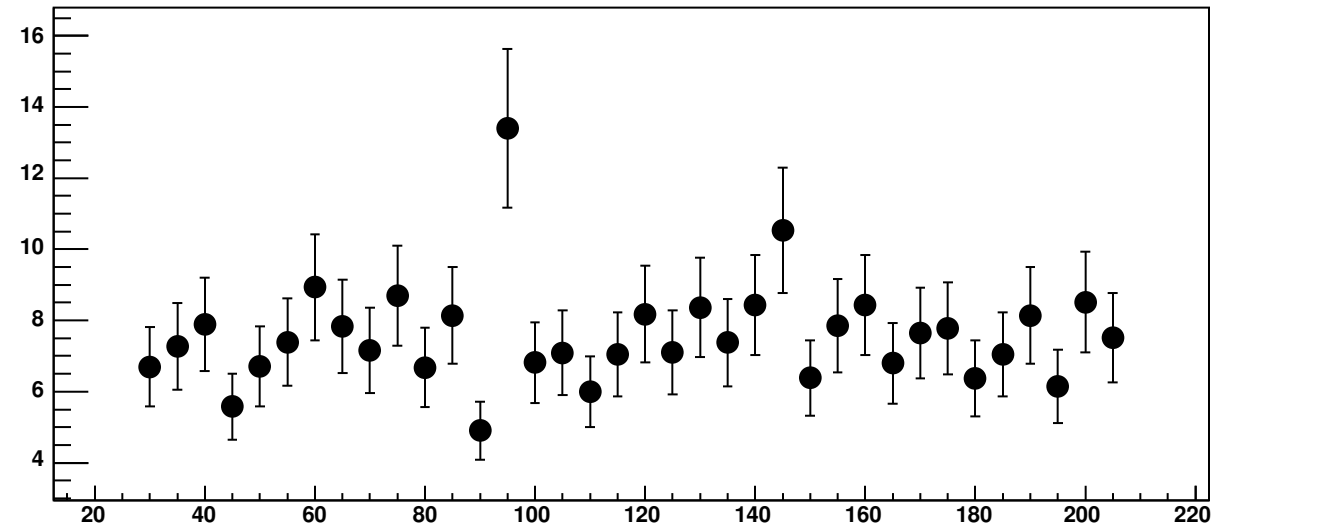

Chip 0, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
50
100
150
200
250


Chip 0, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
-15
50
100
150
200
250

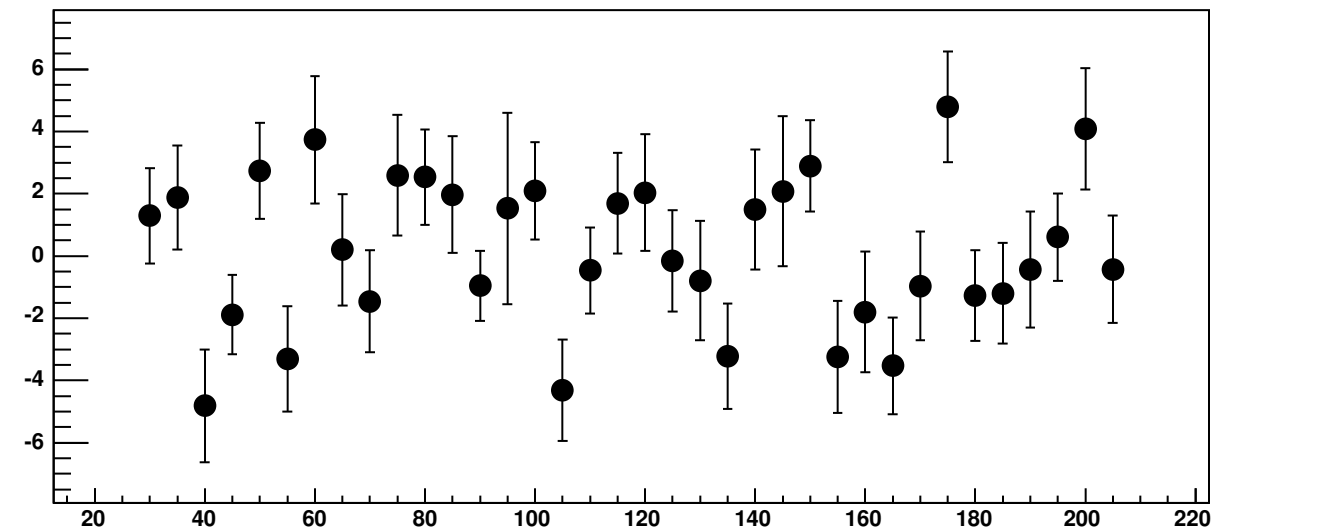
Chip 0, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

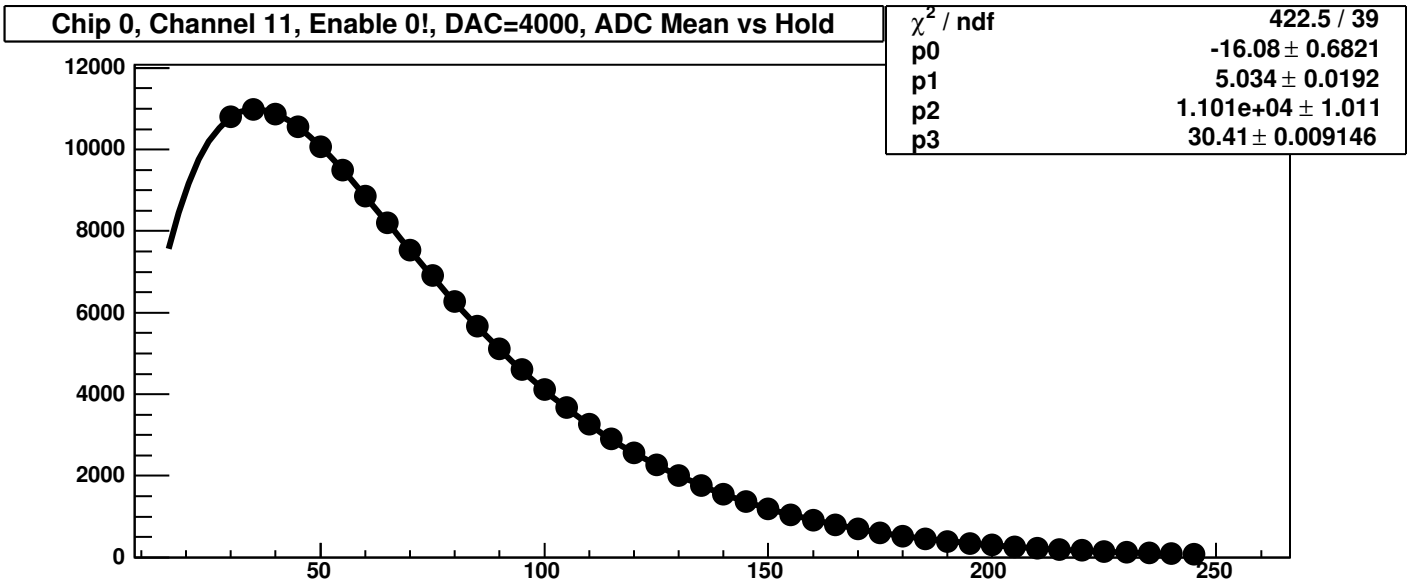


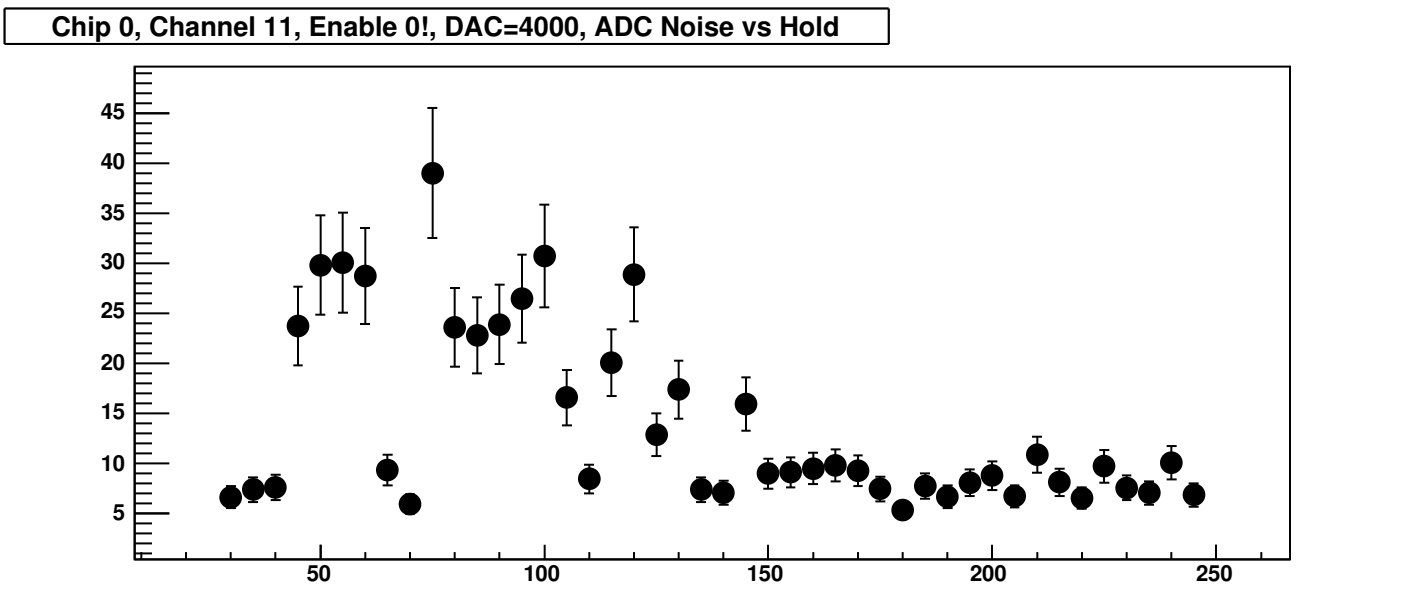
Chip 0, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold

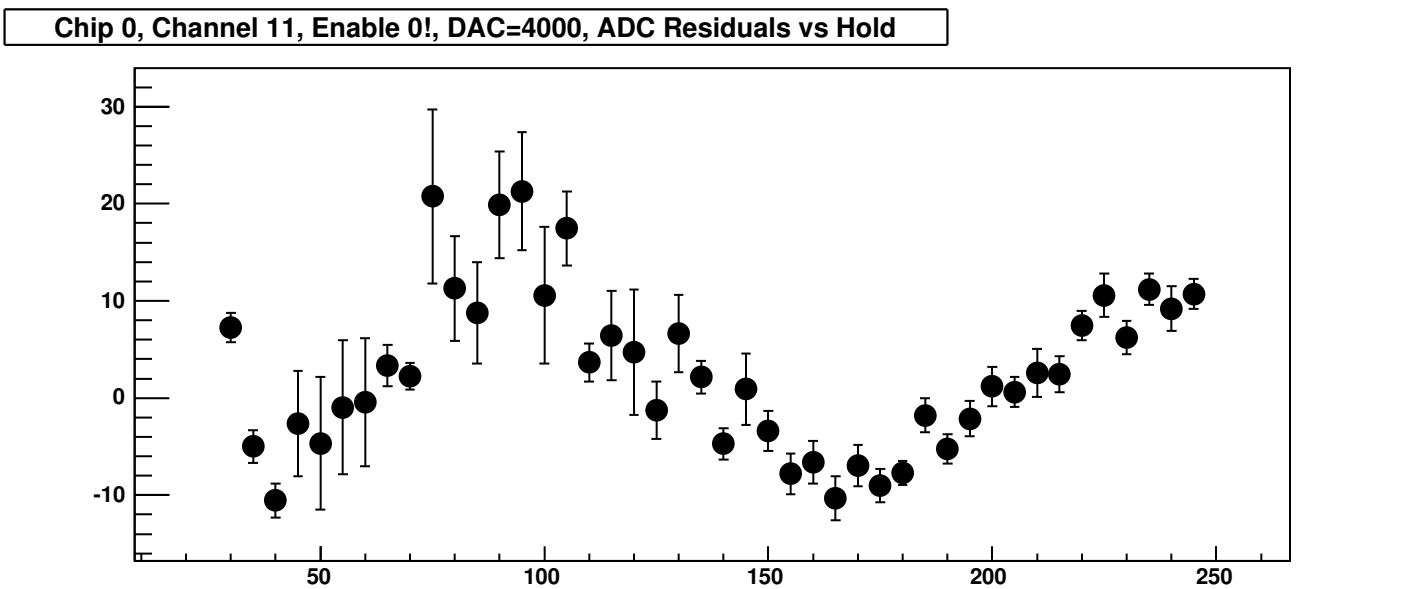


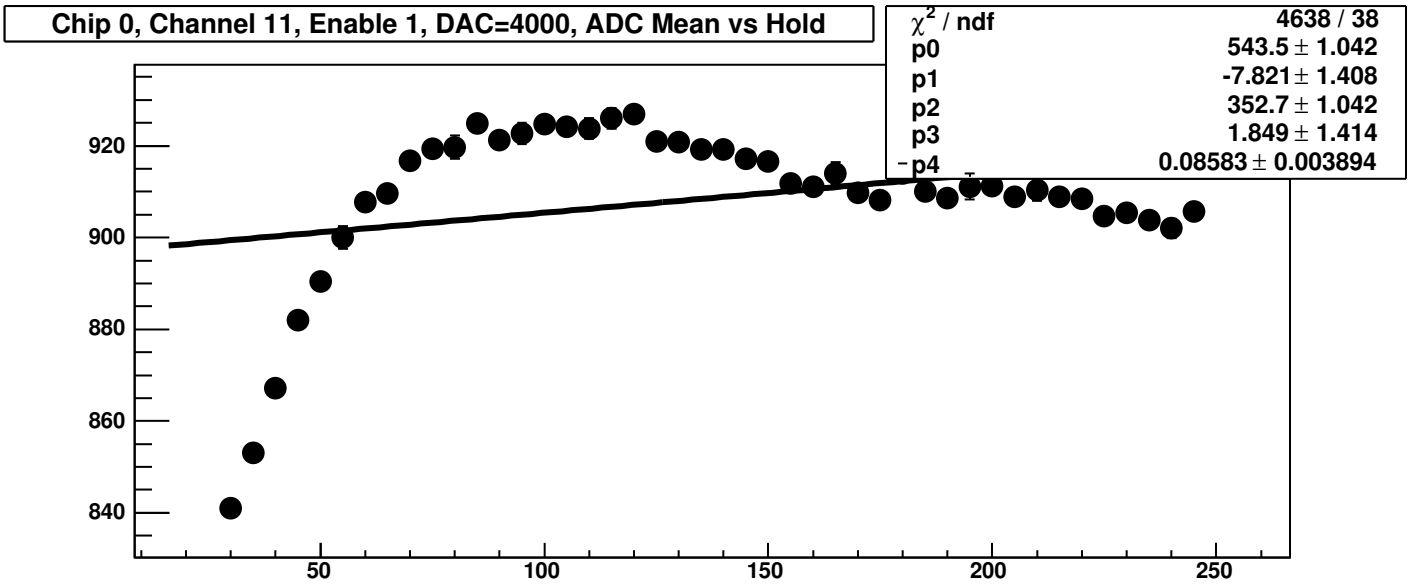
Chip 0, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

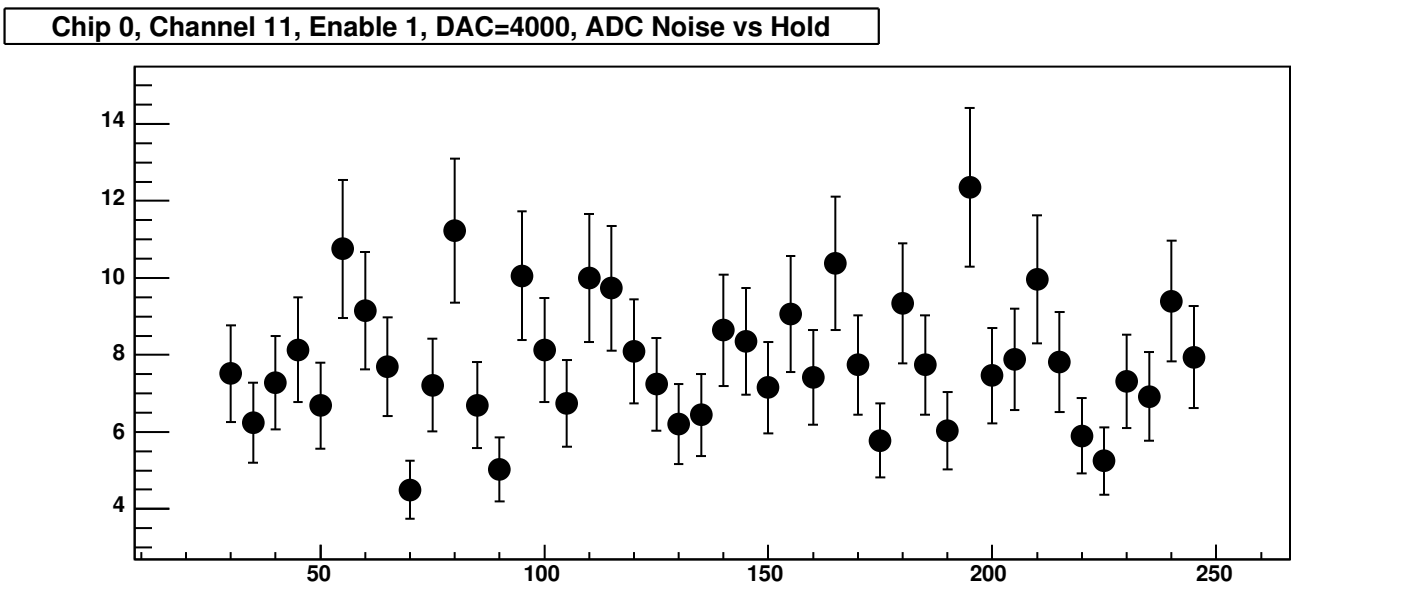


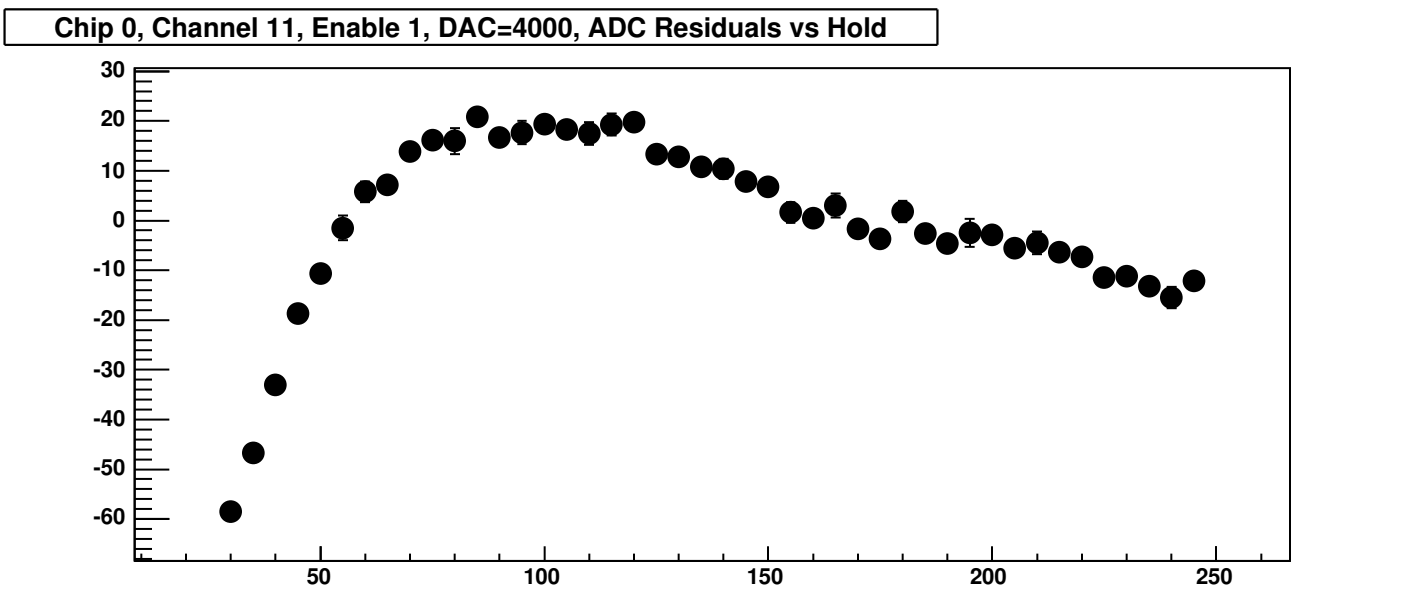

Chip 0, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 422.5 / 39
p0 -16.08 ± 0.6821
p1 5.034 ± 0.0192
p2 1.101e+04 ± 1.011
p3 30.41 ± 0.009146
12000
10000
8000
6000
4000
2000
0
50
100
150
200
250


Chip 0, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold
45
40
35
30
25
20
15
10
5
50
100
150
200
250

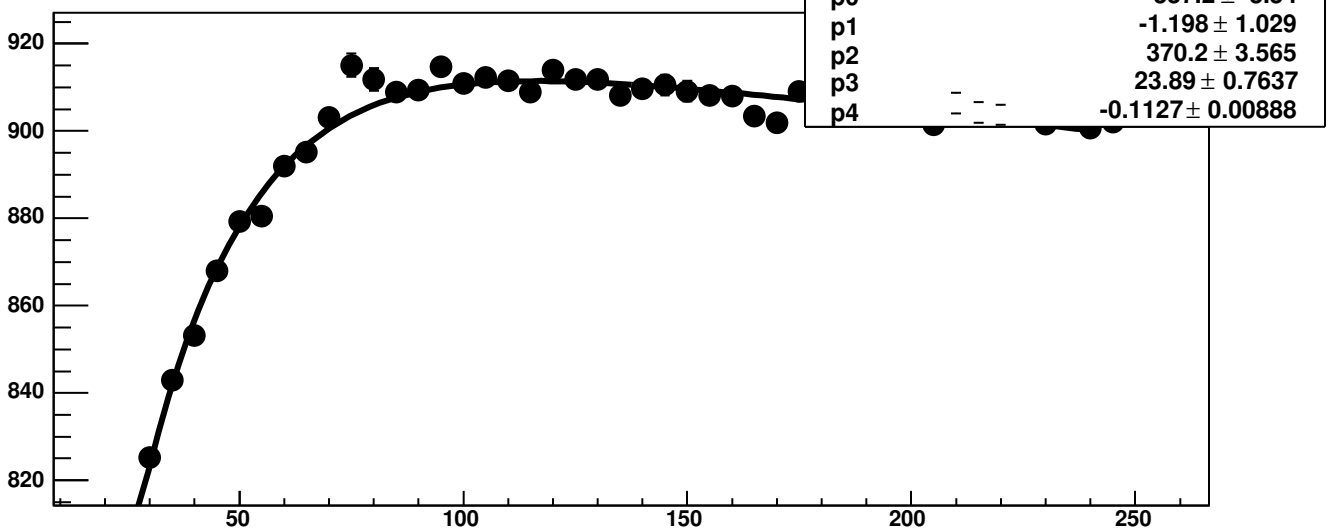

Chip 0, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
50
100
150
200
250


Chip 0, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 4638 / 38
p0 543.5 ± 1.042
p1 -7.821 ± 1.408
p2 352.7 ± 1.042
p3 1.849 ± 1.414
p4 0.08583 ± 0.003894
920
900
880
860
840
50
100
150
200
250

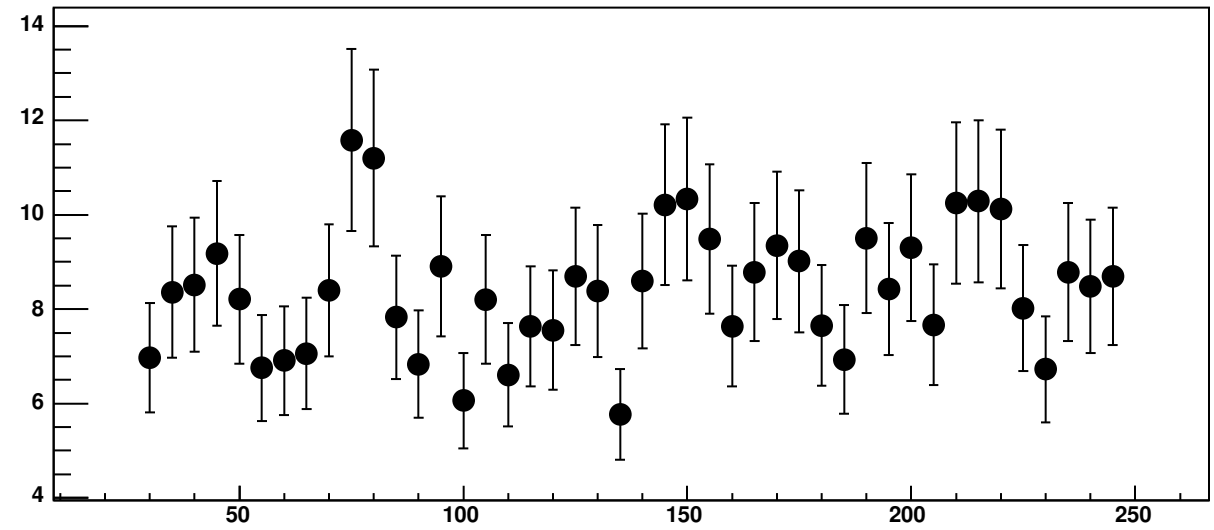

Chip 0, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250


Chip 0, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
-20
-30
-40
-50
-60
50
100
150
200
250

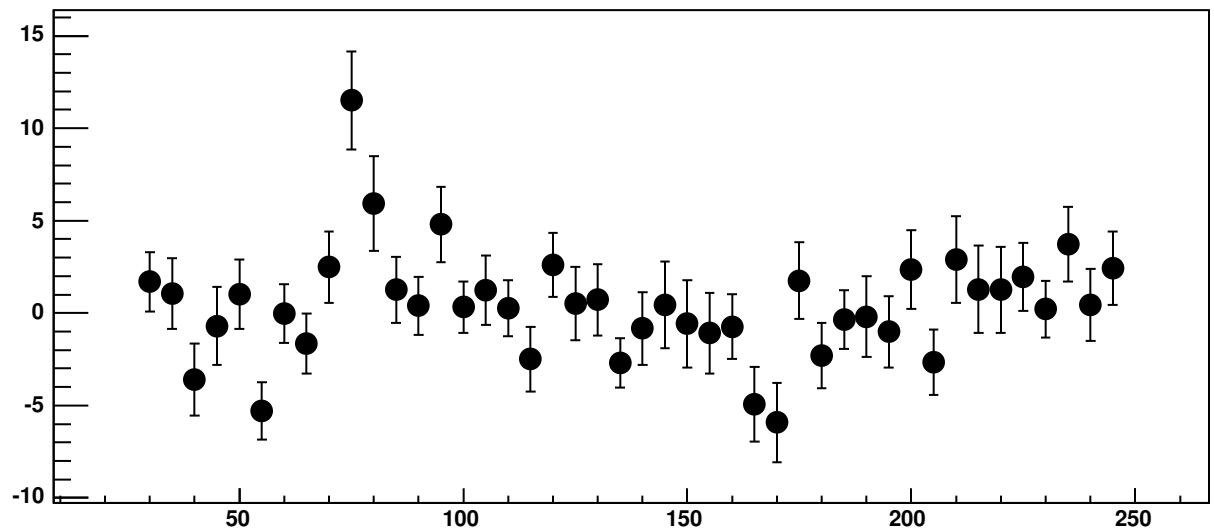
Chip 0, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

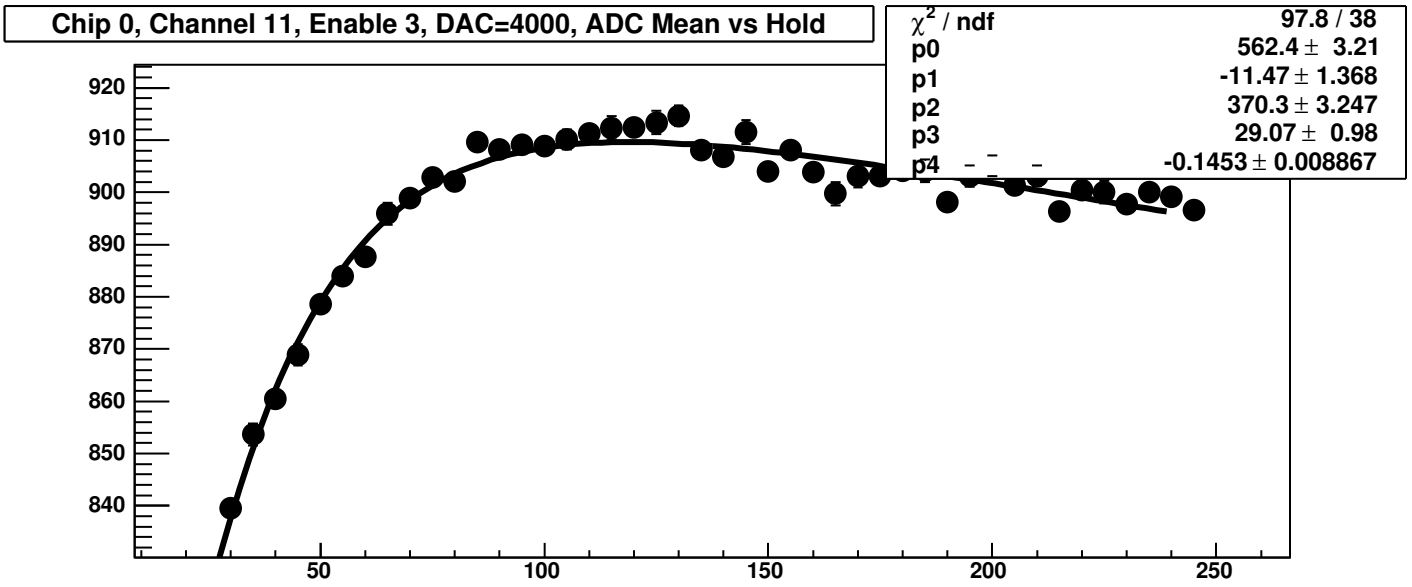


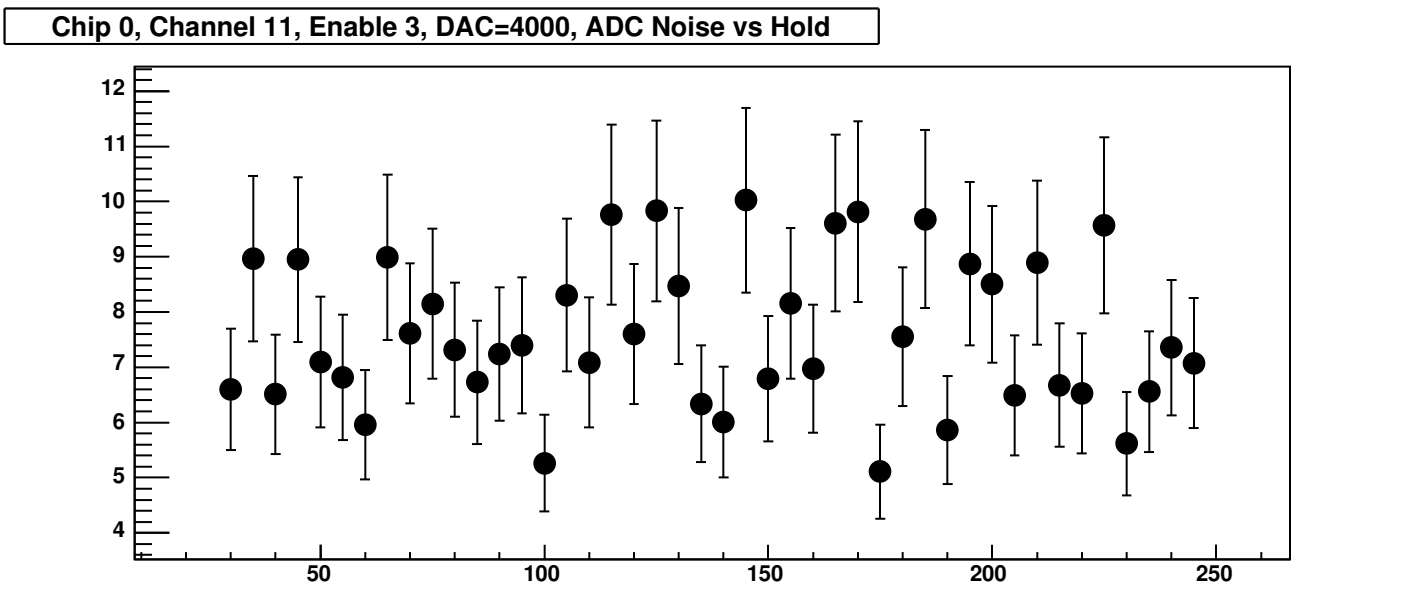
Chip 0, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold

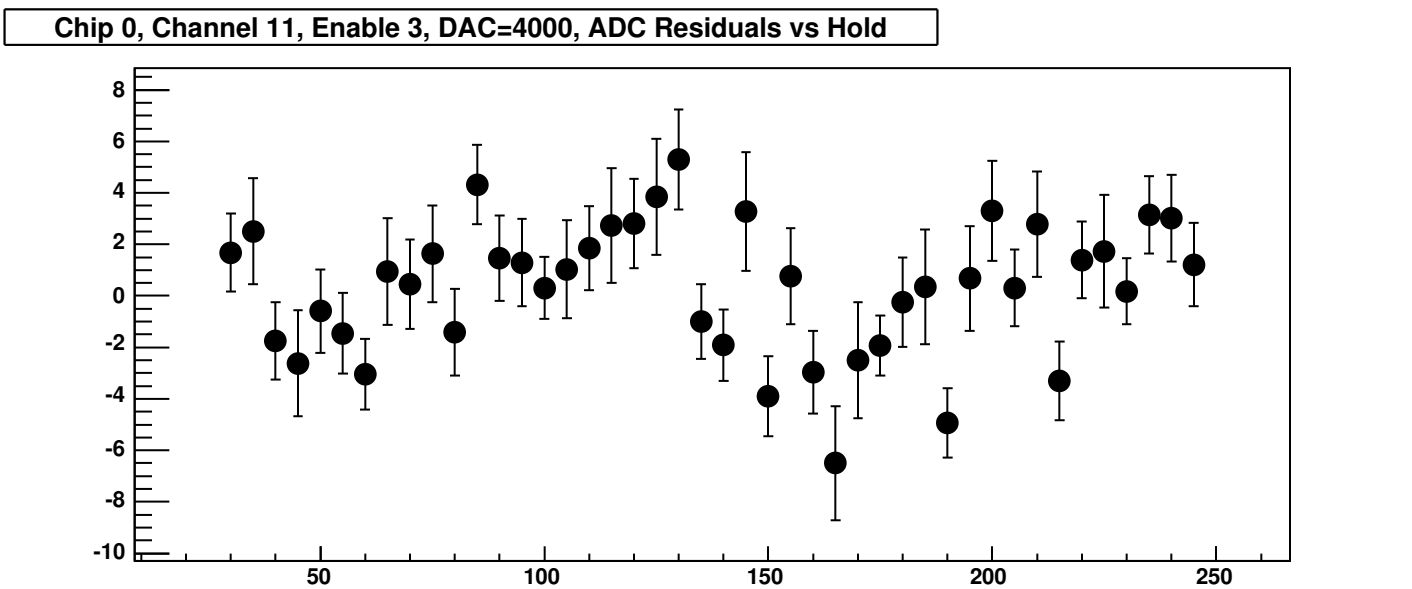


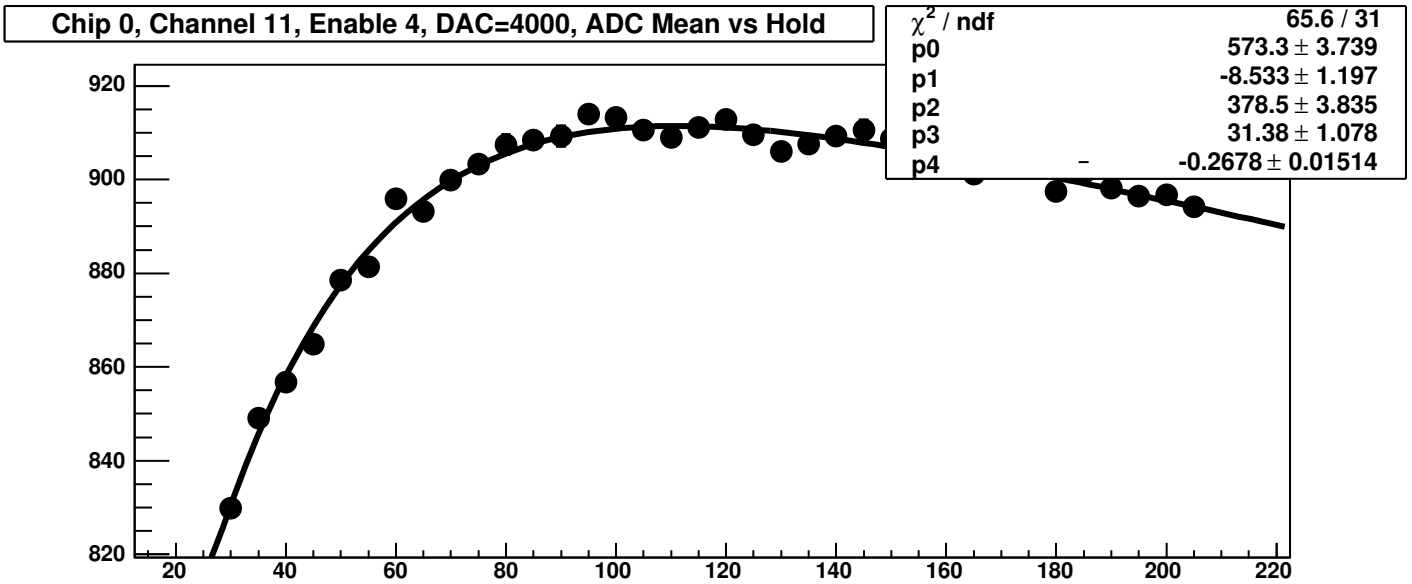
Chip 0, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

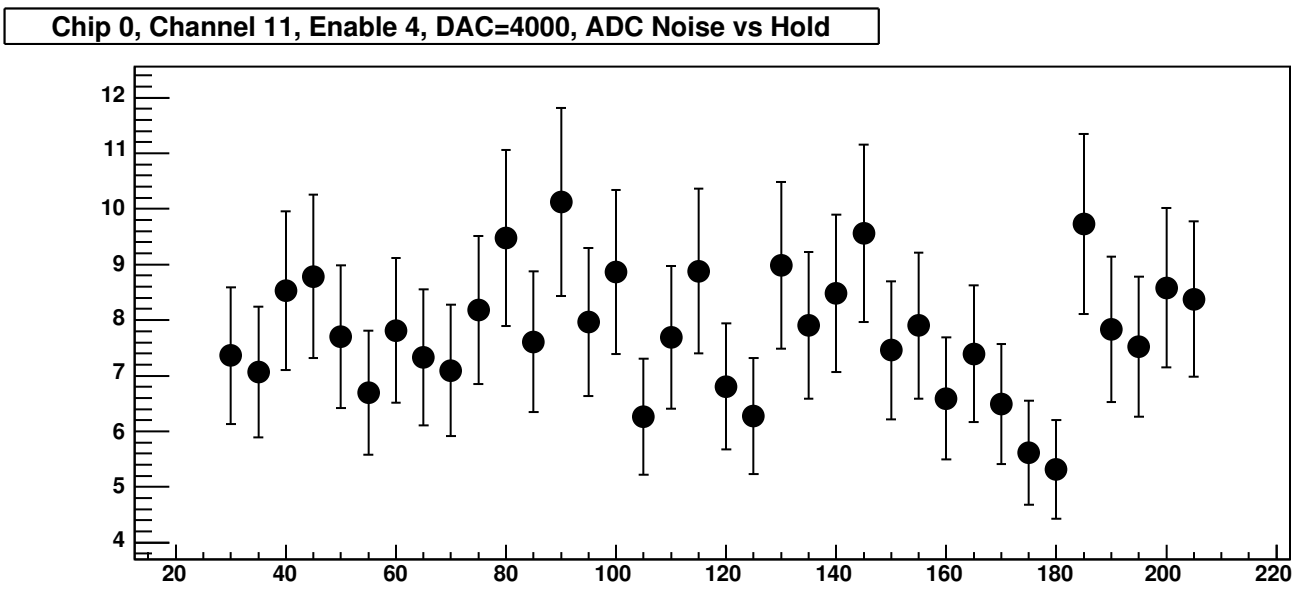


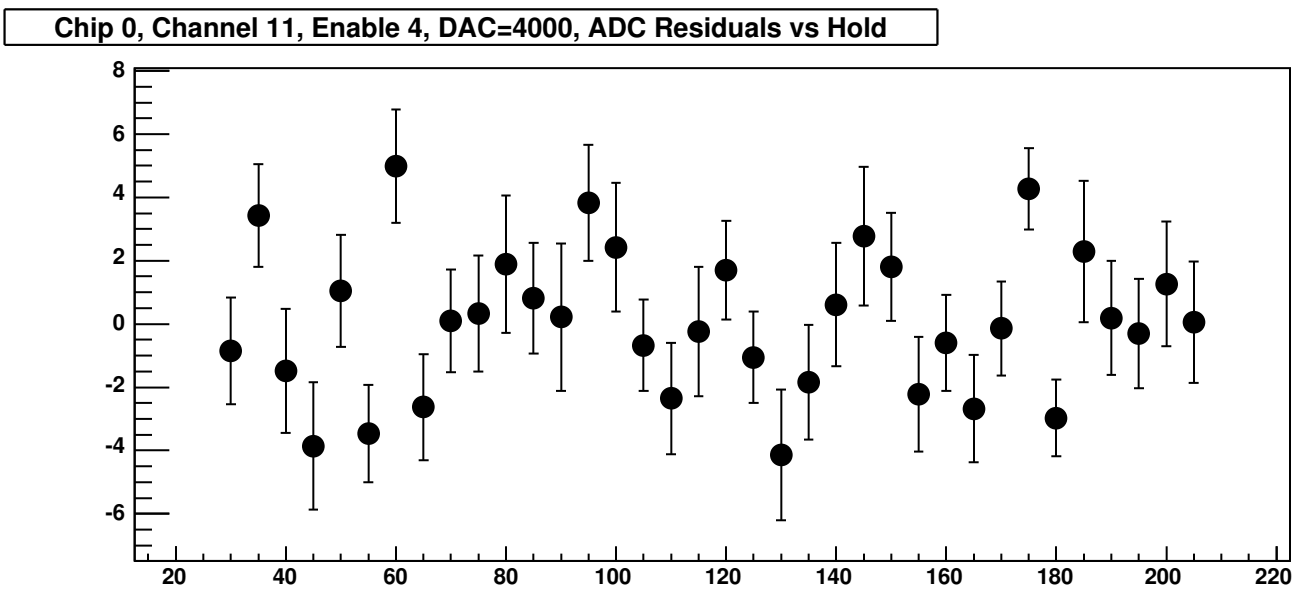

Chip 0, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 97.8 / 38
p0 562.4 ± 3.21
p1 -11.47 ± 1.368
p2 370.3 ± 3.247
p3 29.07 ± 0.98
p4 -0.1453 ± 0.008867
920
910
900
890
880
870
860
850
840
50
100
150
200
250

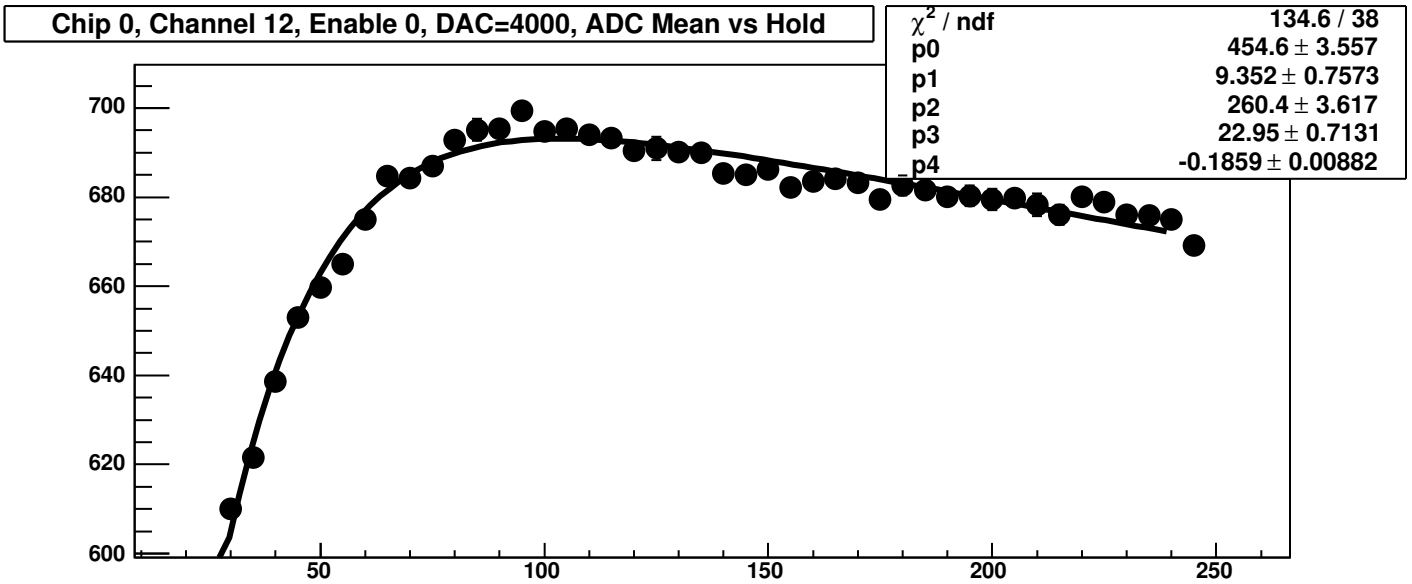

Chip 0, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

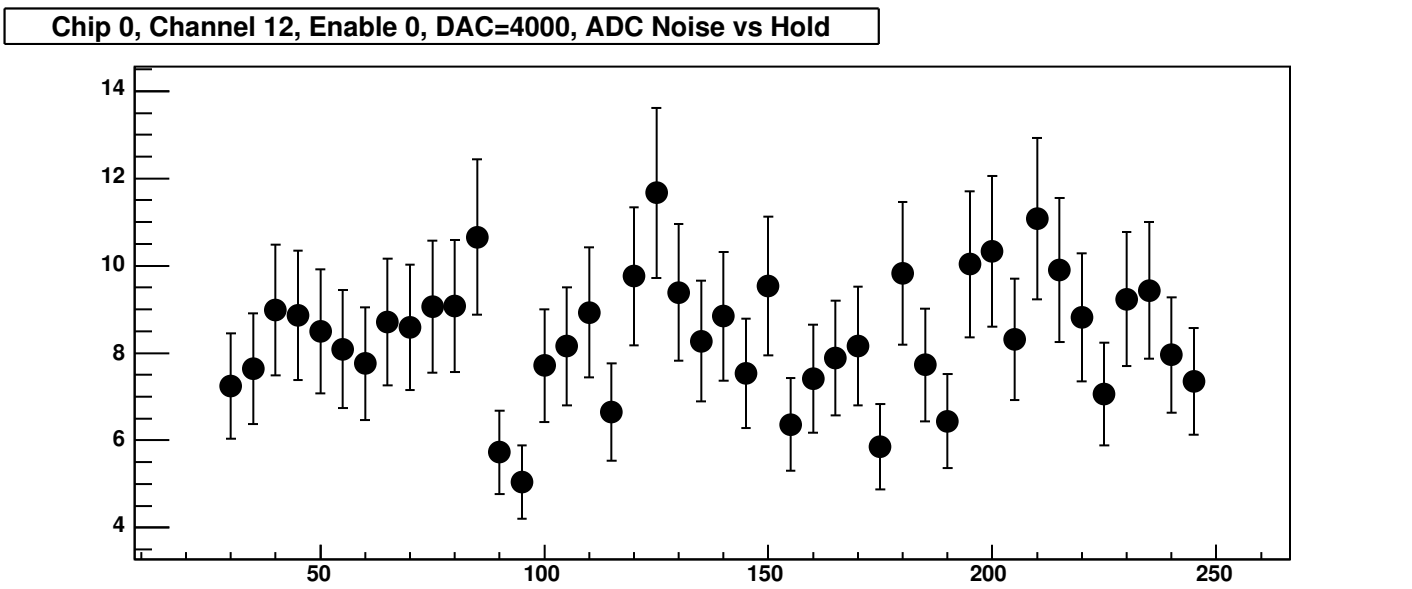

Chip 0, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
50
100
150
200
250

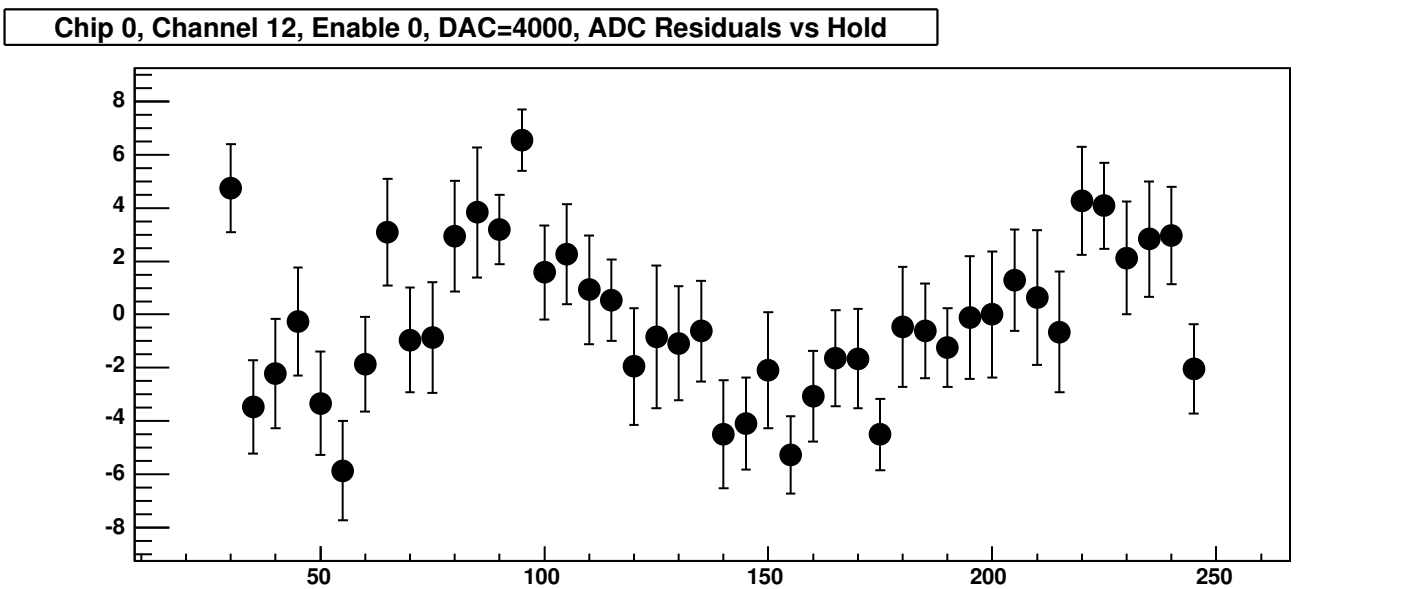

Chip 0, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 65.6 / 31
p0 573.3 ± 3.739
p1 -8.533 ± 1.197
p2 378.5 ± 3.835
p3 31.38 ± 1.078
p4 -0.2678 ± 0.01514
920
900
880
860
840
820
20
40
60
80
100
120
140
160
180
200
220

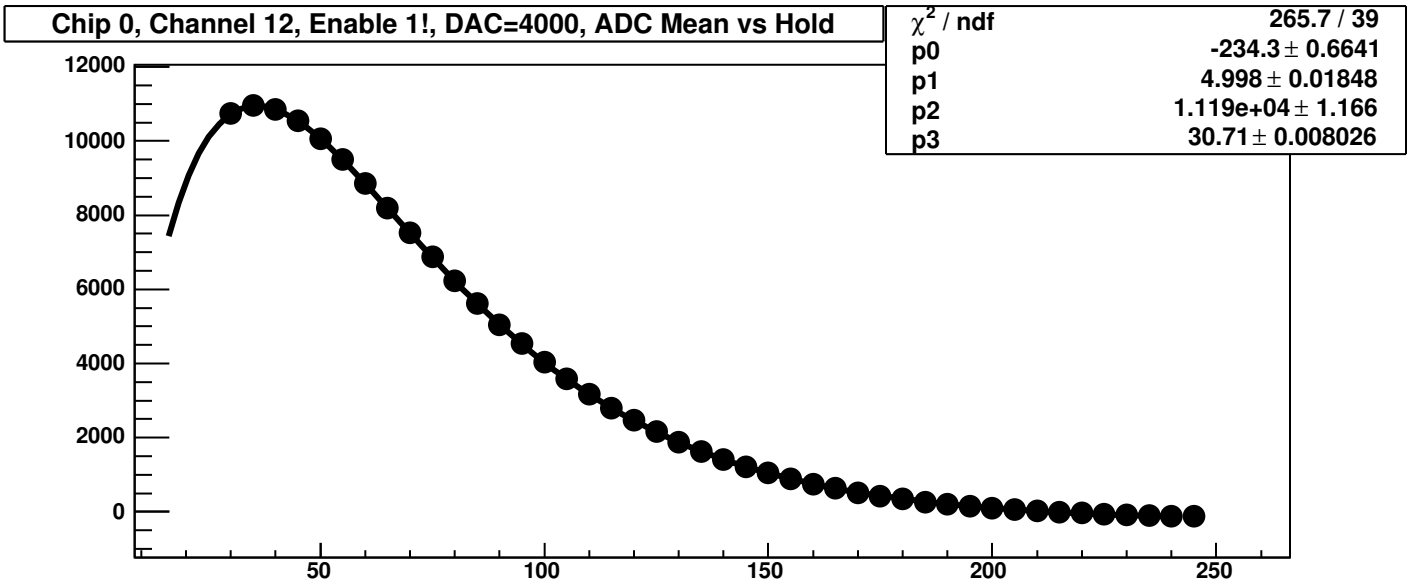

Chip 0, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
20
40
60
80
100
120
140
160
180
200
220

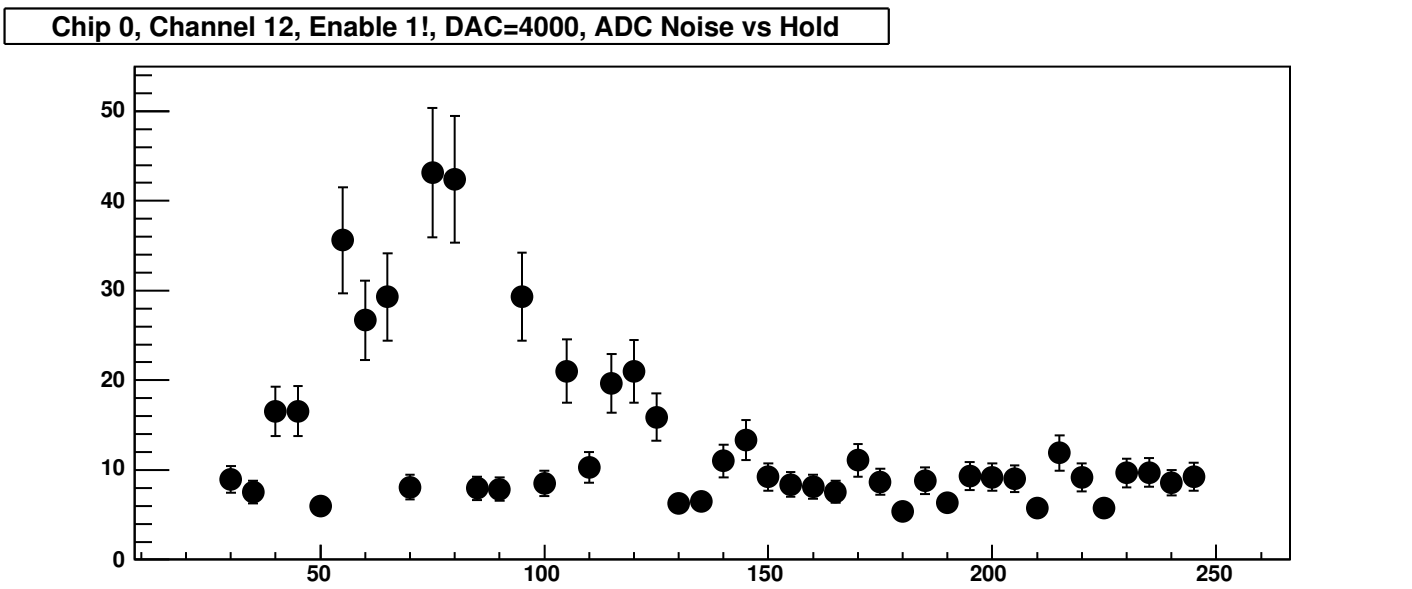

Chip 0, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
20
40
60
80
100
120
140
160
180
200
220

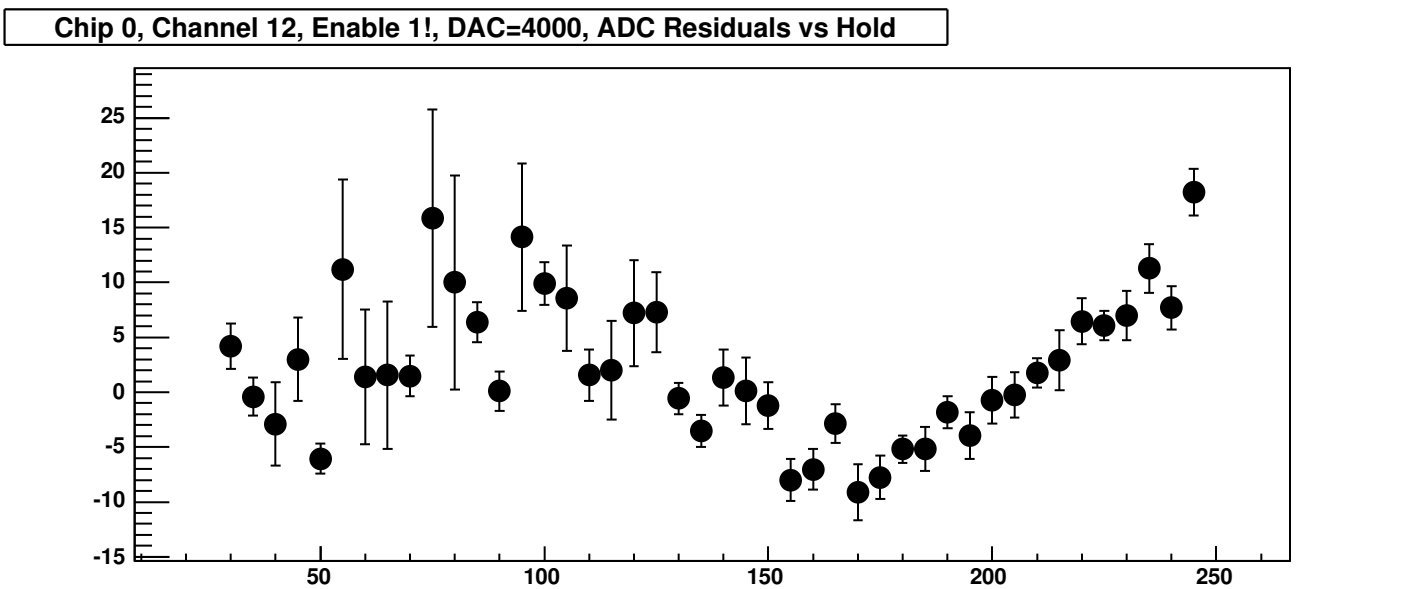

Chip 0, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 134.6 / 38
p0 454.6 ± 3.557
p1 9.352 ± 0.7573
p2 260.4 ± 3.617
p3 22.95 ± 0.7131
p4 -0.1859 ± 0.00882
700
680
660
640
620
600
50
100
150
200
250


Chip 0, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250

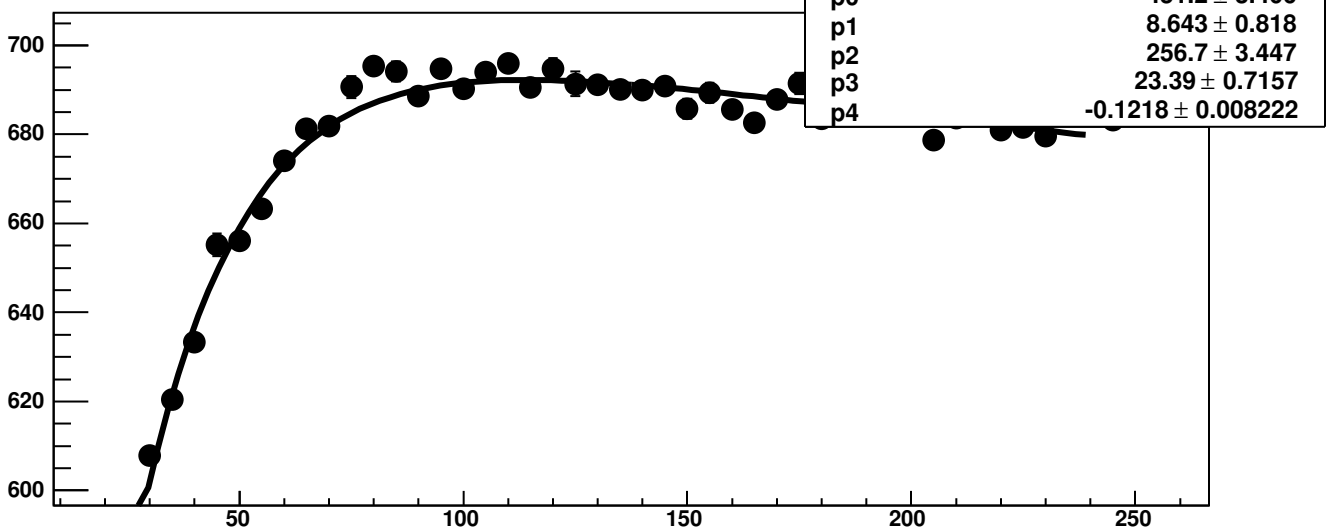

Chip 0, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250


Chip 0, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 265.7 / 39
p0 -234.3 ± 0.6641
p1 4.998 ± 0.01848
p2 1.119e+04 ± 1.166
p3 30.71 ± 0.008026
12000
10000
8000
6000
4000
2000
0
50
100
150
200
250

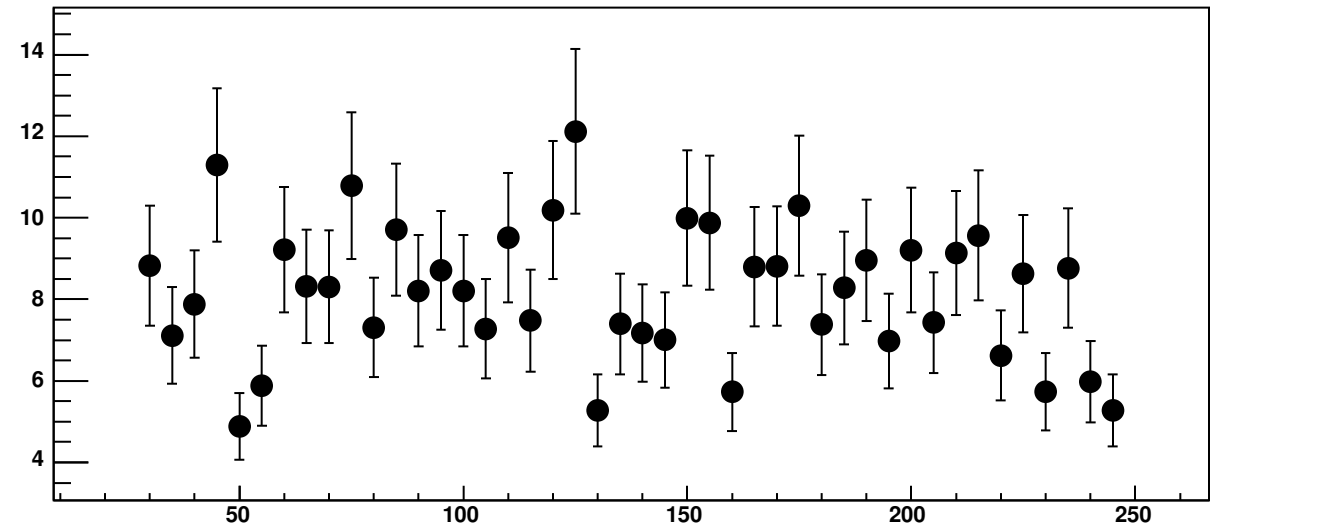

Chip 0, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
0
50
100
150
200
250


Chip 0, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
-15
50
100
150
200
250

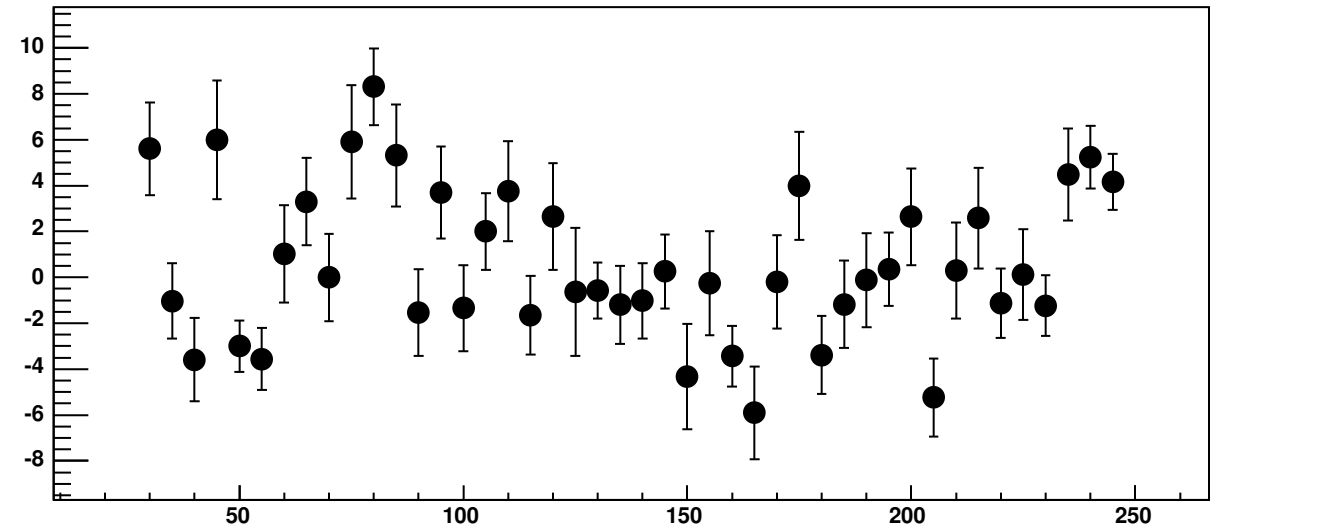
Chip 0, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

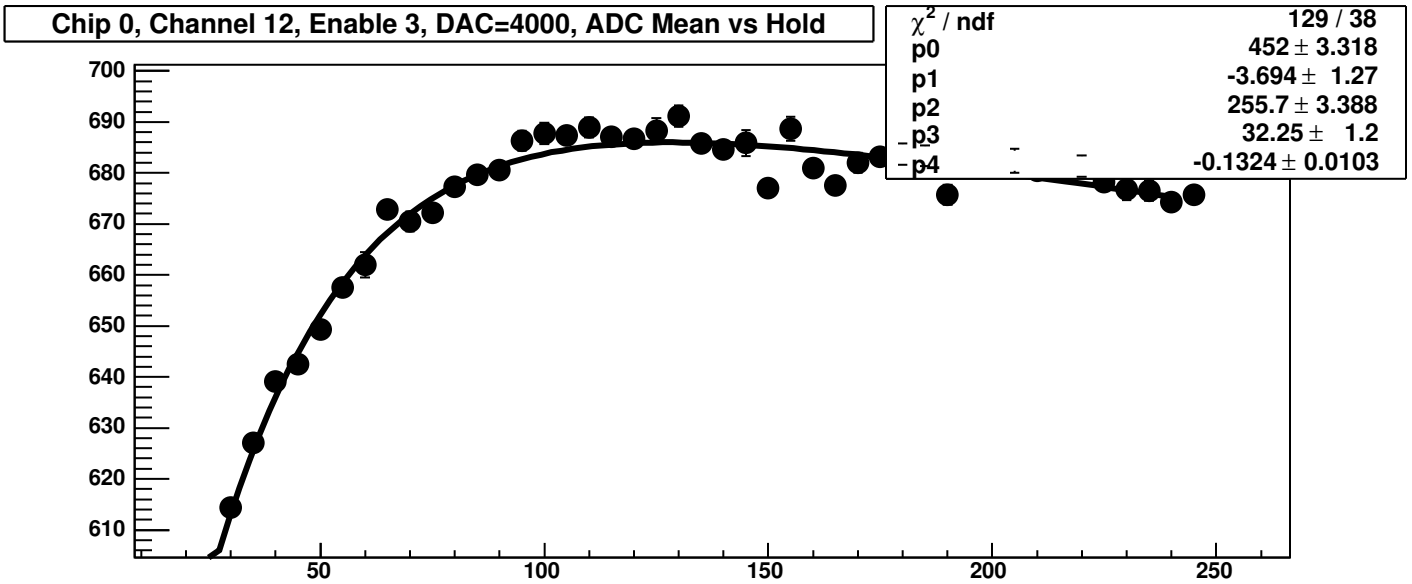


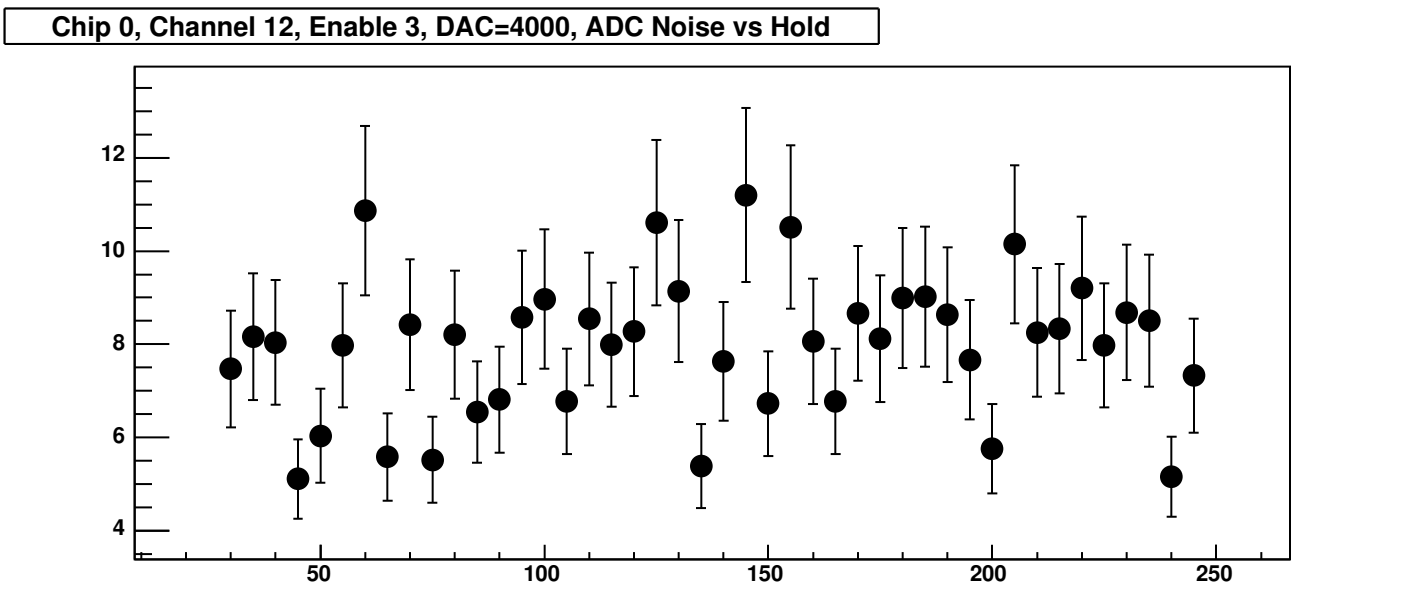
Chip 0, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold

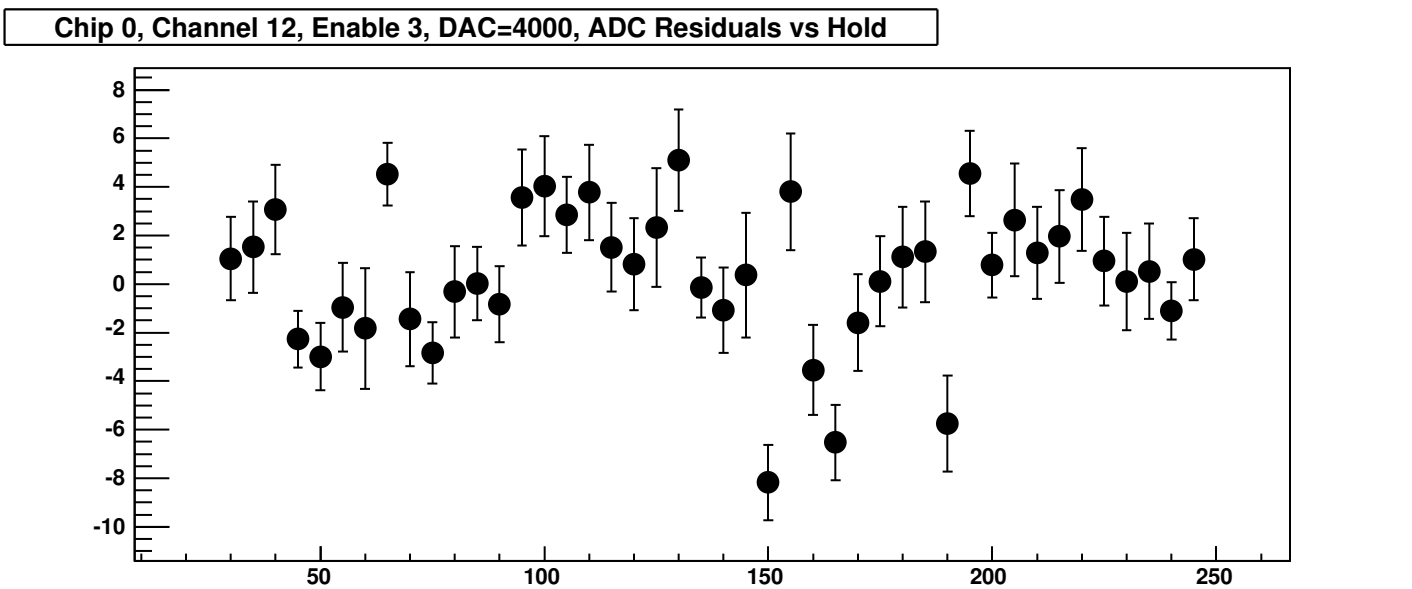


Chip 0, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold

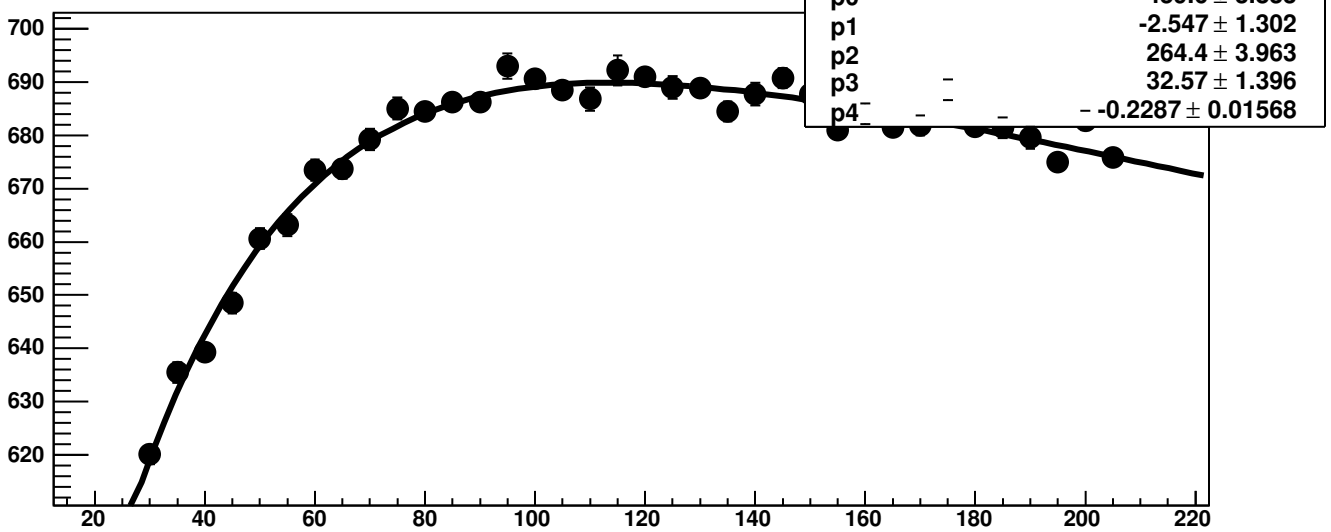



Chip 0, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 129 / 38
p0 452 ± 3.318
p1 -3.694 ± 1.27
p2 255.7 ± 3.388
p3 32.25 ± 1.2
p4 -0.1324 ± 0.0103
700
690
680
670
660
650
640
630
620
610
50
100
150
200
250

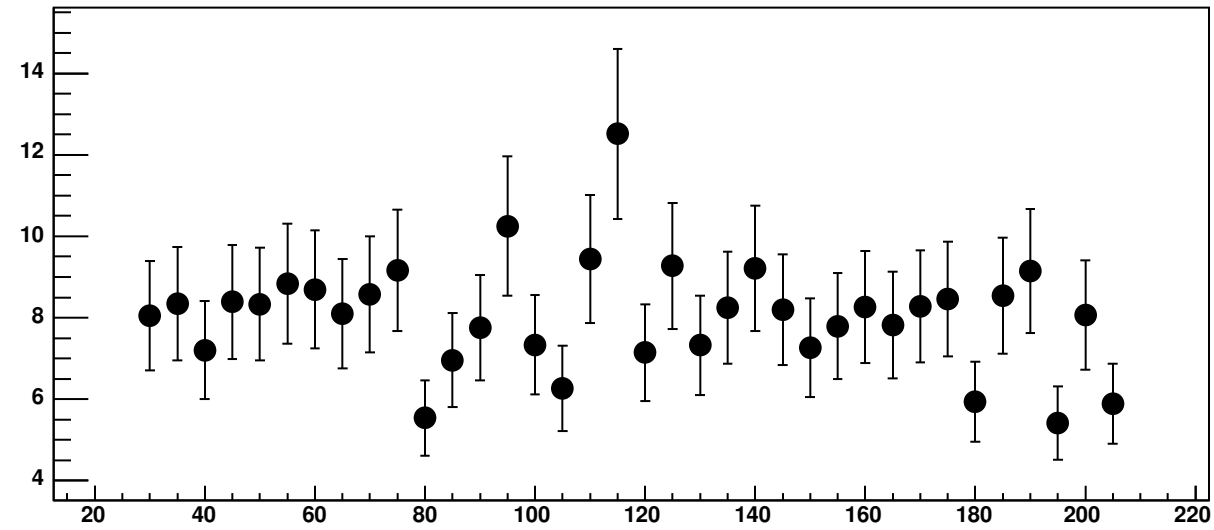

Chip 0, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250


Chip 0, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
50
100
150
200
250

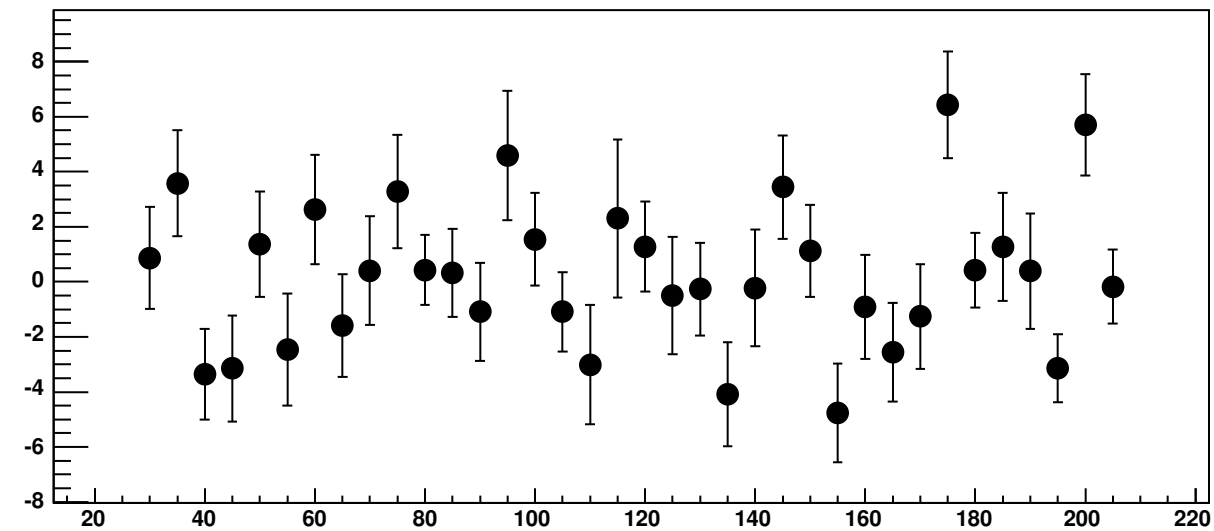
Chip 0, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold



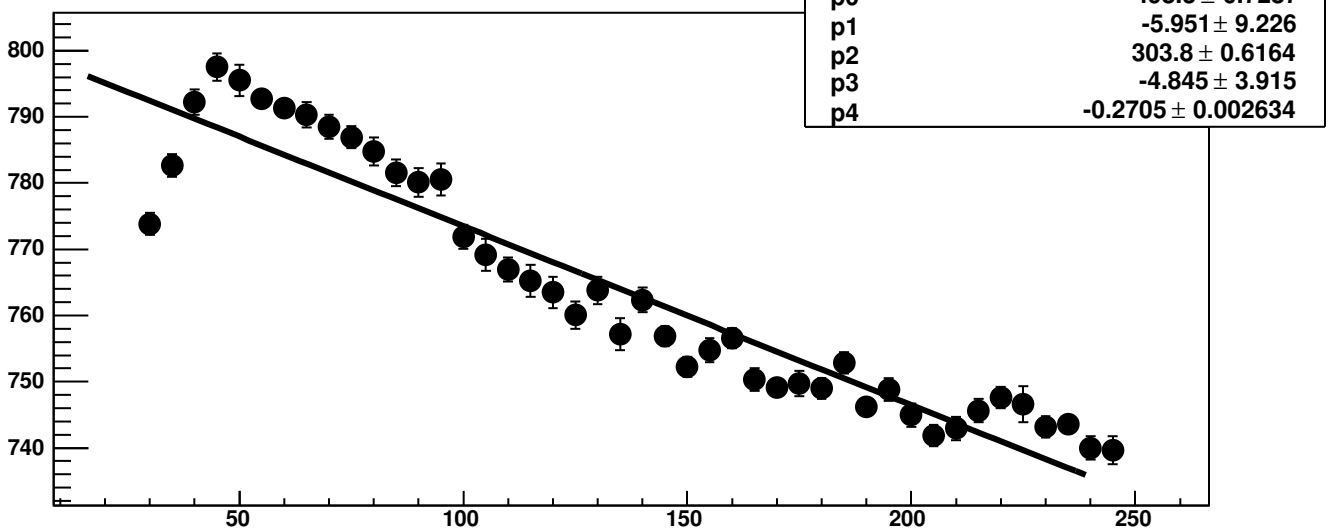
Chip 0, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold



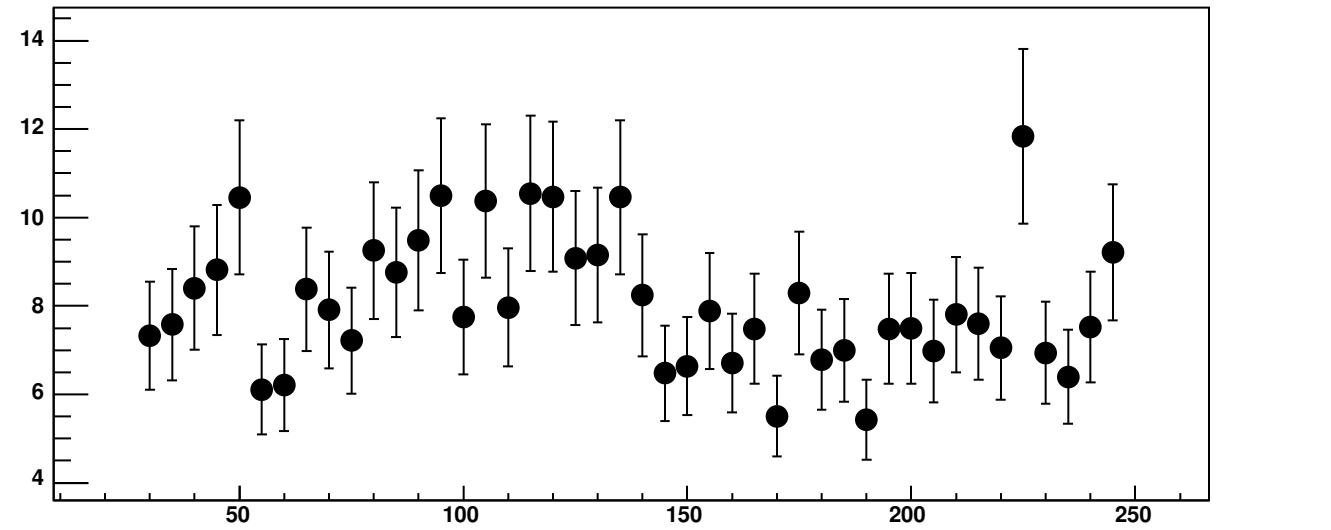
Chip 0, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold



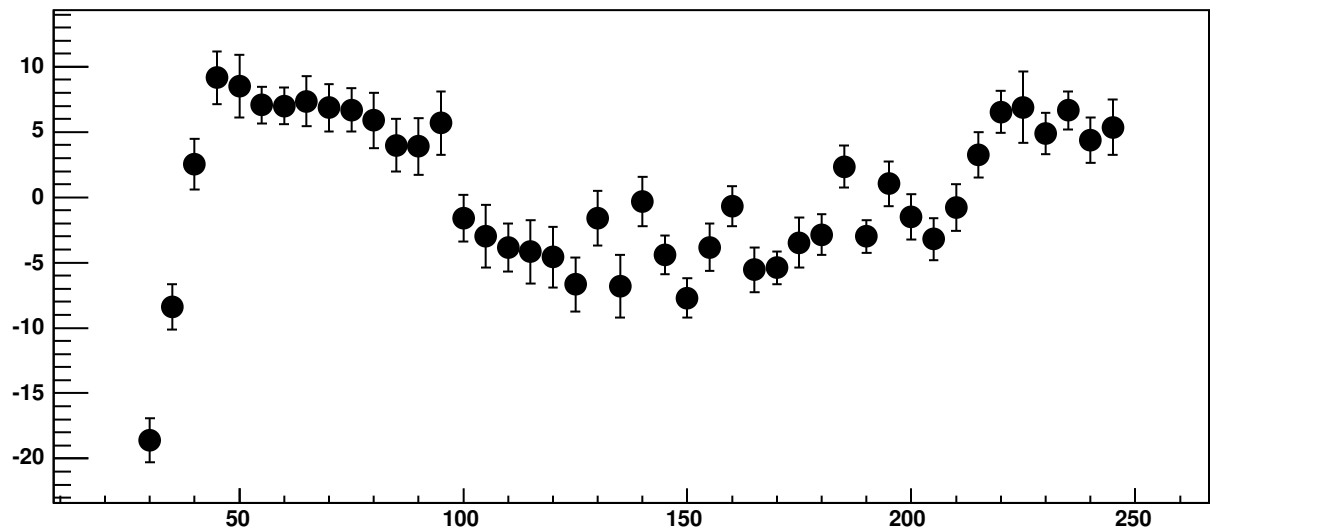
Chip 0, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold



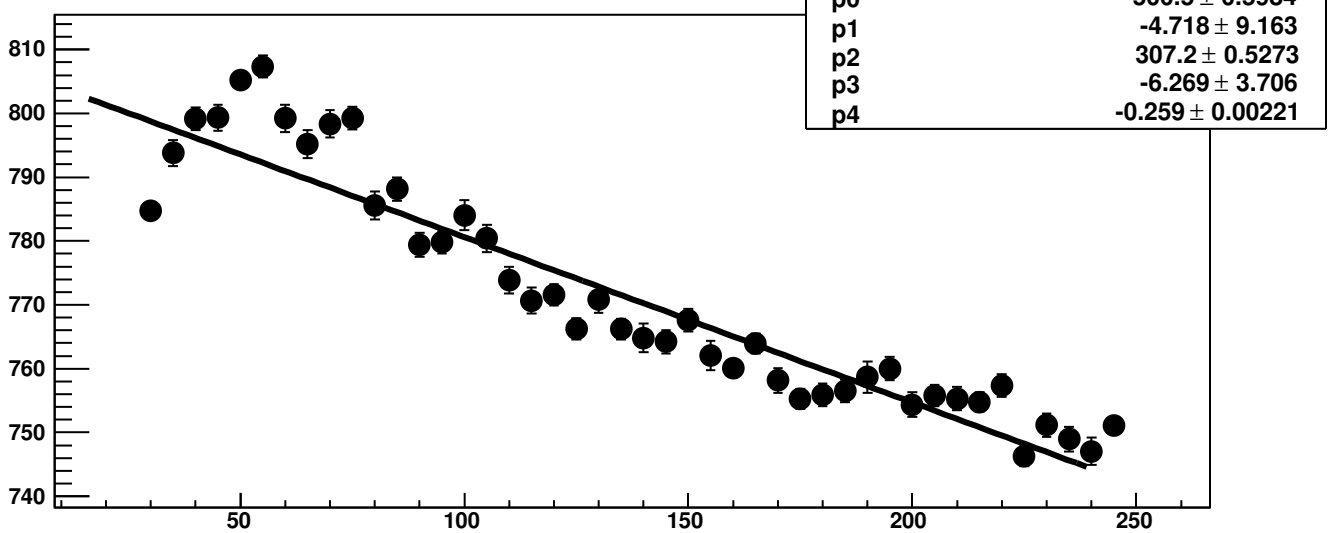
Chip 0, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



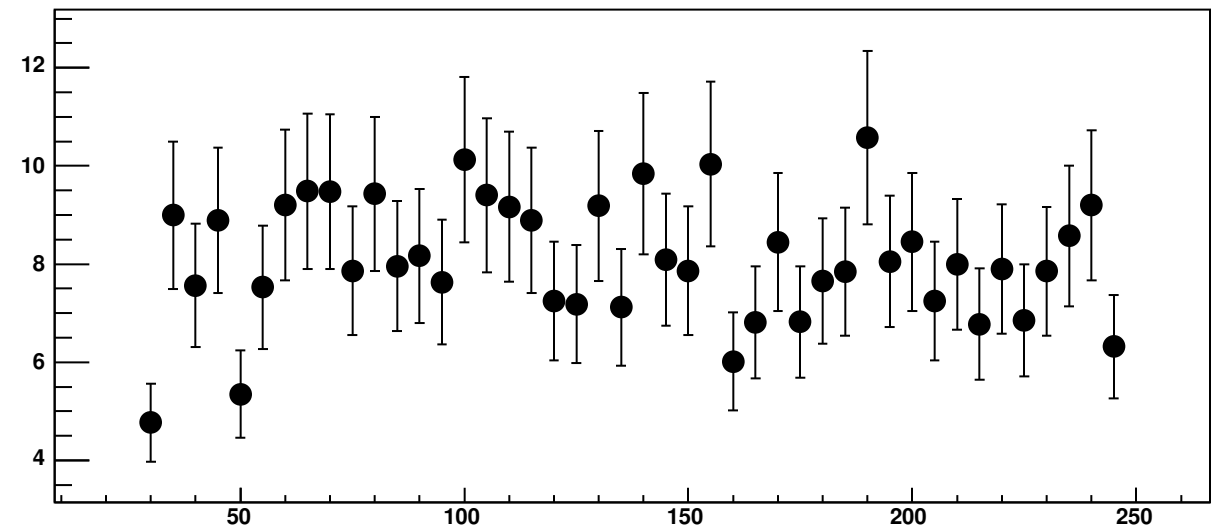
Chip 0, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold



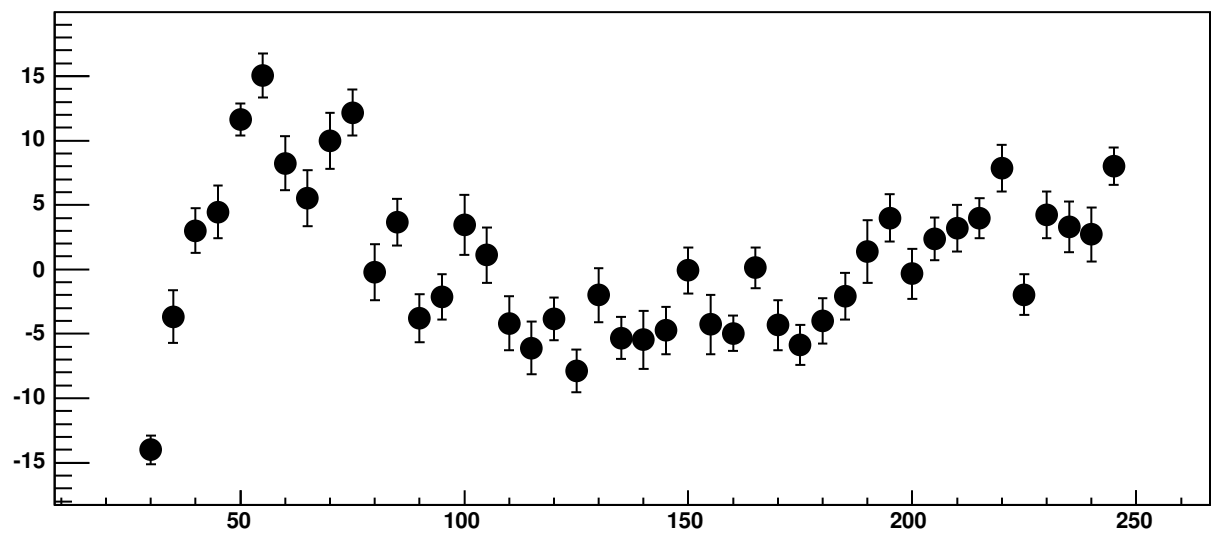
Chip 0, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold



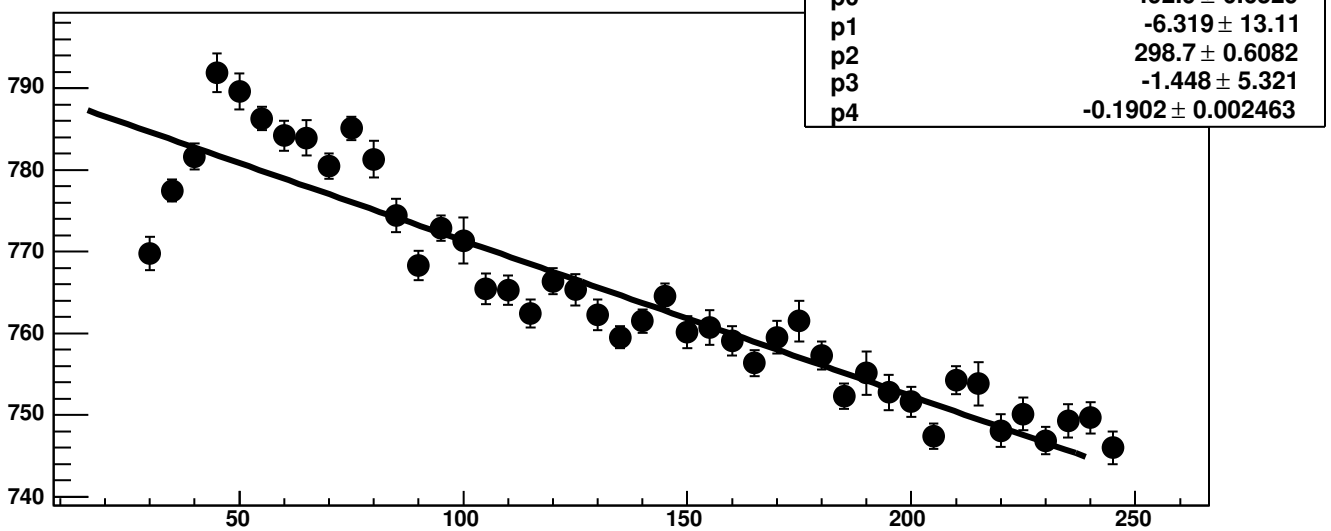
Chip 0, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold



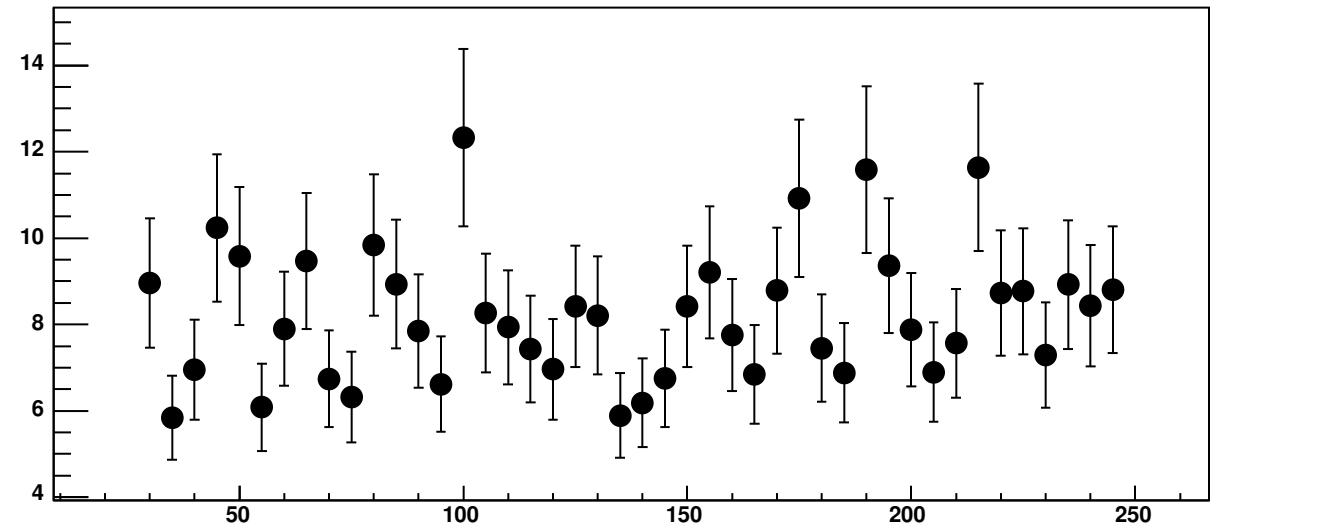
Chip 0, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold



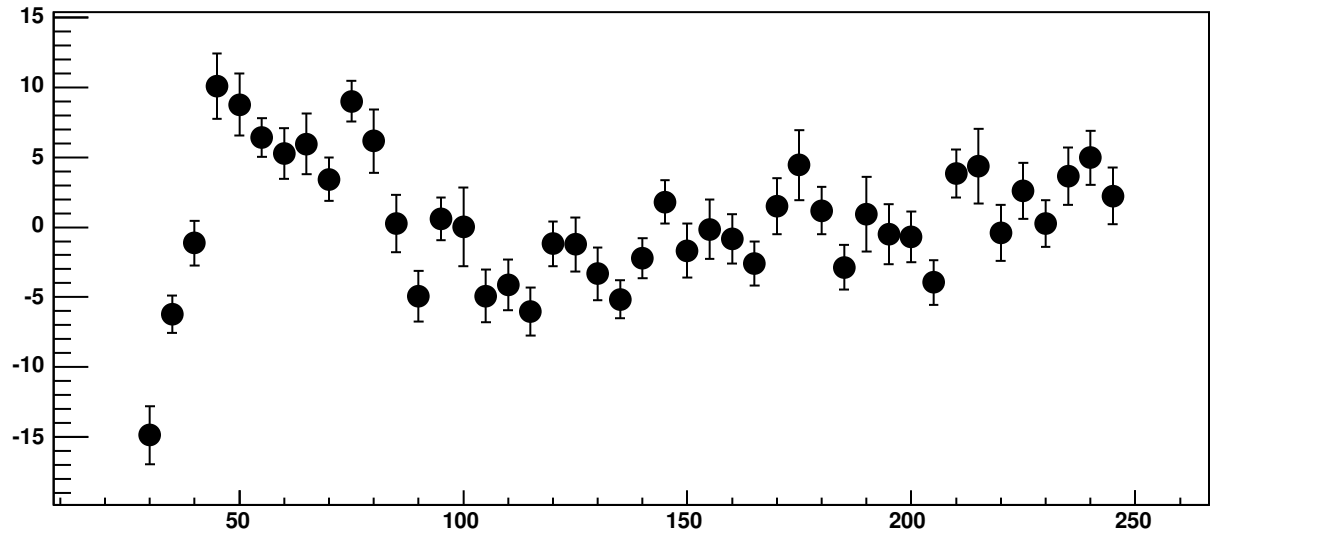
Chip 0, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold



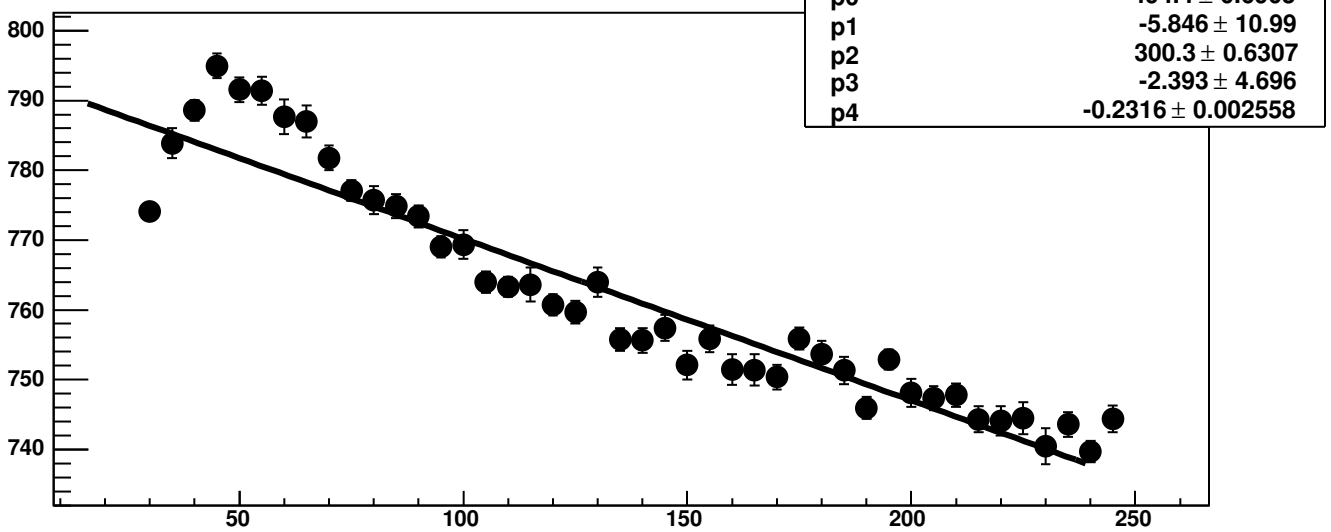
Chip 0, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



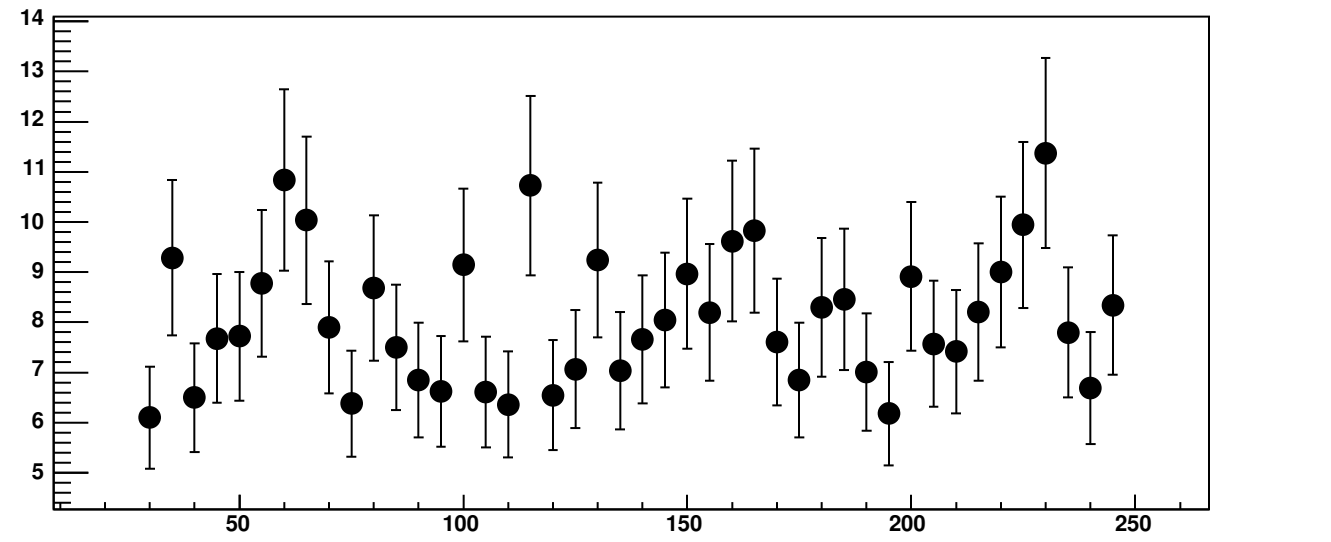
Chip 0, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold



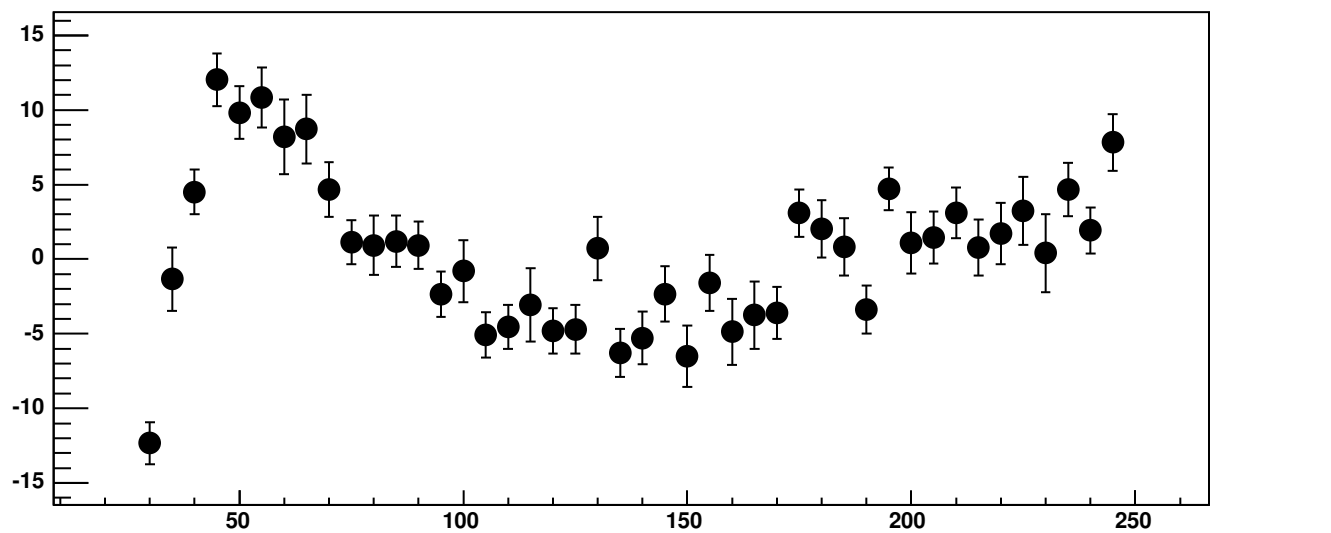
Chip 0, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

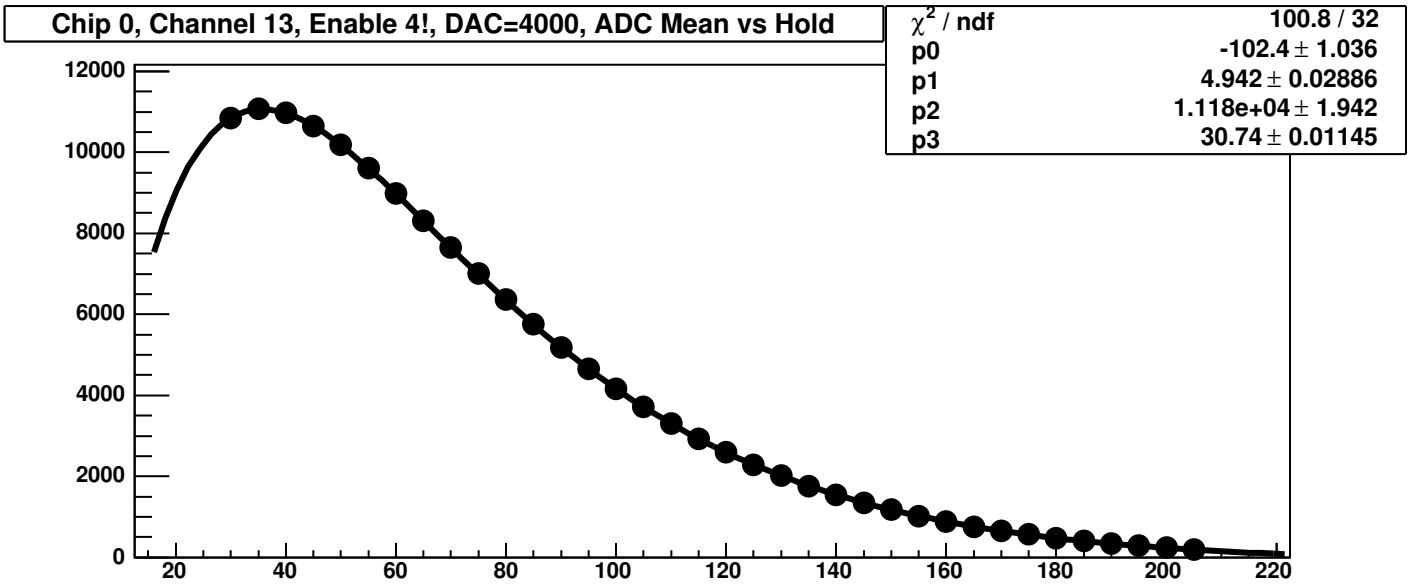


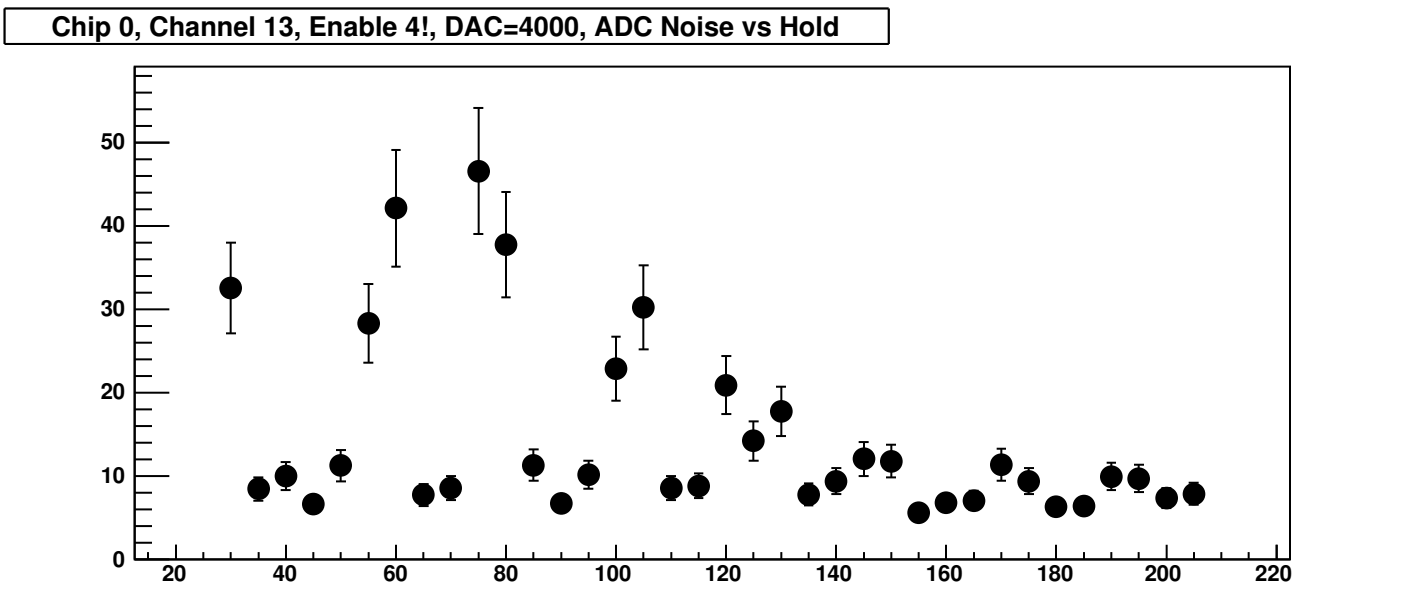
Chip 0, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold

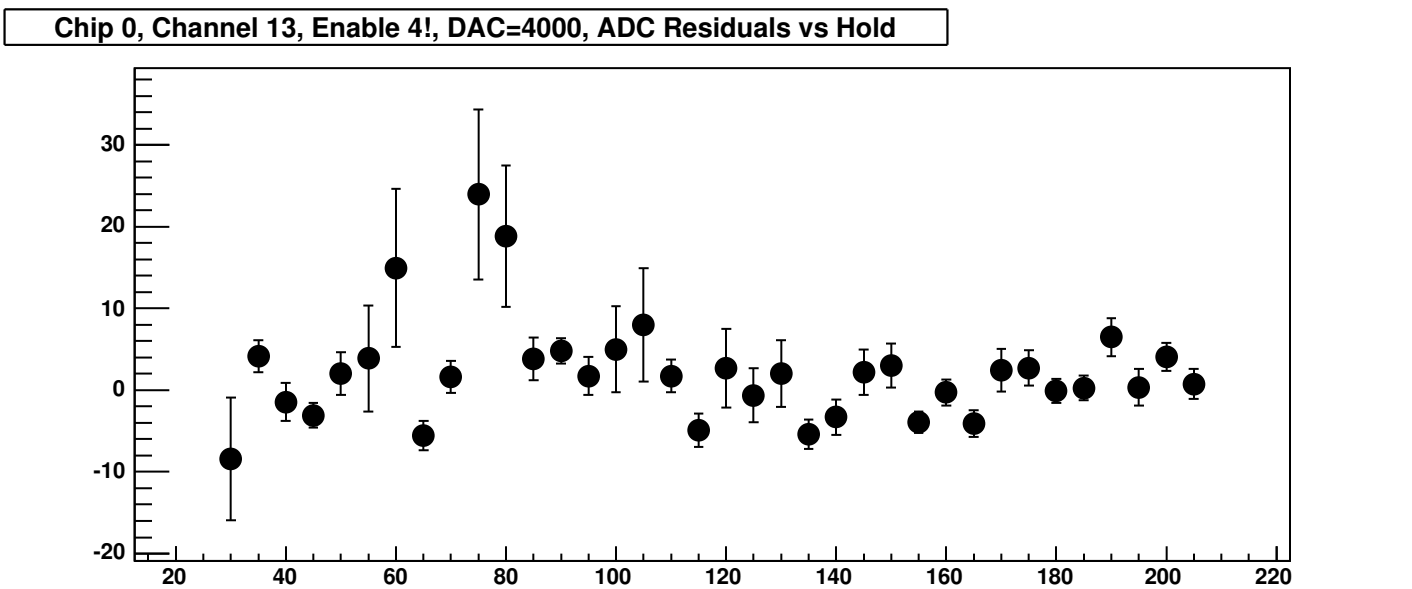


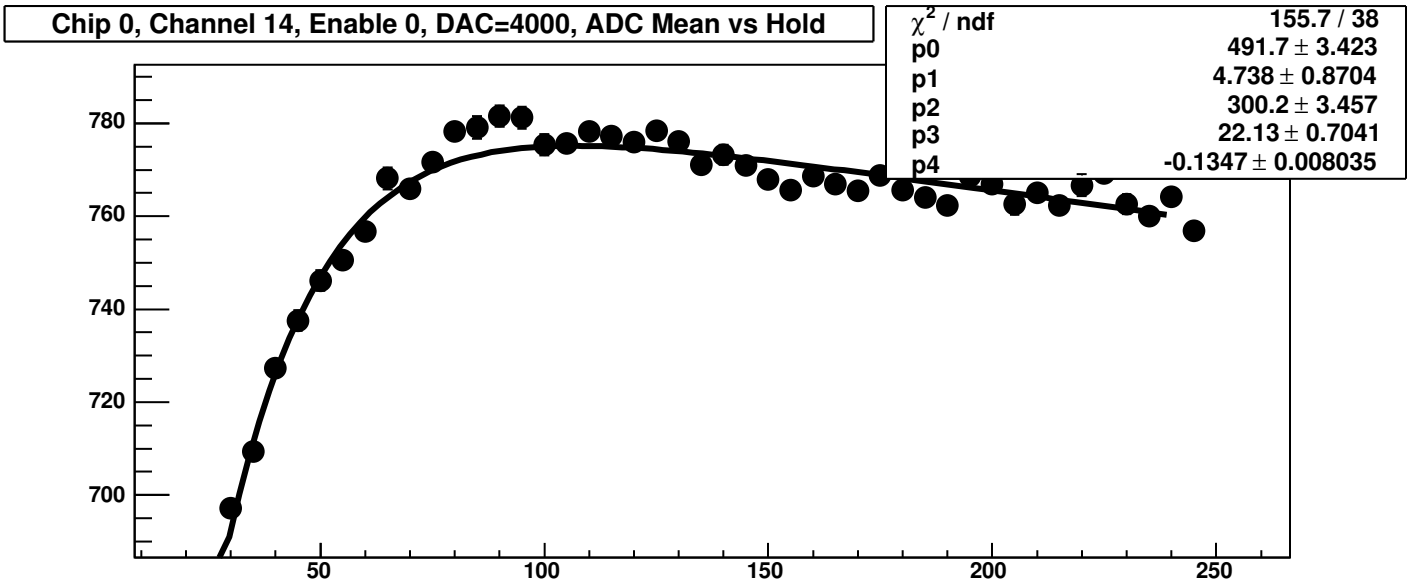
Chip 0, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

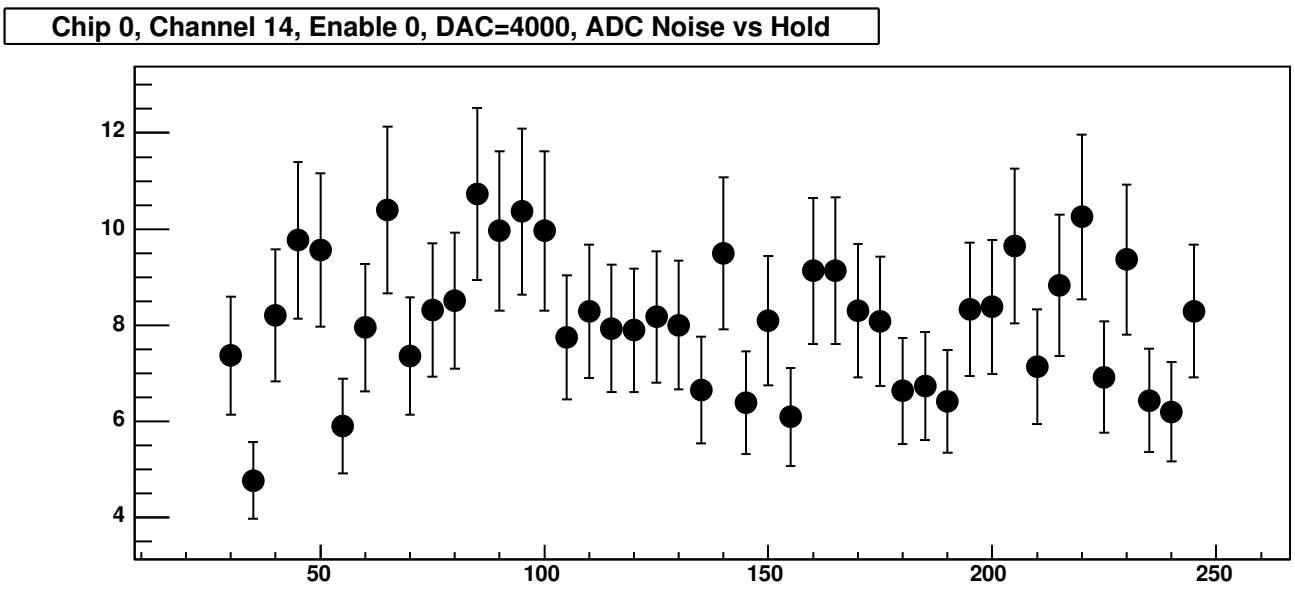


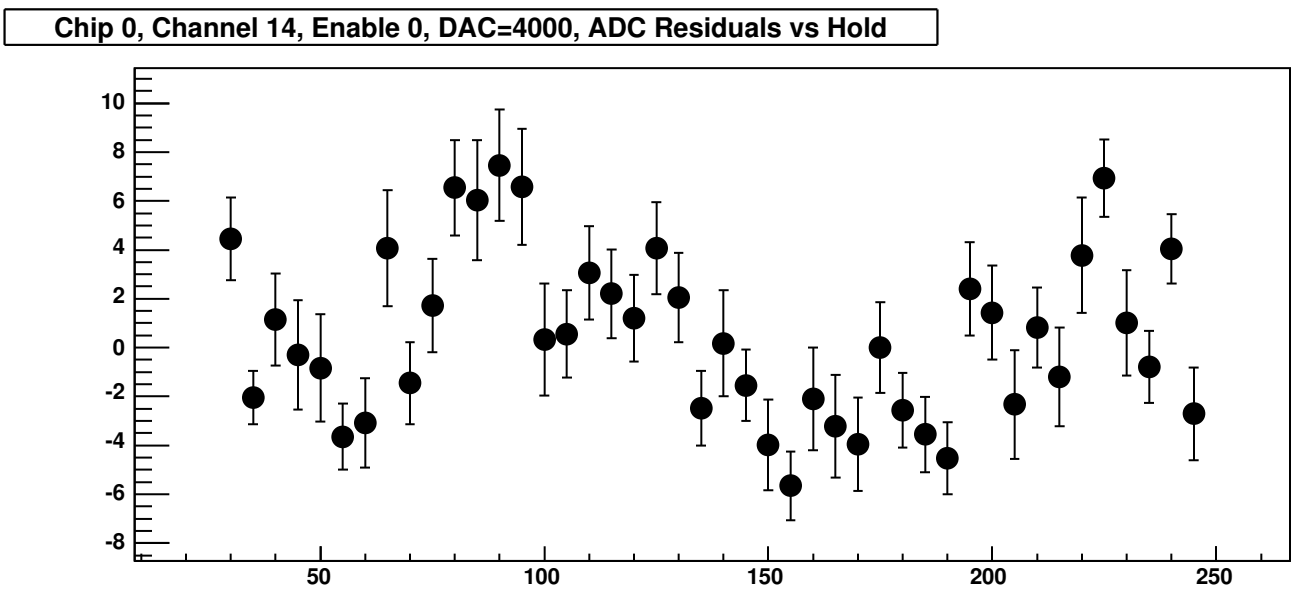

Chip 0, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 100.8 / 32
p0 -102.4 ± 1.036
p1 4.942 ± 0.02886
p2 1.118e+04 ± 1.942
p3 30.74 ± 0.01145
12000
10000
8000
6000
4000
2000
0
20
40
60
80
100
120
140
160
180
200
220

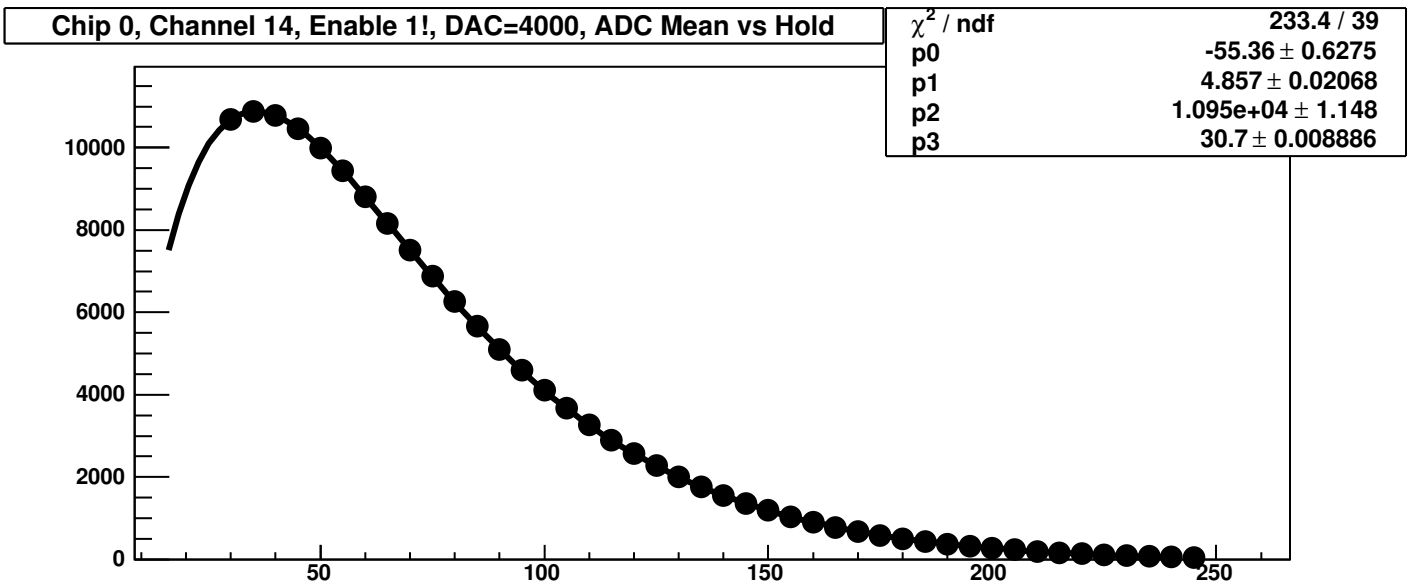

Chip 0, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
0
20
40
60
80
100
120
140
160
180
200
220

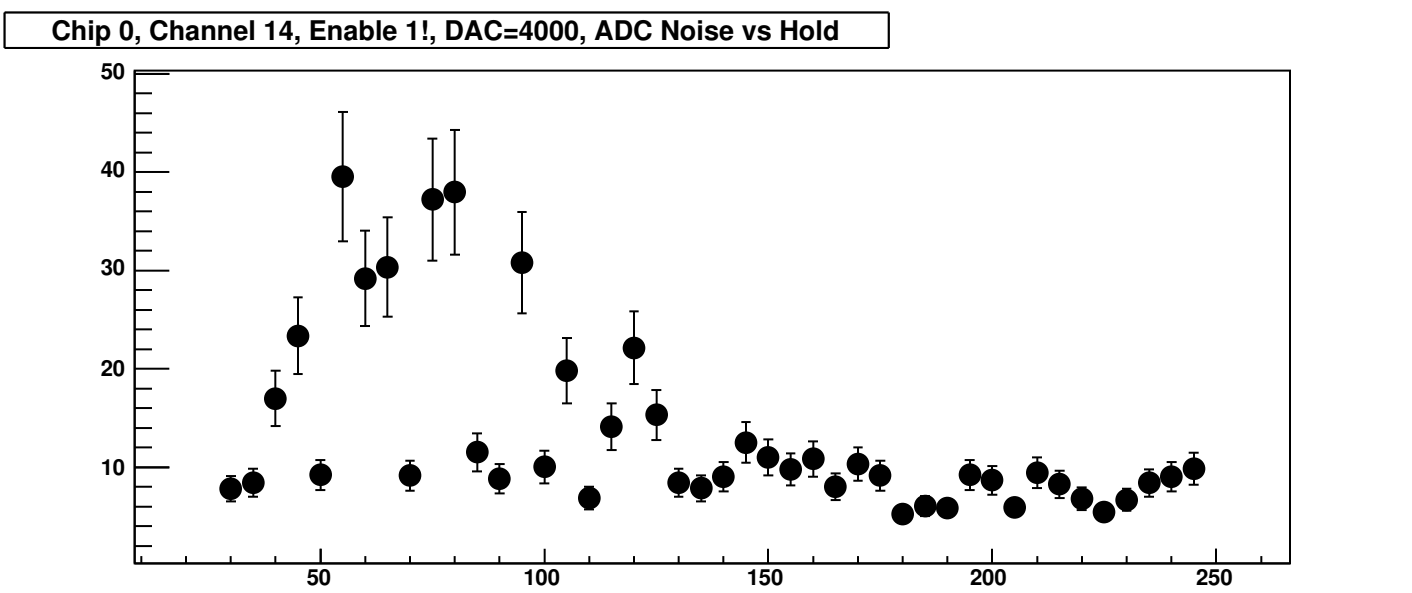

Chip 0, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
-20
20
40
60
80
100
120
140
160
180
200
220

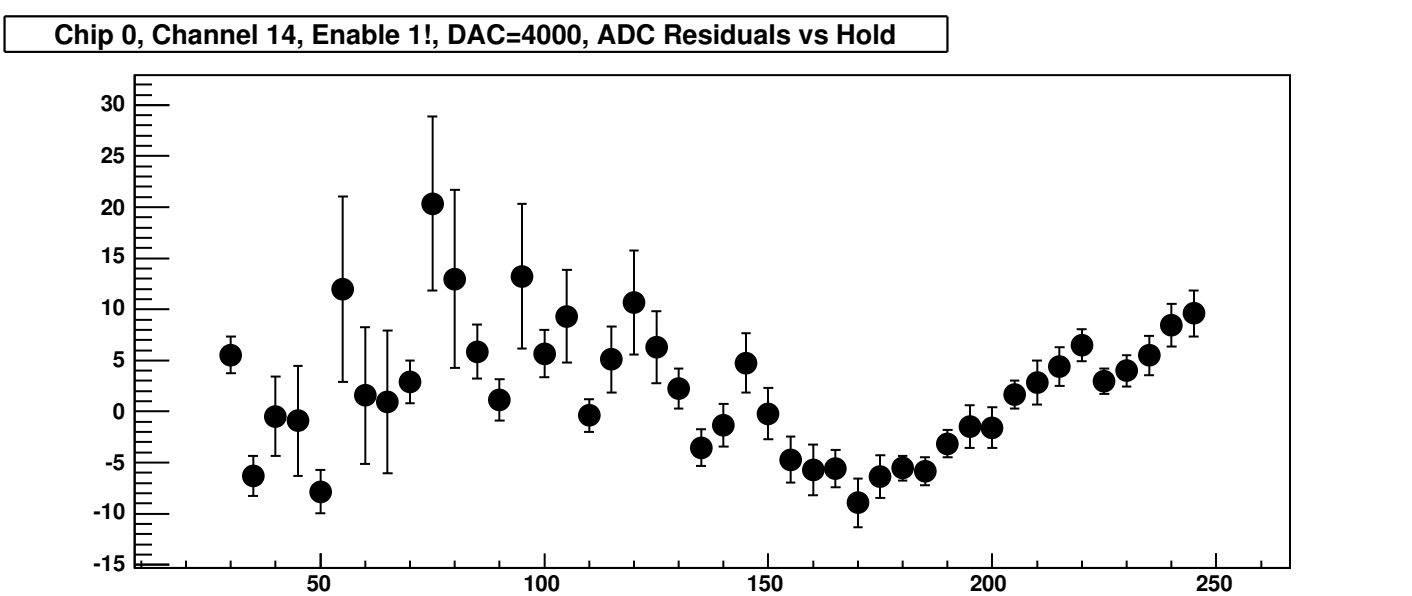

Chip 0, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 155.7 / 38
p0 491.7 ± 3.423
p1 4.738 ± 0.8704
p2 300.2 ± 3.457
p3 22.13 ± 0.7041
p4 -0.1347 ± 0.008035
780
760
740
720
700
50
100
150
200
250

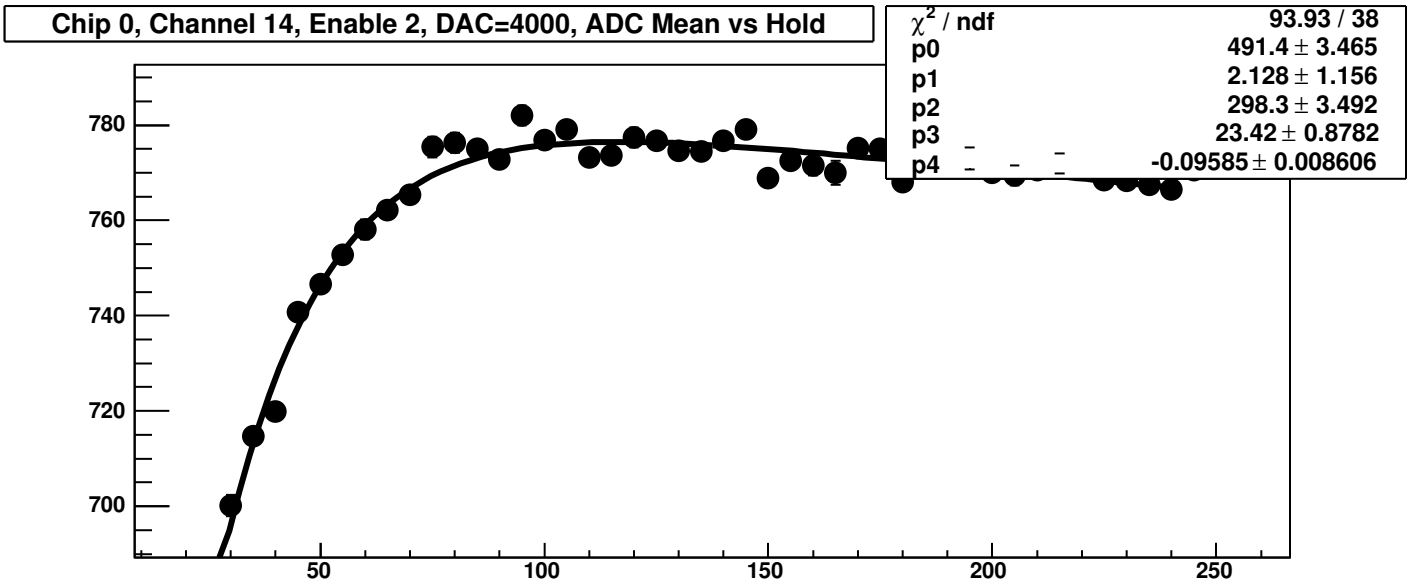

Chip 0, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250

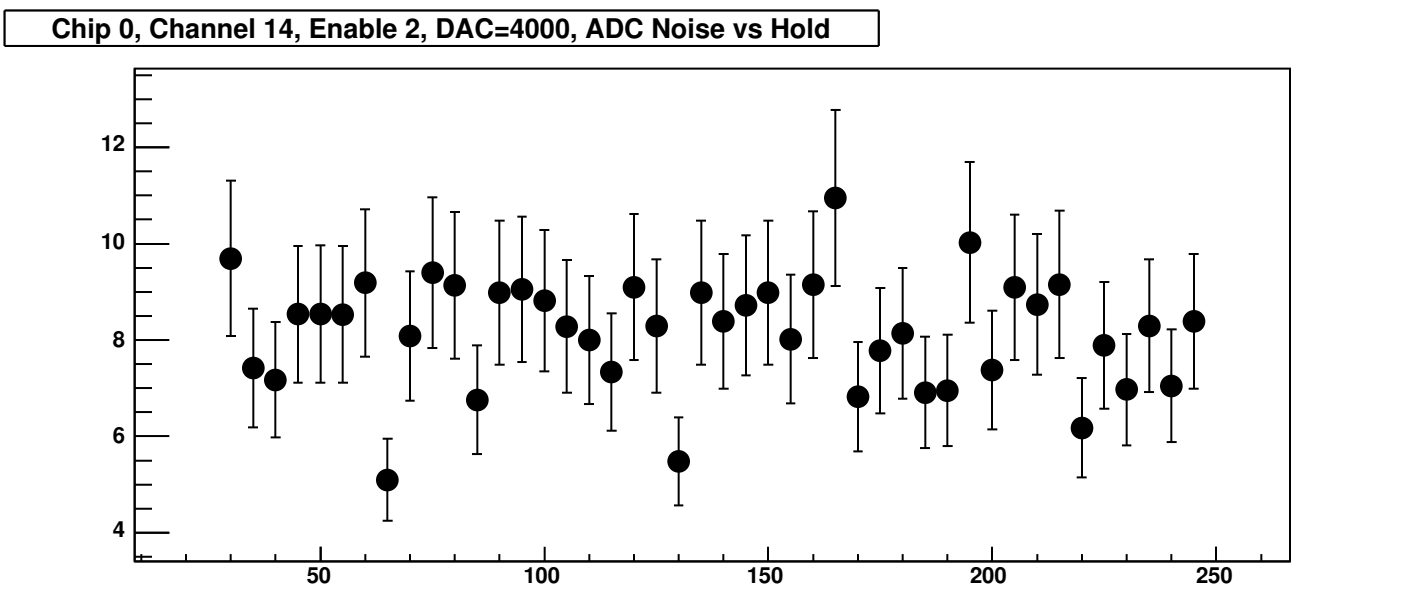

Chip 0, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

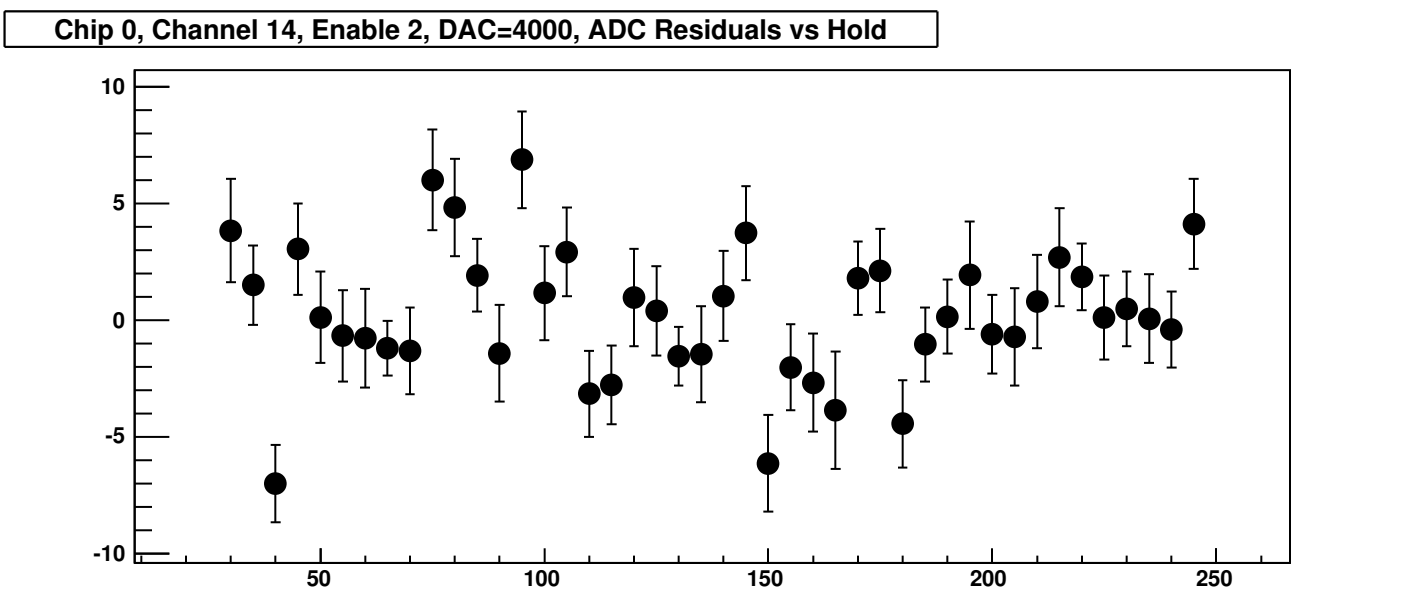

Chip 0, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 233.4 / 39
p0 -55.36 ± 0.6275
p1 4.857 ± 0.02068
p2 1.095e+04 ± 1.148
p3 30.7 ± 0.008886
10000
8000
6000
4000
2000
0
50
100
150
200
250

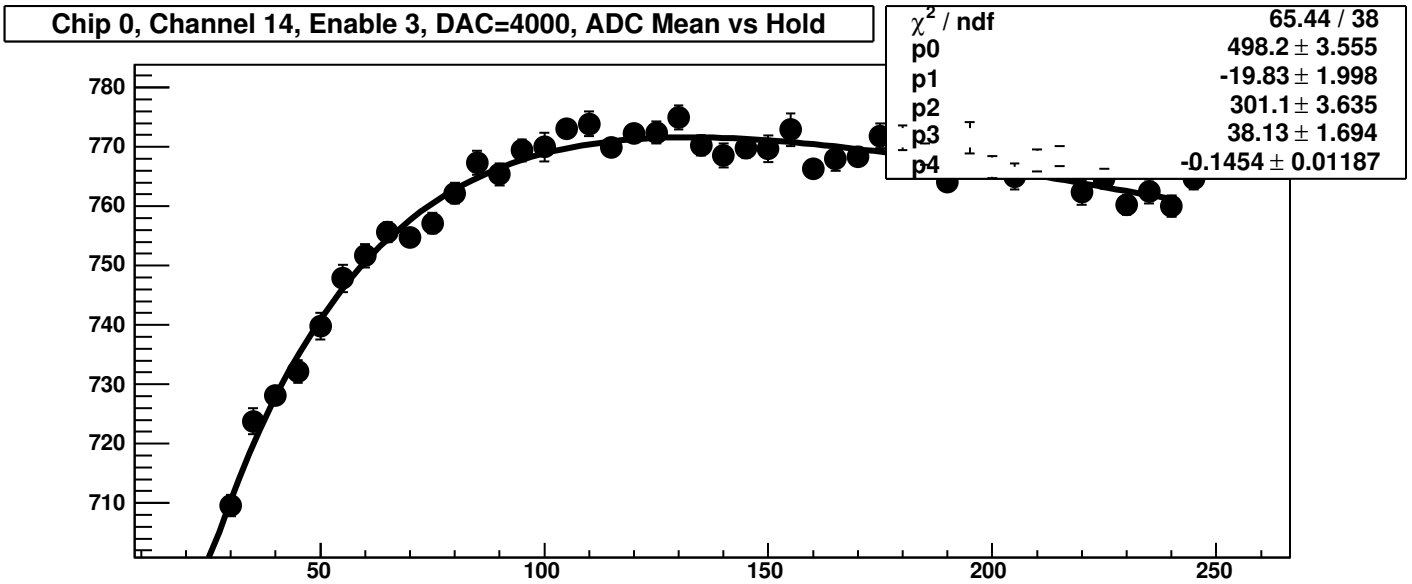

Chip 0, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
50
100
150
200
250

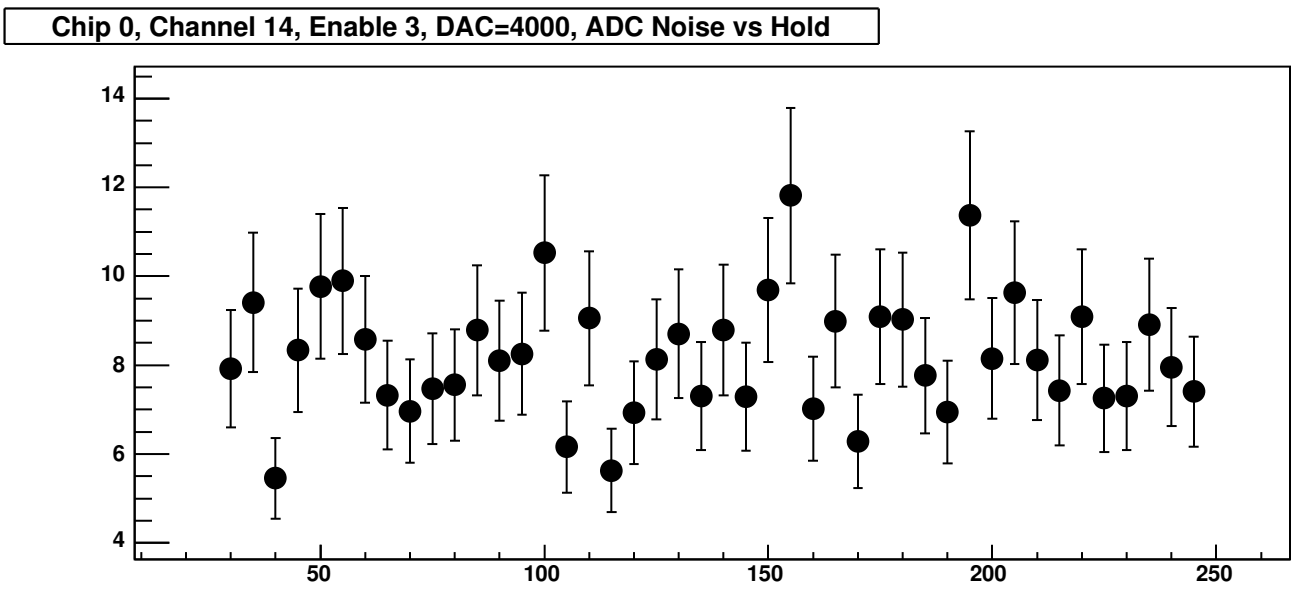

Chip 0, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
-15
50
100
150
200
250

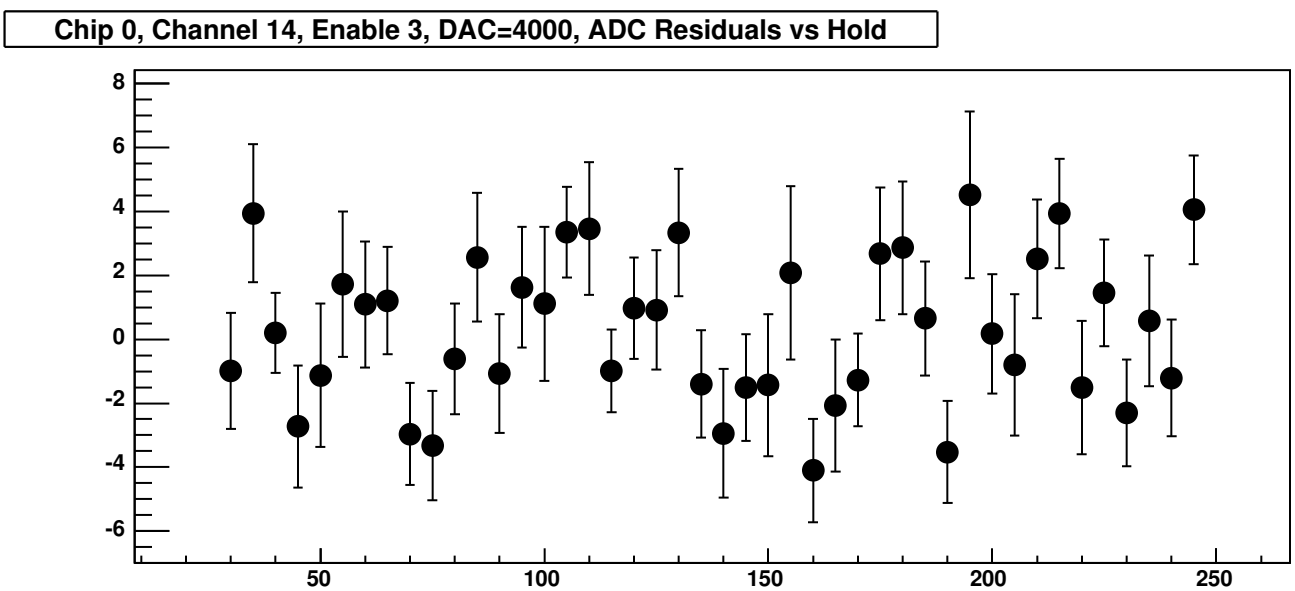

Chip 0, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 93.93 / 38
p0 491.4 ± 3.465
p1 2.128 ± 1.156
p2 298.3 ± 3.492
p3 23.42 ± 0.8782
p4 -0.09585 ± 0.008606
780
760
740
720
700
50
100
150
200
250

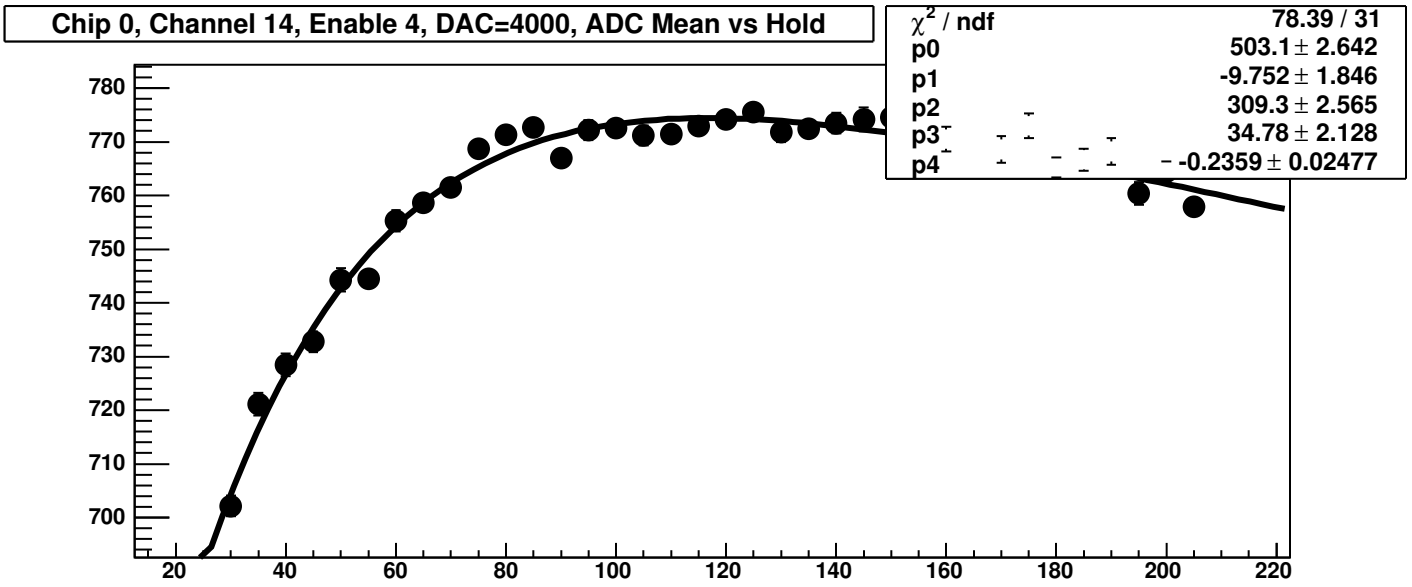

Chip 0, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250

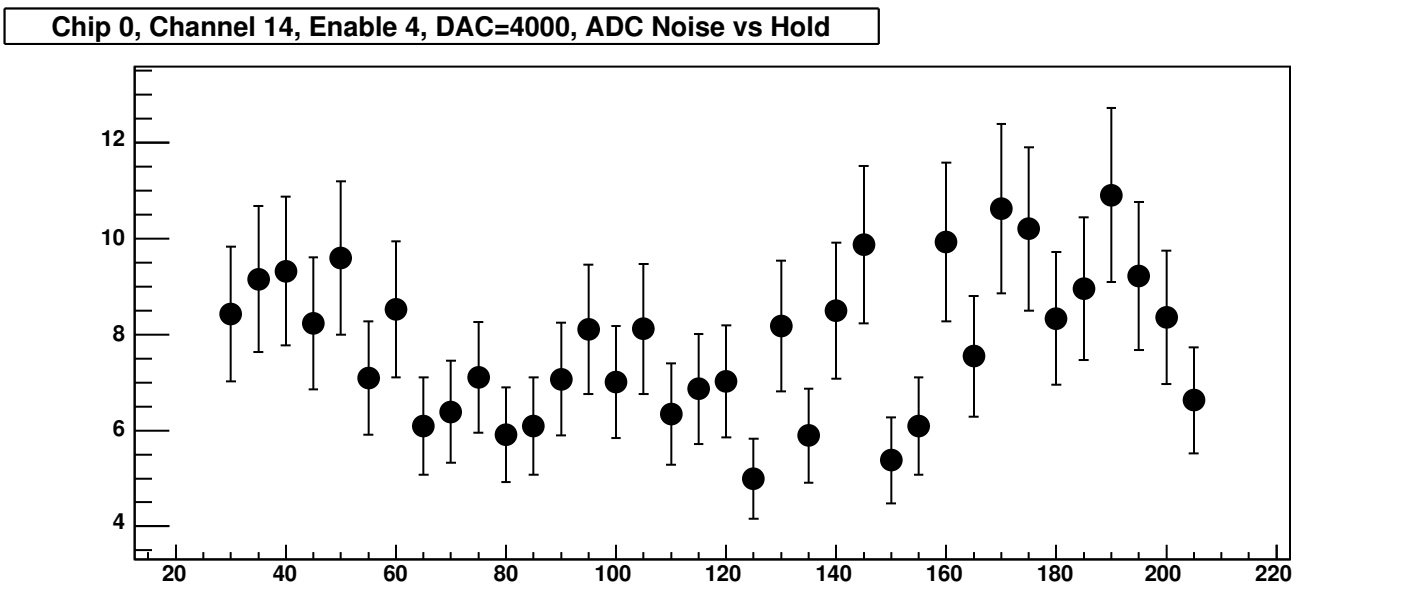

Chip 0, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

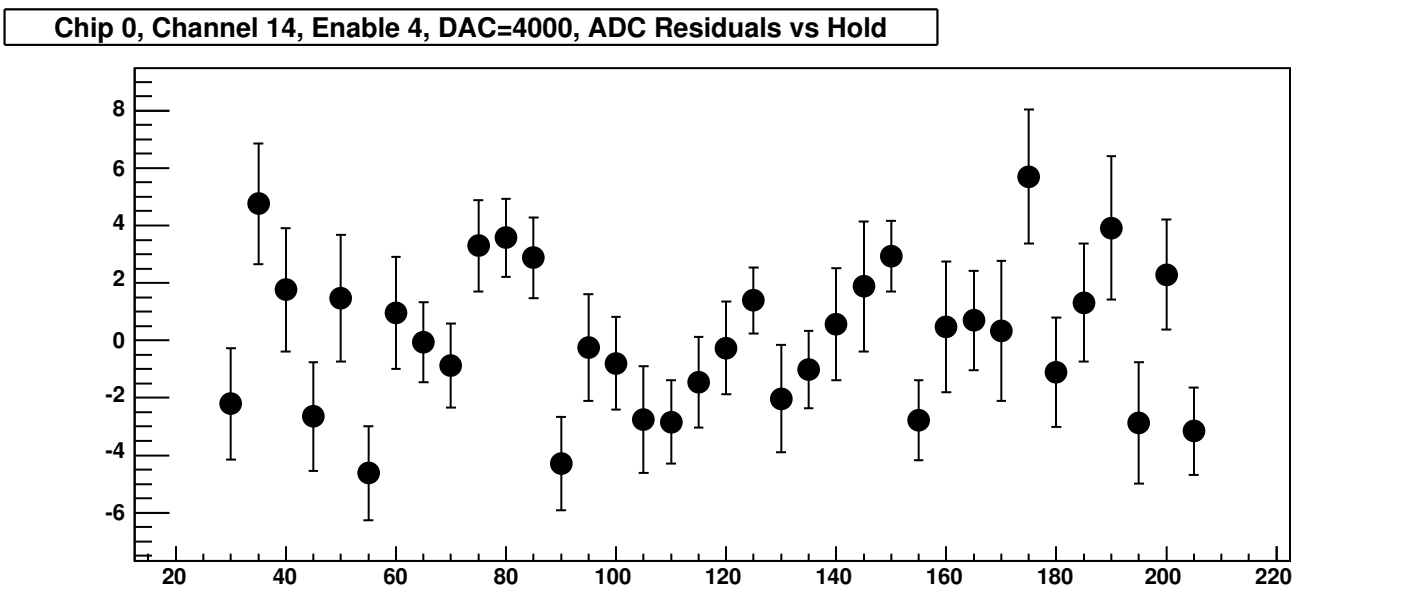

Chip 0, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 65.44 / 38
p0 498.2 ± 3.555
p1 -19.83 ± 1.998
p2 301.1 ± 3.635
p3 38.13 ± 1.694
p4 -0.1454 ± 0.01187
780
770
760
750
740
730
720
710
50
100
150
200
250

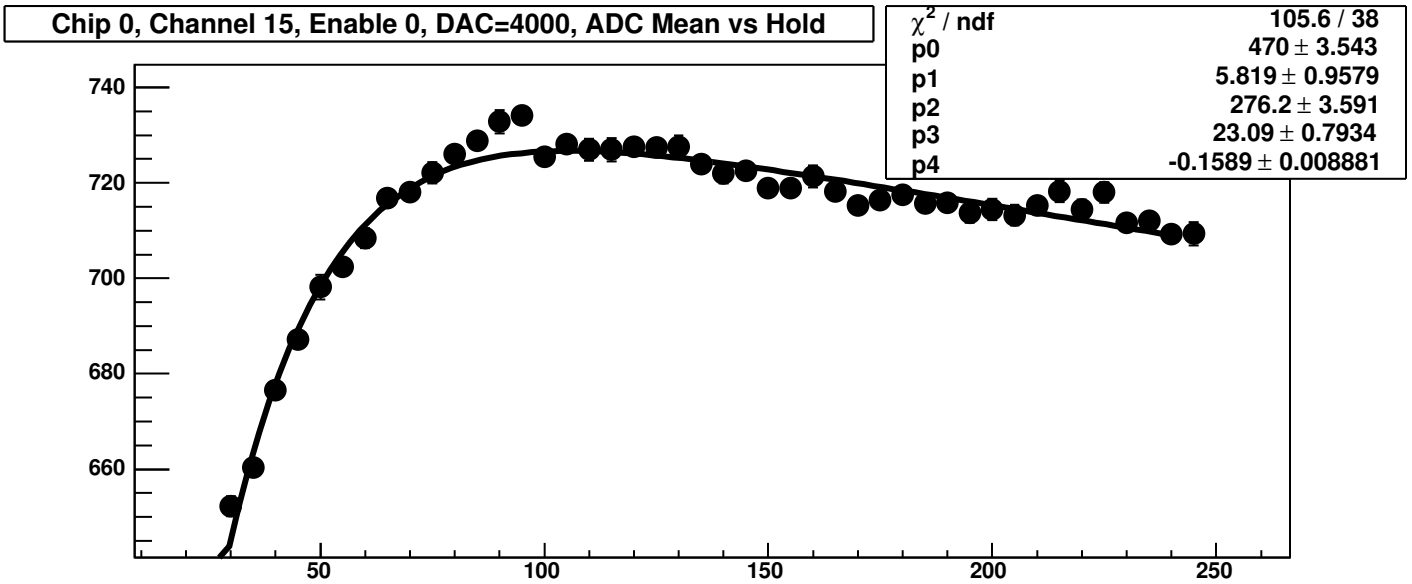

Chip 0, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250

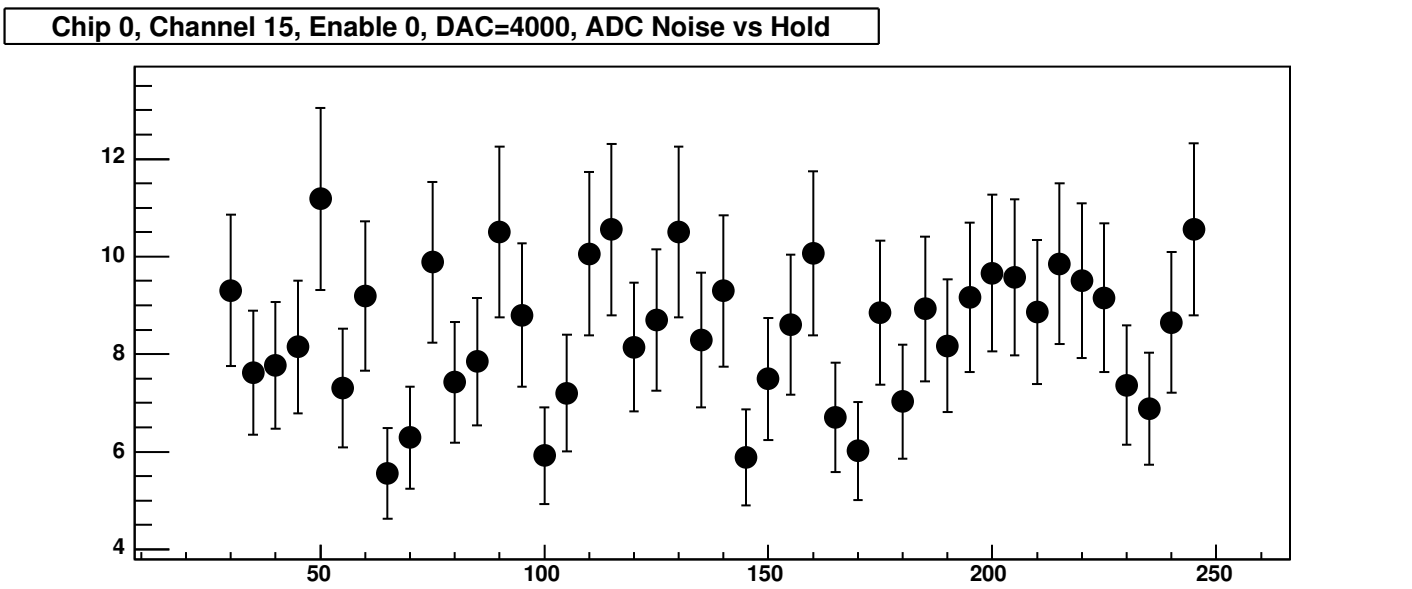

Chip 0, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
50
100
150
200
250

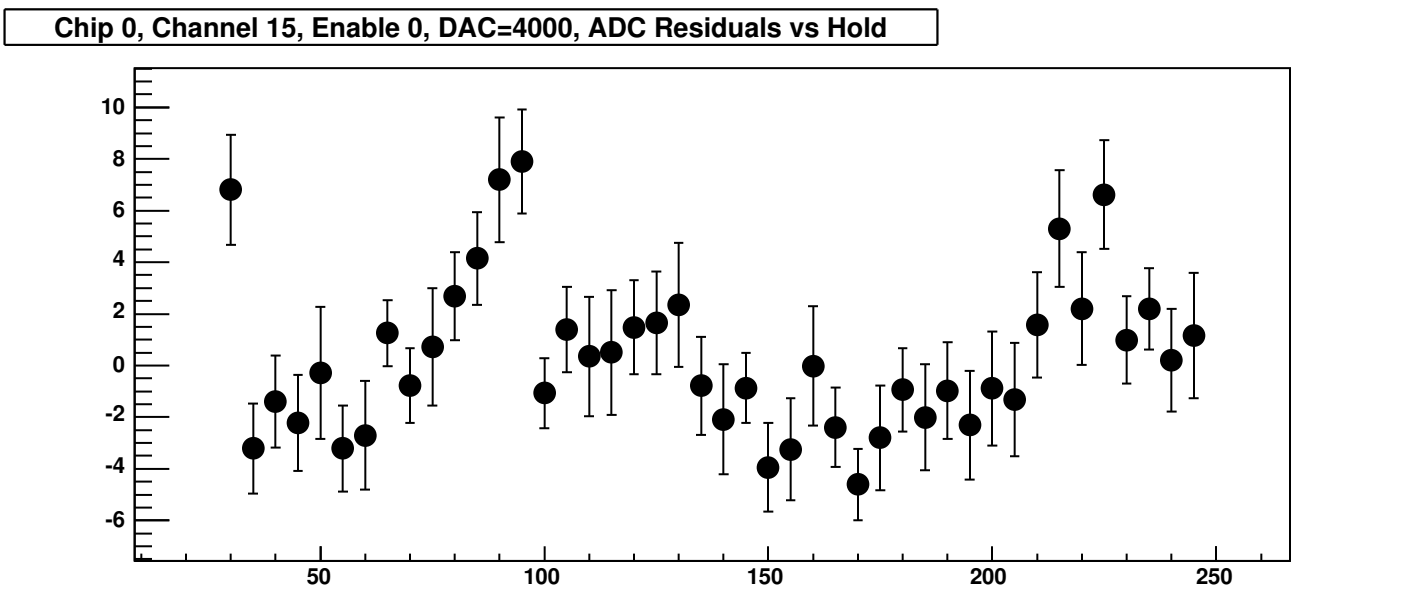

Chip 0, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 78.39 / 31
p0 503.1 ± 2.642
p1 -9.752 ± 1.846
p2 309.3 ± 2.565
p3 34.78 ± 2.128
p4 -0.2359 ± 0.02477
780
770
760
750
740
730
720
710
700
20
40
60
80
100
120
140
160
180
200
220

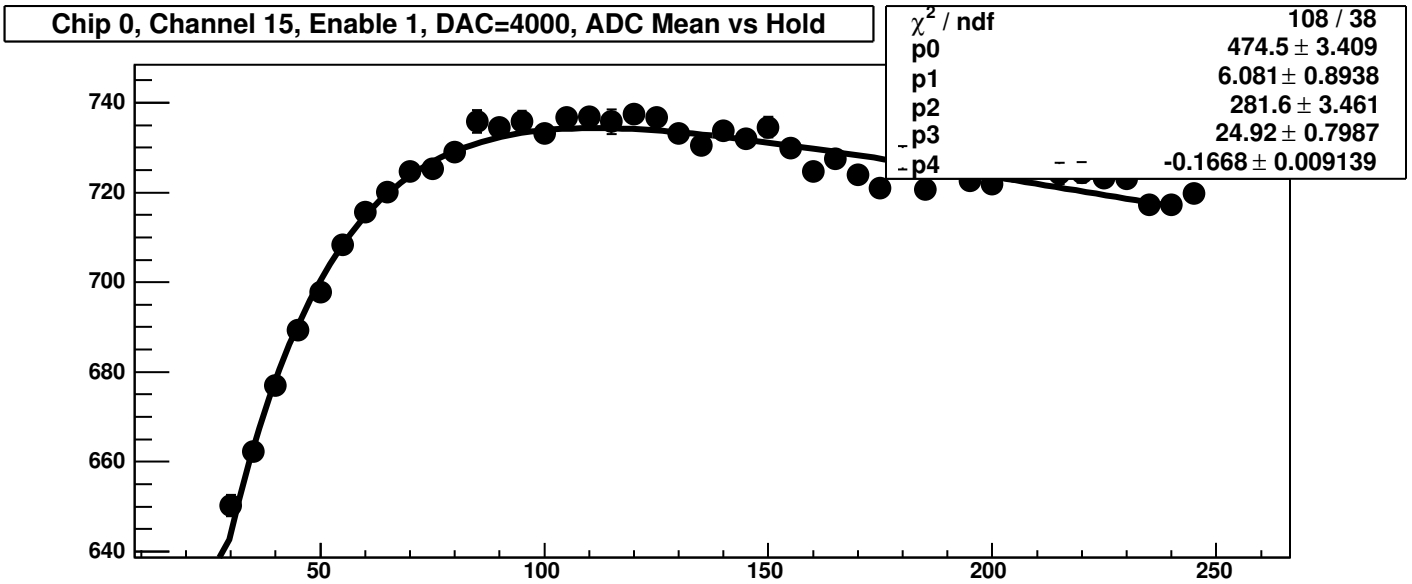

Chip 0, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
20
40
60
80
100
120
140
160
180
200
220

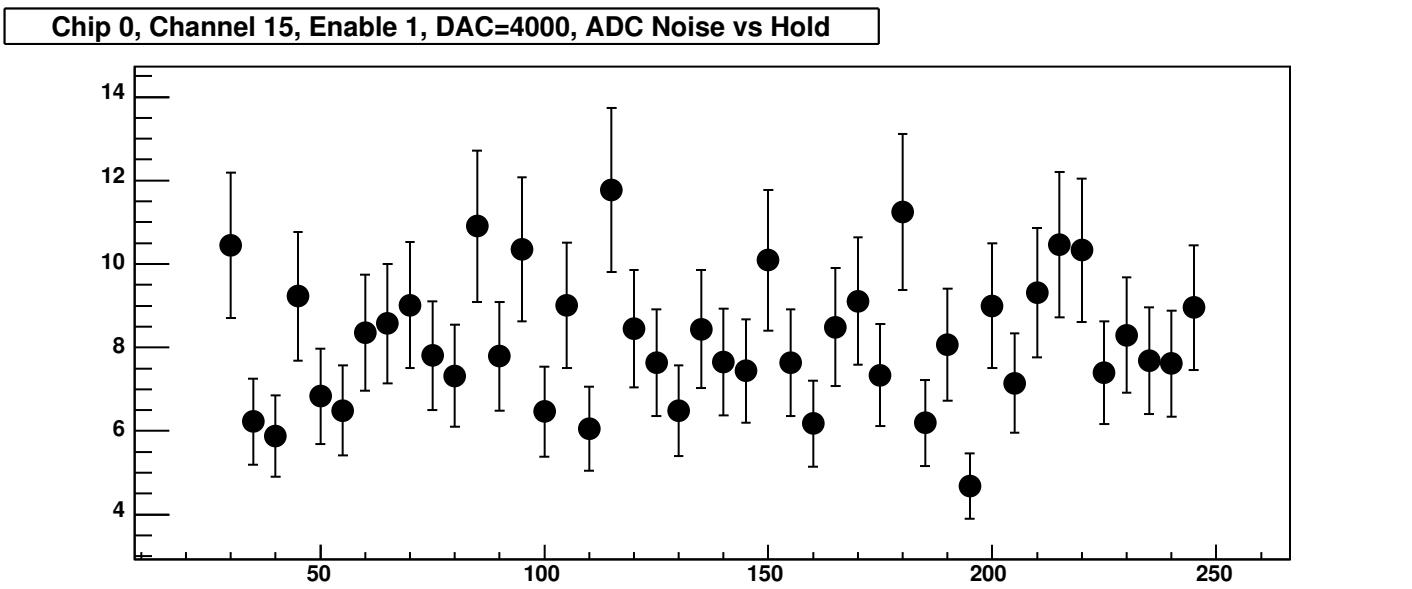

Chip 0, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
20
40
60
80
100
120
140
160
180
200
220

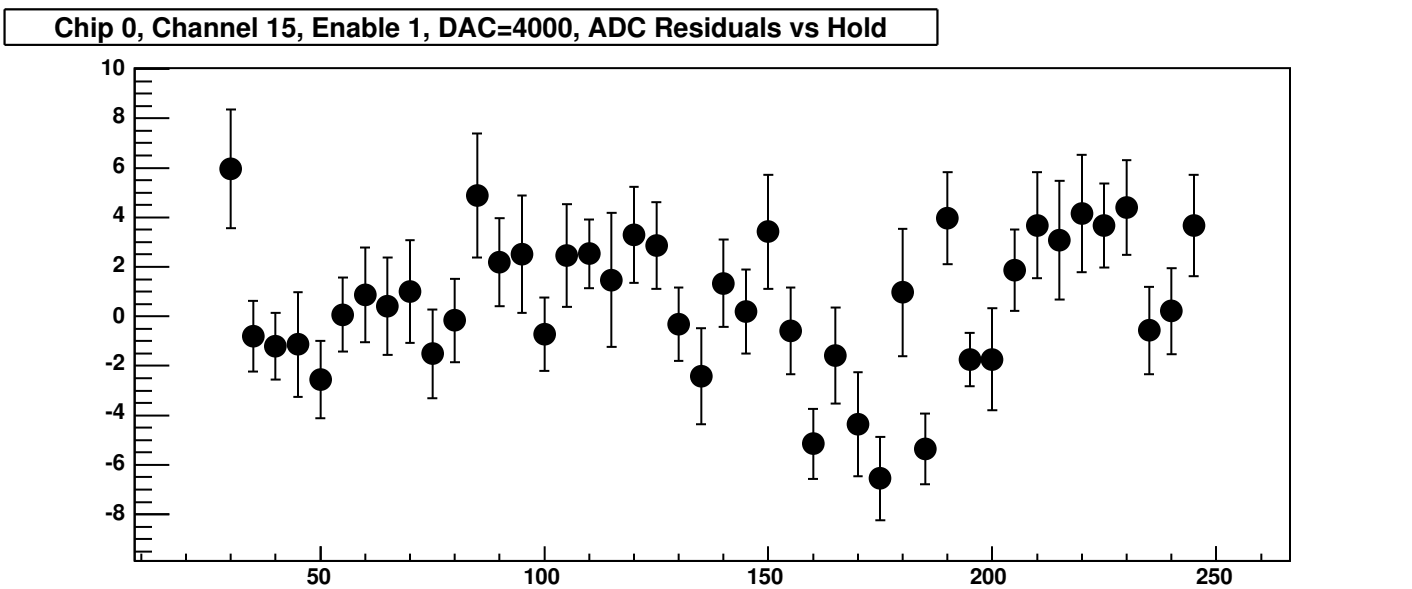

Chip 0, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 105.6 / 38
p0 470 ± 3.543
p1 5.819 ± 0.9579
p2 276.2 ± 3.591
p3 23.09 ± 0.7934
p4 -0.1589 ± 0.008881
740
720
700
680
660
50
100
150
200
250

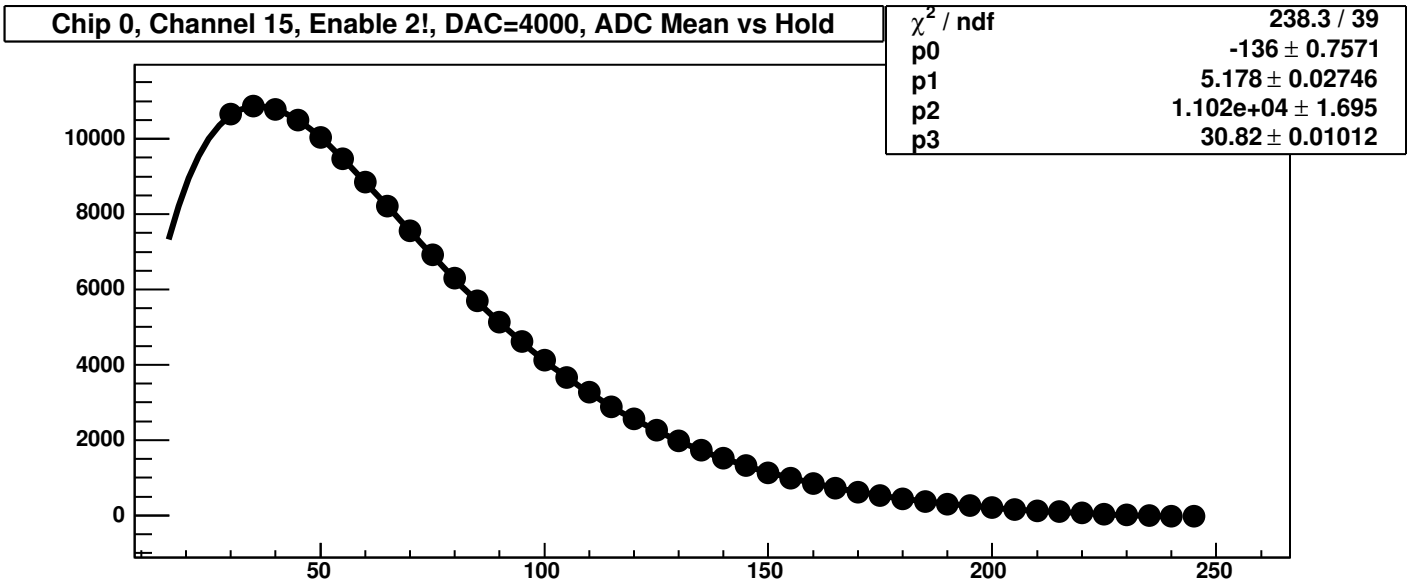

Chip 0, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250

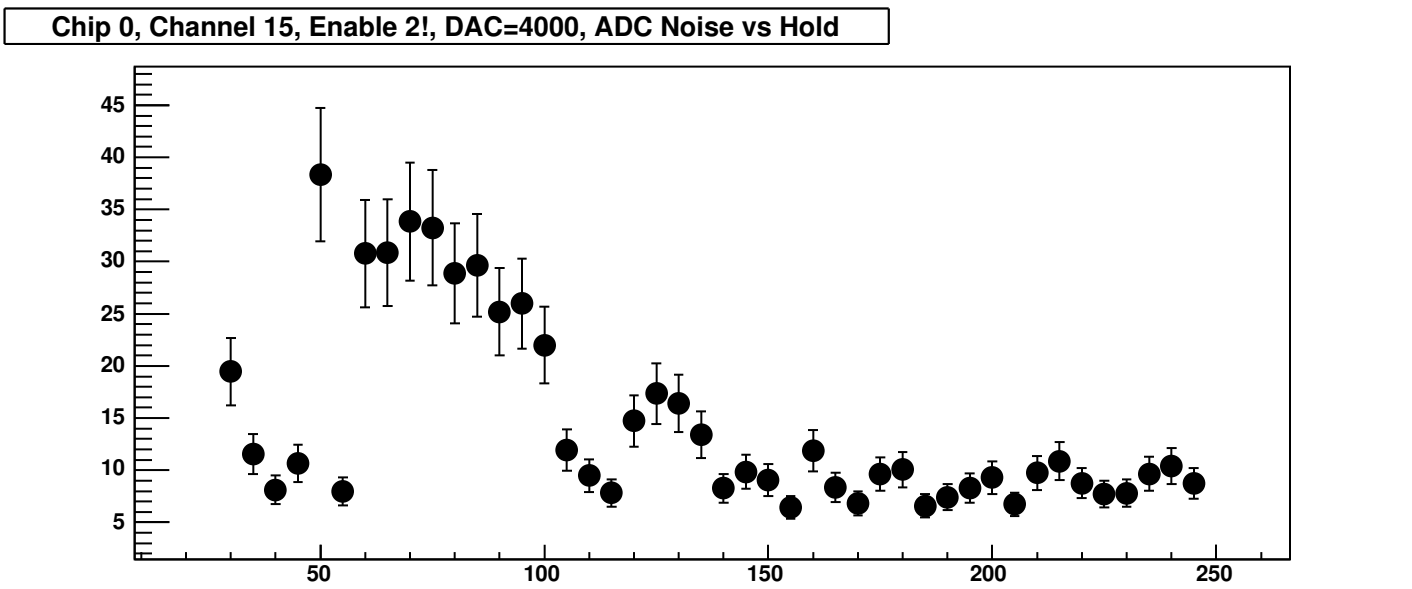

Chip 0, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
50
100
150
200
250

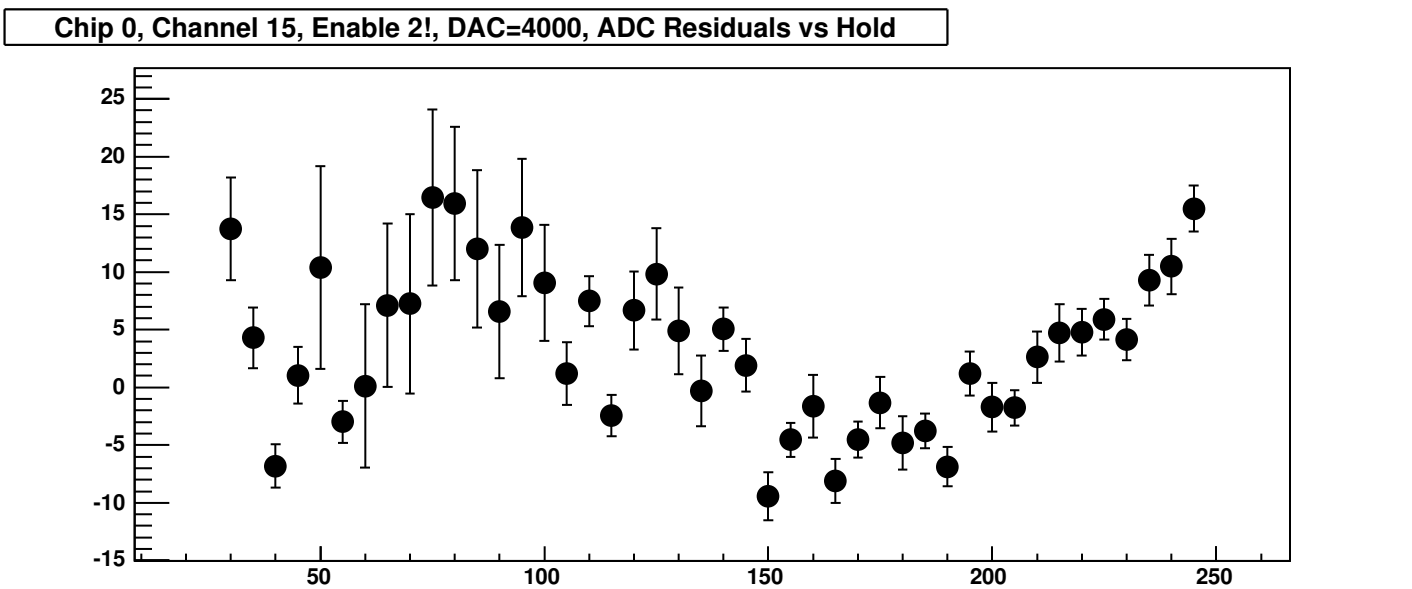

Chip 0, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 108 / 38
p0 474.5 ± 3.409
p1 6.081 ± 0.8938
p2 281.6 ± 3.461
p3 24.92 ± 0.7987
p4 -0.1668 ± 0.009139
740
720
700
680
660
640
50
100
150
200
250

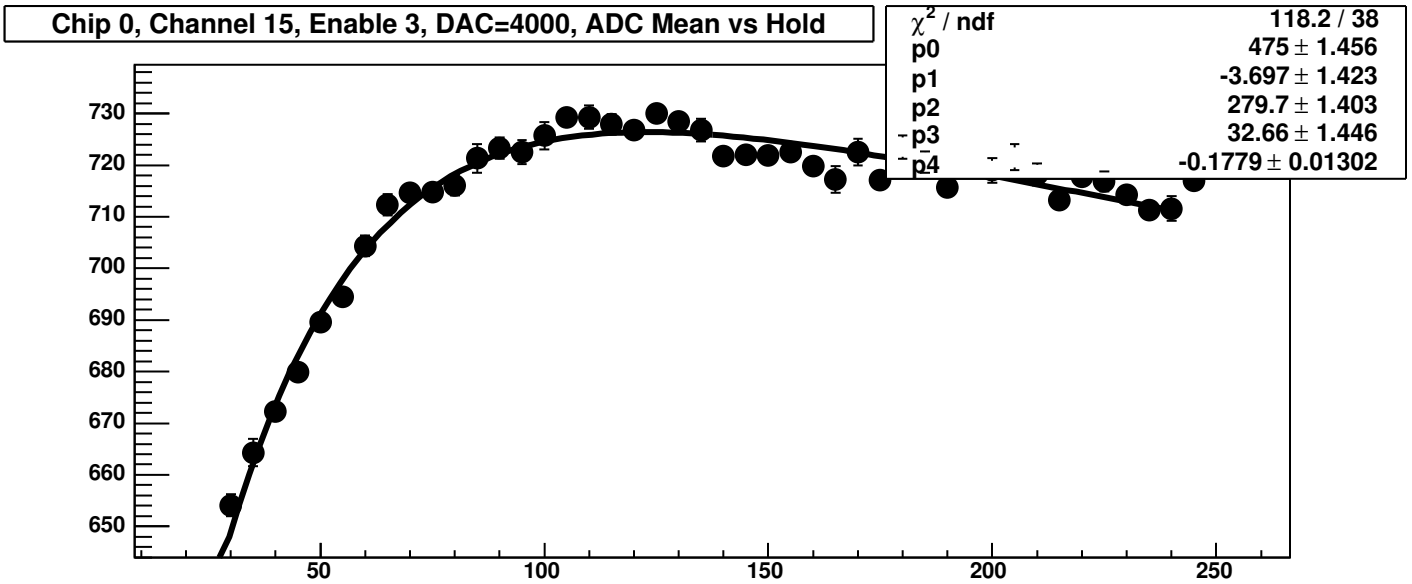

Chip 0, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250

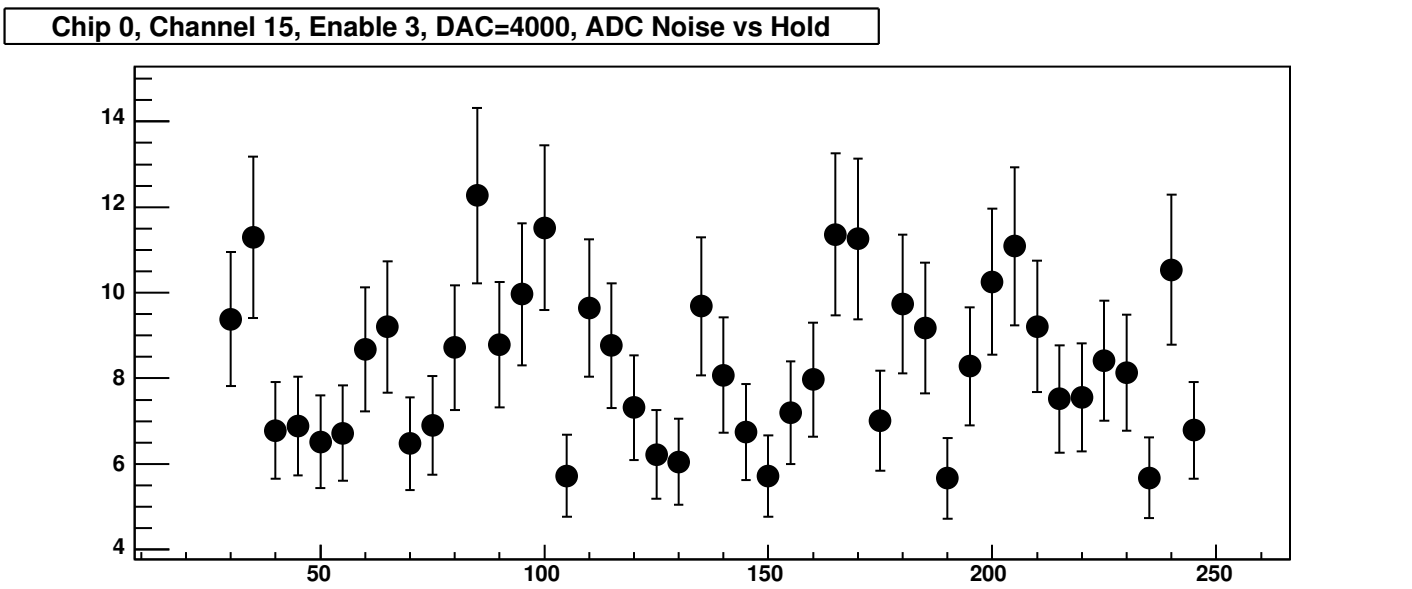

Chip 0, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

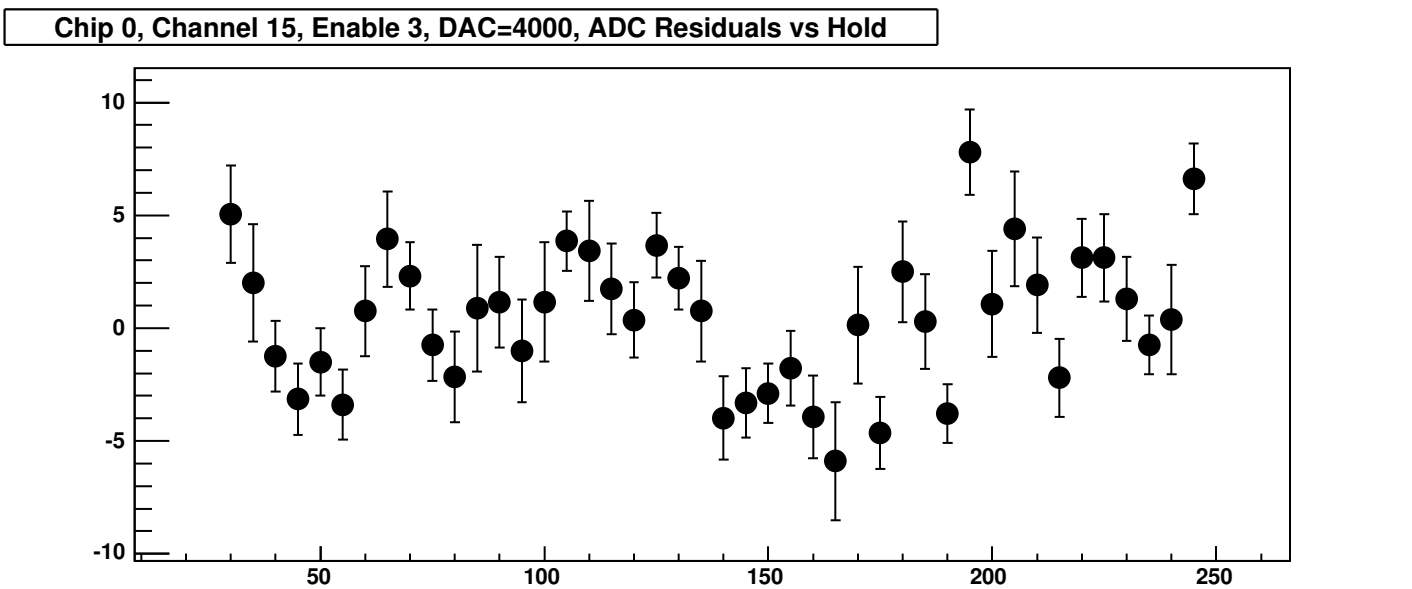

Chip 0, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 238.3 / 39
p0 -136 ± 0.7571
p1 5.178 ± 0.02746
p2 1.102e+04 ± 1.695
p3 30.82 ± 0.01012
10000
8000
6000
4000
2000
0
50
100
150
200
250

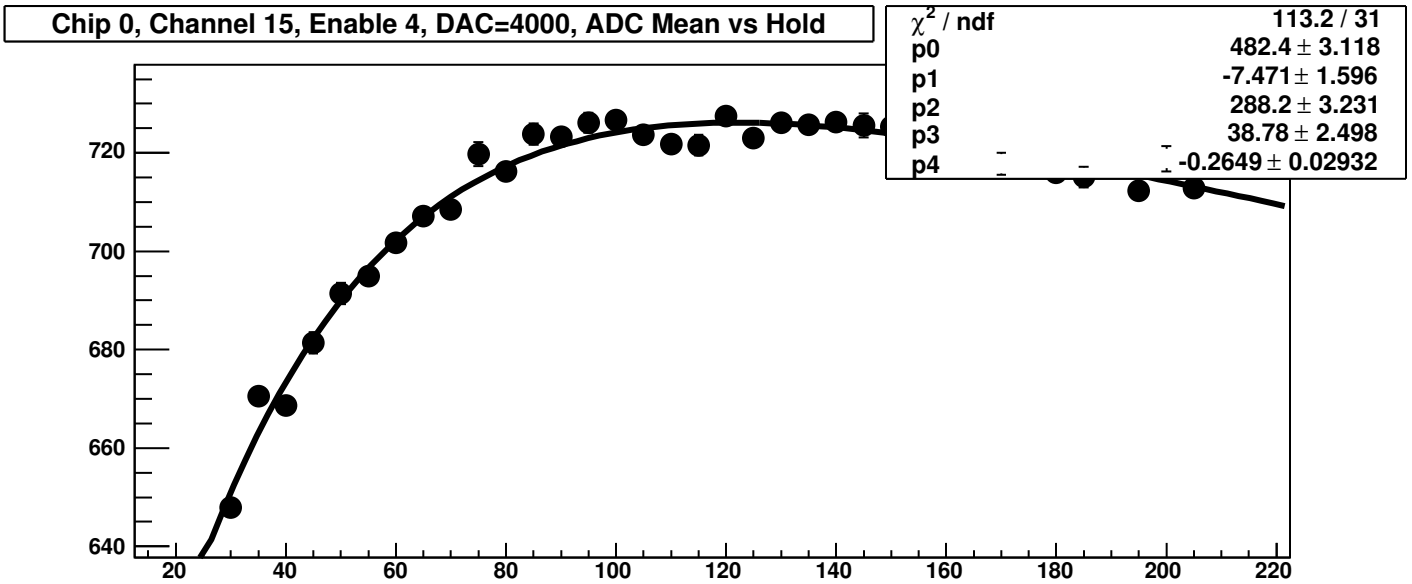

Chip 0, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold
45
40
35
30
25
20
15
10
5
50
100
150
200
250

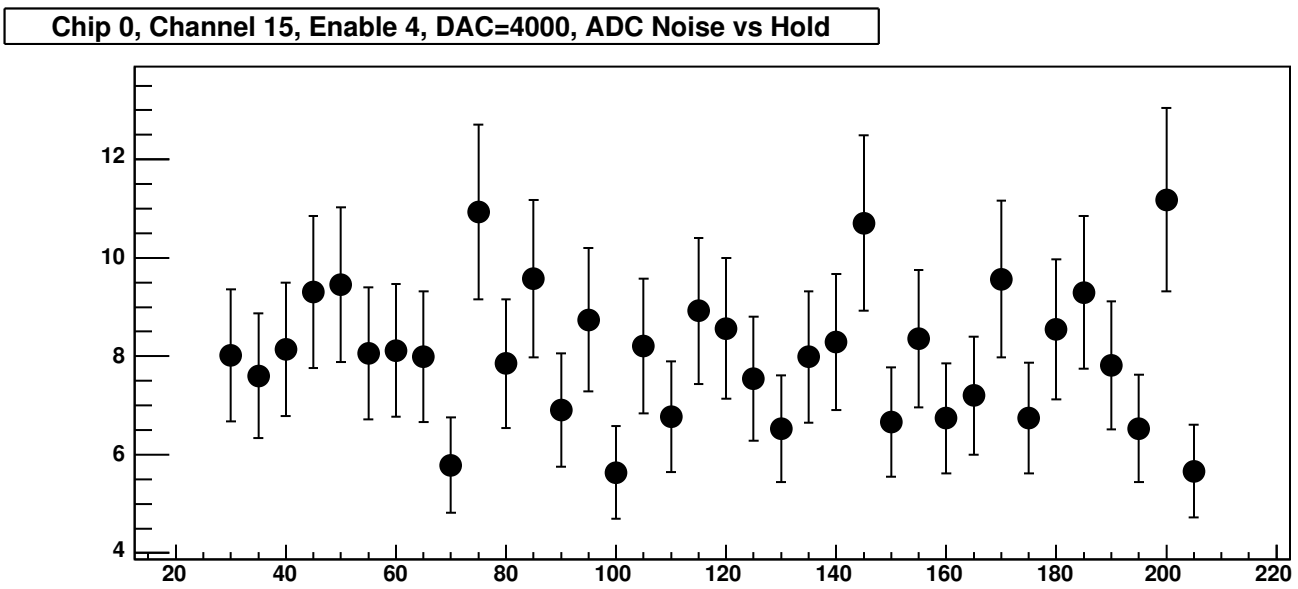

Chip 0, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
-15
50
100
150
200
250

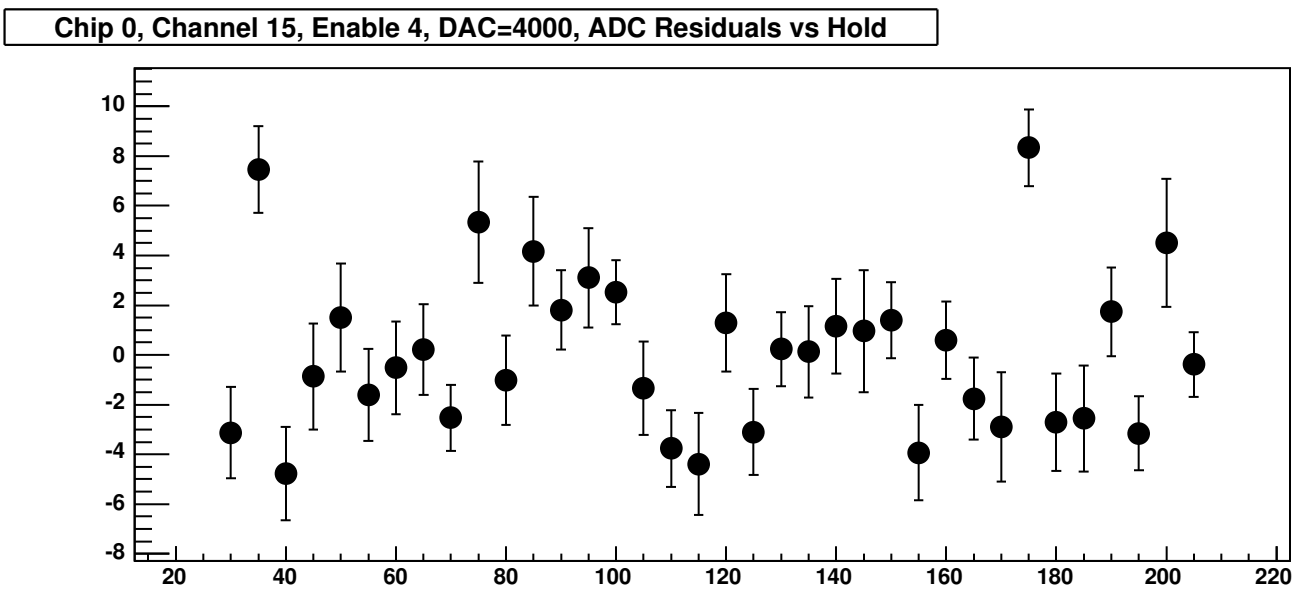

Chip 0, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 118.2 / 38
p0 475 ± 1.456
p1 -3.697 ± 1.423
p2 279.7 ± 1.403
p3 32.66 ± 1.446
p4 -0.1779 ± 0.01302
730
720
710
700
690
680
670
660
650
50
100
150
200
250

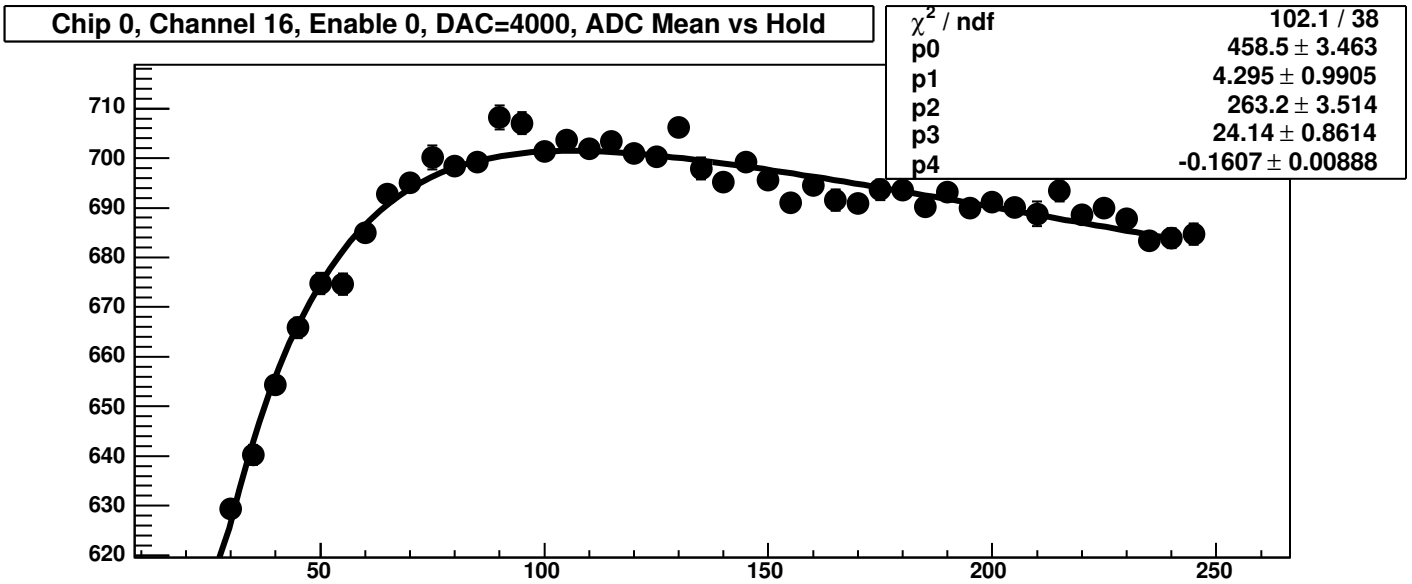

Chip 0, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250

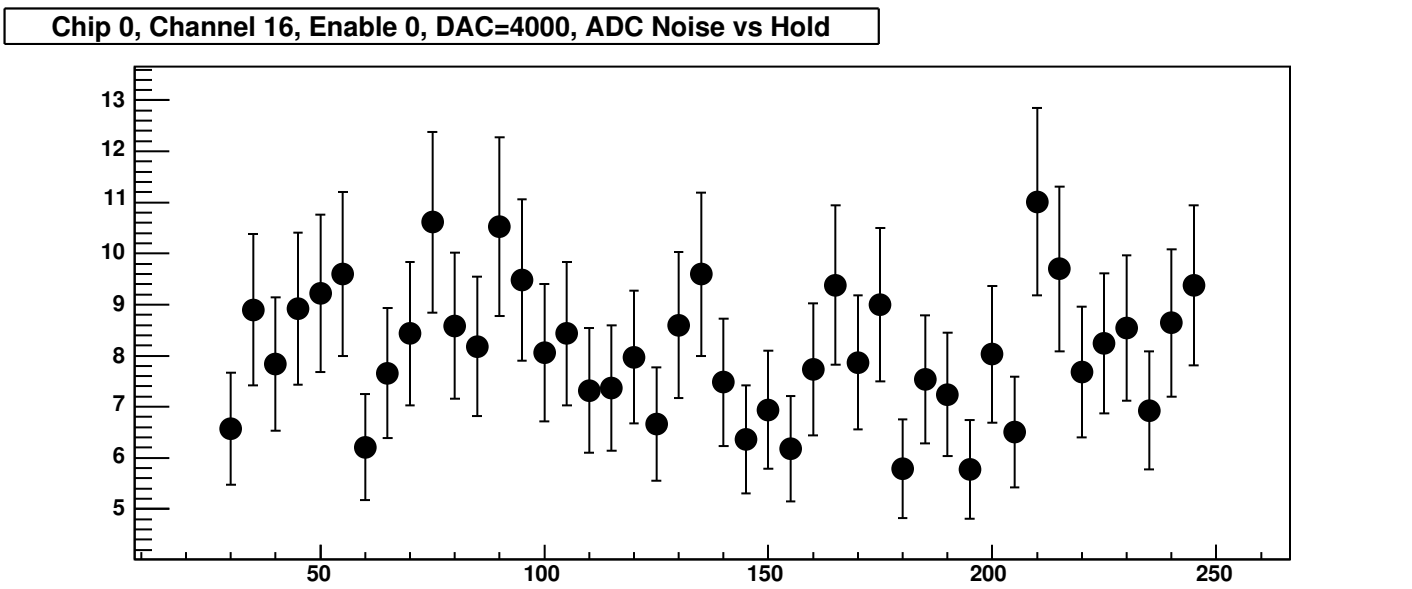

Chip 0, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

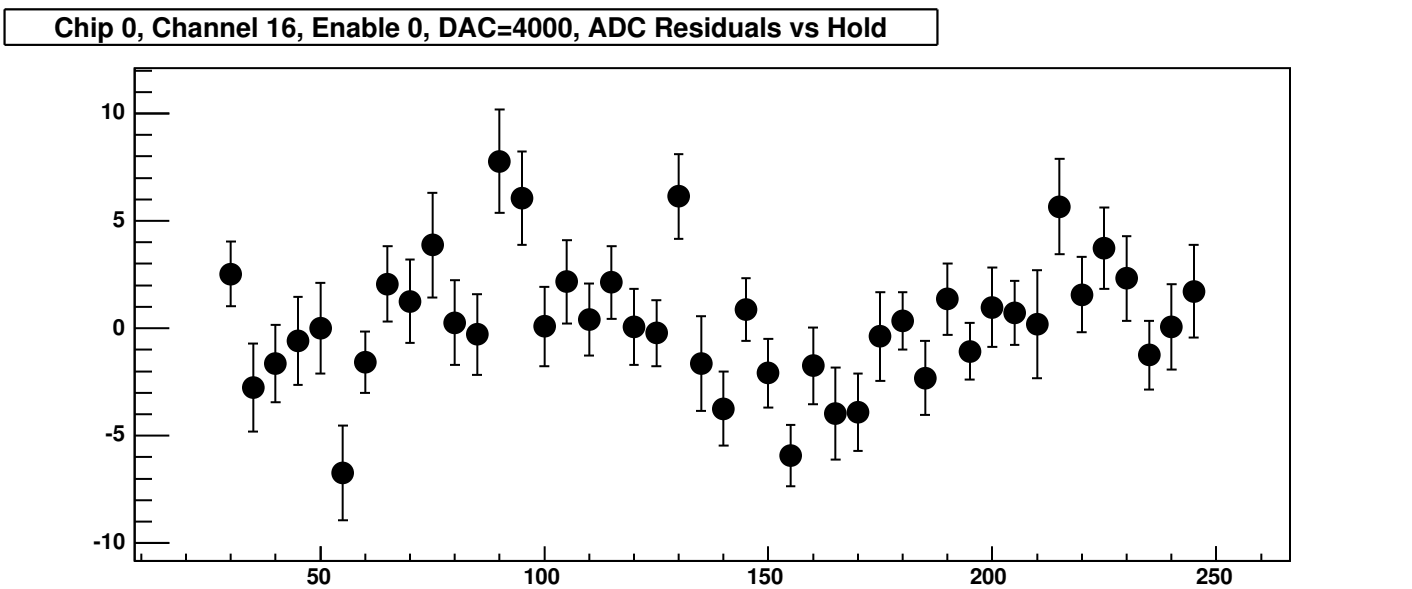

Chip 0, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 113.2 / 31
p0 482.4 ± 3.118
p1 -7.471 ± 1.596
p2 288.2 ± 3.231
p3 38.78 ± 2.498
p4 -0.2649 ± 0.02932
720
700
680
660
640
20
40
60
80
100
120
140
160
180
200
220


Chip 0, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
20
40
60
80
100
120
140
160
180
200
220

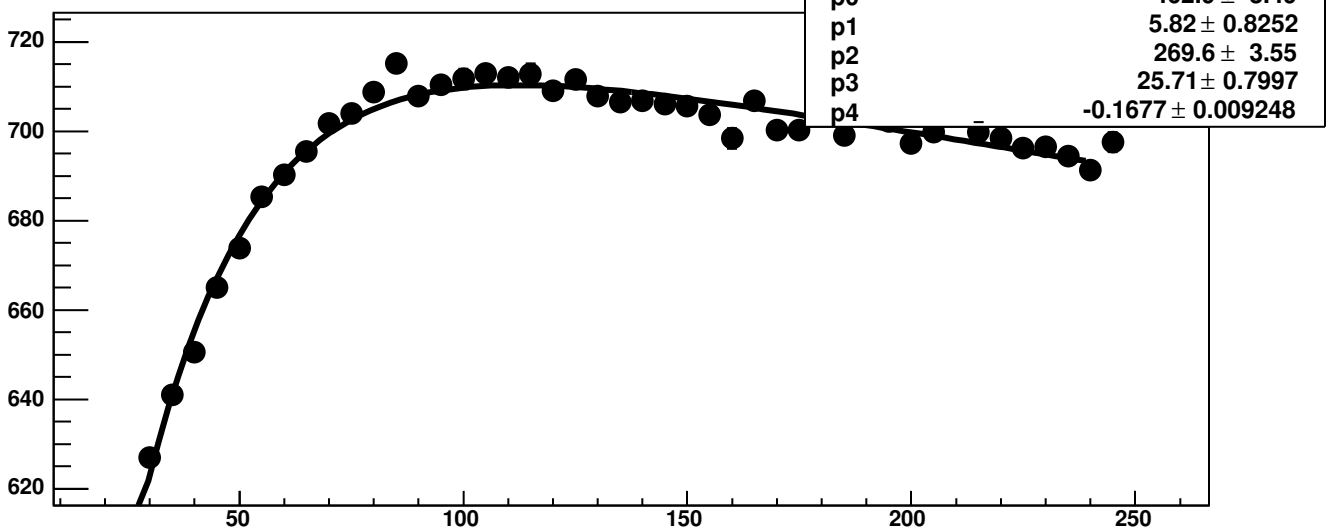

Chip 0, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
-8
20
40
60
80
100
120
140
160
180
200
220


Chip 0, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 102.1 / 38
p0 458.5 ± 3.463
p1 4.295 ± 0.9905
p2 263.2 ± 3.514
p3 24.14 ± 0.8614
p4 -0.1607 ± 0.00888
710
700
690
680
670
660
650
640
630
620
50
100
150
200
250

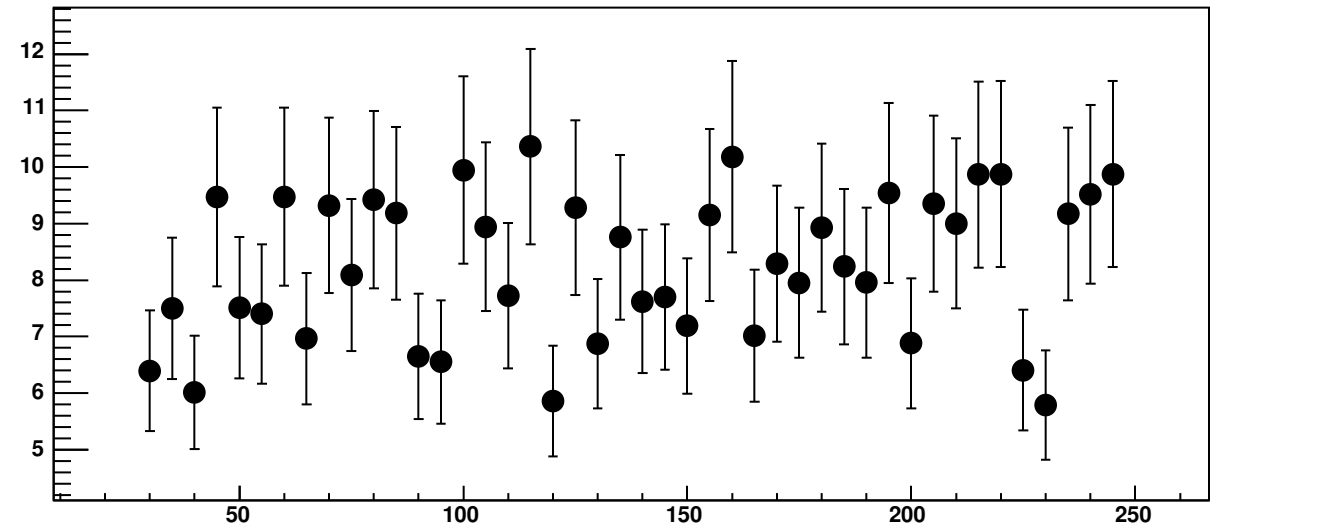

Chip 0, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
50
100
150
200
250


Chip 0, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

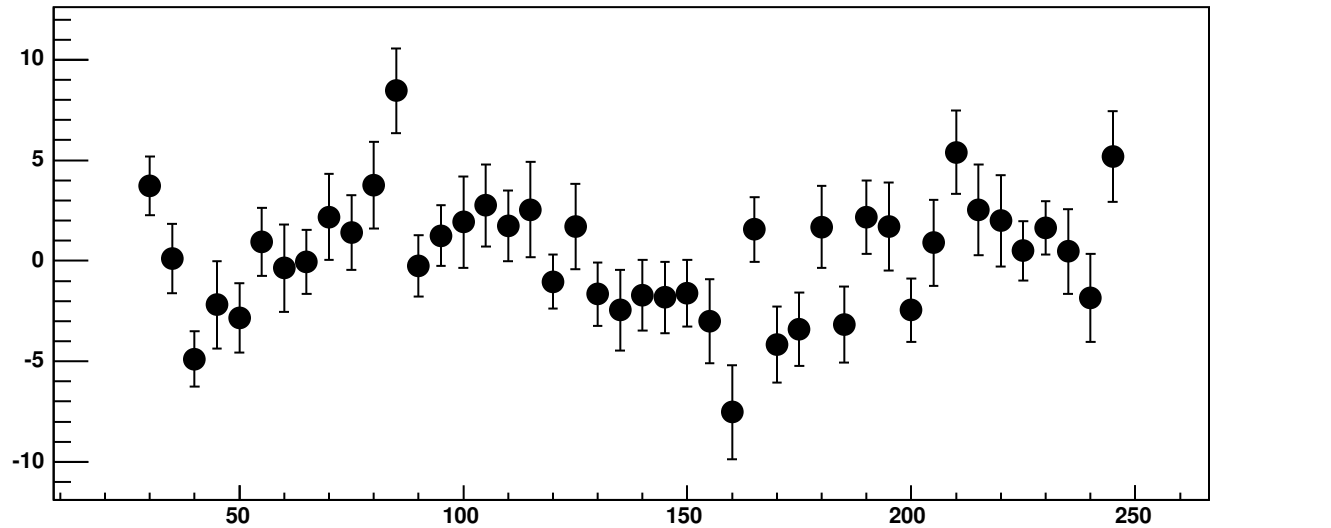
Chip 0, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold



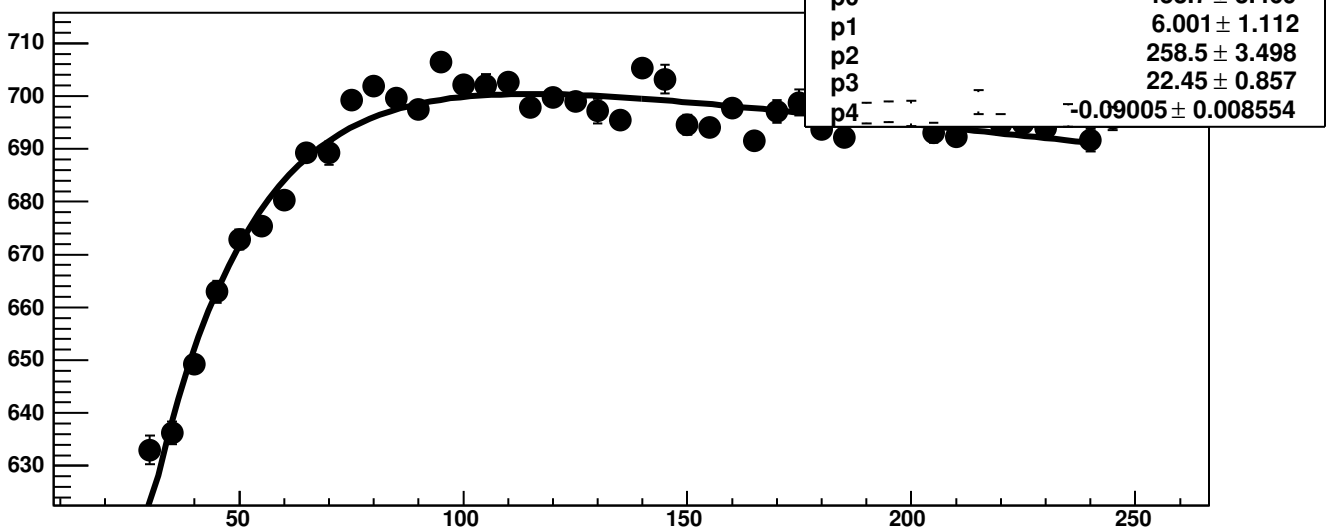
Chip 0, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



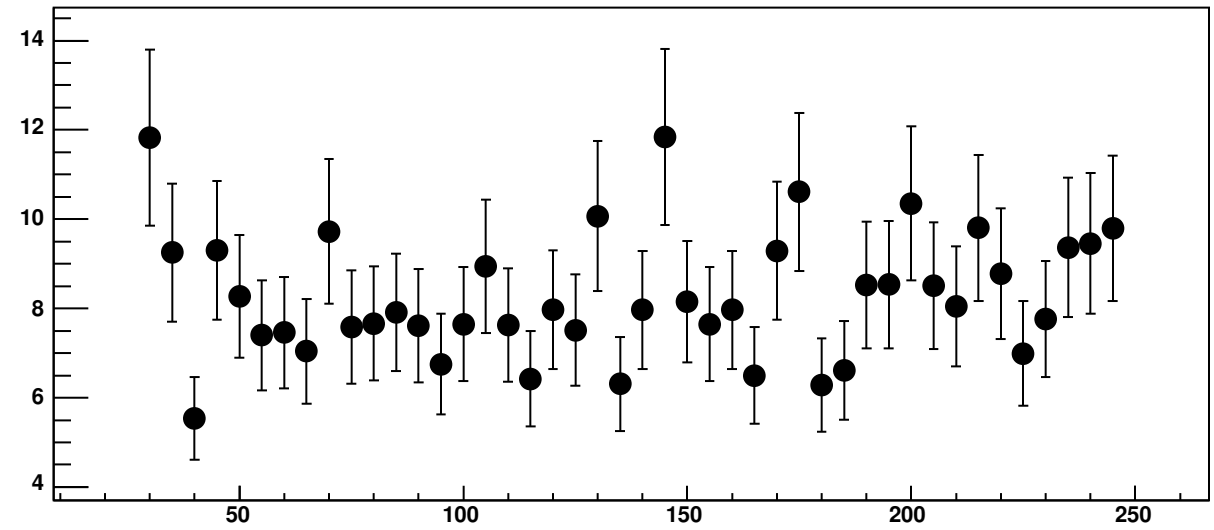
Chip 0, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold



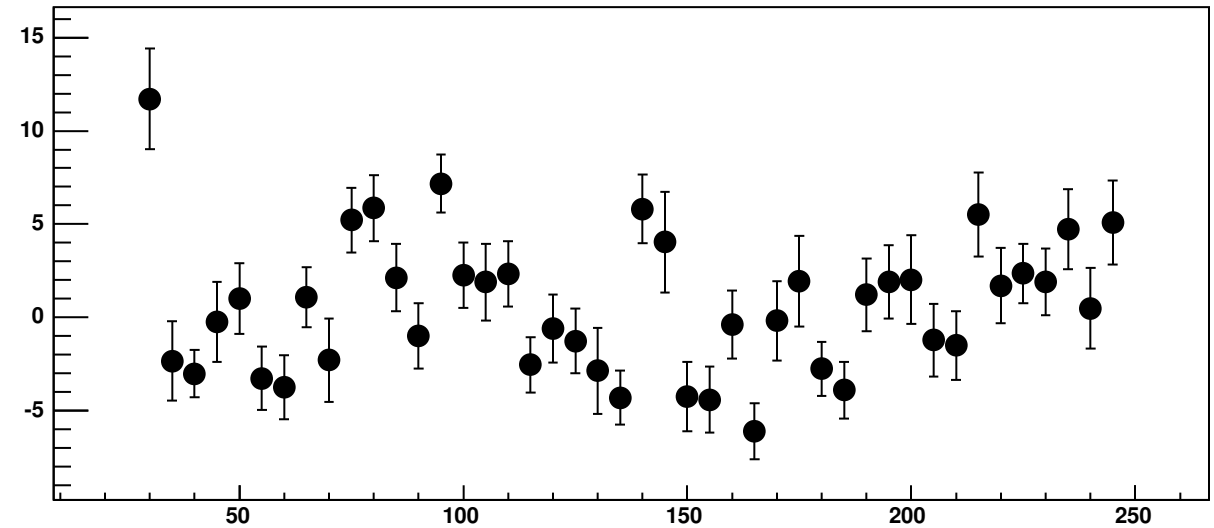
Chip 0, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold

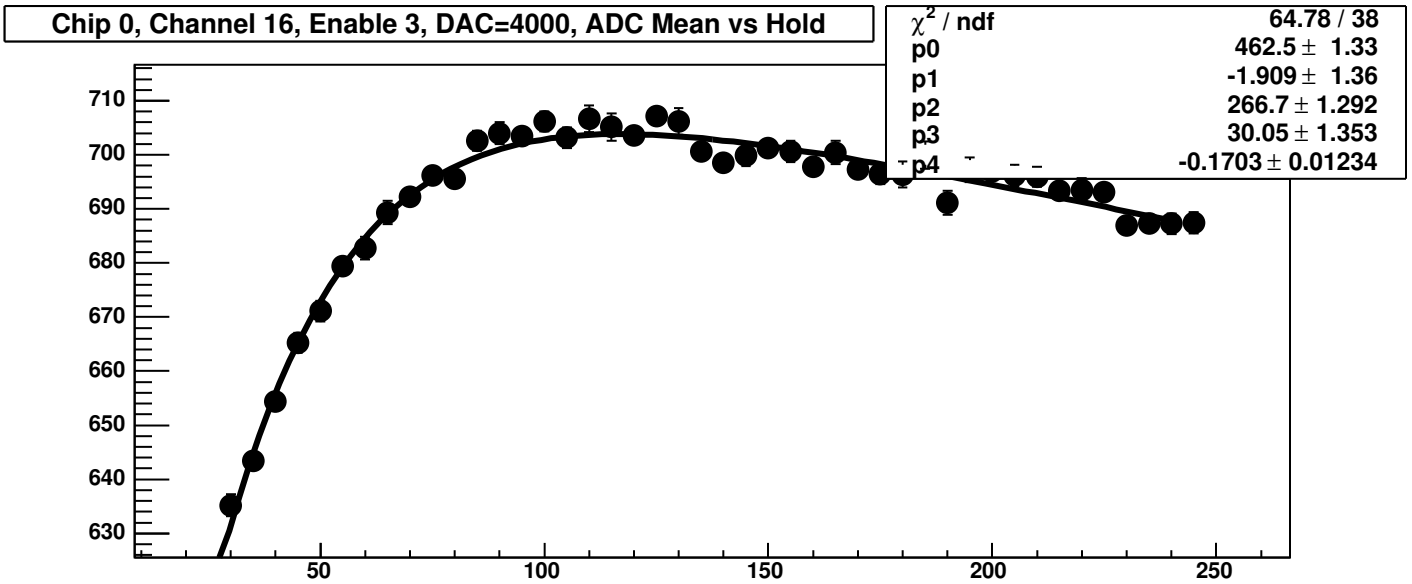


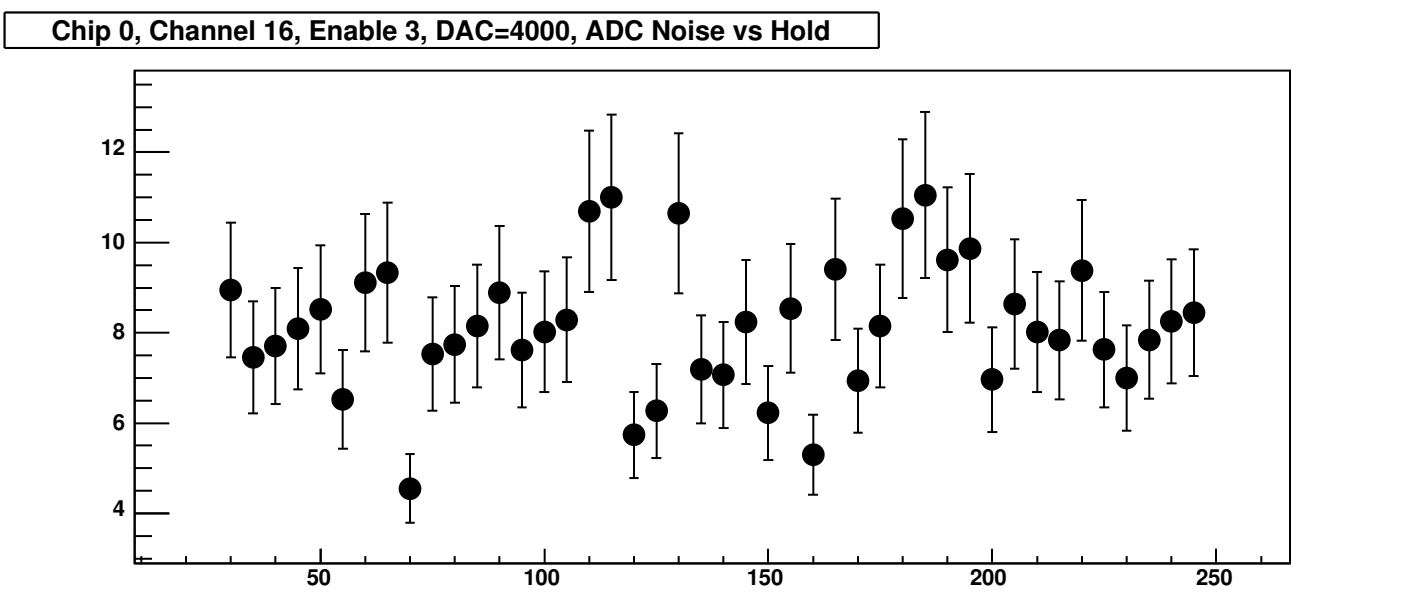
Chip 0, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold

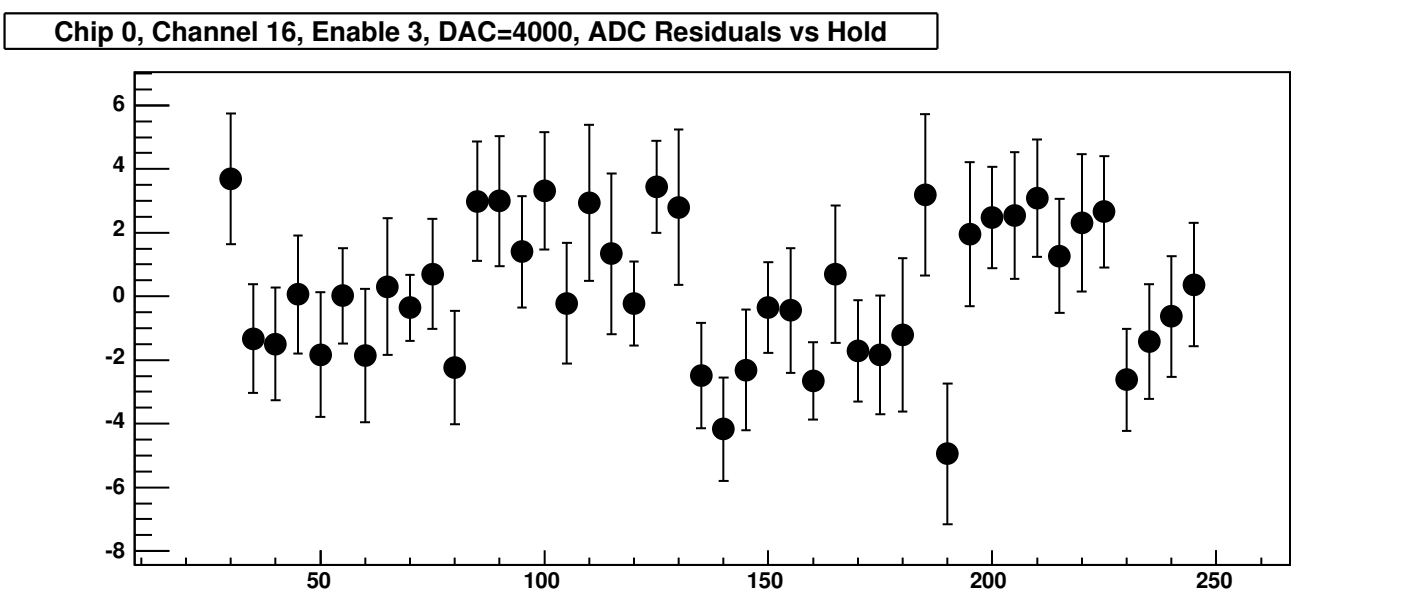


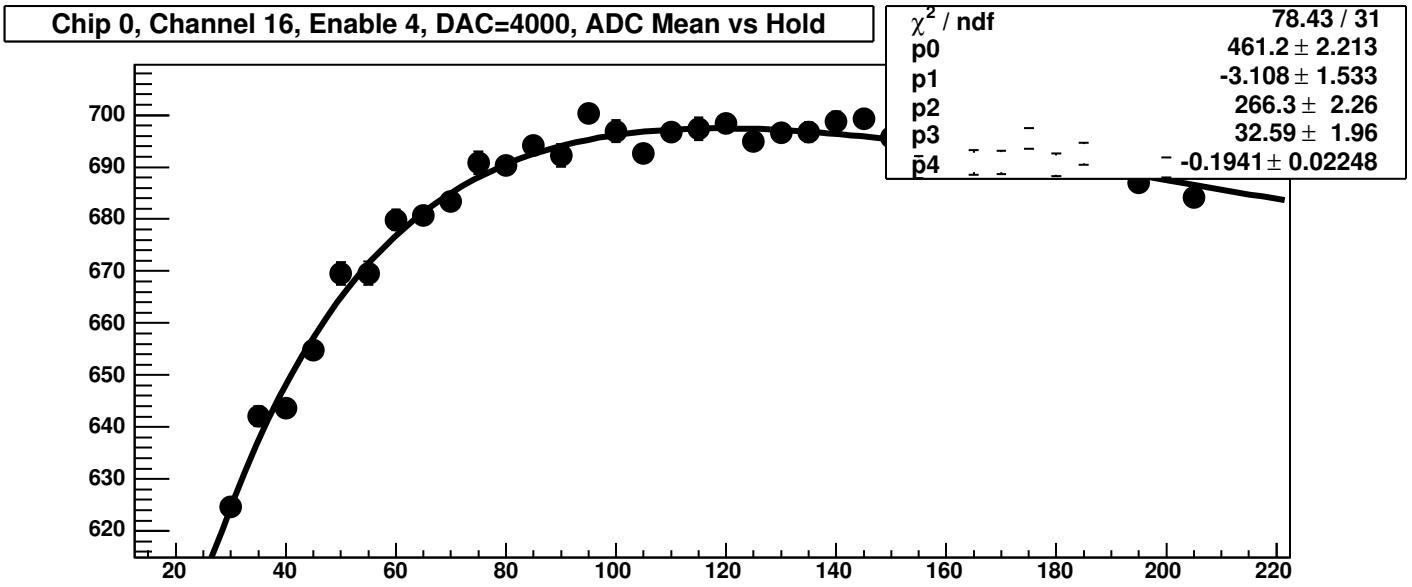
Chip 0, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold

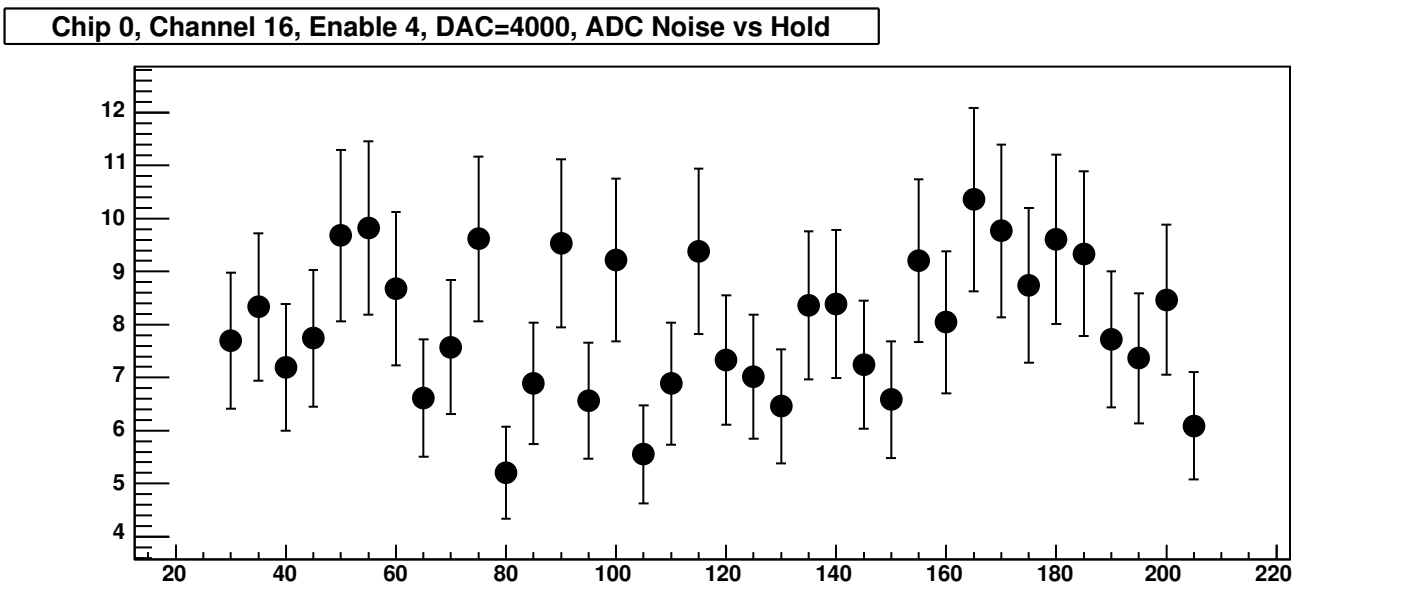


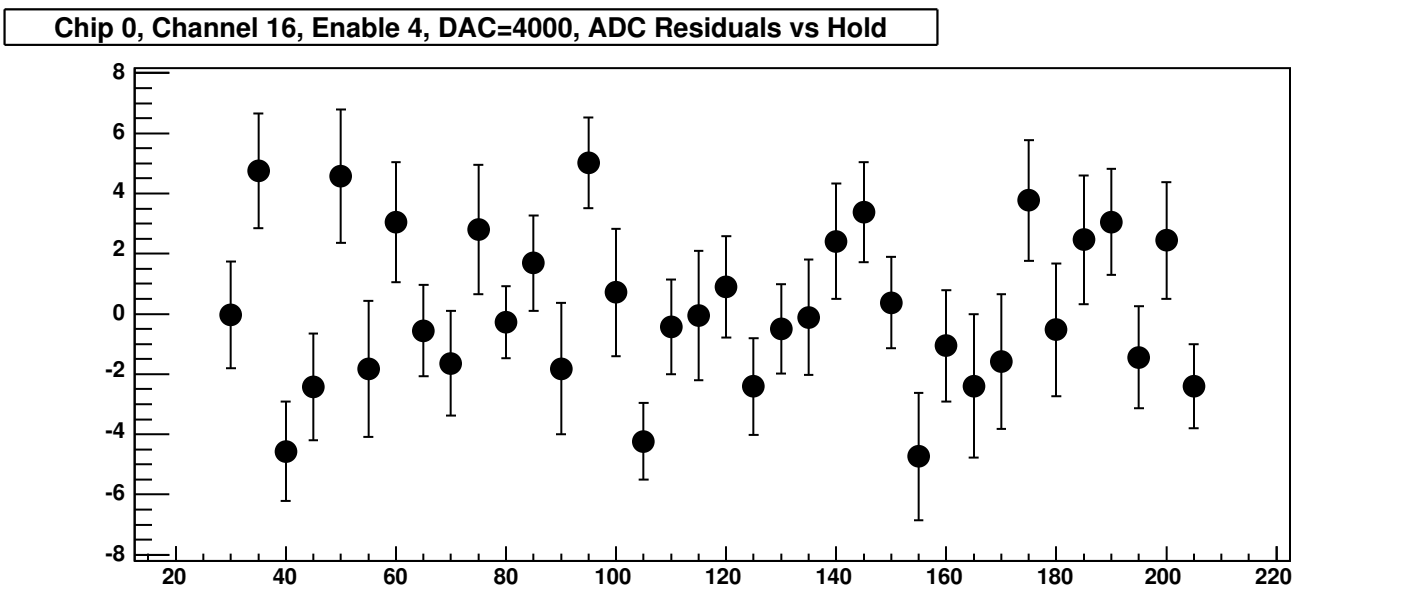

Chip 0, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 64.78 / 38
p0 462.5 ± 1.33
p1 -1.909 ± 1.36
p2 266.7 ± 1.292
p3 30.05 ± 1.353
p4 -0.1703 ± 0.01234
710
700
690
680
670
660
650
640
630
50
100
150
200
250


Chip 0, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250

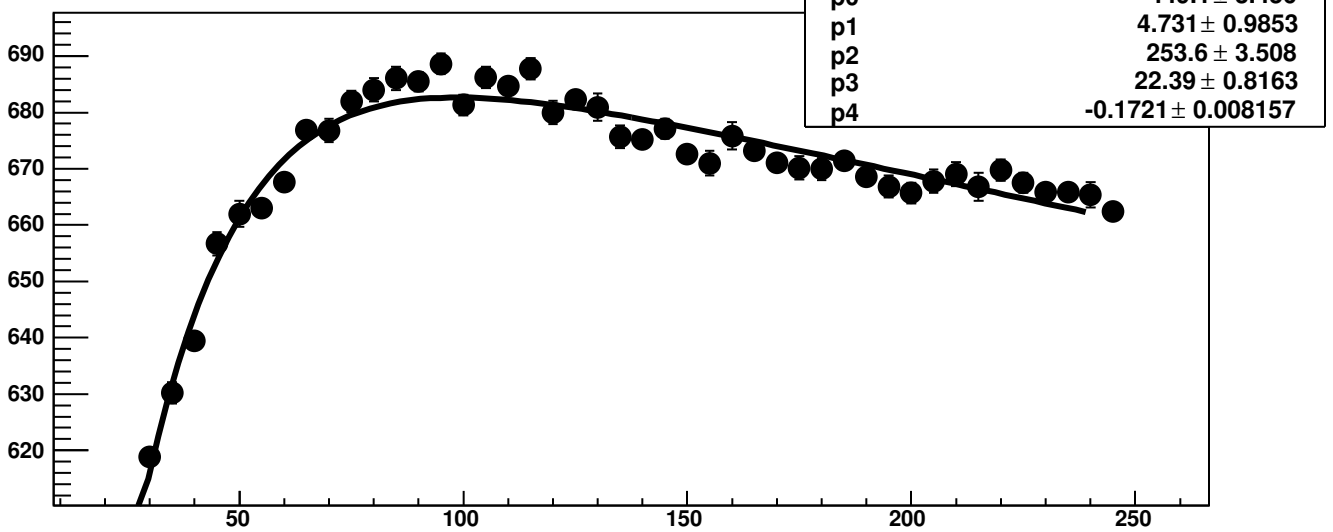

Chip 0, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250


Chip 0, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 78.43 / 31
p0 461.2 ± 2.213
p1 -3.108 ± 1.533
p2 266.3 ± 2.26
p3 32.59 ± 1.96
p4 -0.1941 ± 0.02248
700
690
680
670
660
650
640
630
620
20
40
60
80
100
120
140
160
180
200
220

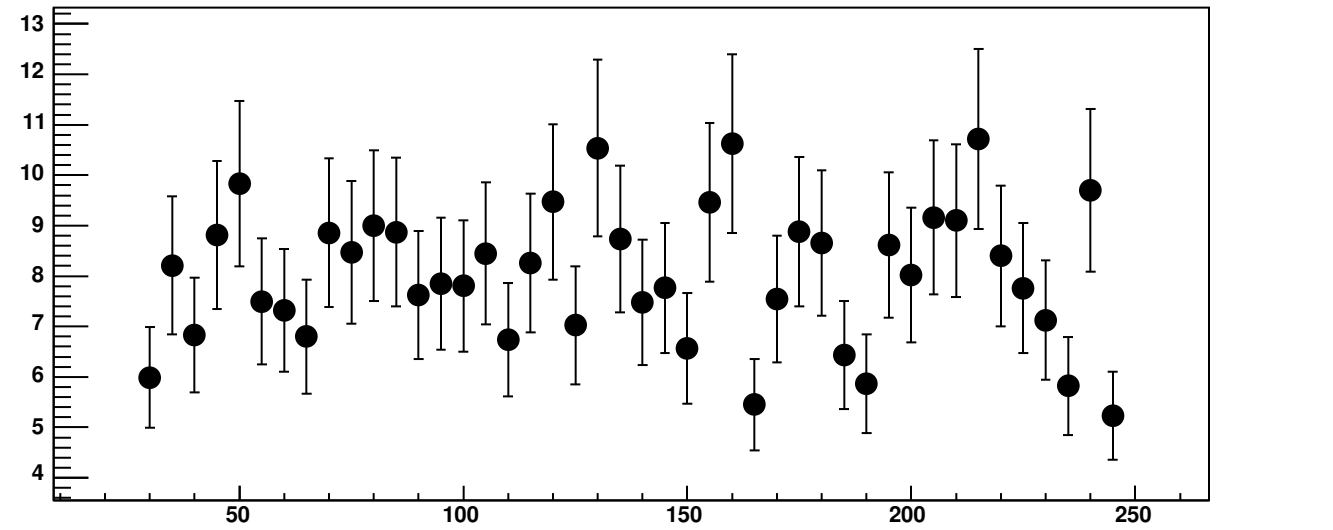

Chip 0, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
20
40
60
80
100
120
140
160
180
200
220


Chip 0, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
20
40
60
80
100
120
140
160
180
200
220

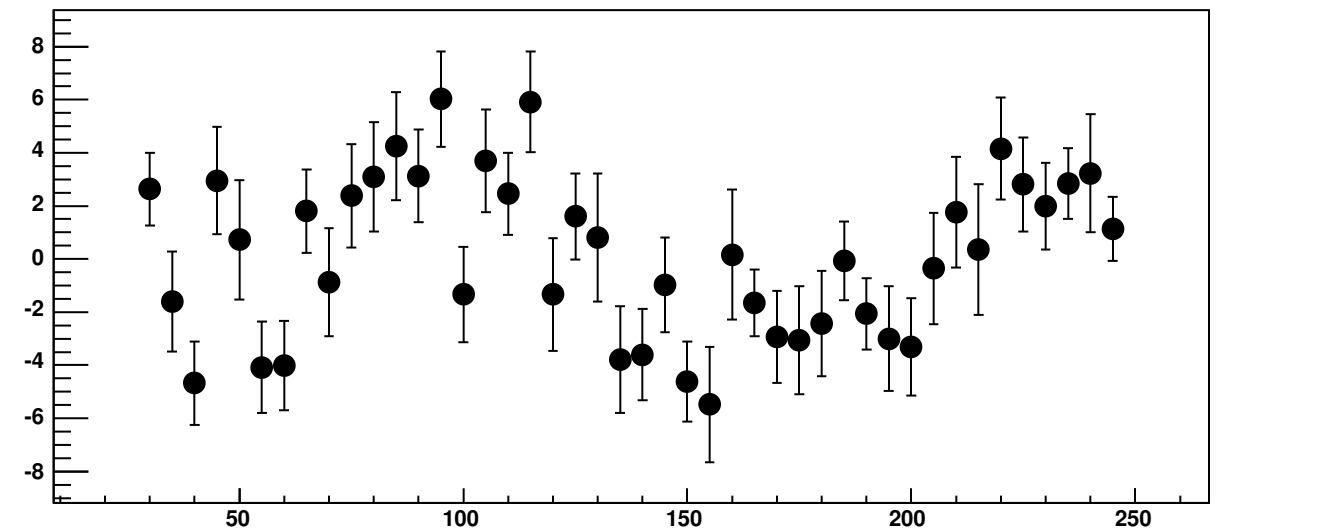
Chip 0, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold



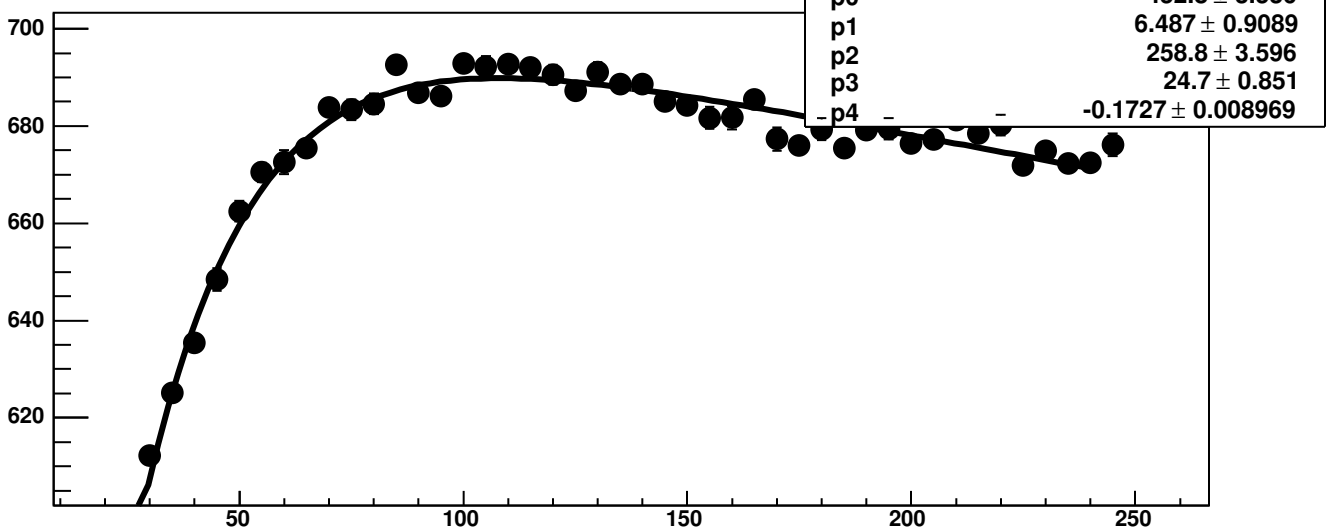
Chip 0, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



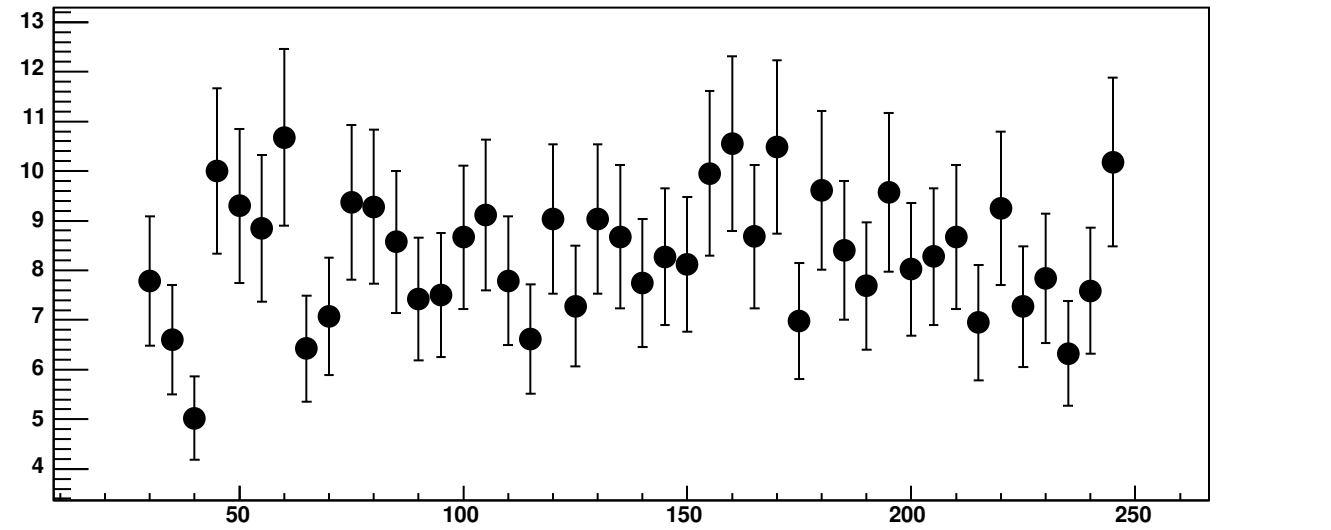
Chip 0, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold



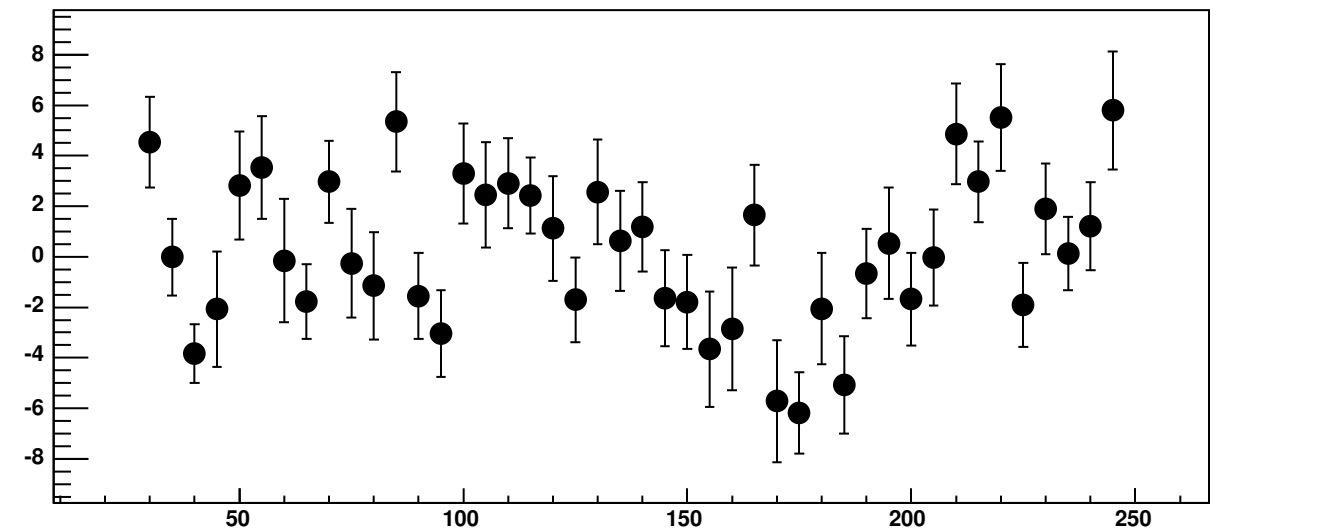
Chip 0, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

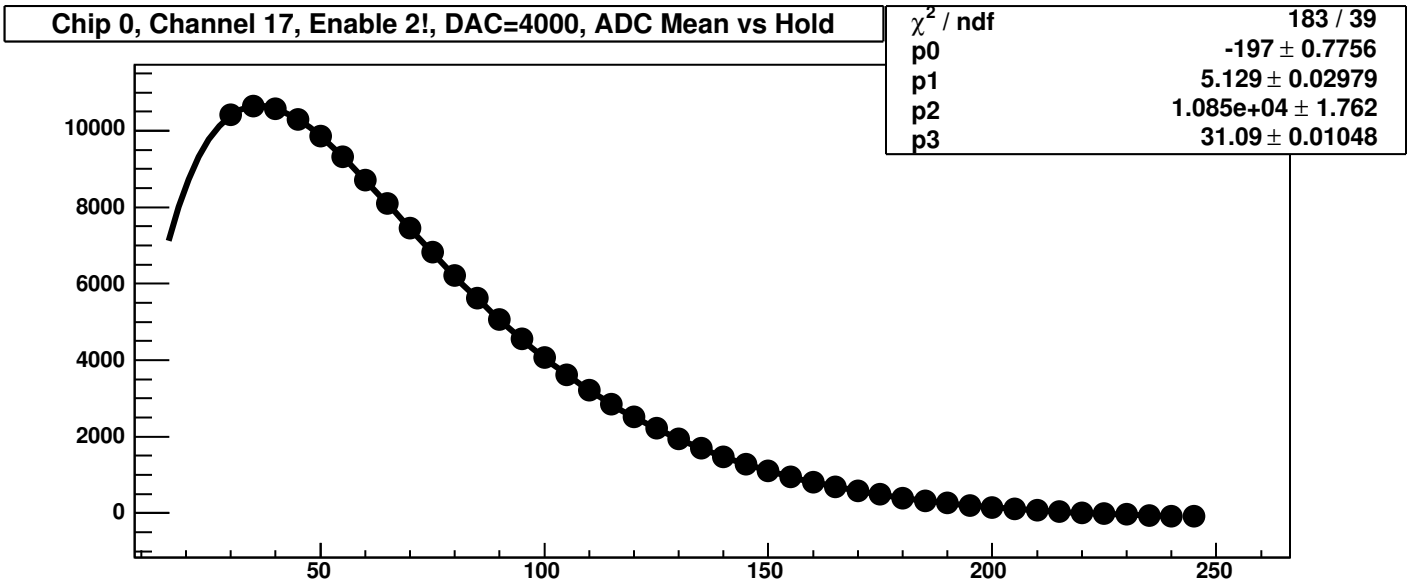


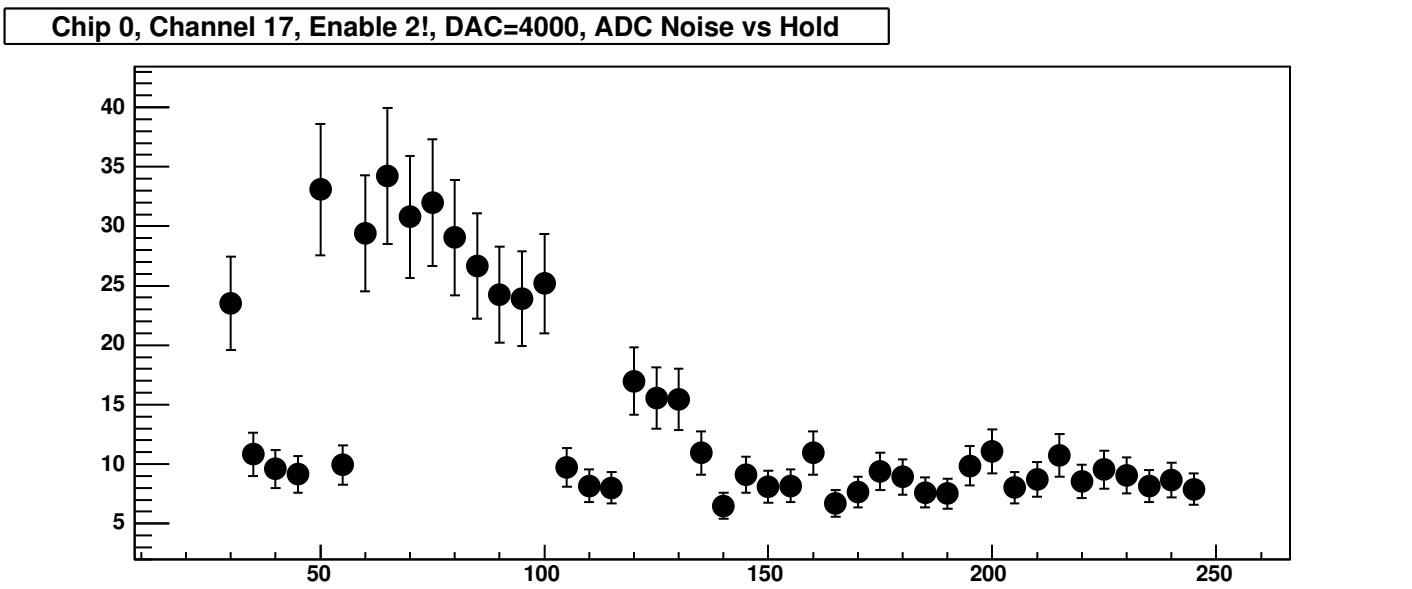
Chip 0, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold

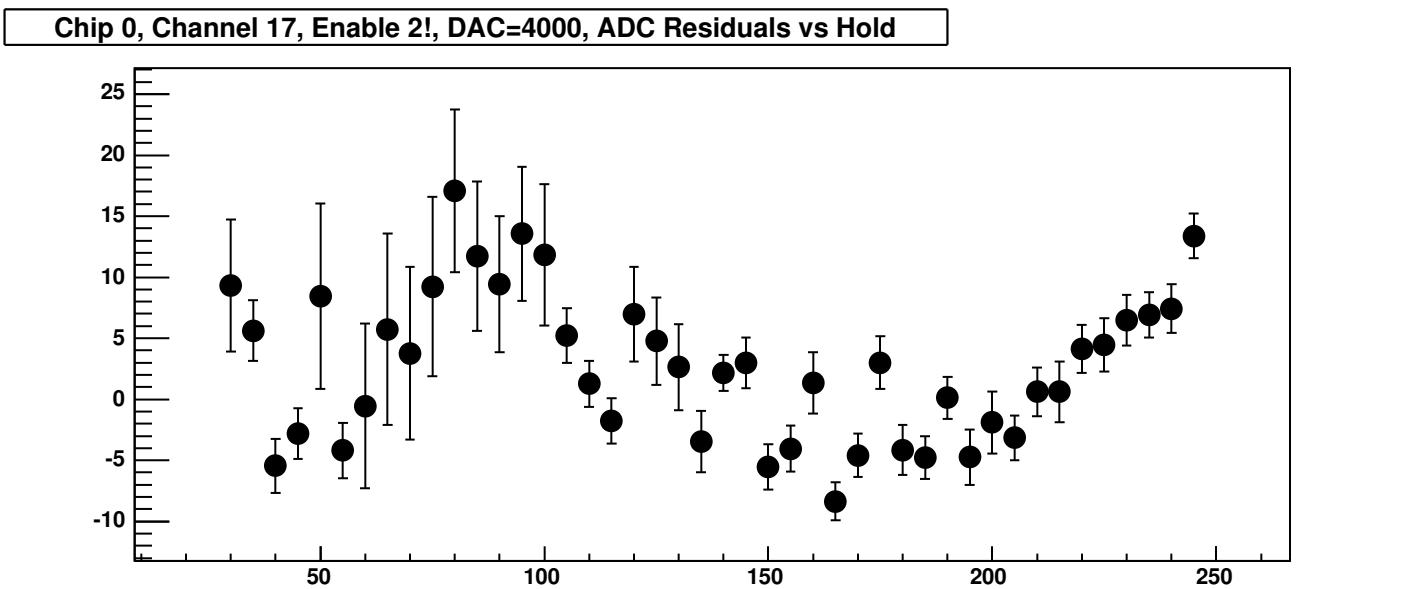


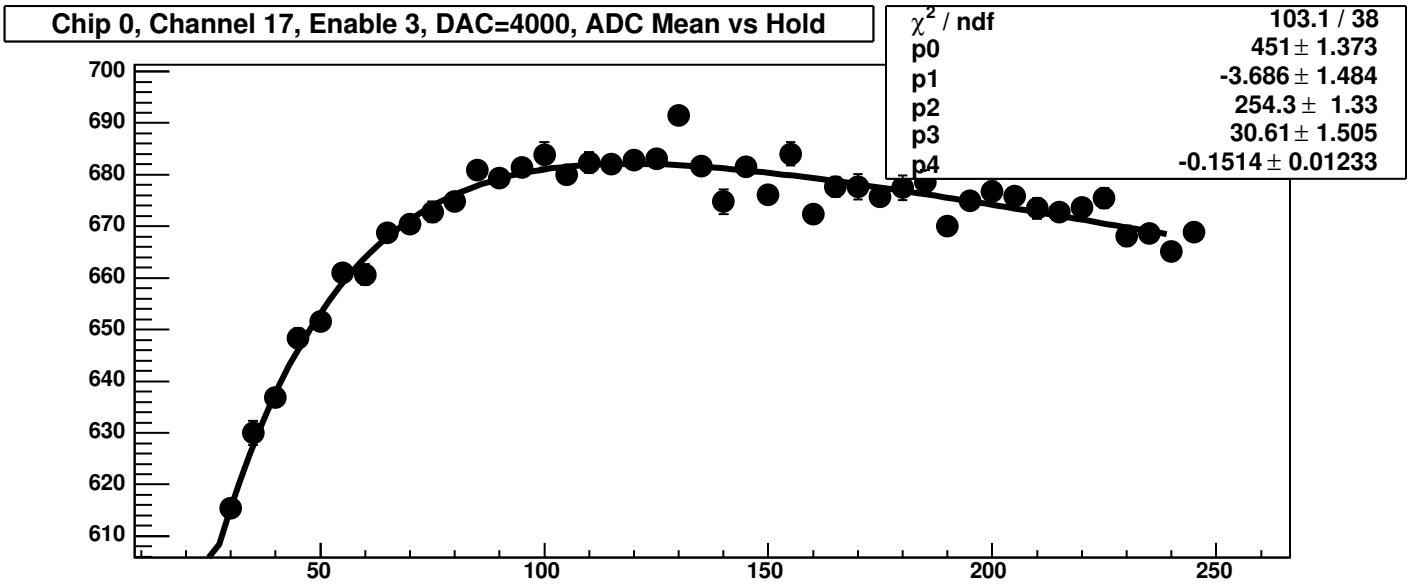
Chip 0, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

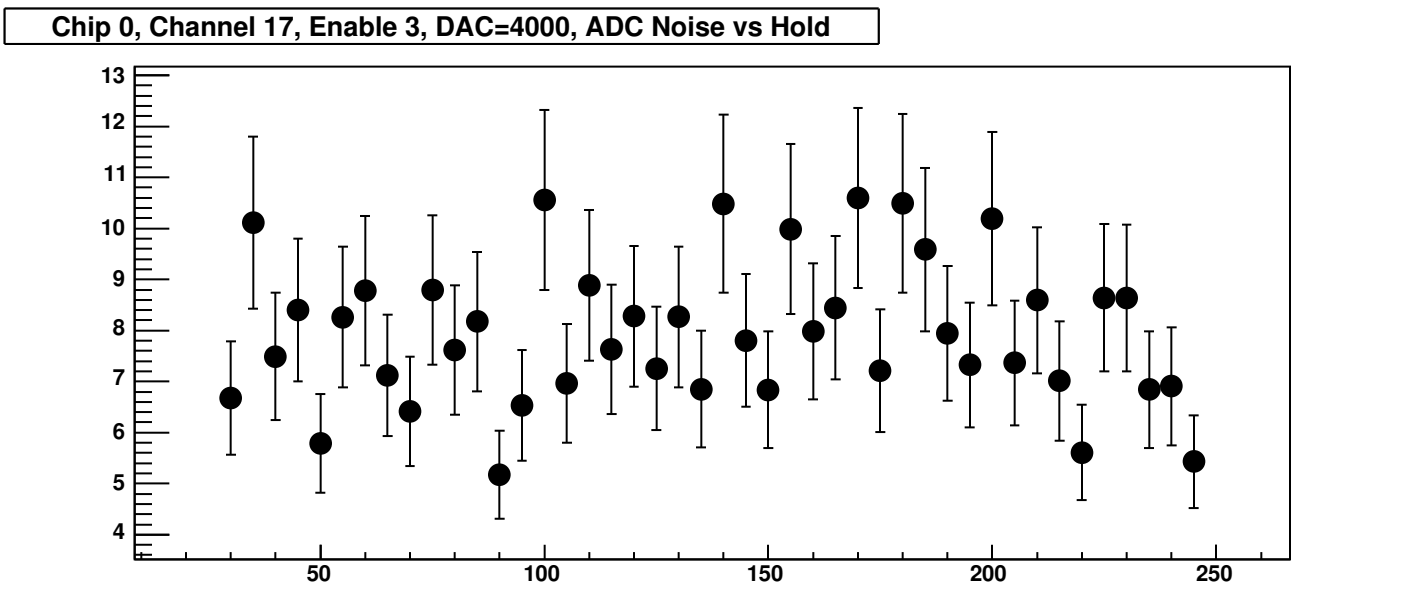


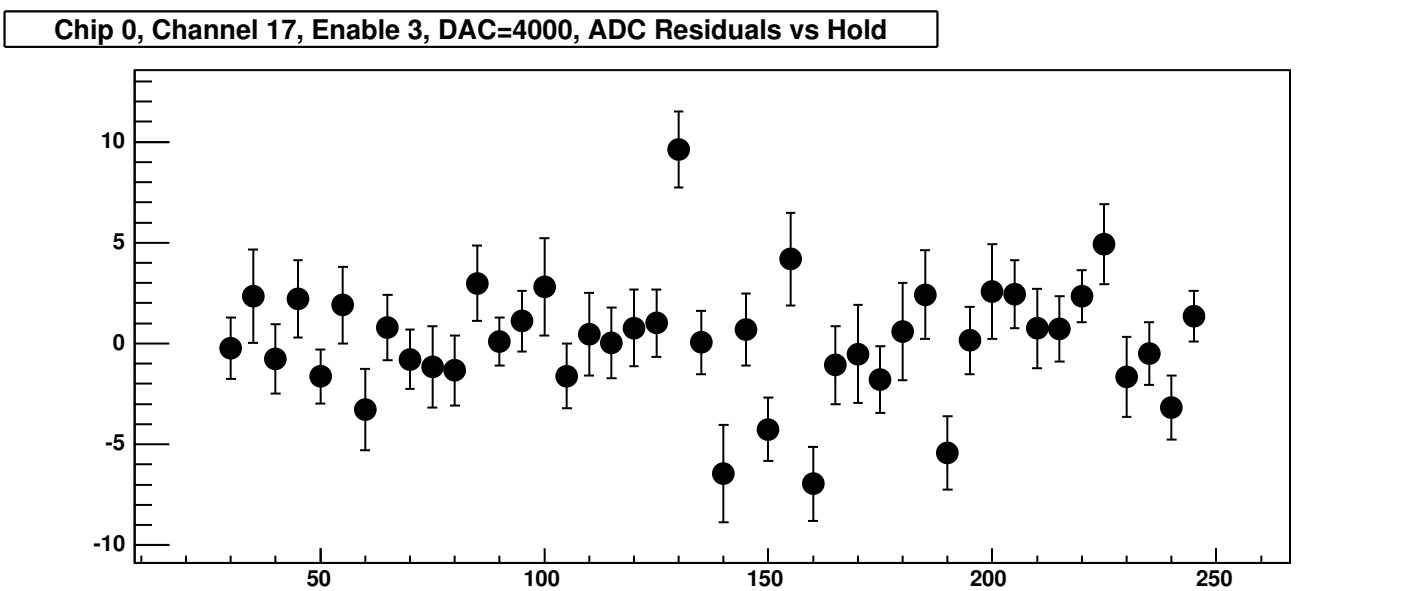

Chip 0, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 183 / 39
p0 -197 ± 0.7756
p1 5.129 ± 0.02979
p2 1.085e+04 ± 1.762
p3 31.09 ± 0.01048
10000
8000
6000
4000
2000
0
50
100
150
200
250


Chip 0, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold
40
35
30
25
20
15
10
5
50
100
150
200
250

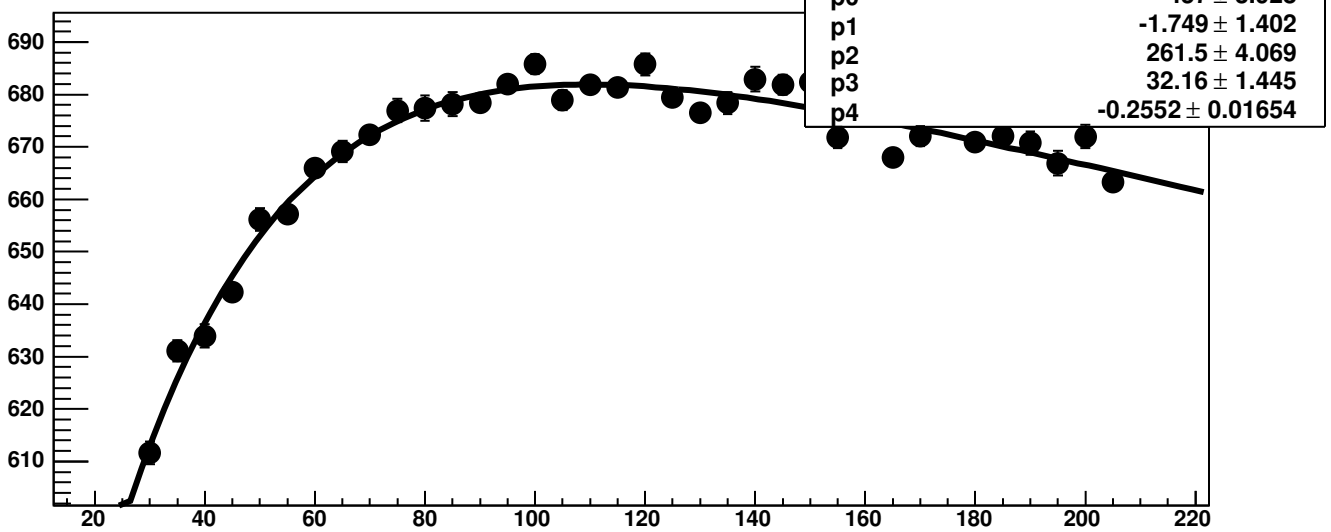

Chip 0, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
50
100
150
200
250


Chip 0, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 103.1 / 38
p0 451 ± 1.373
p1 -3.686 ± 1.484
p2 254.3 ± 1.33
p3 30.61 ± 1.505
p4 -0.1514 ± 0.01233
700
690
680
670
660
650
640
630
620
610
50
100
150
200
250

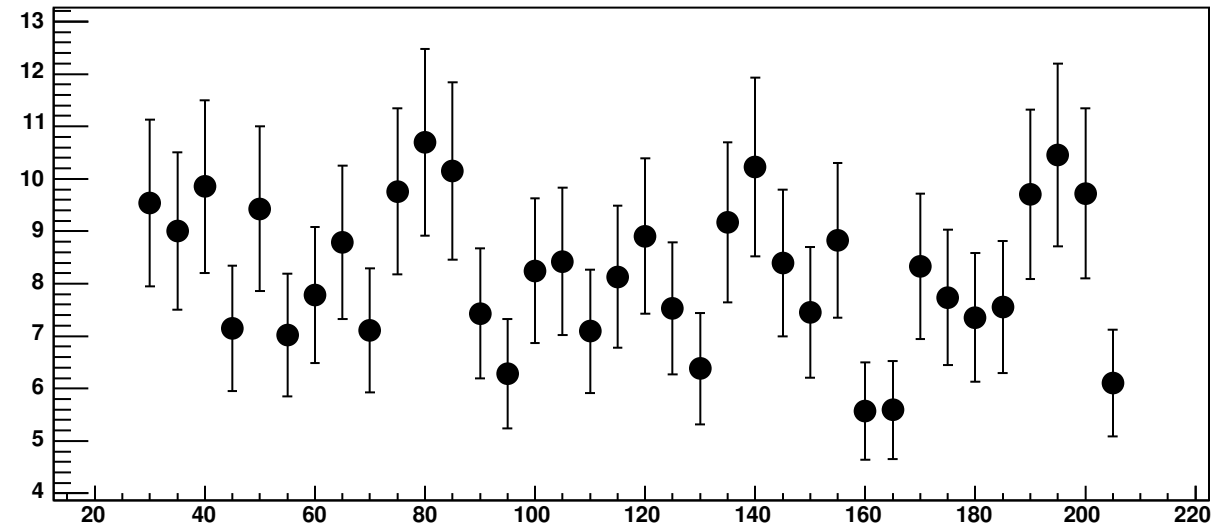

Chip 0, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 0, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

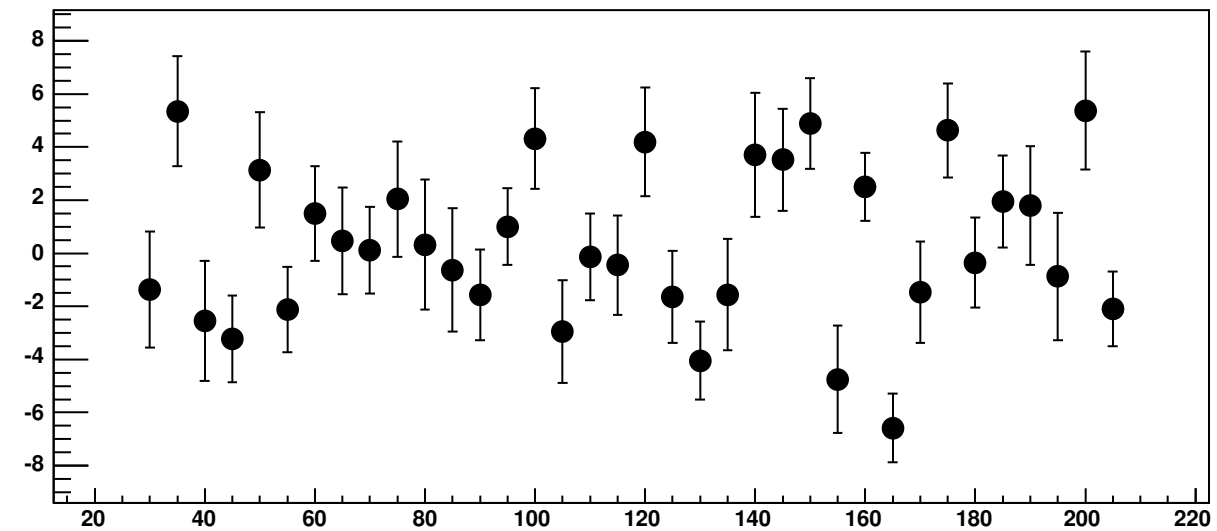
Chip 0, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold

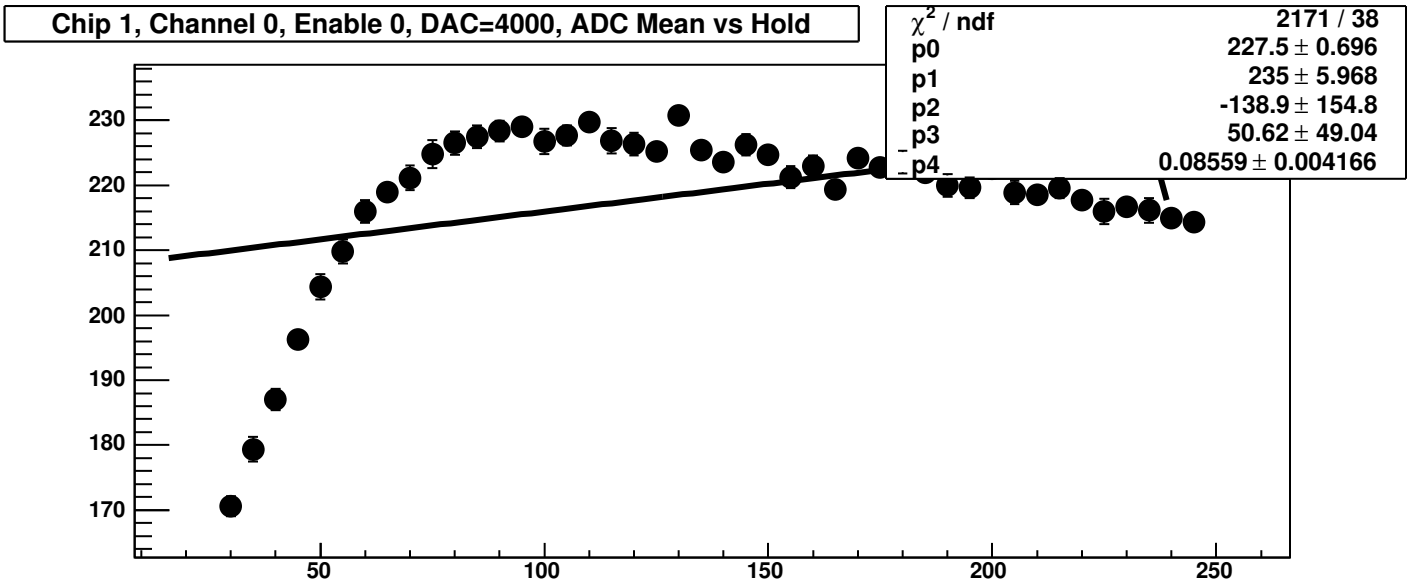


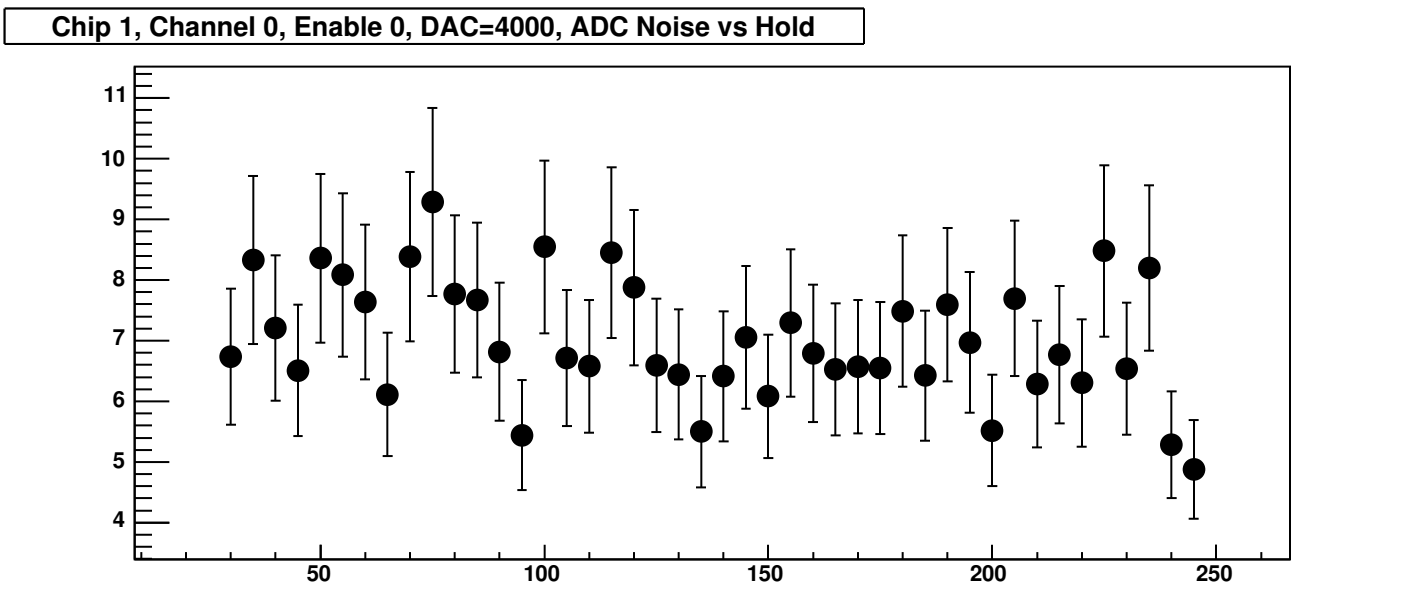
Chip 0, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold

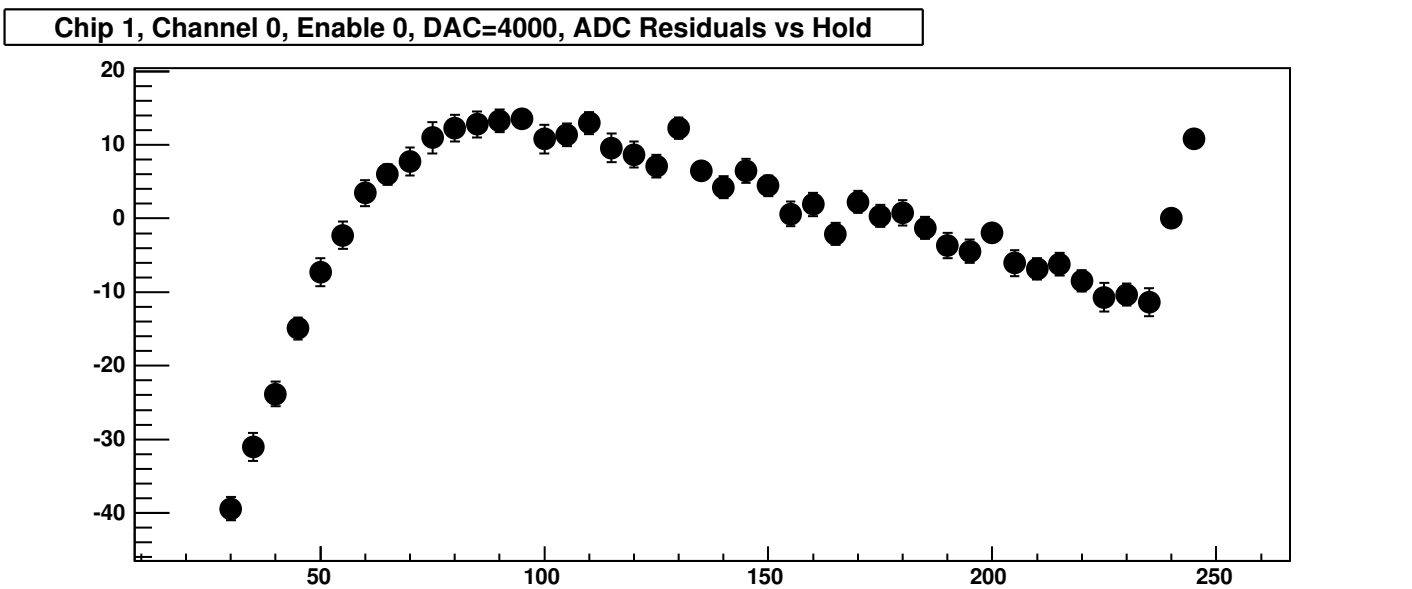


Chip 0, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

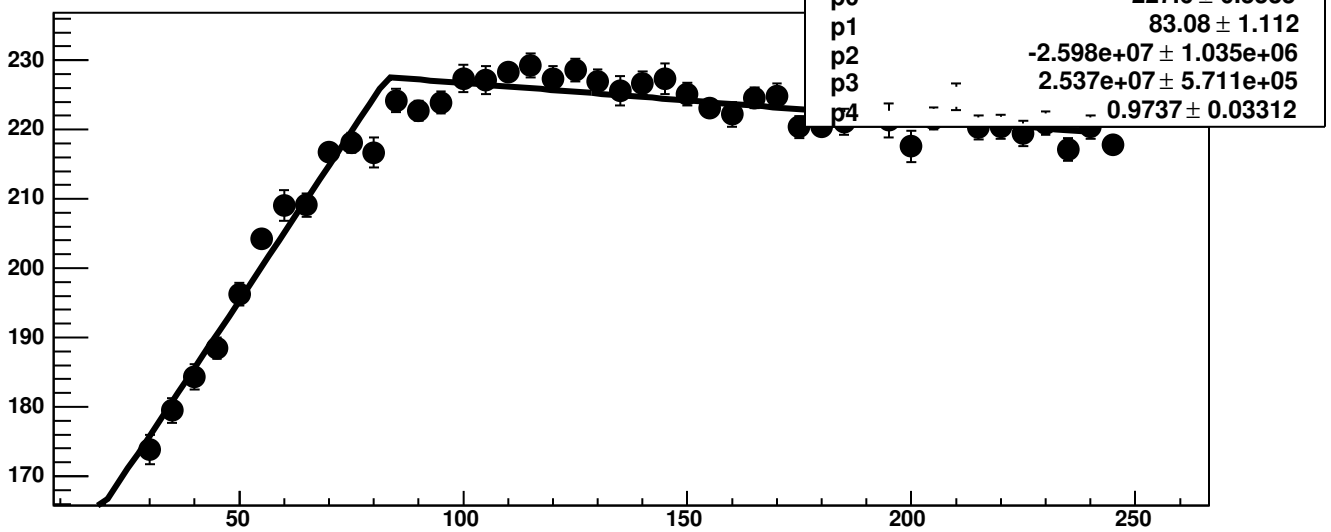



Chip 1, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 2171 / 38
p0 227.5 ± 0.696
p1 235 ± 5.968
p2 -138.9 ± 154.8
p3 50.62 ± 49.04
p4 0.08559 ± 0.004166
230
220
210
200
190
180
170
50
100
150
200
250

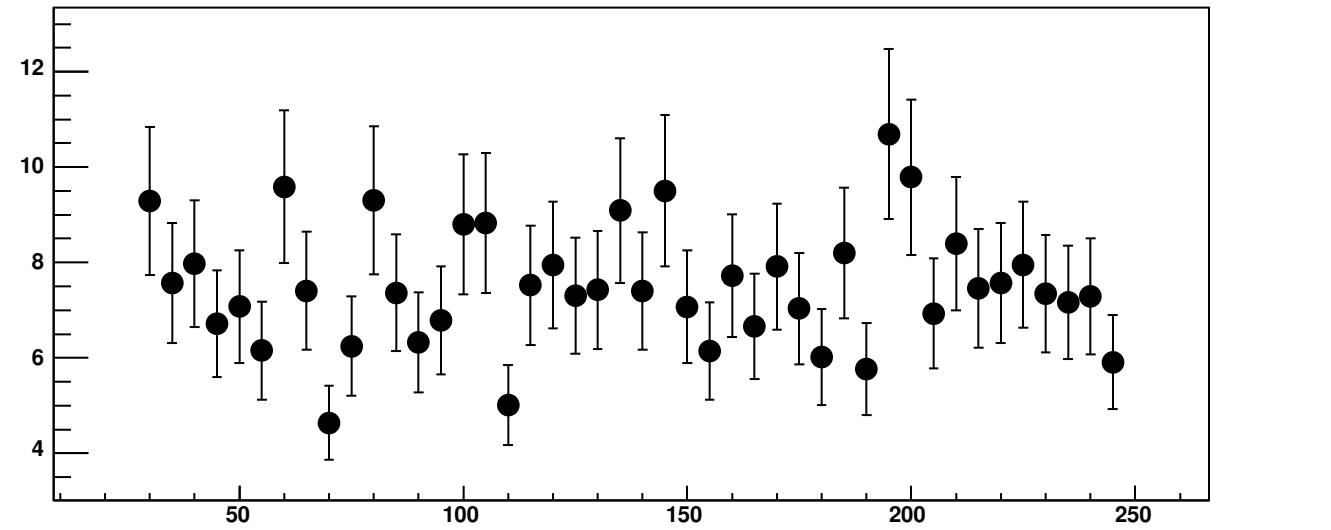

Chip 1, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 1, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
50
100
150
200
250

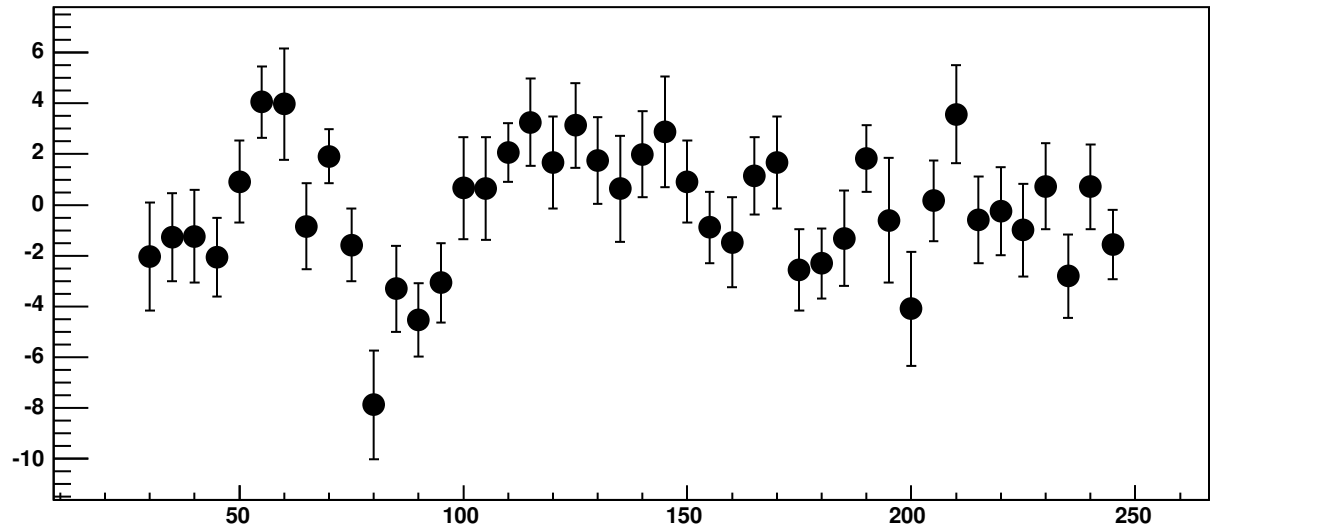
Chip 1, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold



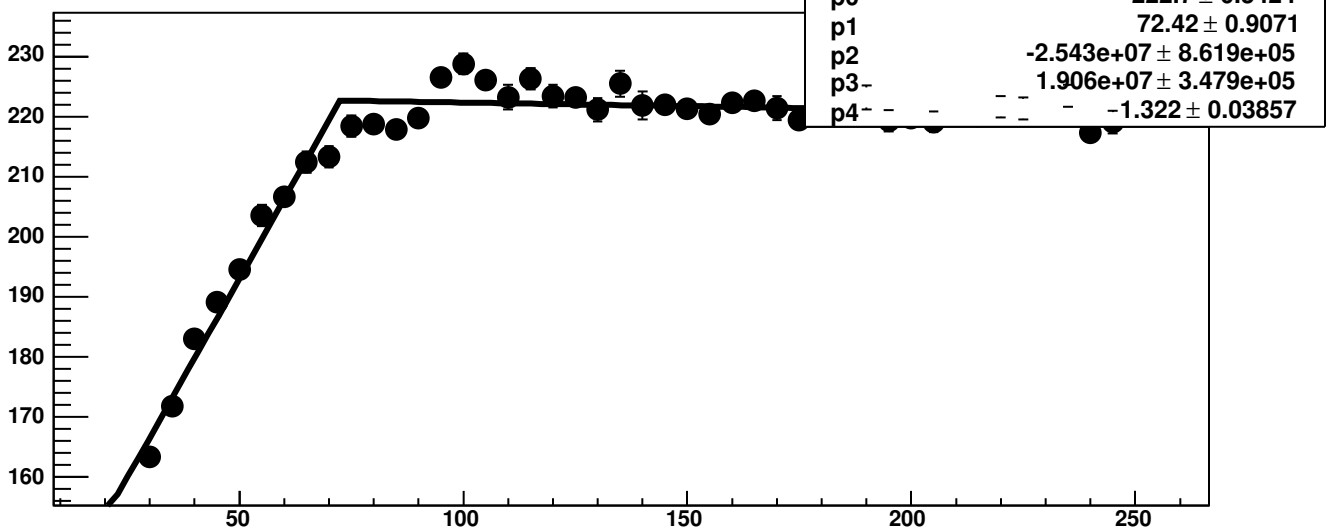
Chip 1, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



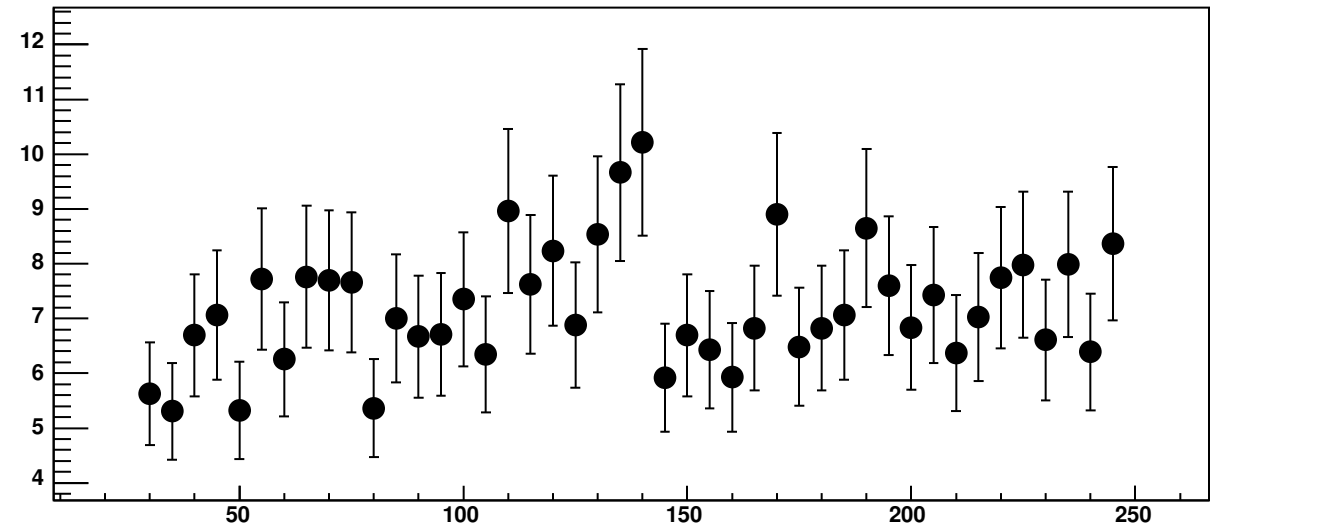
Chip 1, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold



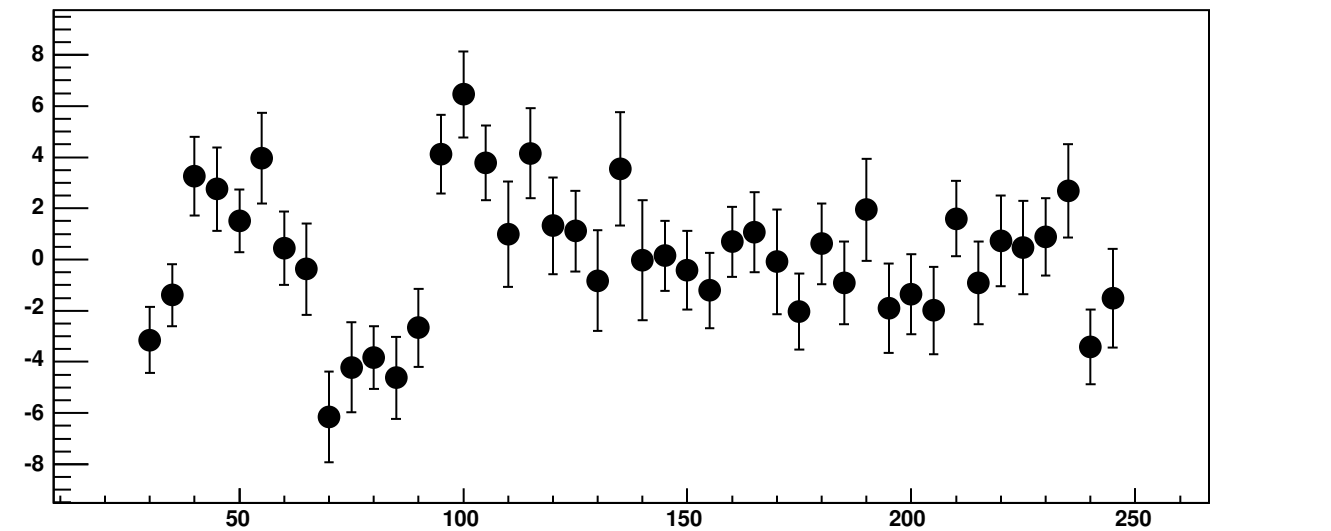
Chip 1, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold

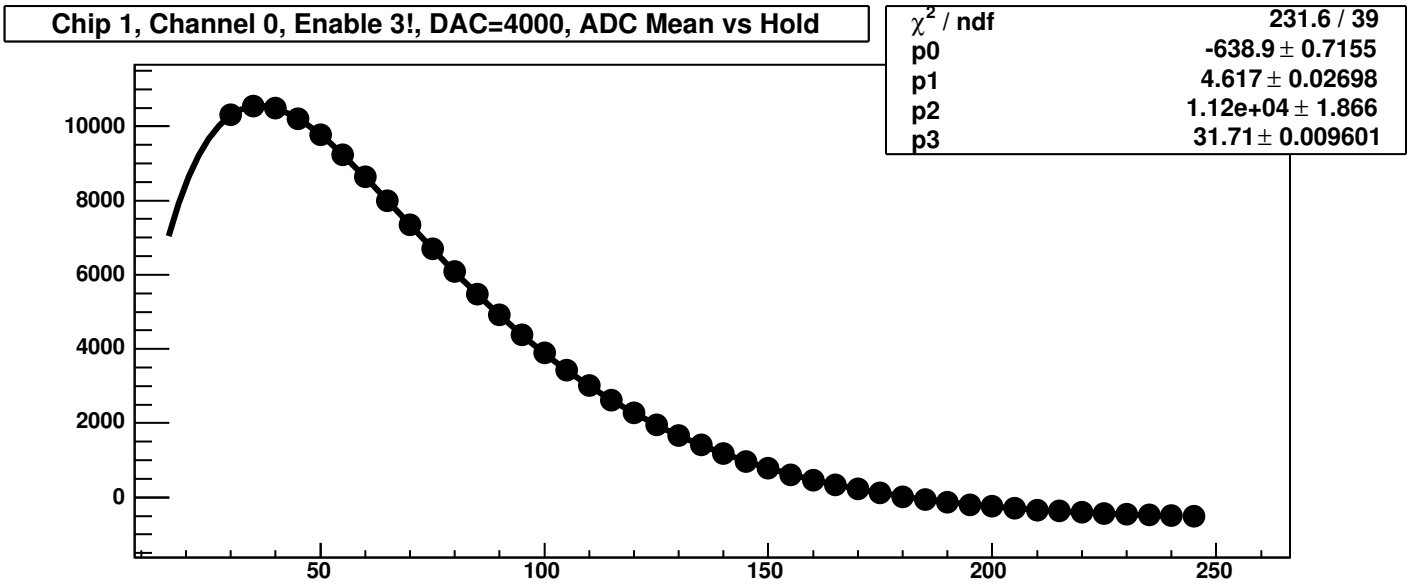


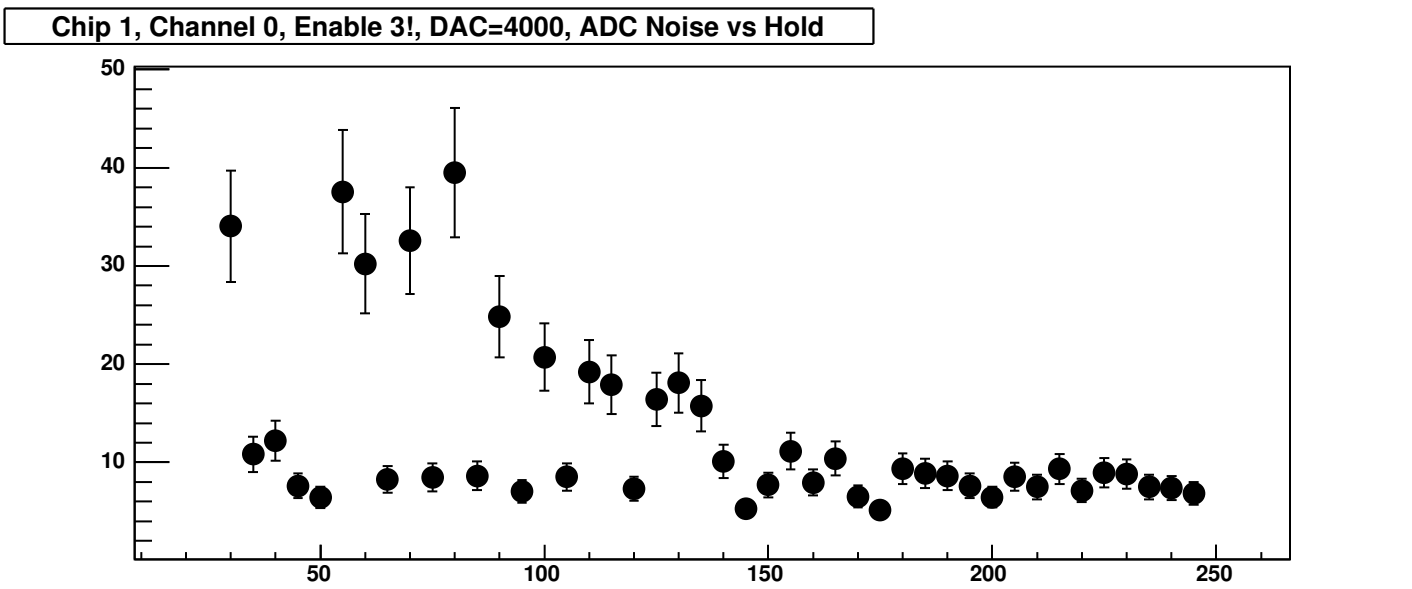
Chip 1, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold

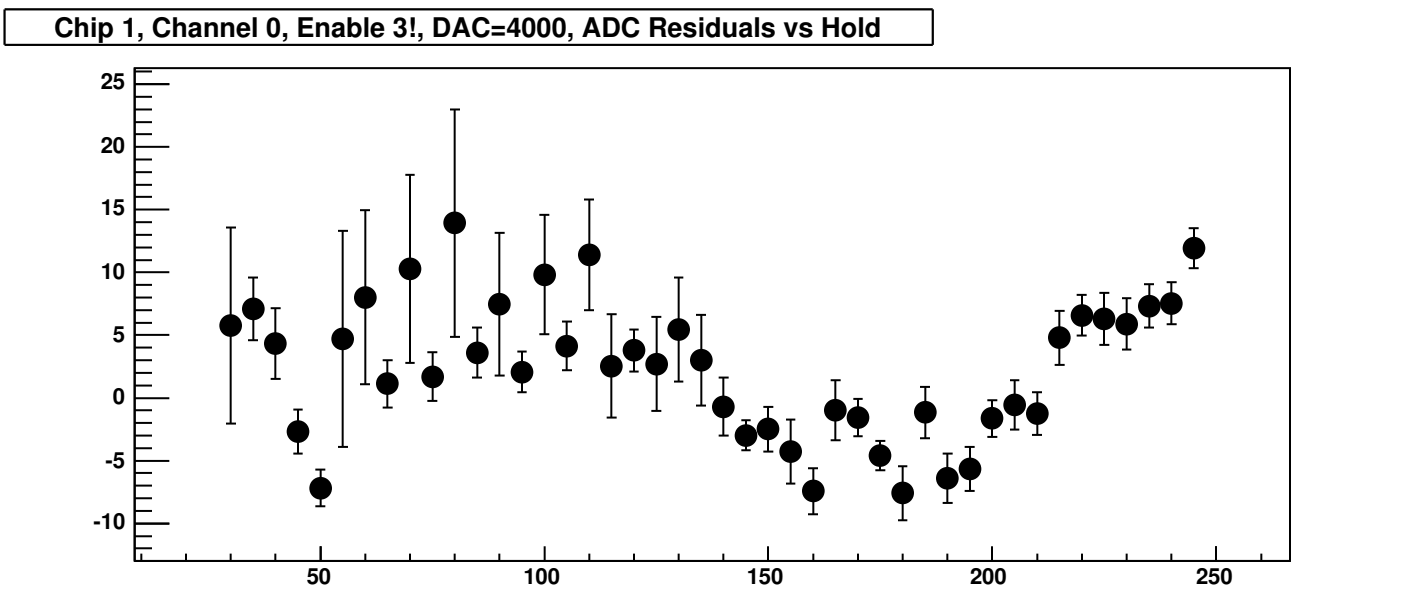


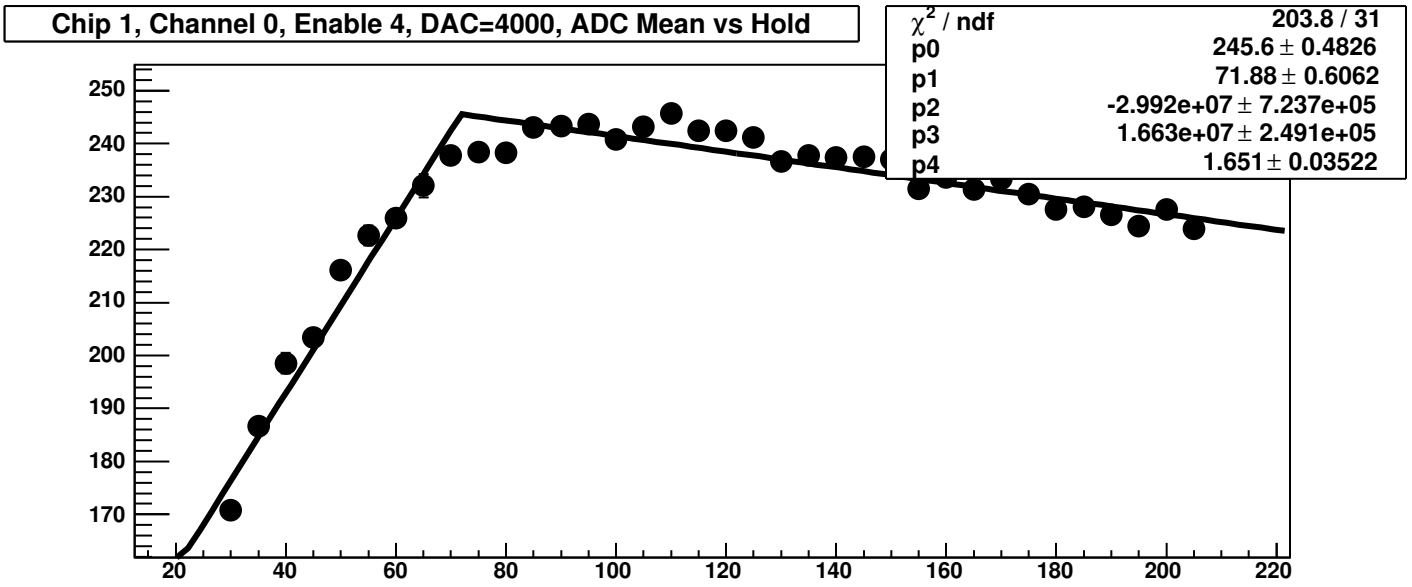
Chip 1, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold

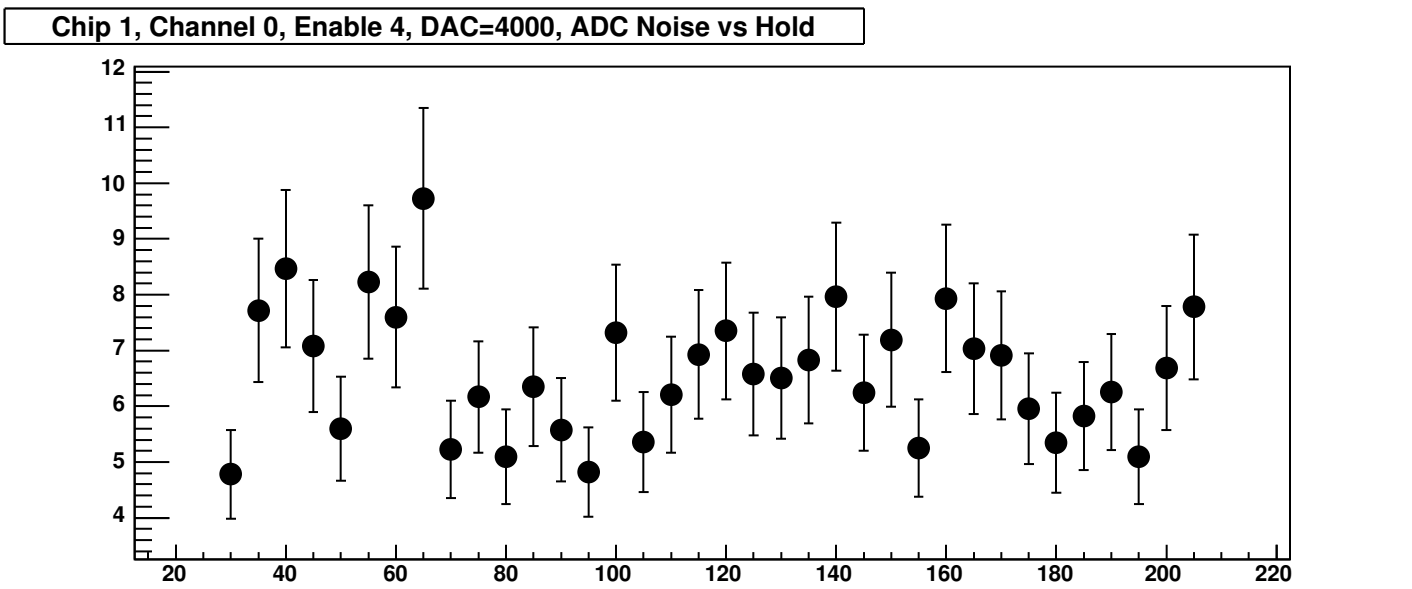


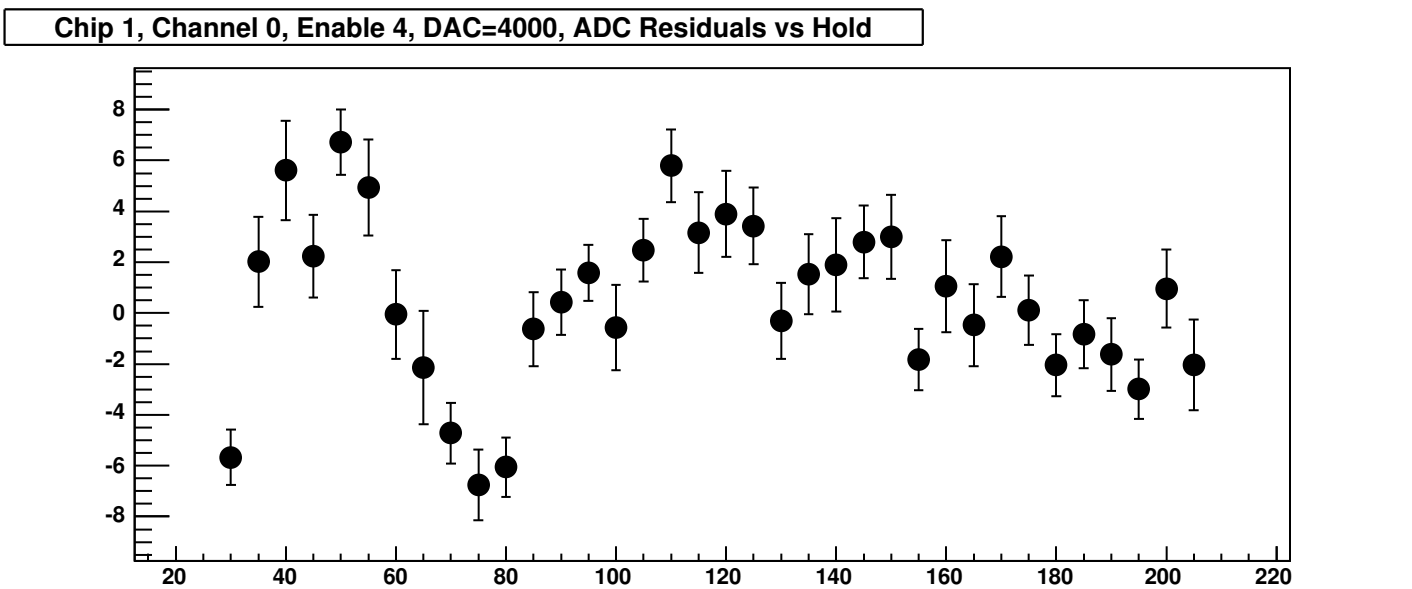

Chip 1, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 231.6 / 39
p0 -638.9 ± 0.7155
p1 4.617 ± 0.02698
p2 1.12e+04 ± 1.866
p3 31.71 ± 0.009601
10000
8000
6000
4000
2000
0
50
100
150
200
250


Chip 1, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
50
100
150
200
250


Chip 1, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
50
100
150
200
250

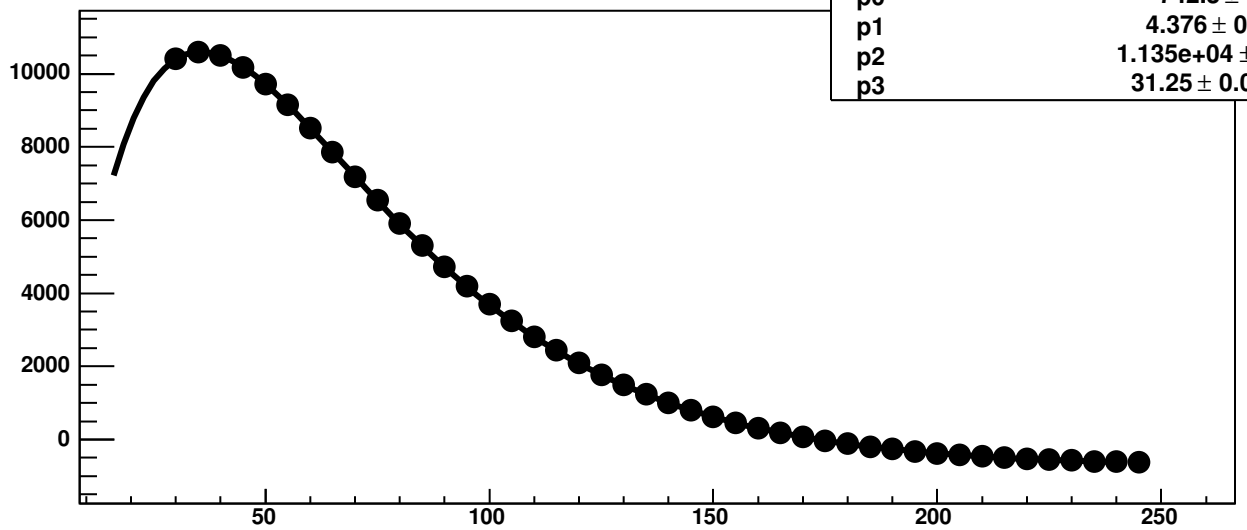

Chip 1, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 203.8 / 31
p0 245.6 ± 0.4826
p1 71.88 ± 0.6062
p2 -2.992e+07 ± 7.237e+05
p3 1.663e+07 ± 2.491e+05
p4 1.651 ± 0.03522


Chip 1, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold

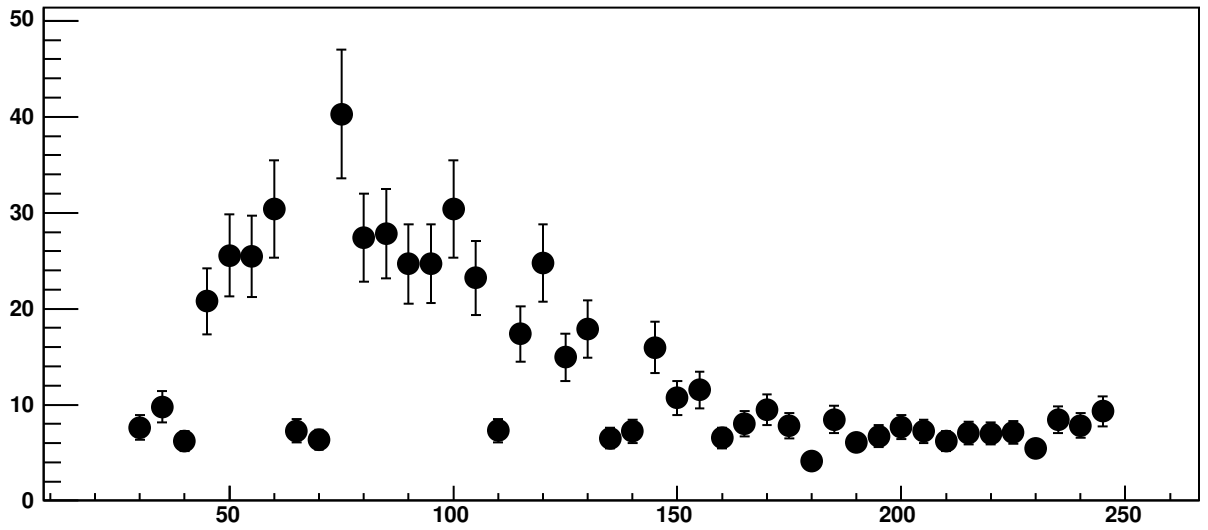

Chip 1, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

Chip 1, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold

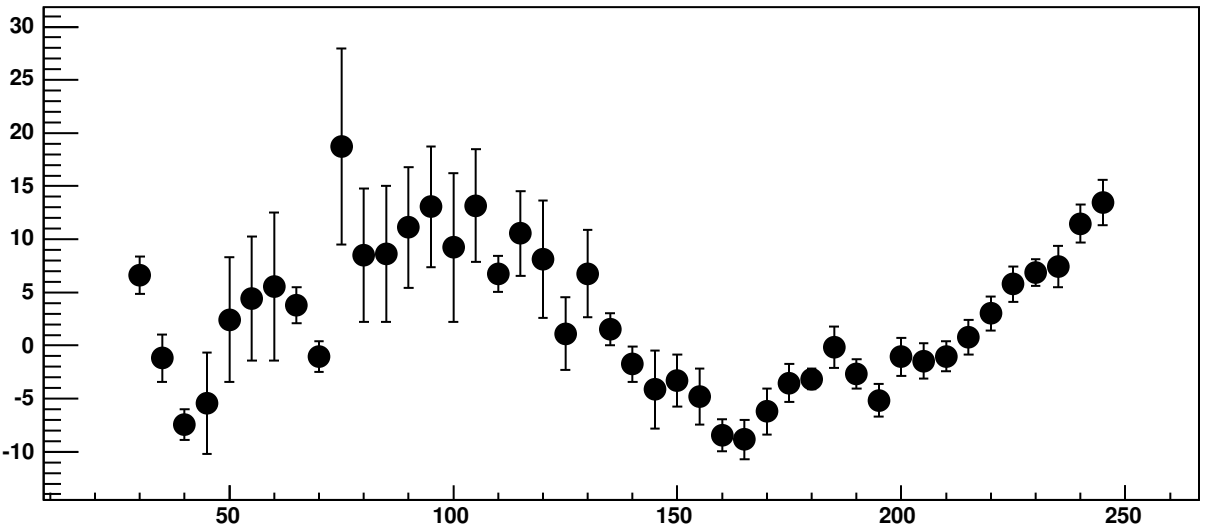
χ^2 / ndf	308 / 39
p0	-742.3 ± 0.6356
p1	4.376 ± 0.01863
p2	1.135e+04 ± 1.046
p3	31.25 ± 0.008567

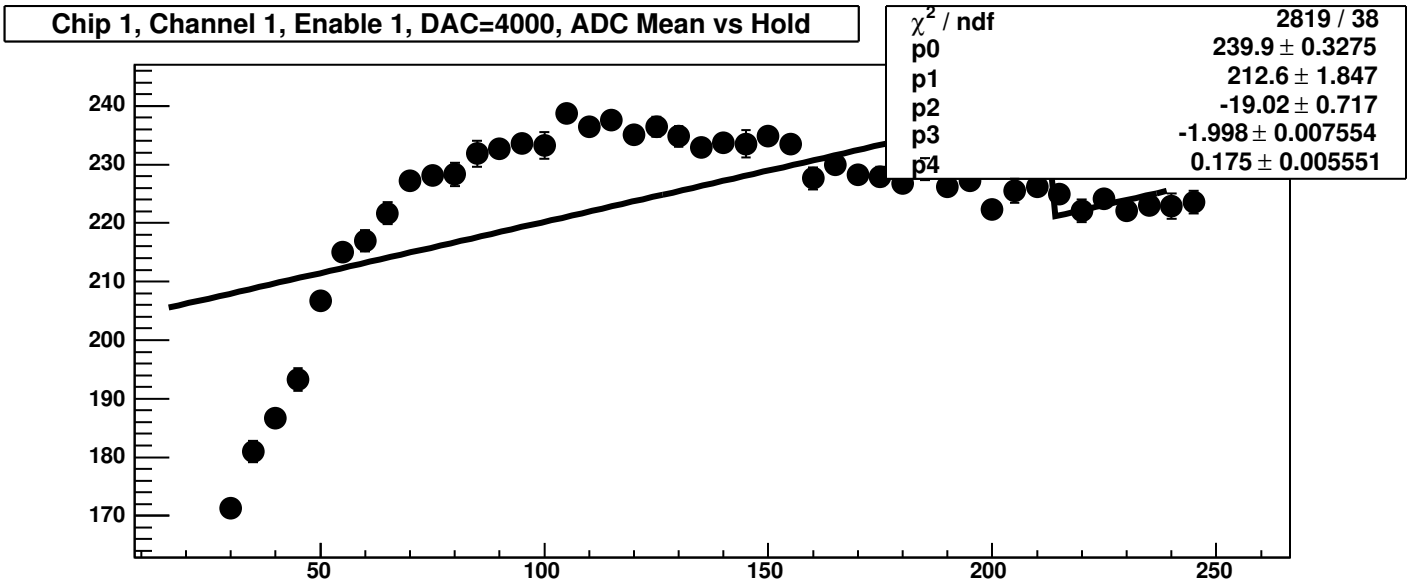


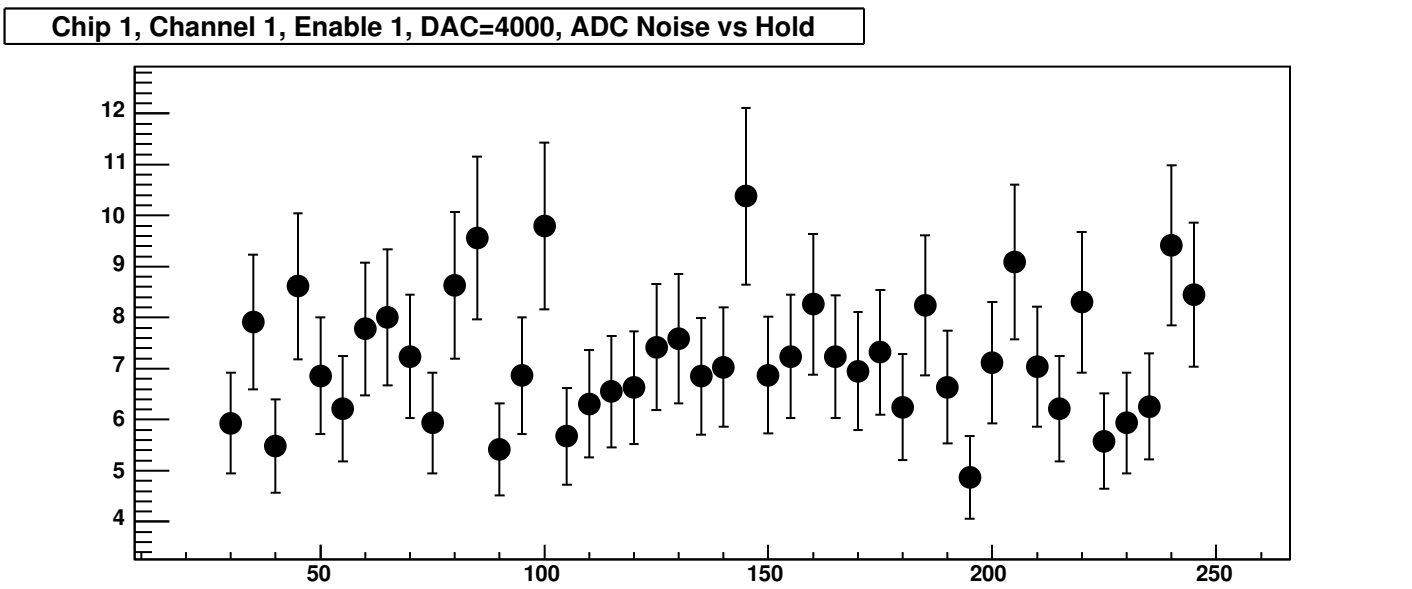
Chip 1, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold

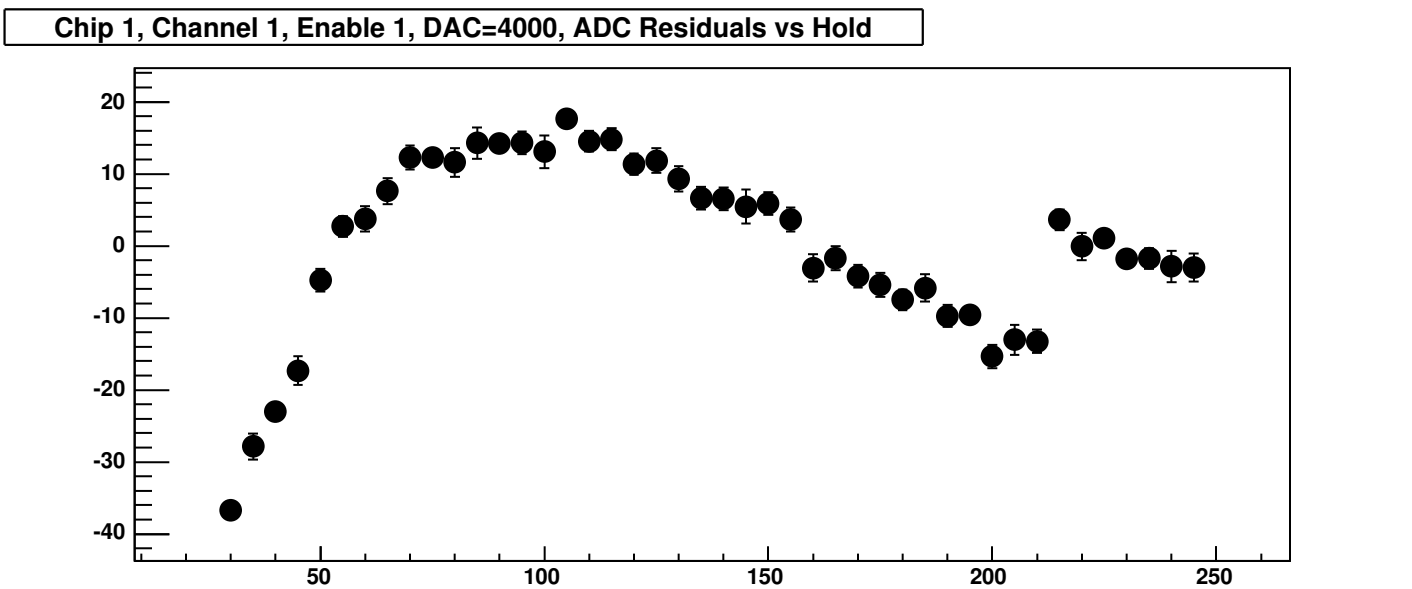


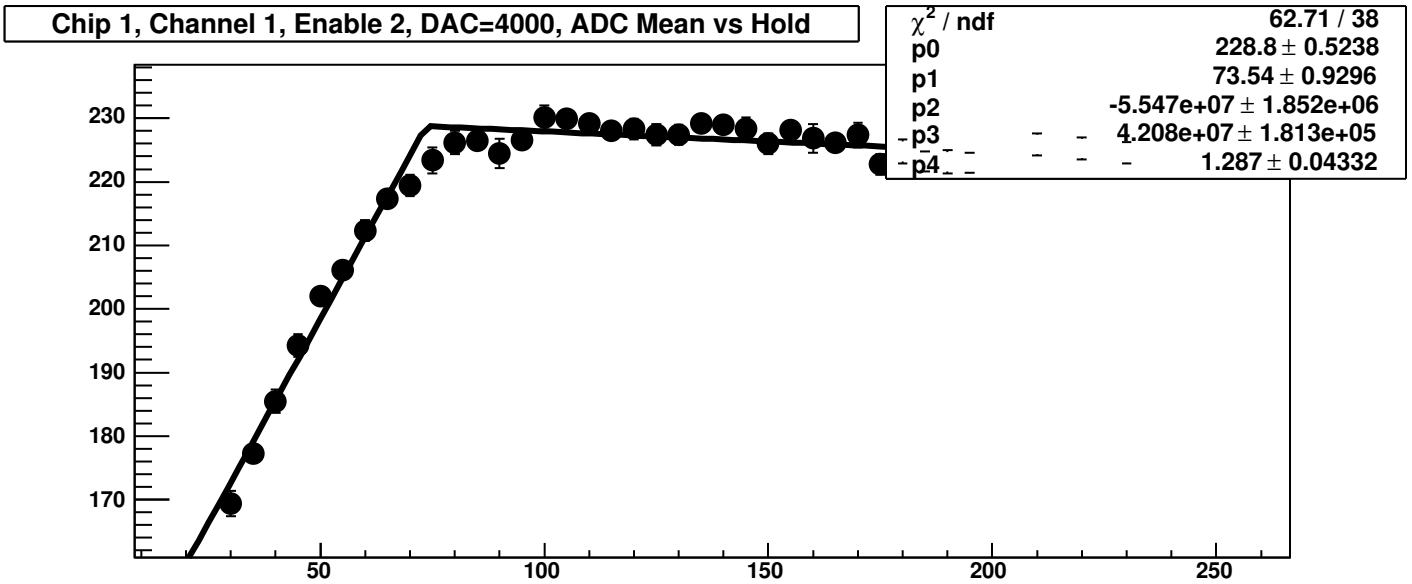
Chip 1, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold

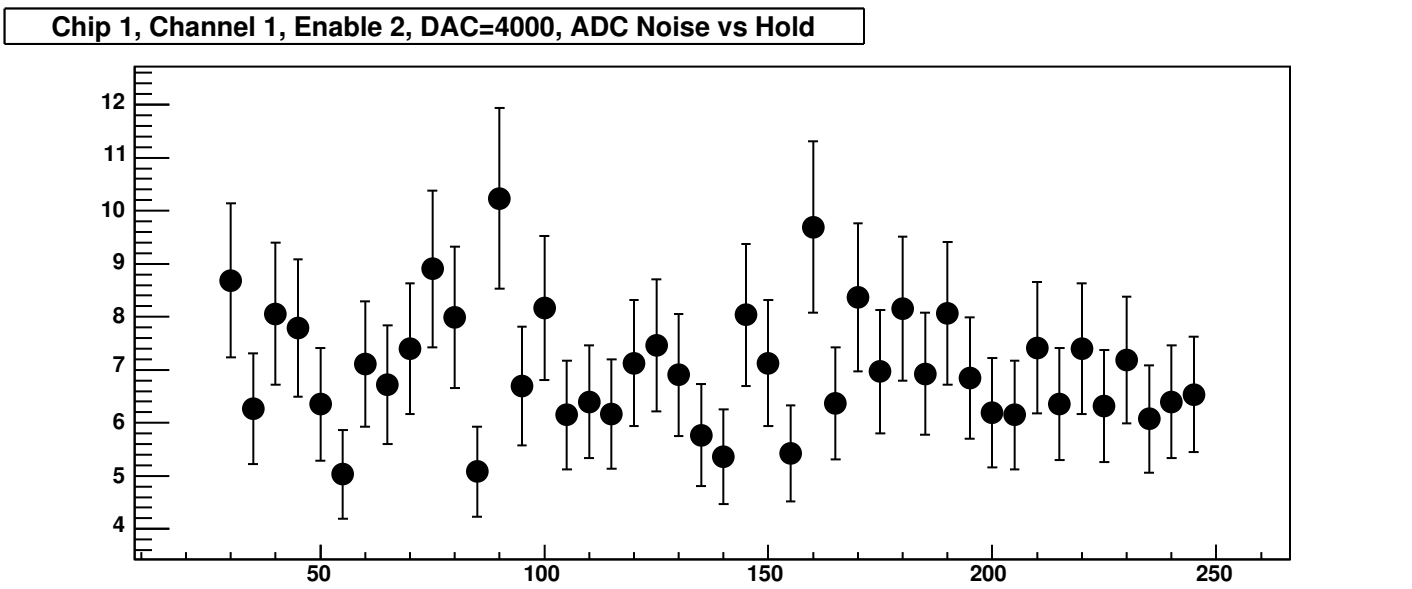


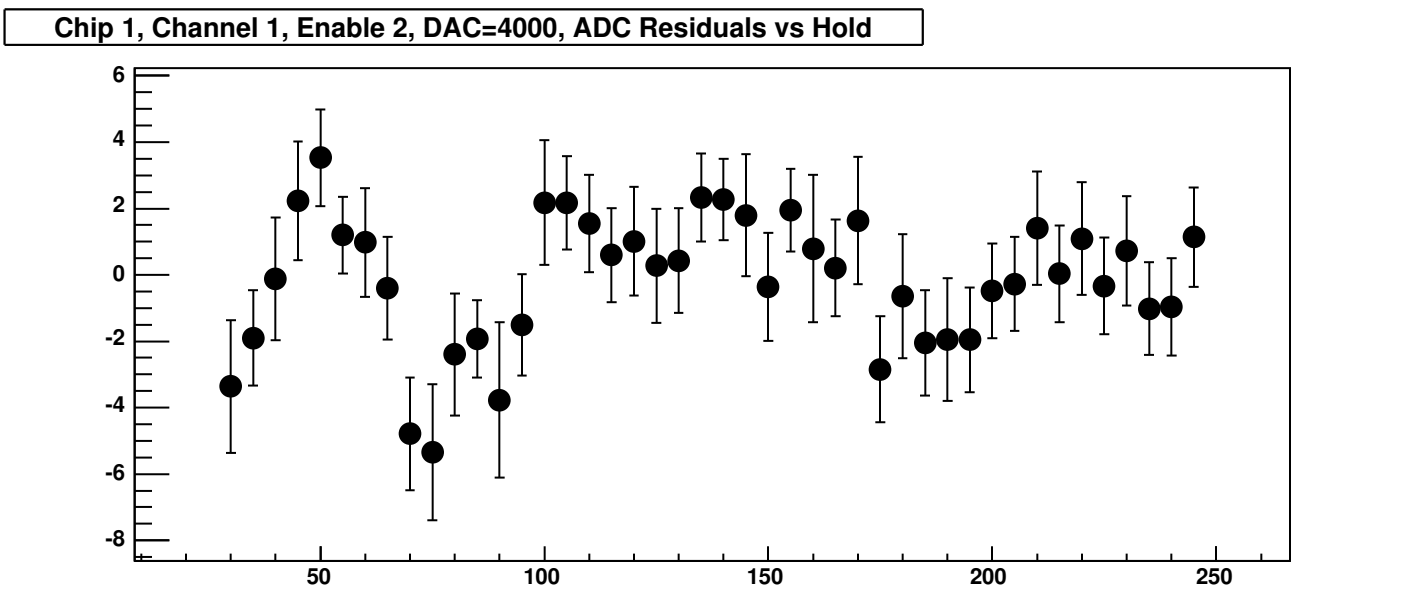

Chip 1, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 2819 / 38
p0 239.9 ± 0.3275
p1 212.6 ± 1.847
p2 -19.02 ± 0.717
p3 -1.998 ± 0.007554
p4 0.175 ± 0.005551
240
230
220
210
200
190
180
170
50
100
150
200
250

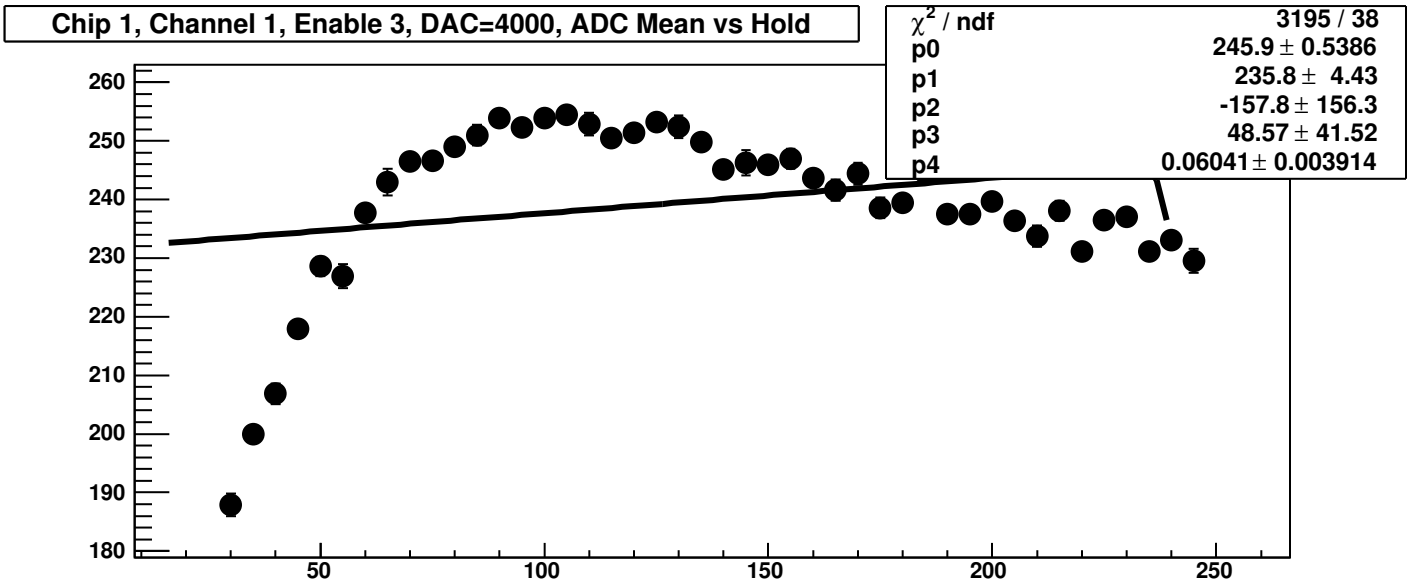

Chip 1, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

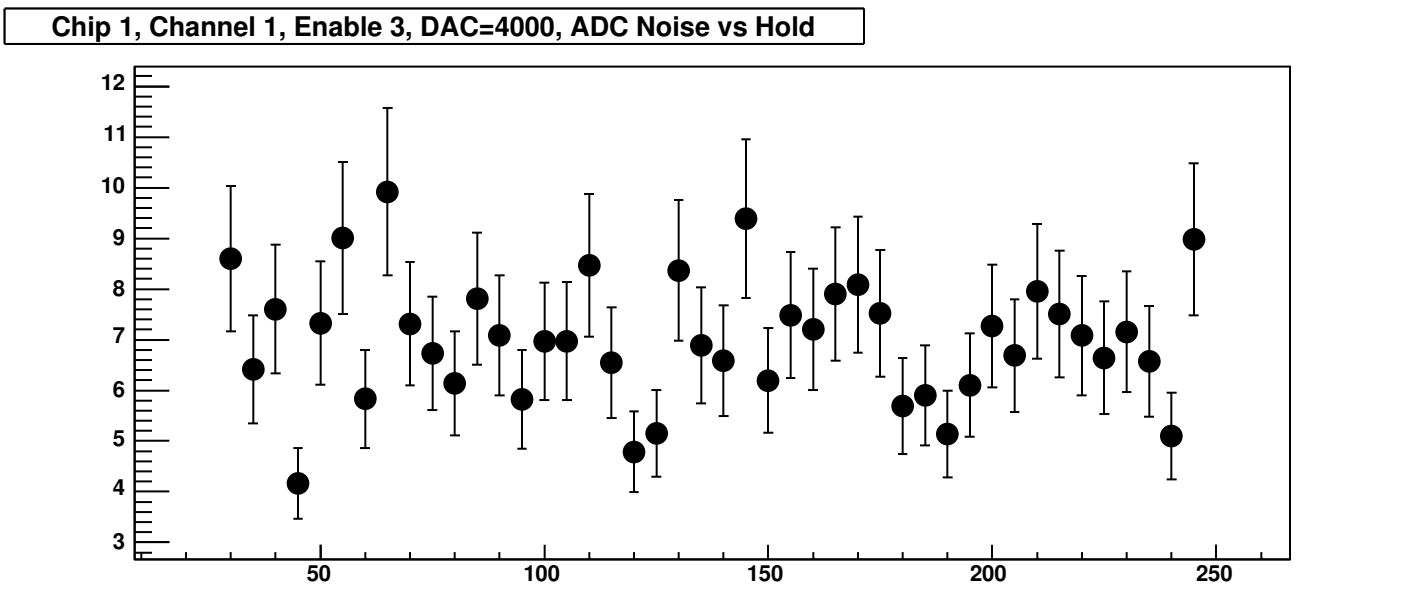

Chip 1, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
50
100
150
200
250

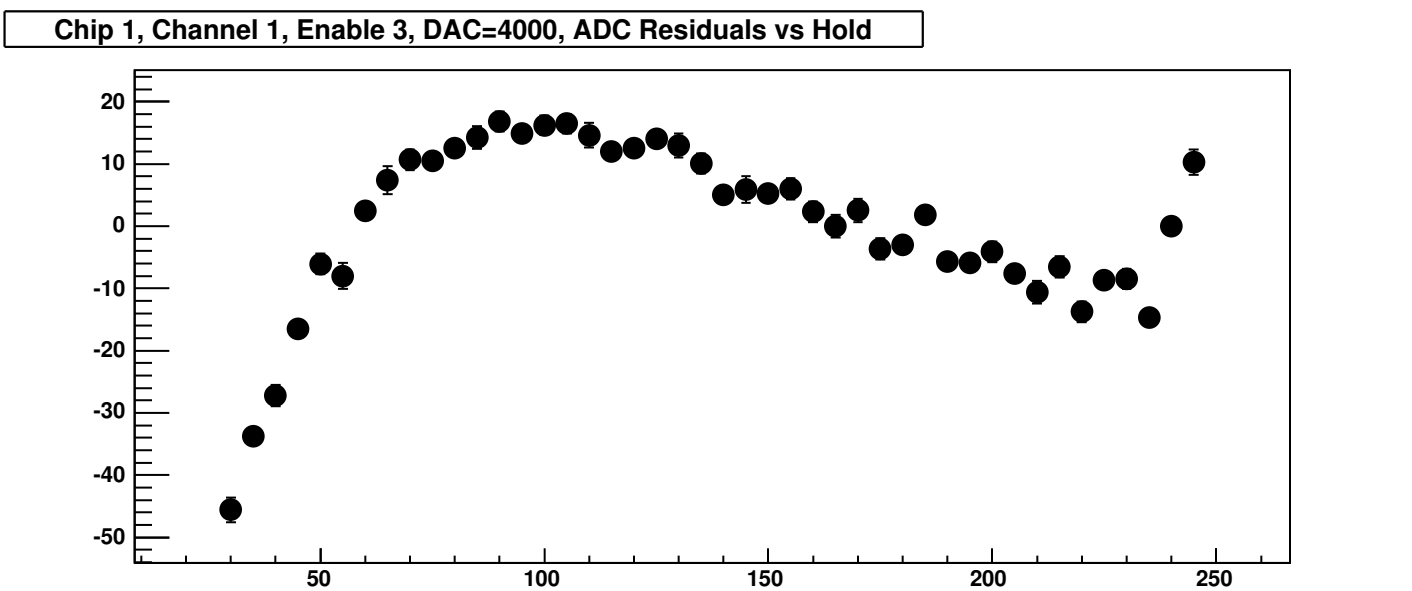

Chip 1, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 62.71 / 38
p0 228.8 ± 0.5238
p1 73.54 ± 0.9296
p2 -5.547e+07 ± 1.852e+06
p3 4.208e+07 ± 1.813e+05
p4 1.287 ± 0.04332
230
220
210
200
190
180
170
50
100
150
200
250

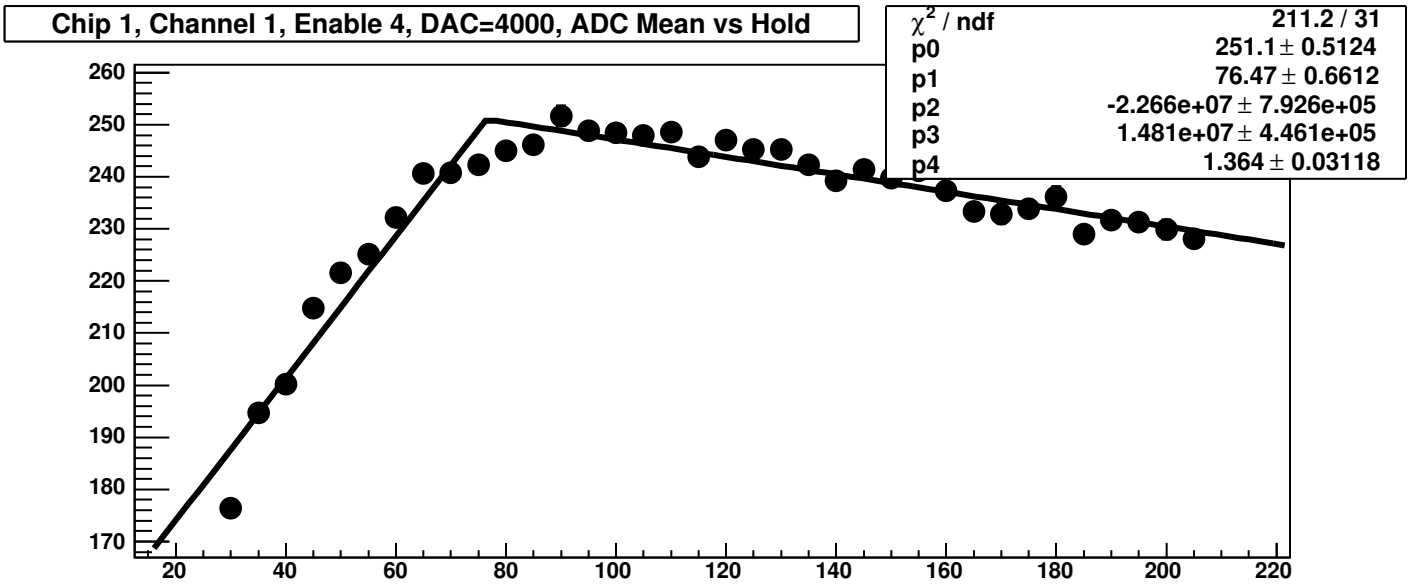

Chip 1, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

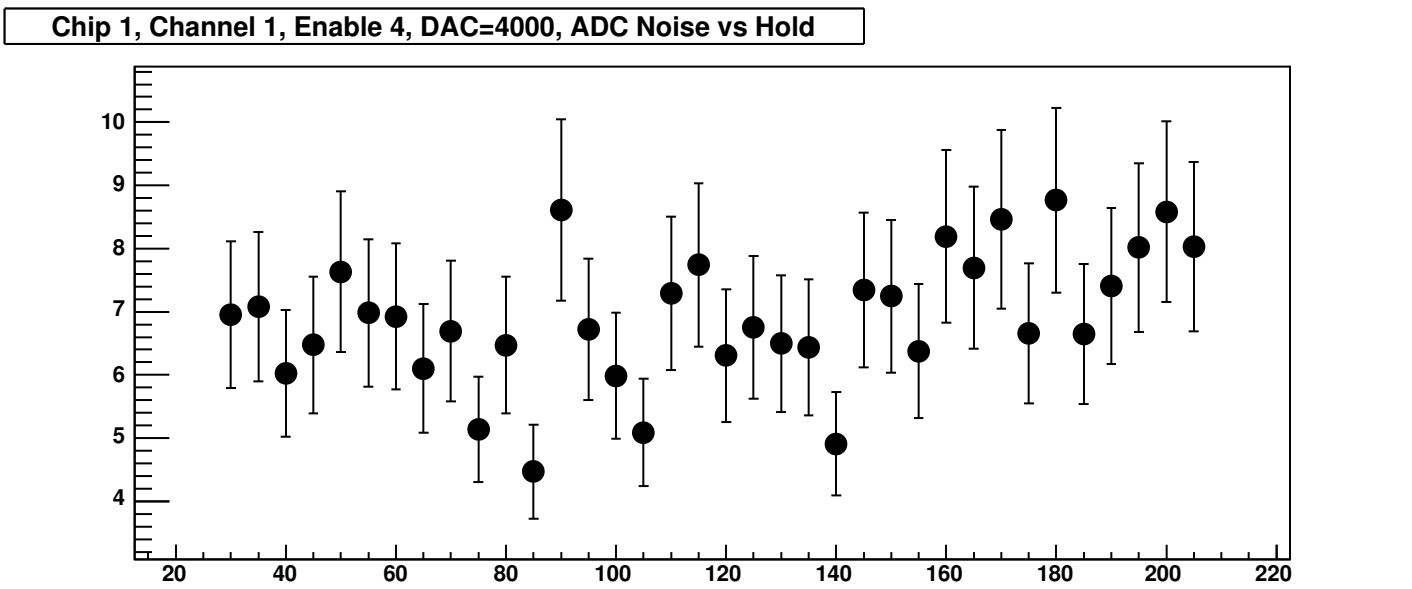

Chip 1, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

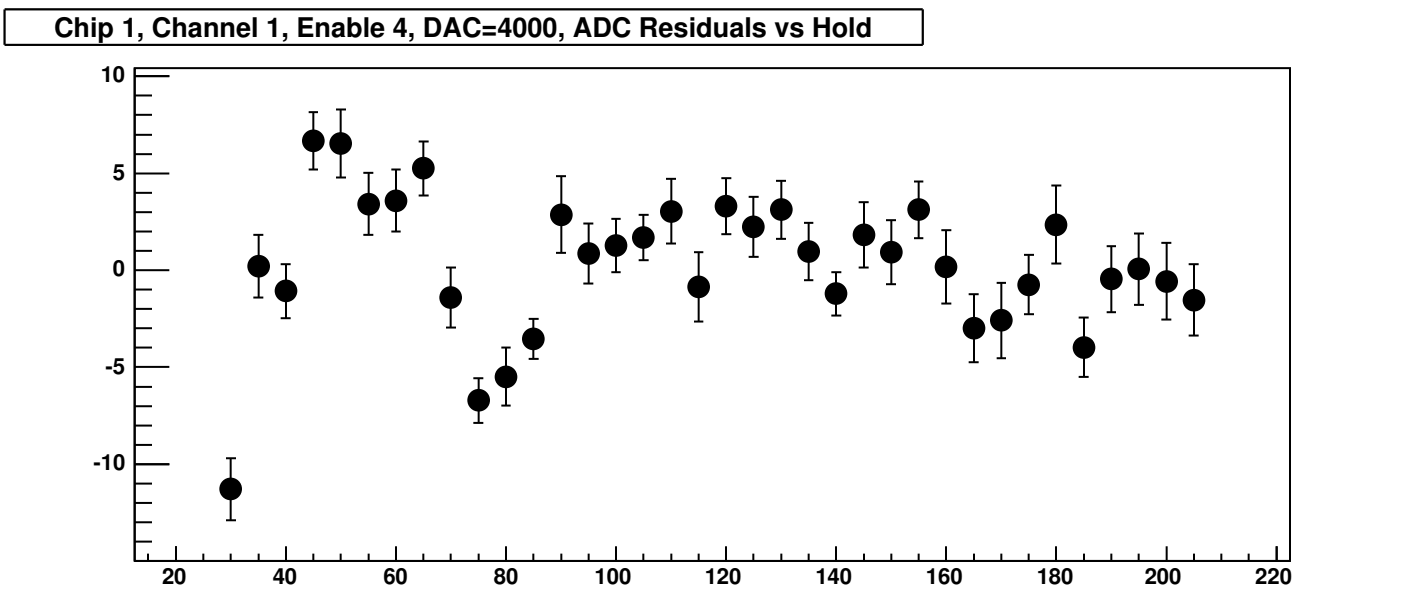

Chip 1, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 3195 / 38
p0 245.9 ± 0.5386
p1 235.8 ± 4.43
p2 -157.8 ± 156.3
p3 48.57 ± 41.52
p4 0.06041 ± 0.003914
260
250
240
230
220
210
200
190
180
50
100
150
200
250

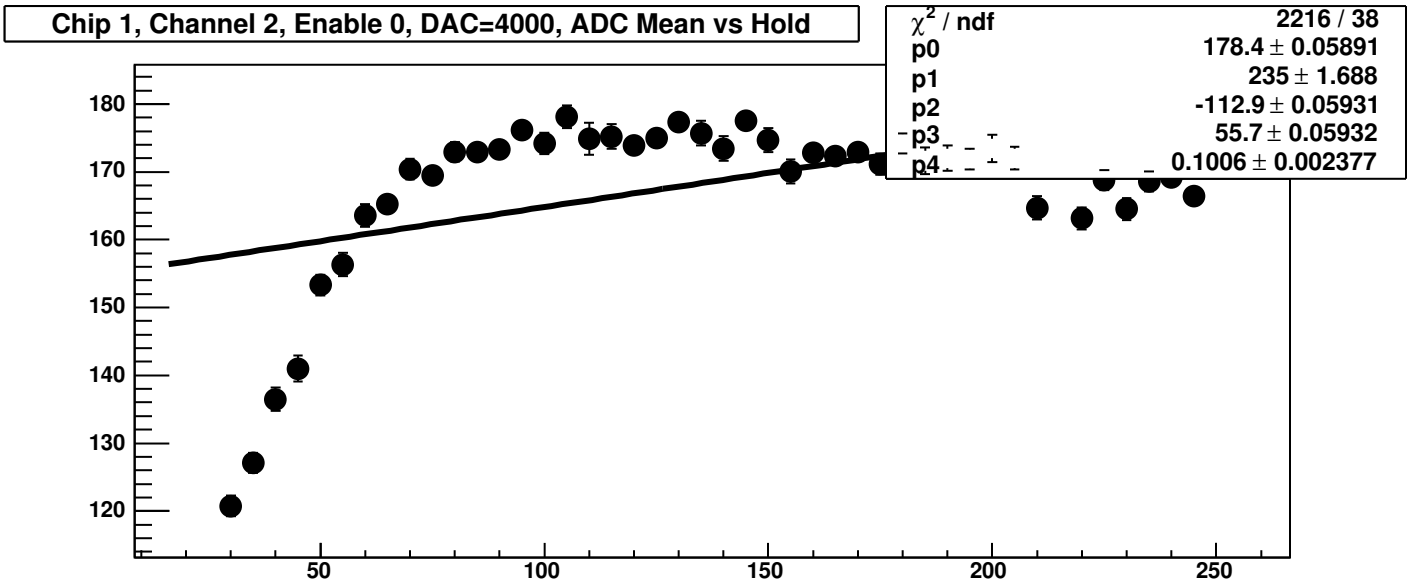

Chip 1, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250

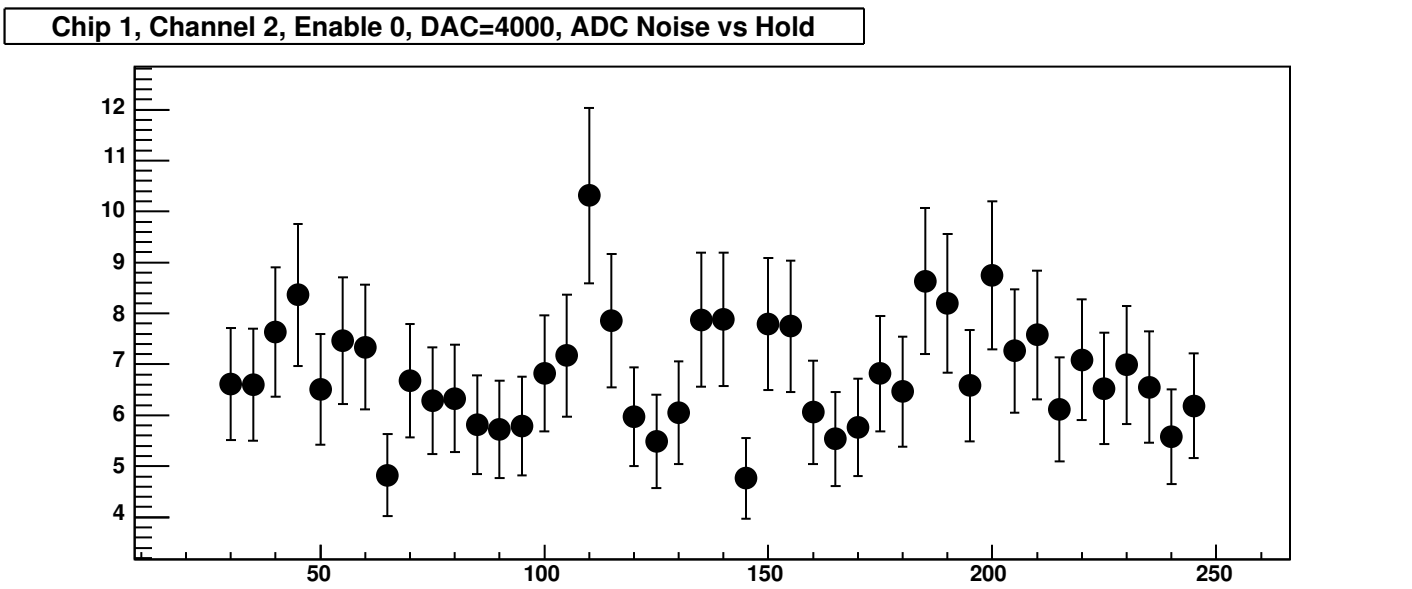

Chip 1, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
-50
50
100
150
200
250

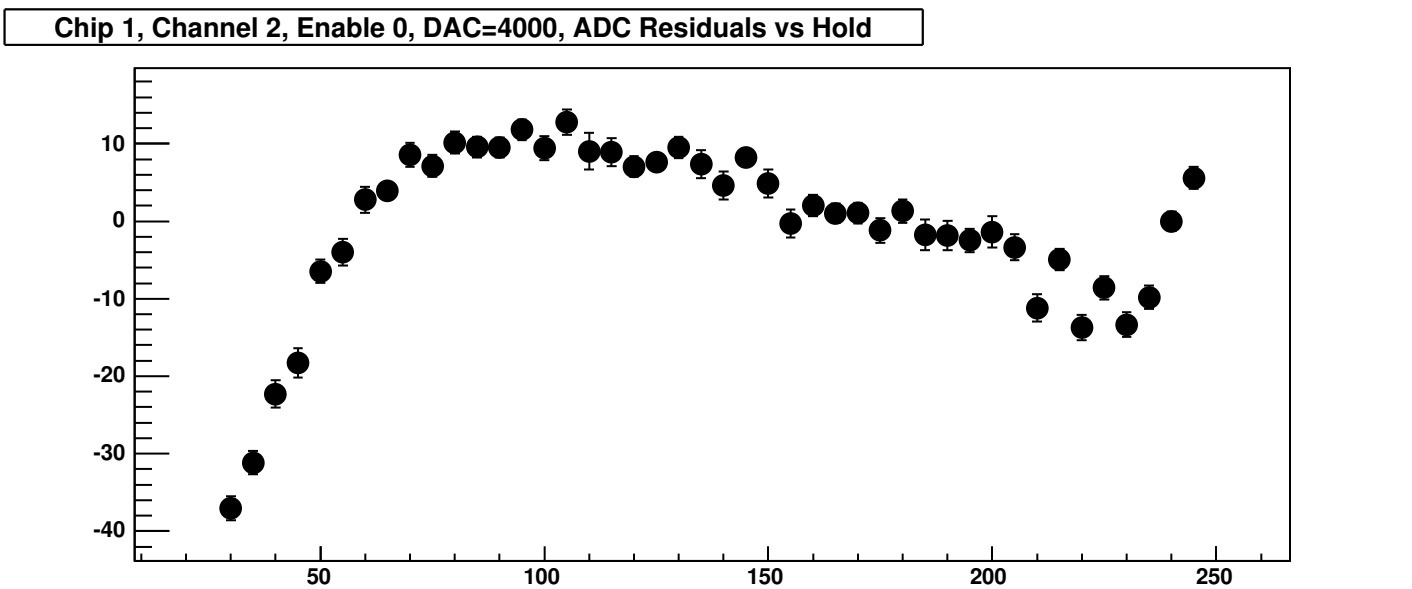

Chip 1, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 211.2 / 31
p0 251.1 ± 0.5124
p1 76.47 ± 0.6612
p2 -2.266e+07 ± 7.926e+05
p3 1.481e+07 ± 4.461e+05
p4 1.364 ± 0.03118


Chip 1, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold

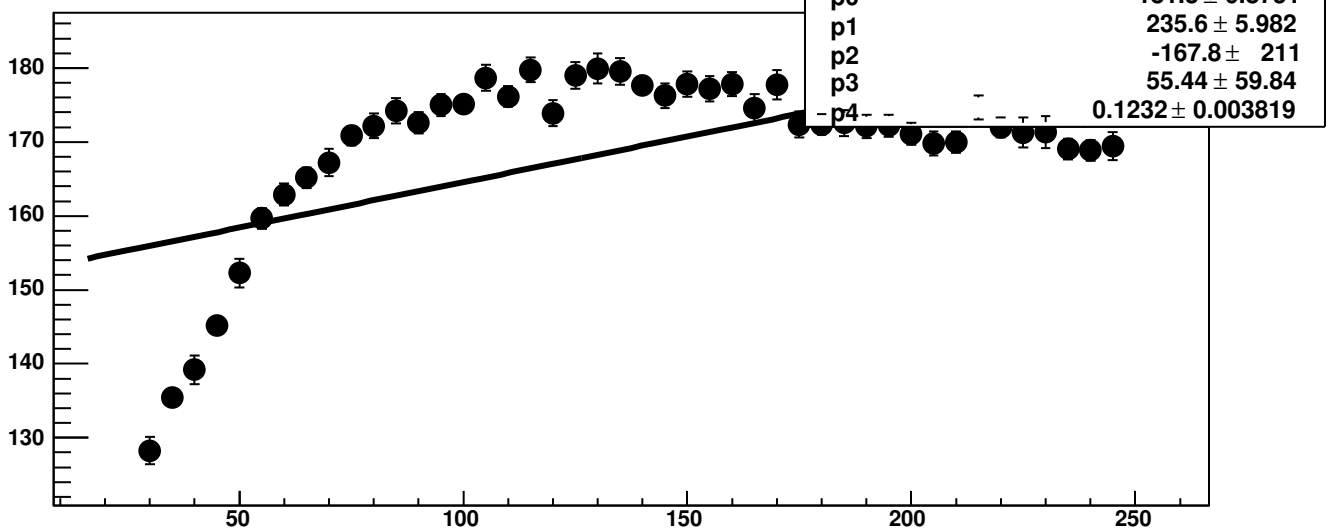

Chip 1, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold


Chip 1, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 2216 / 38
p0 178.4 ± 0.05891
p1 235 ± 1.688
p2 -112.9 ± 0.05931
p3 55.7 ± 0.05932
p4 0.1006 ± 0.002377
180
170
160
150
140
130
120
50
100
150
200
250

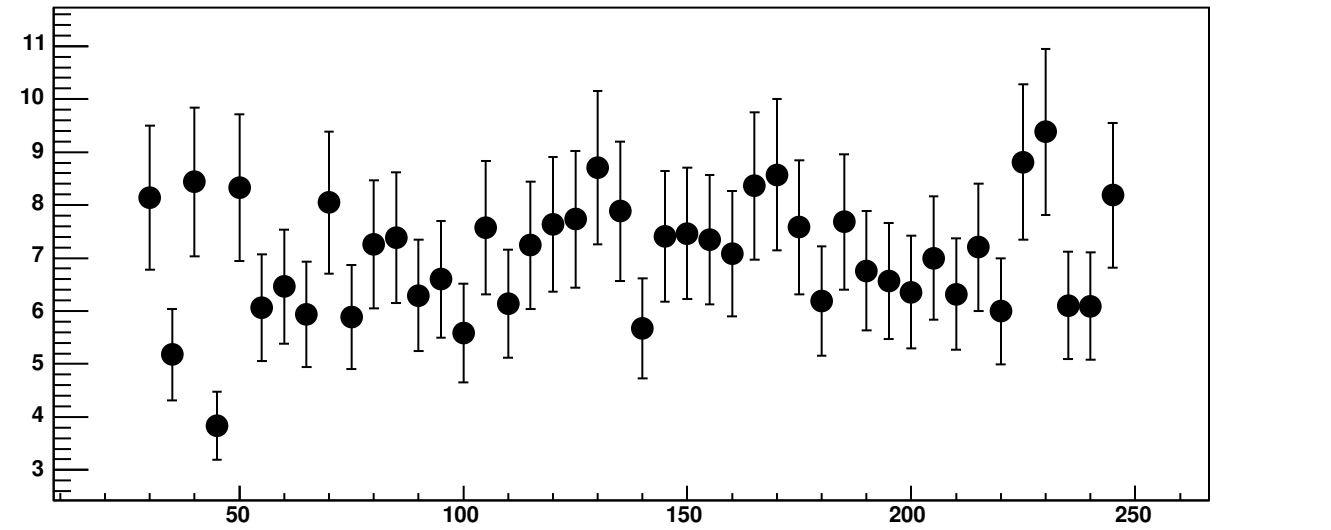

Chip 1, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 1, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold
10
0
-10
-20
-30
-40
50
100
150
200
250

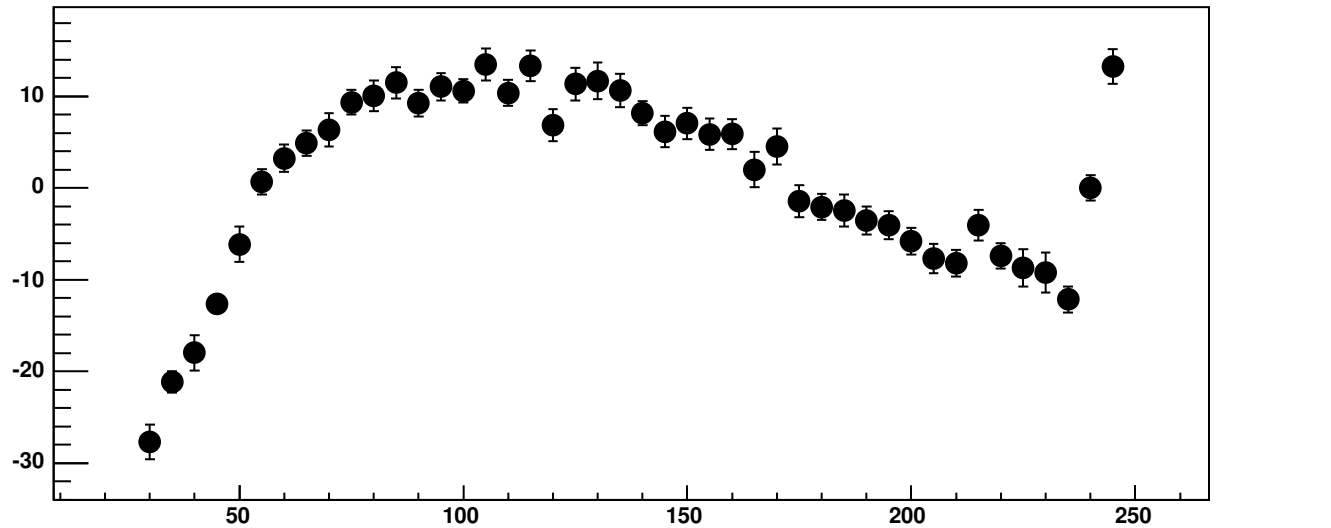
Chip 1, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold



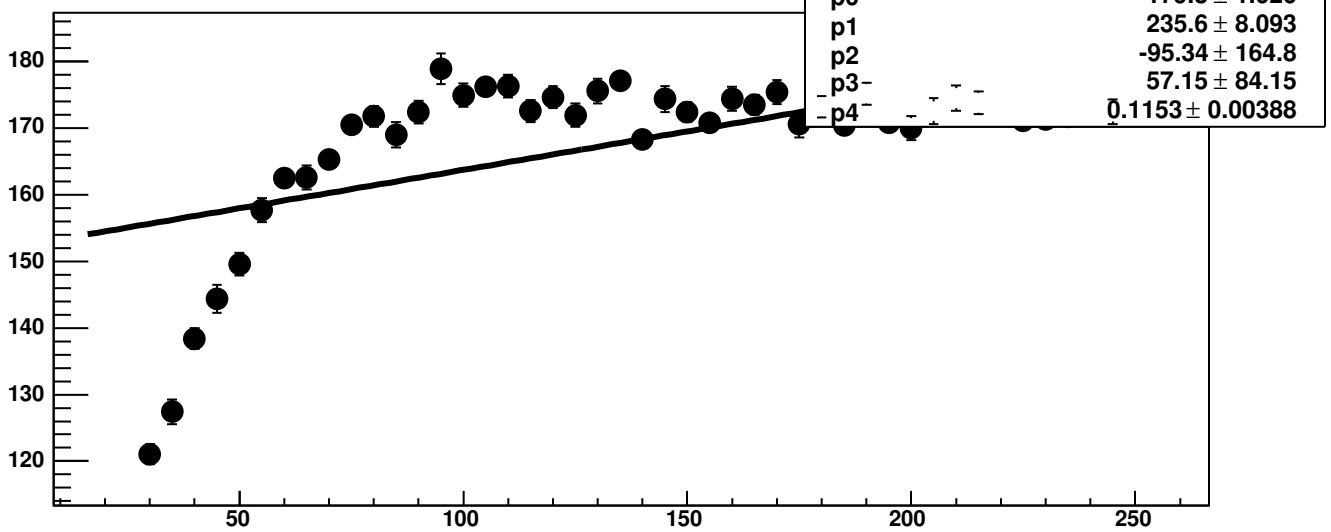
Chip 1, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



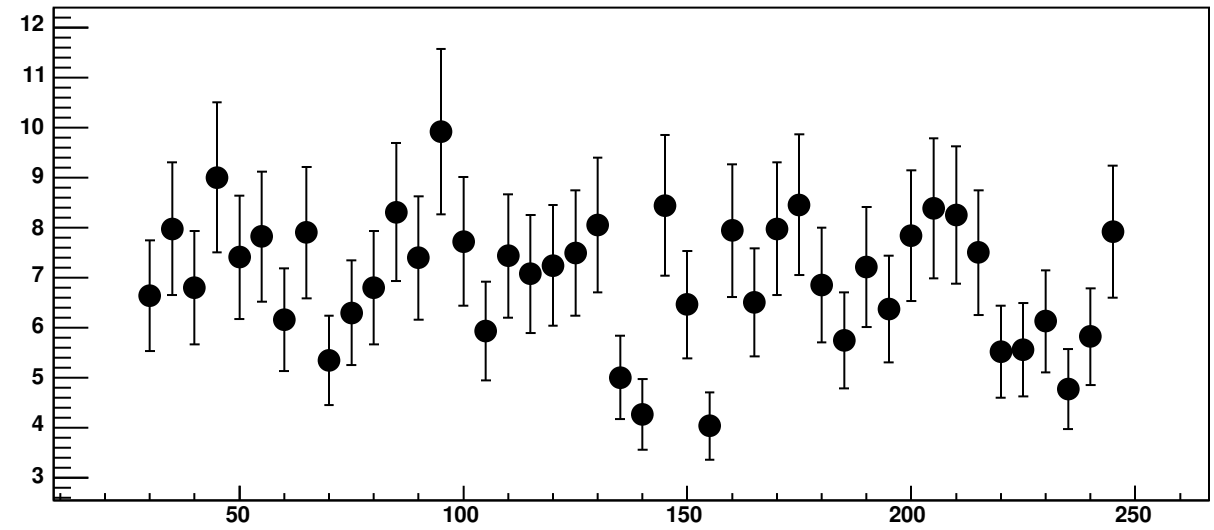
Chip 1, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold



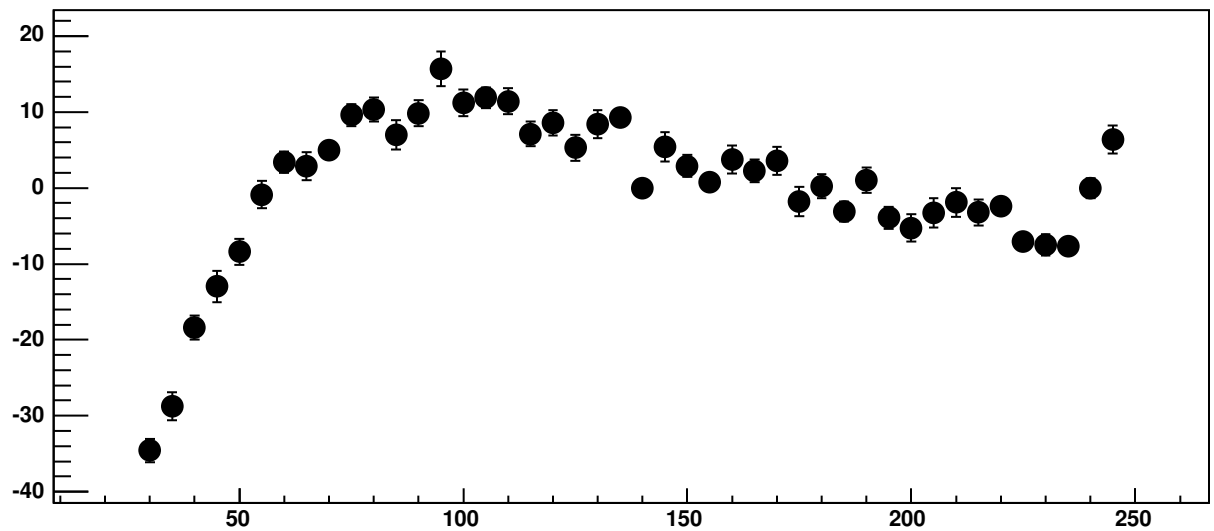
Chip 1, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

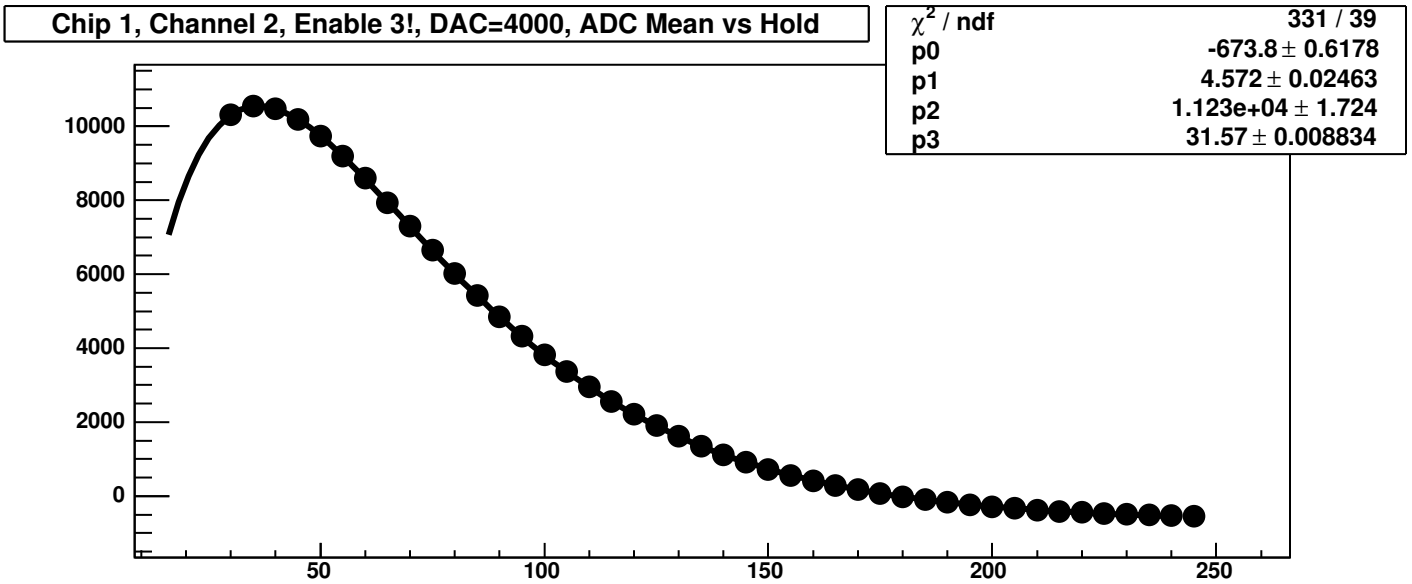


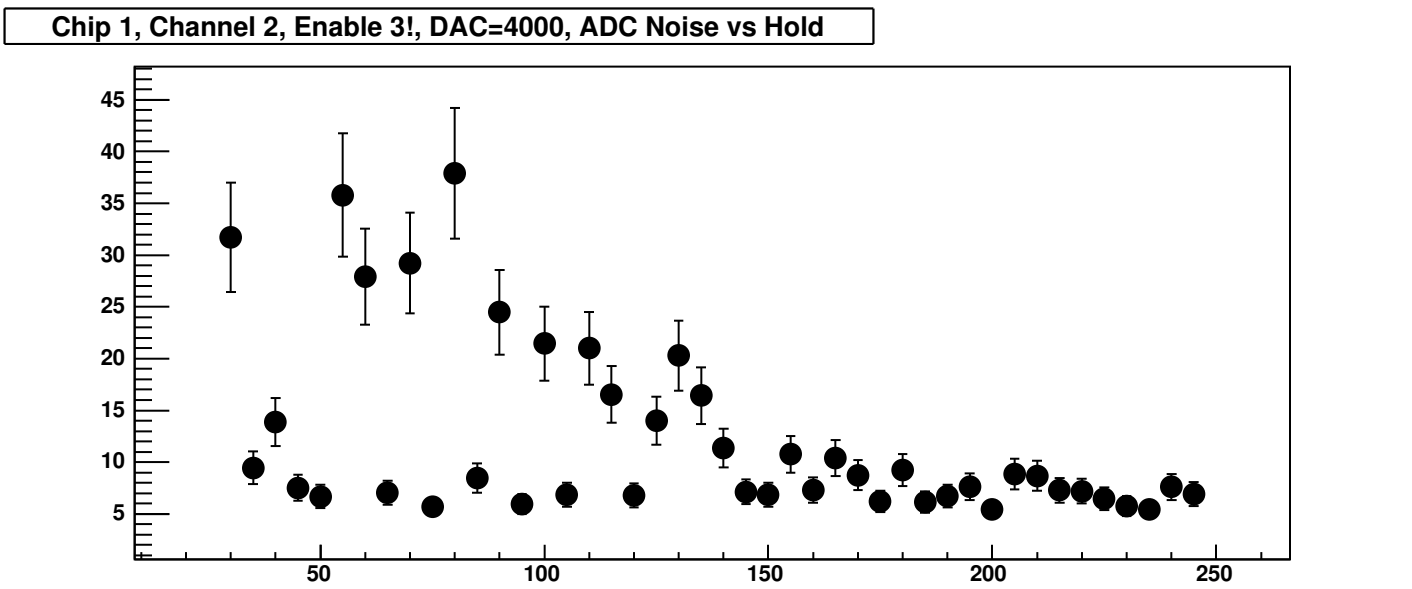
Chip 1, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold

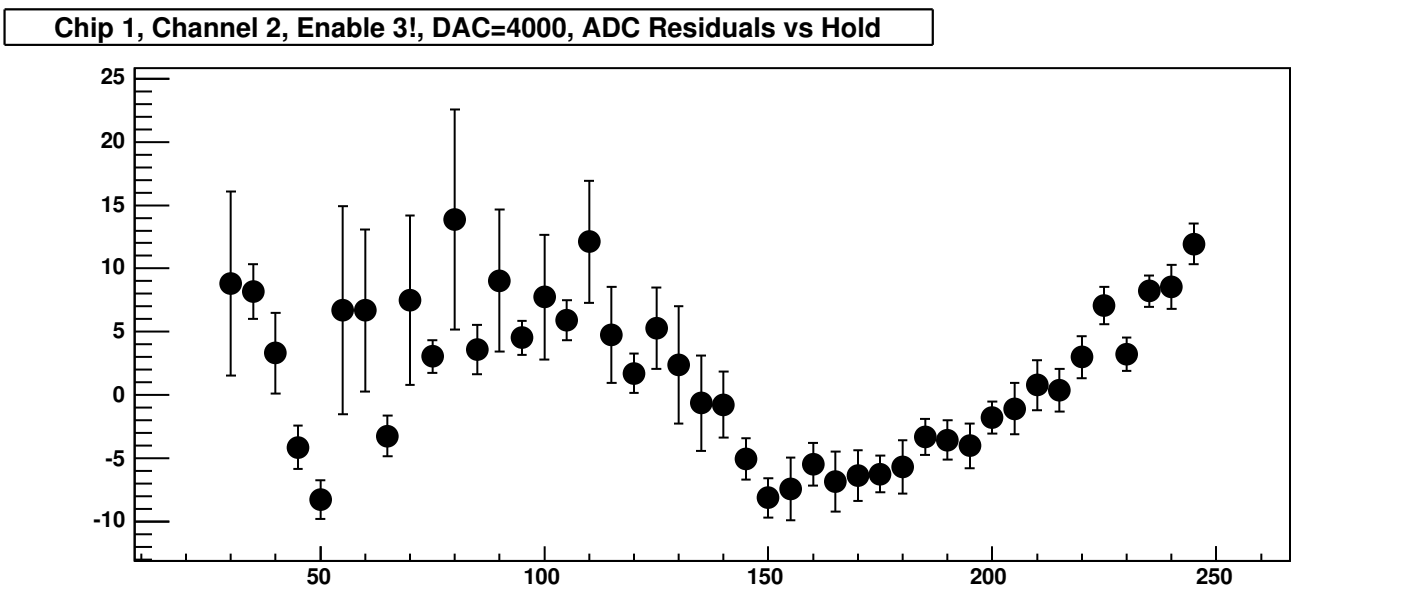


Chip 1, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

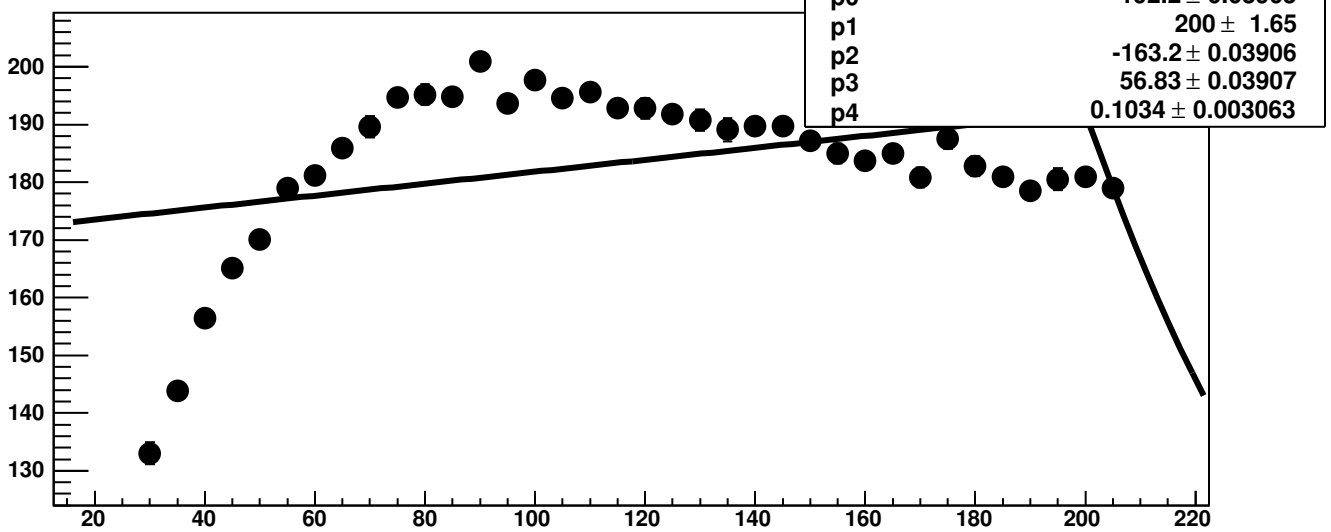



Chip 1, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 331 / 39
p0 -673.8 ± 0.6178
p1 4.572 ± 0.02463
p2 1.123e+04 ± 1.724
p3 31.57 ± 0.008834
10000
8000
6000
4000
2000
0
50
100
150
200
250

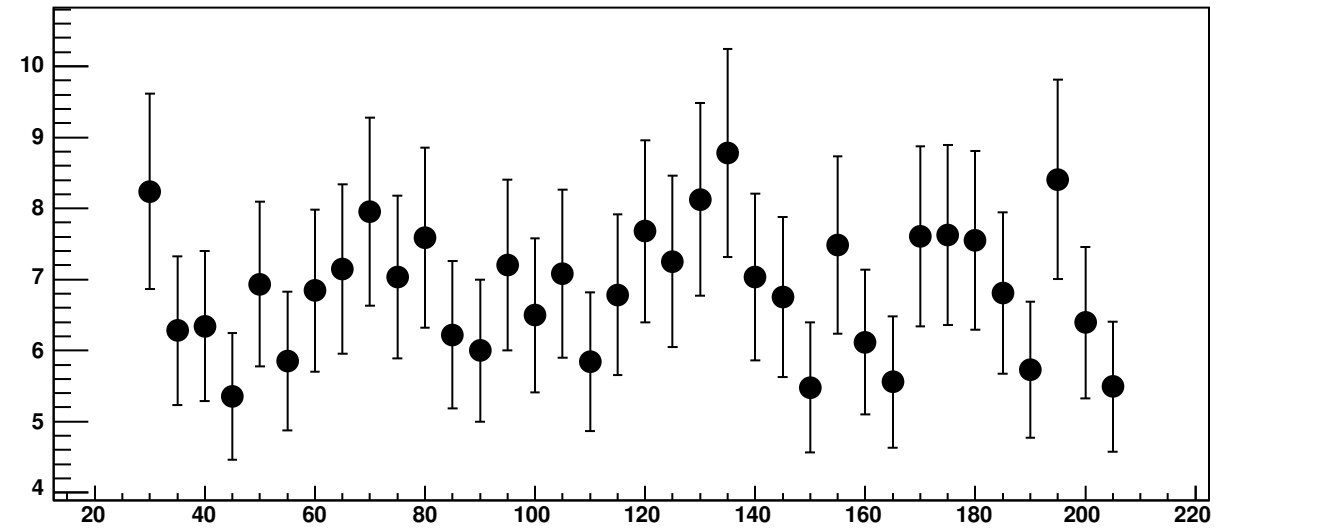

Chip 1, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold
45
40
35
30
25
20
15
10
5
50
100
150
200
250


Chip 1, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
50
100
150
200
250

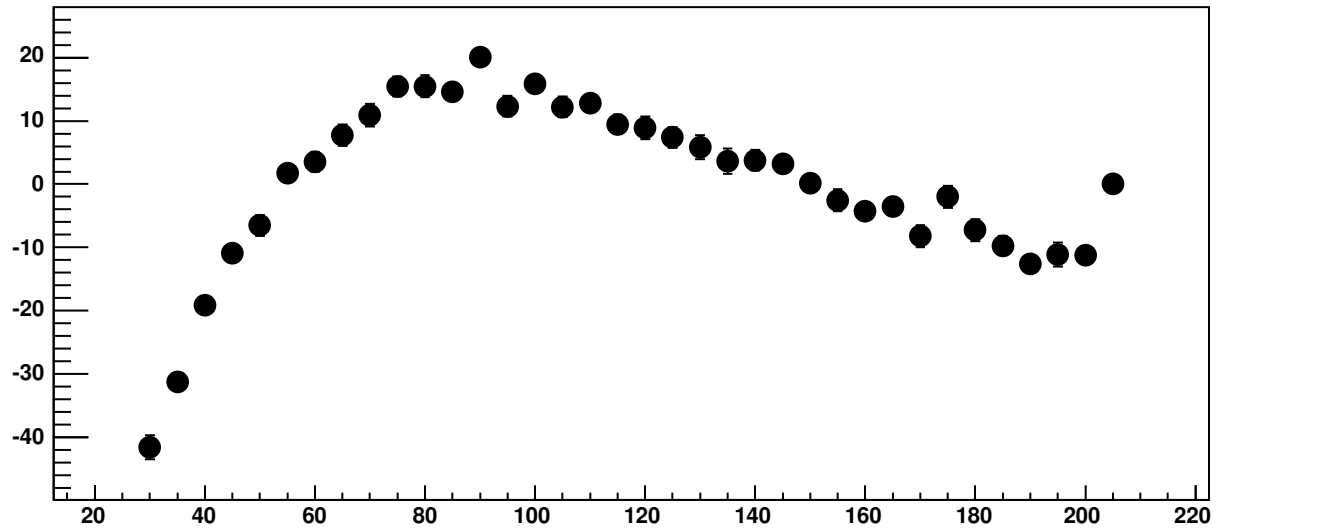
Chip 1, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold

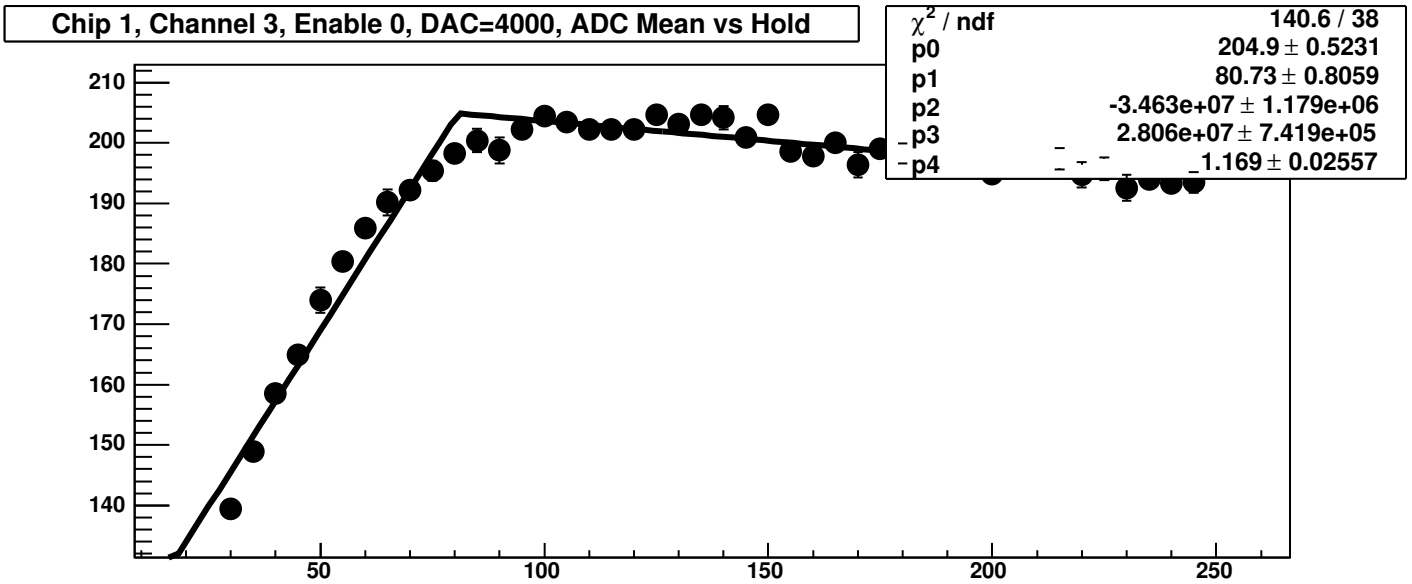


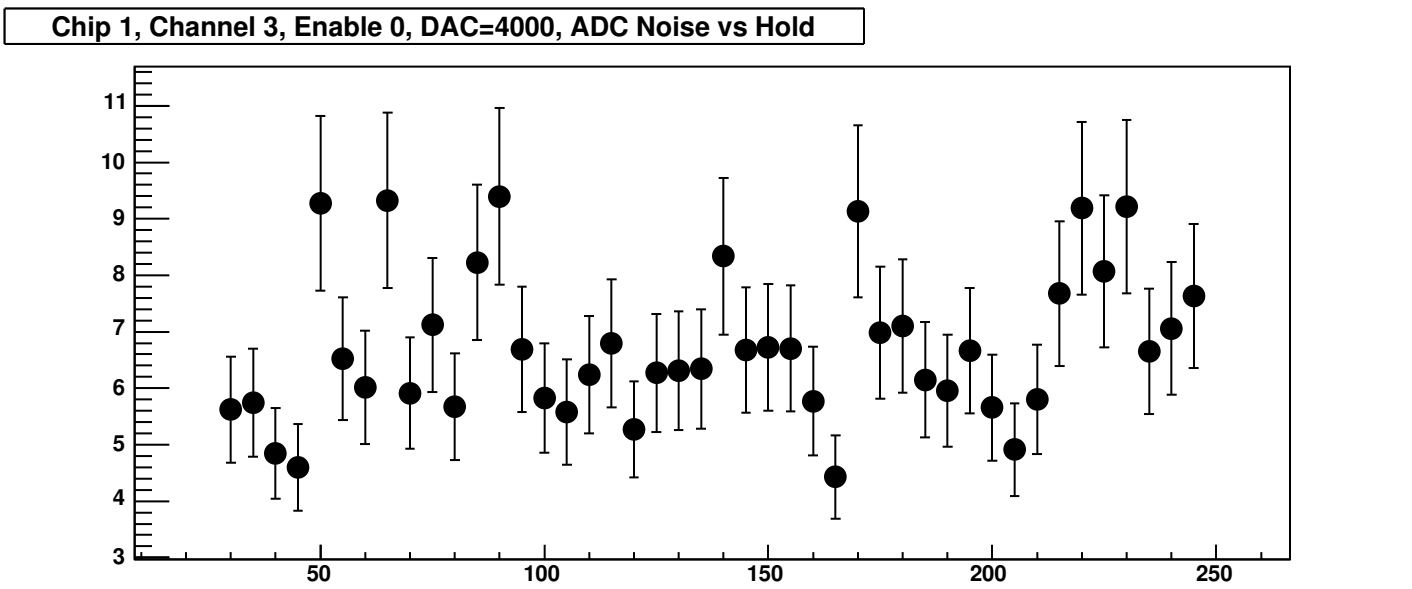
Chip 1, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold

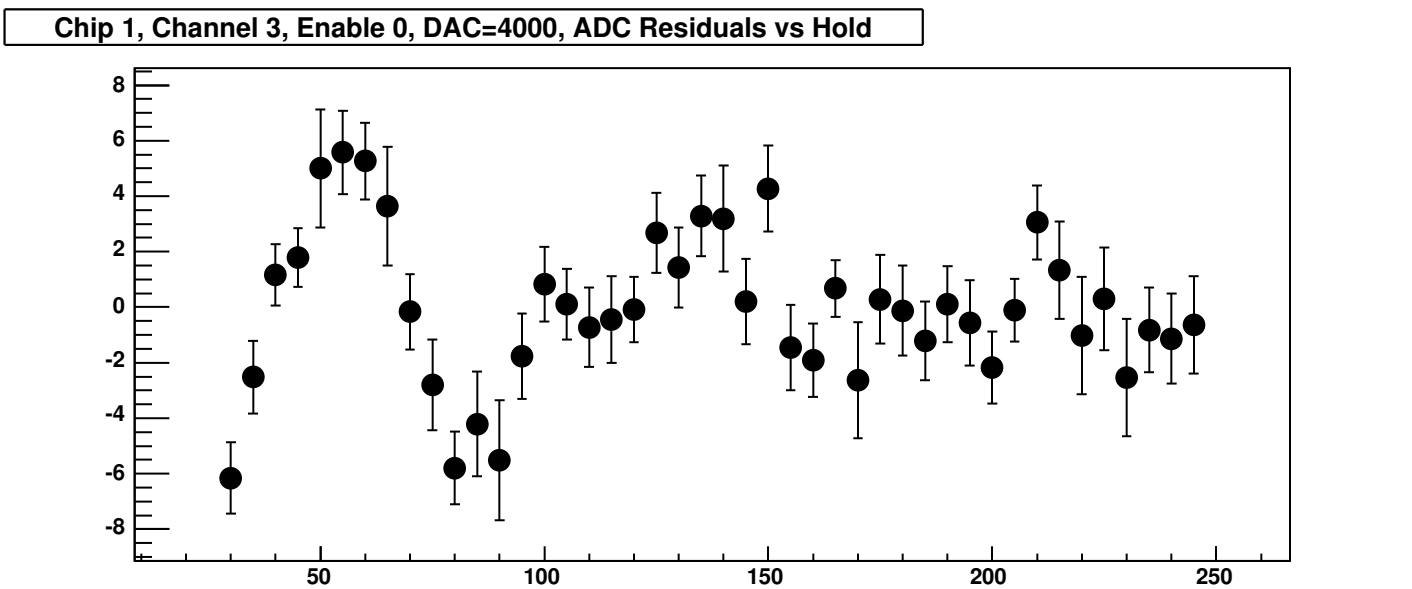


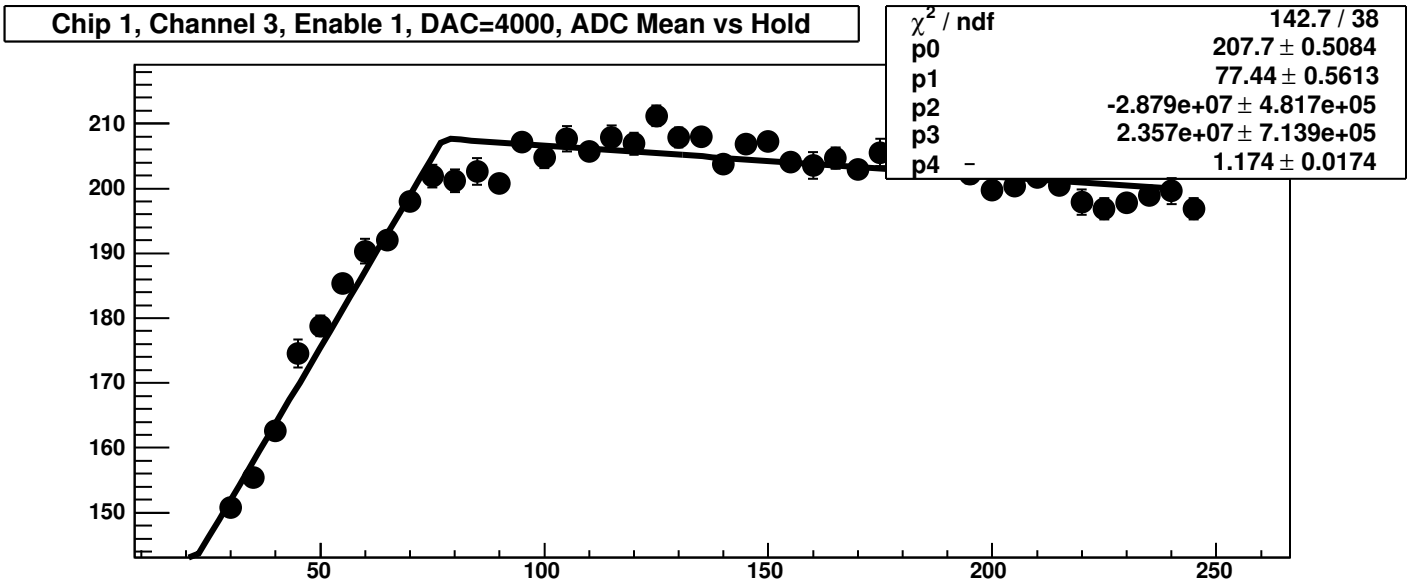
Chip 1, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

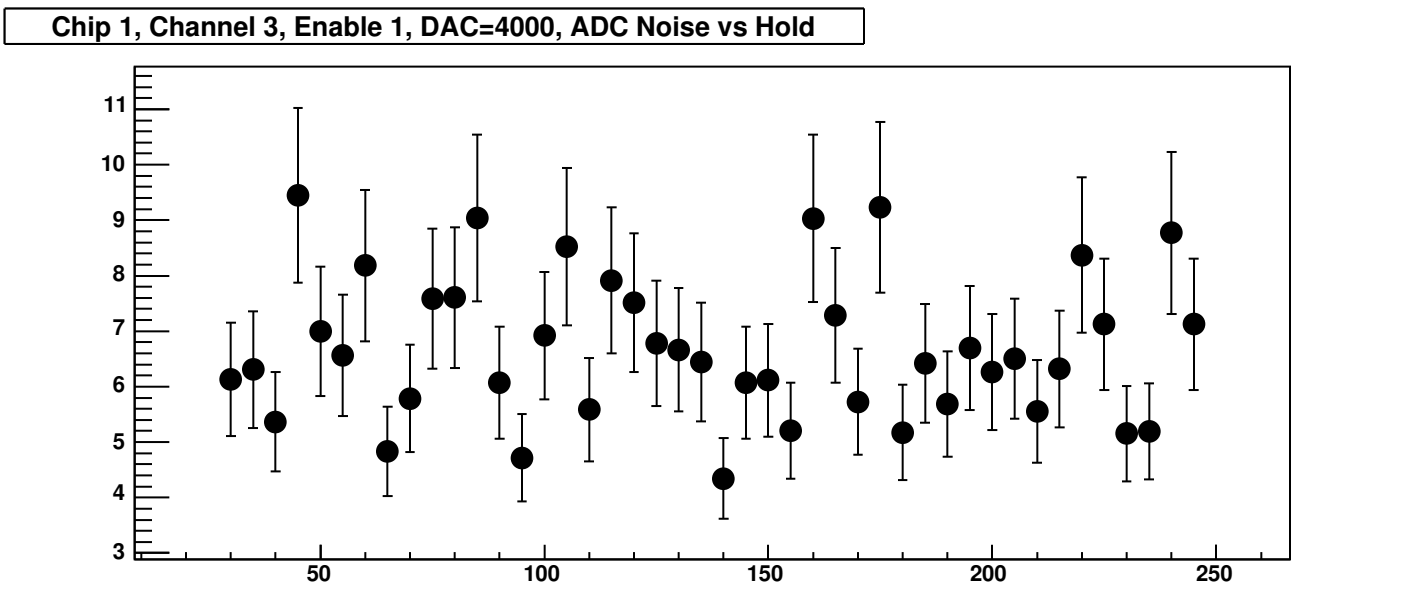


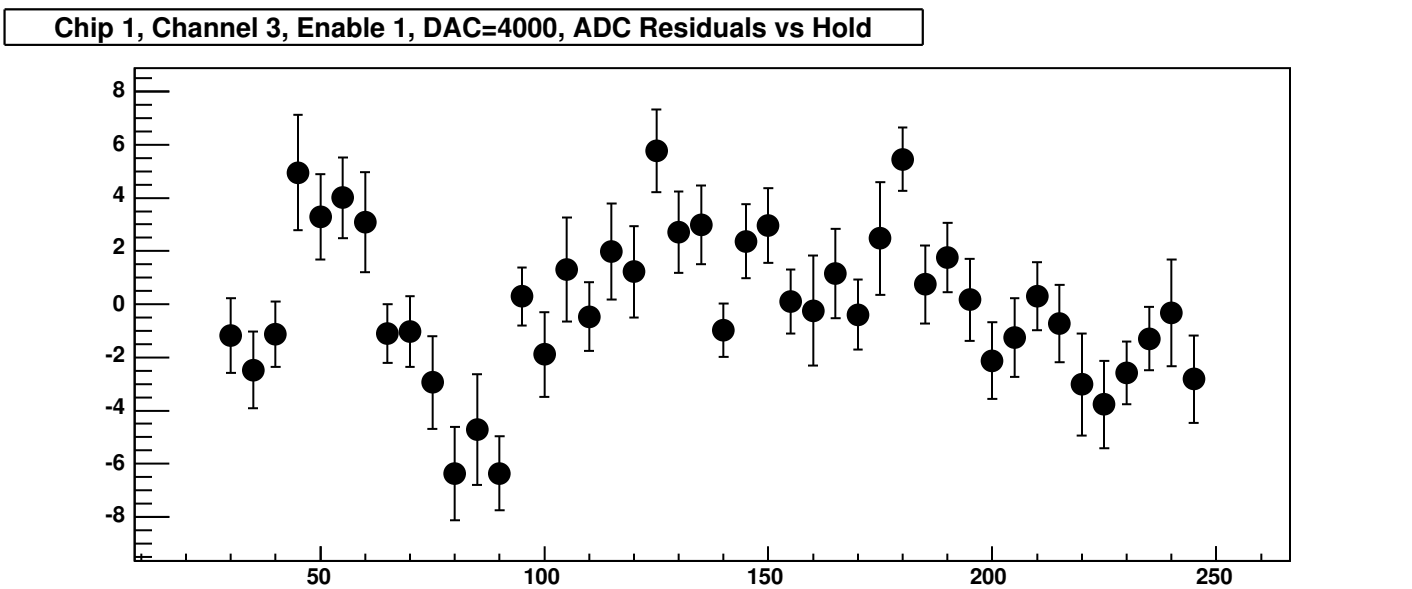

Chip 1, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 140.6 / 38
p0 204.9 ± 0.5231
p1 80.73 ± 0.8059
p2 -3.463e+07 ± 1.179e+06
p3 2.806e+07 ± 7.419e+05
p4 -1.169 ± 0.02557
210
200
190
180
170
160
150
140
50
100
150
200
250


Chip 1, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
50
100
150
200
250

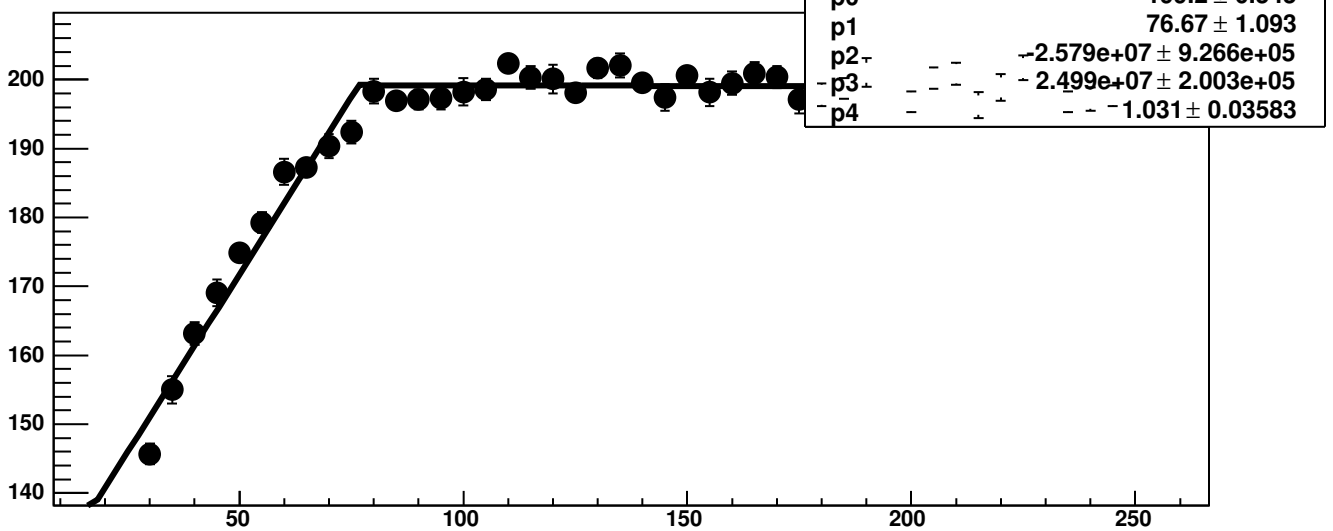

Chip 1, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250


Chip 1, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 142.7 / 38
p0 207.7 ± 0.5084
p1 77.44 ± 0.5613
p2 -2.879e+07 ± 4.817e+05
p3 2.357e+07 ± 7.139e+05
p4 – 1.174 ± 0.0174
210
200
190
180
170
160
150
50
100
150
200
250

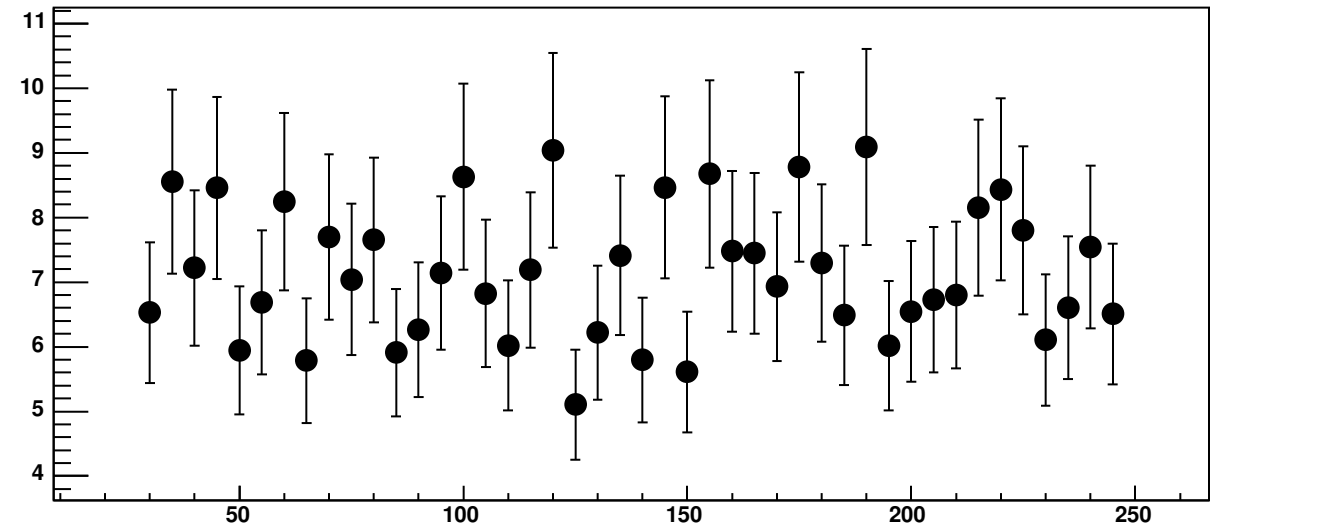

Chip 1, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
50
100
150
200
250


Chip 1, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

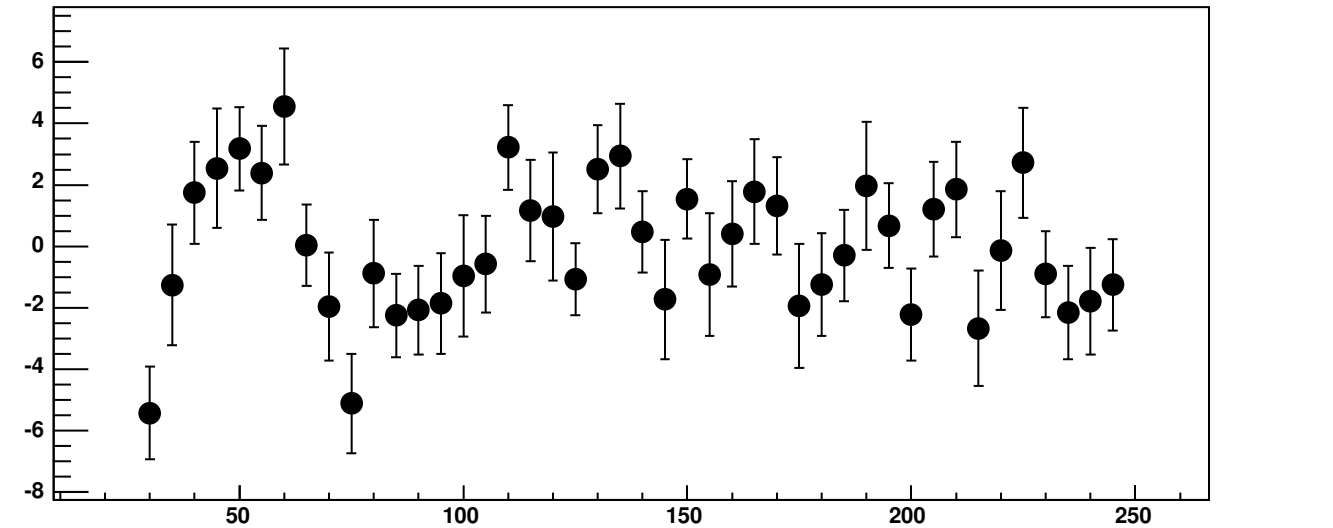
Chip 1, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold



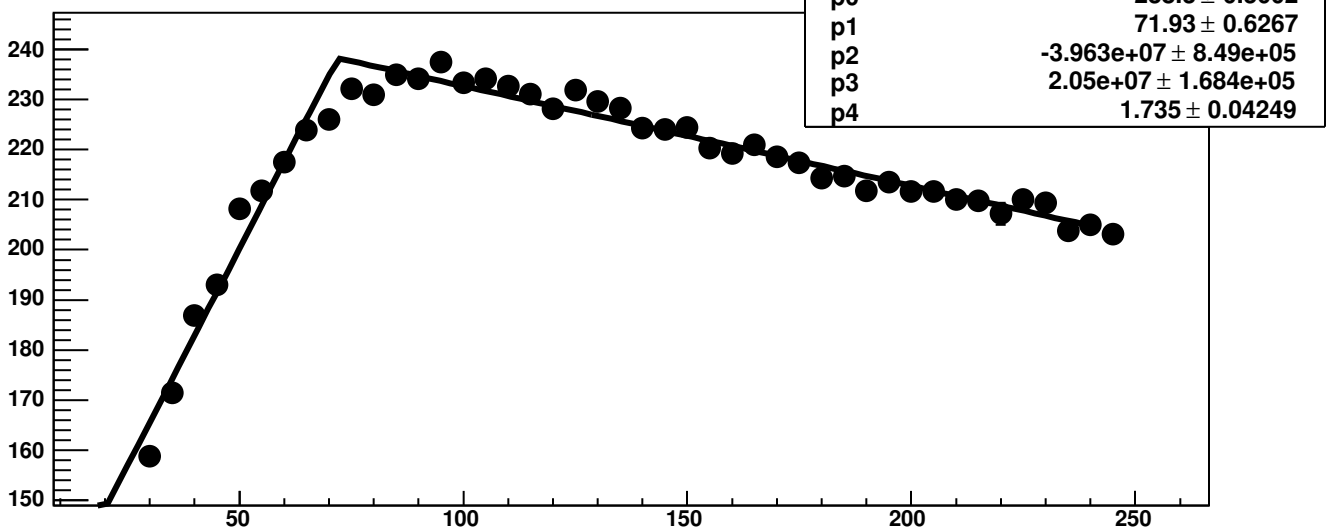
Chip 1, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



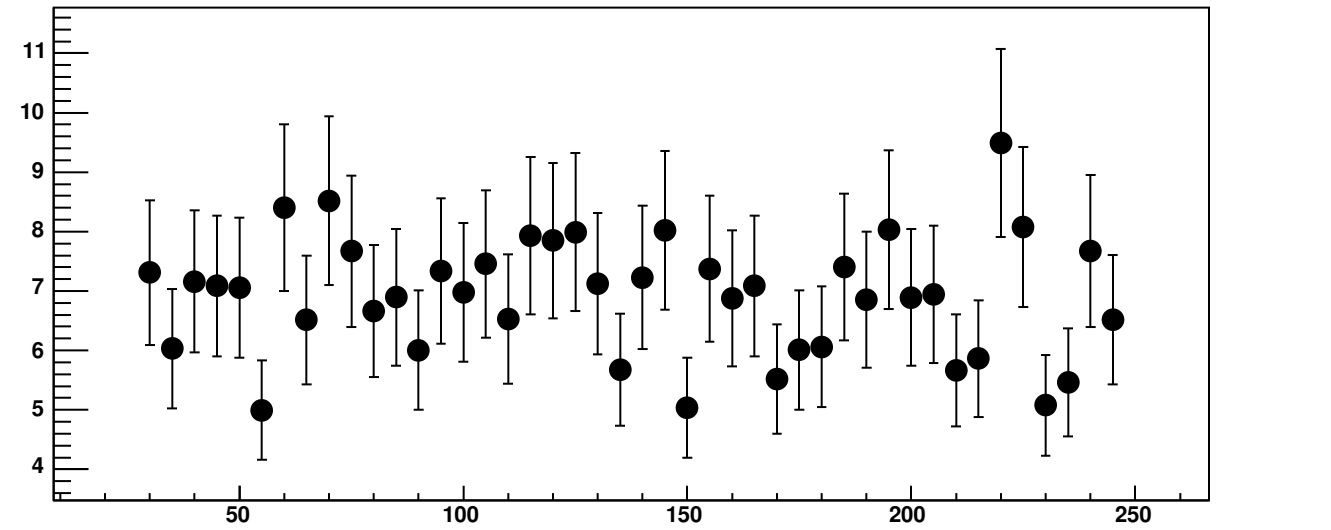
Chip 1, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold



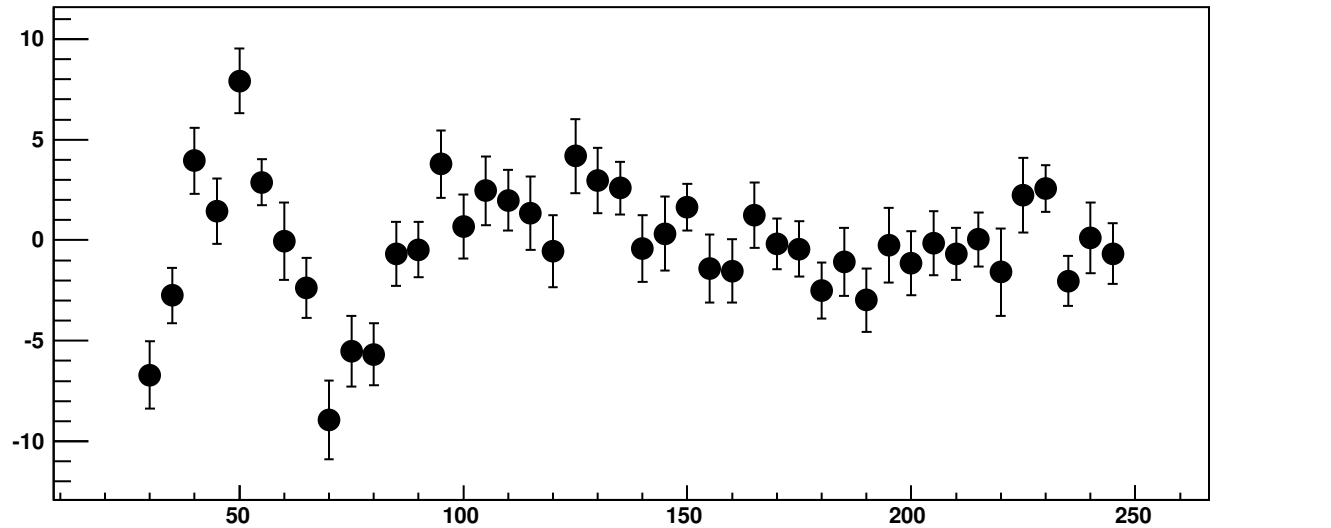
Chip 1, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

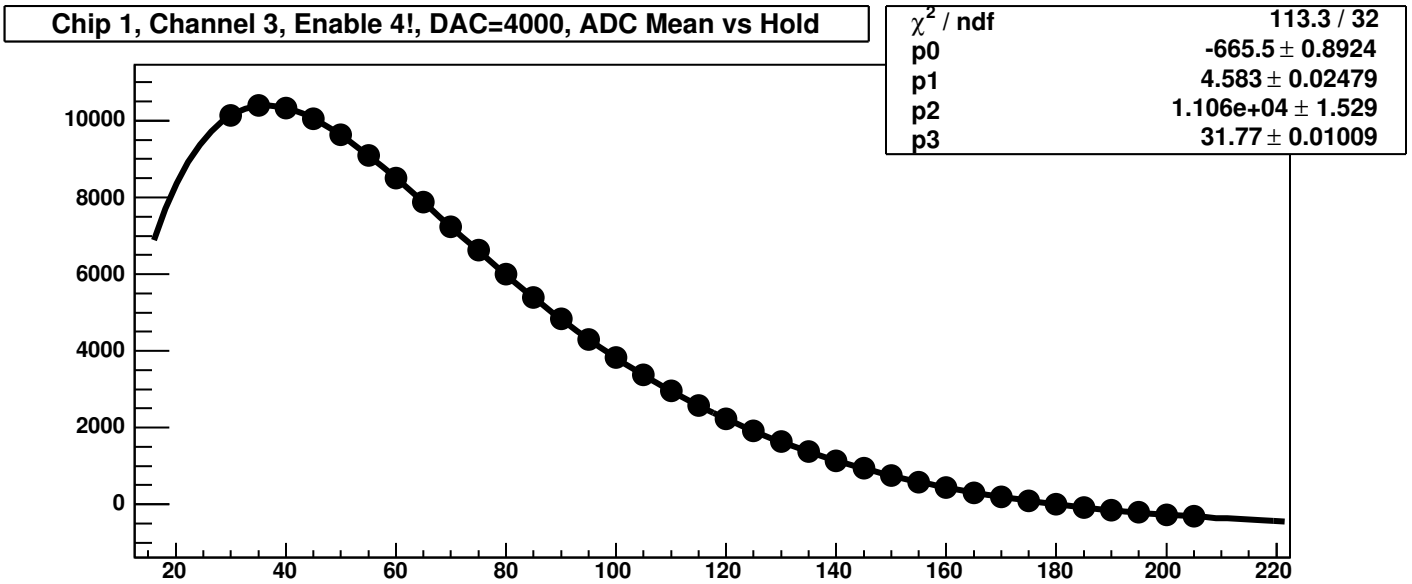


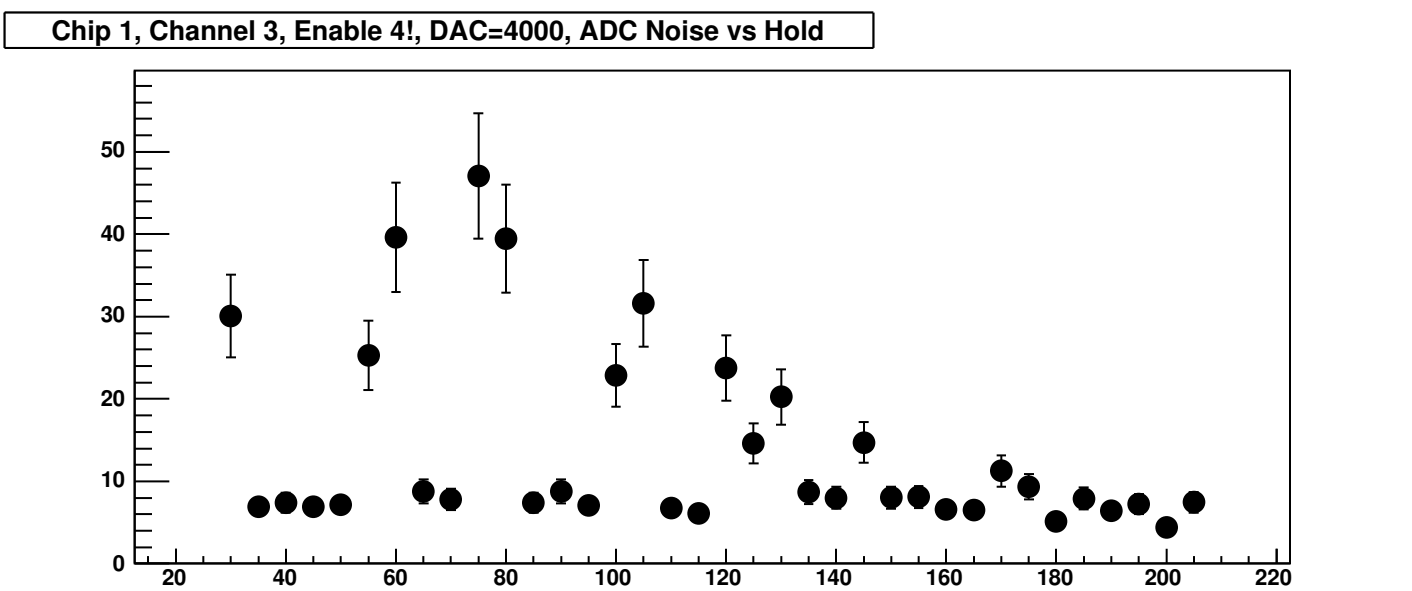
Chip 1, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold

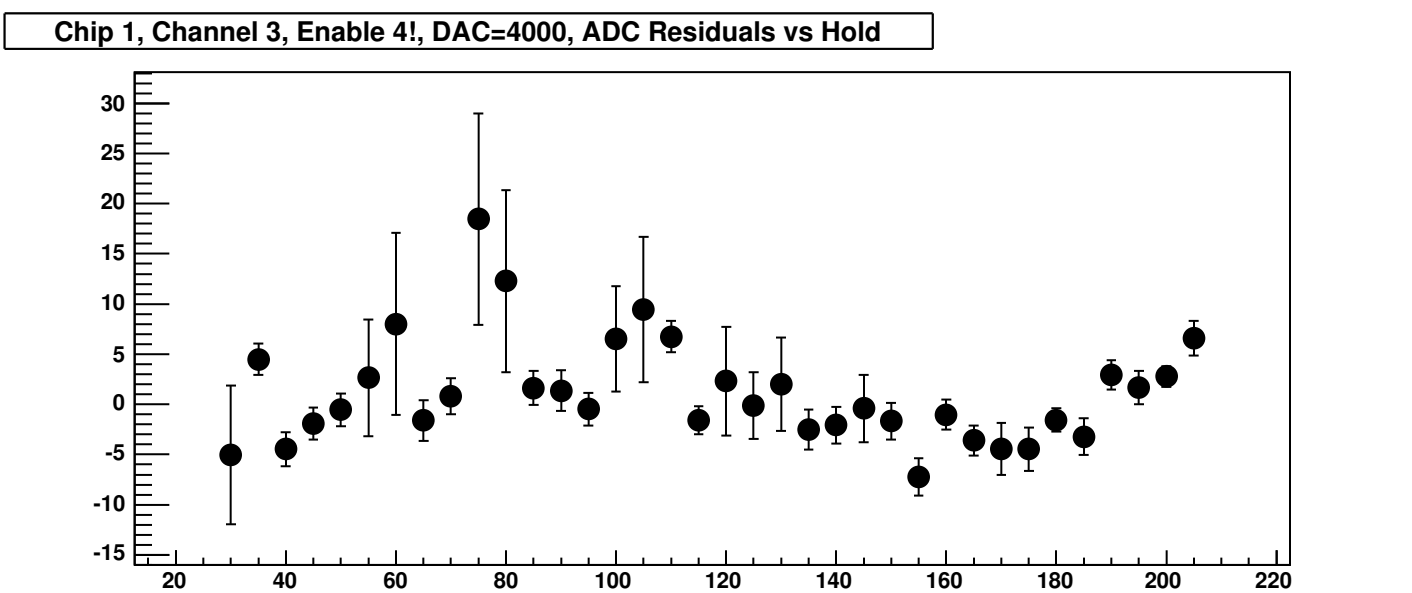


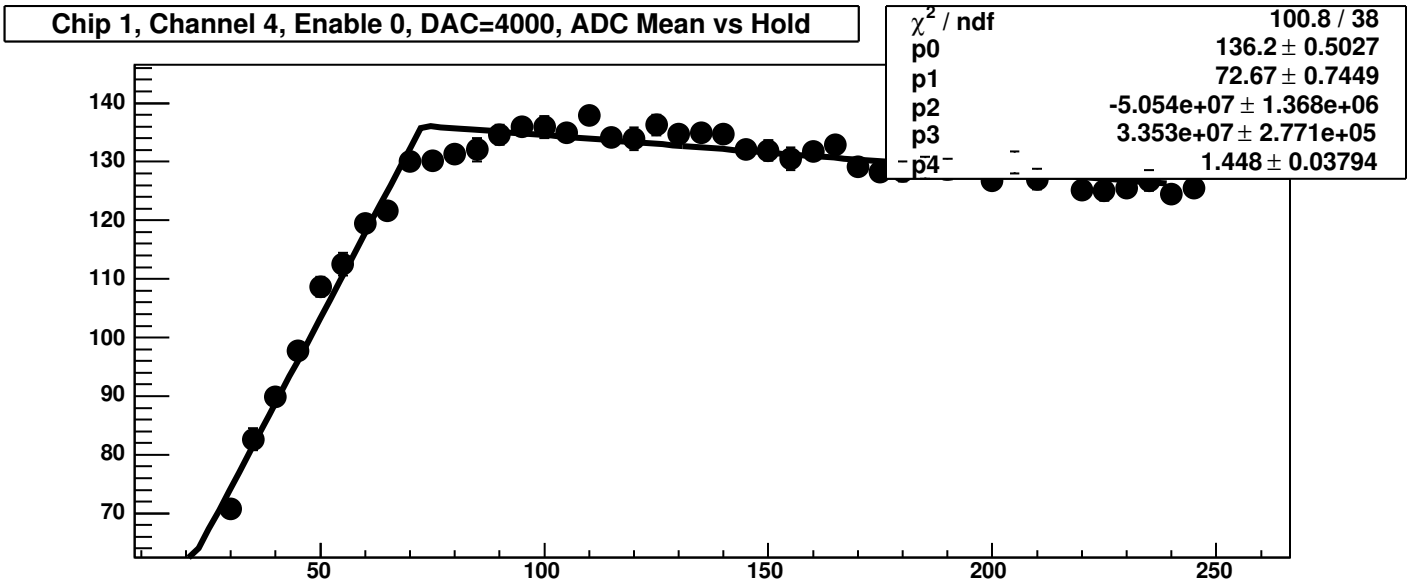
Chip 1, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

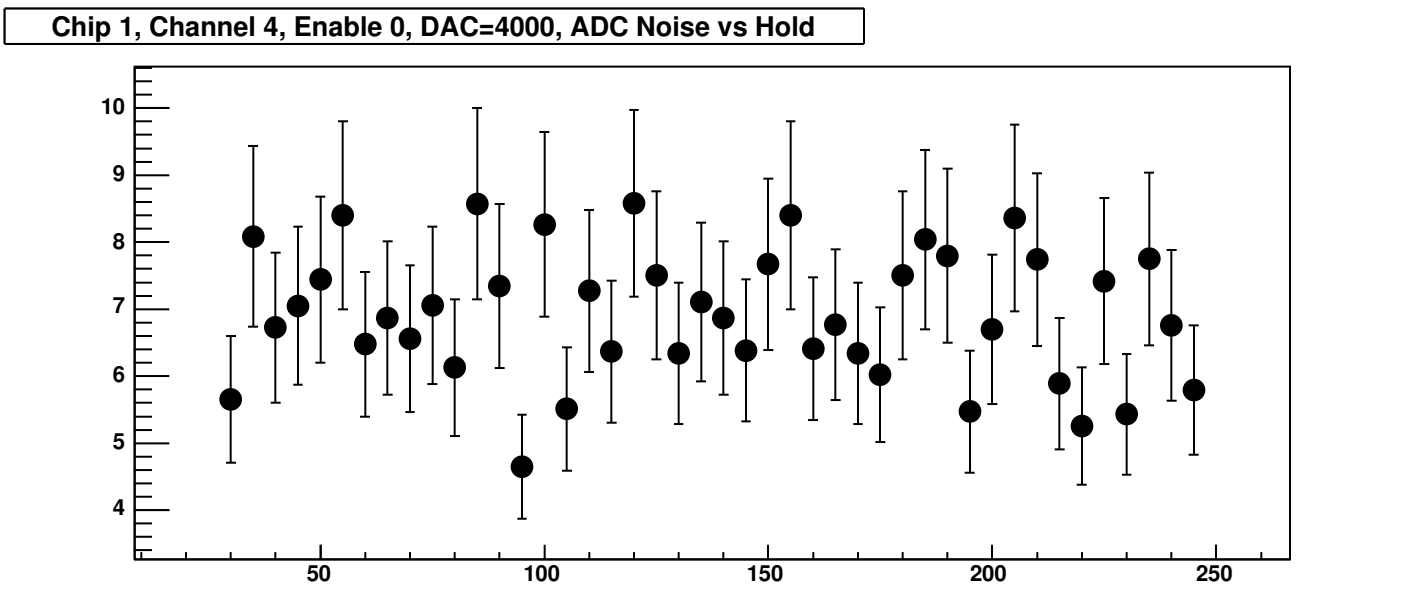


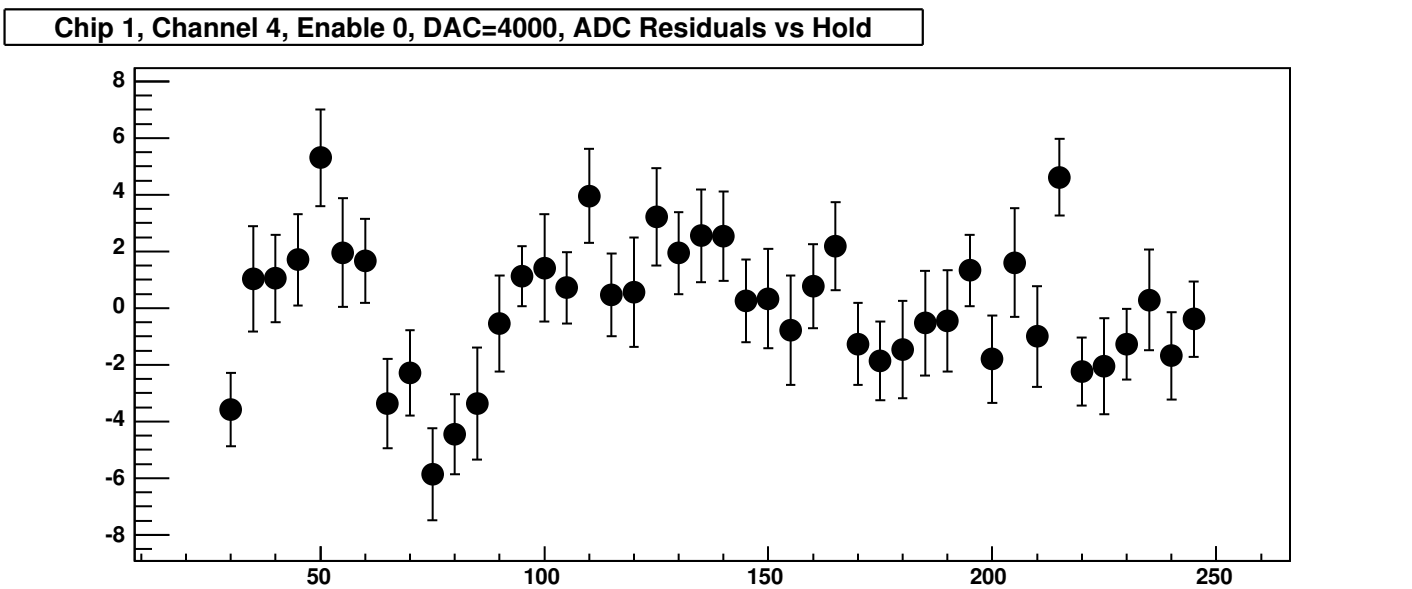

Chip 1, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 113.3 / 32
p0 -665.5 ± 0.8924
p1 4.583 ± 0.02479
p2 1.106e+04 ± 1.529
p3 31.77 ± 0.01009

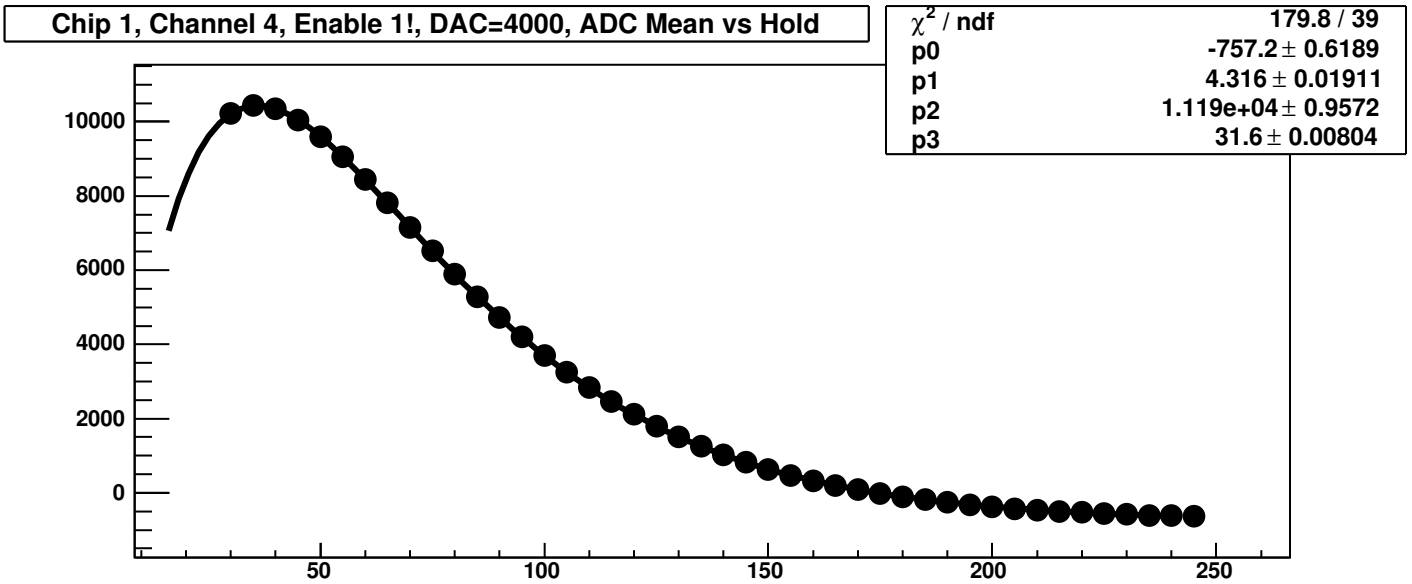

Chip 1, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold

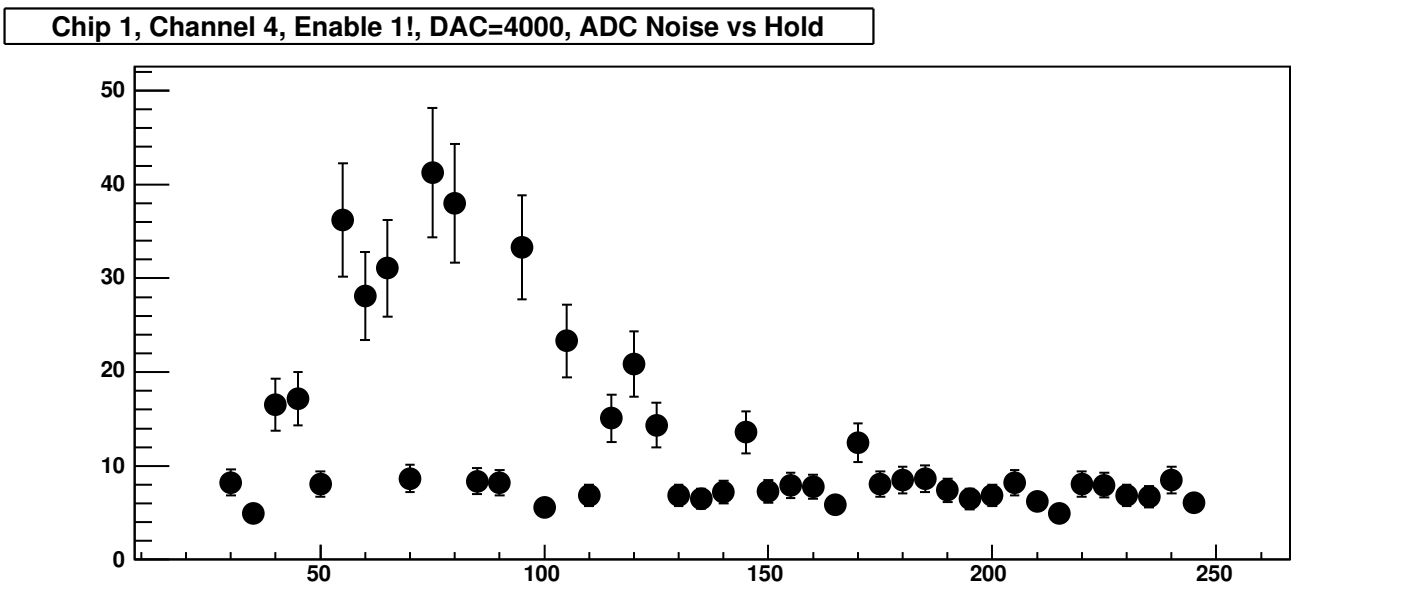

Chip 1, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold

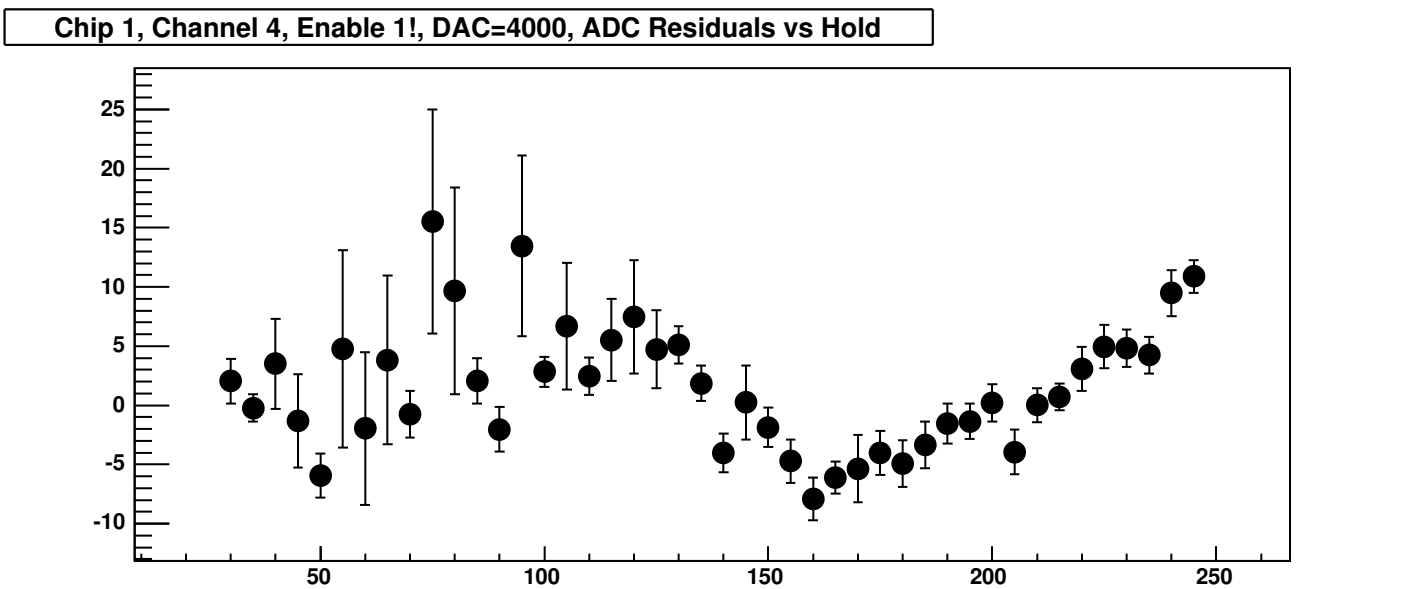

Chip 1, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 100.8 / 38
p0 136.2 ± 0.5027
p1 72.67 ± 0.7449
p2 -5.054e+07 ± 1.368e+06
p3 3.353e+07 ± 2.771e+05
p4 1.448 ± 0.03794
140
130
120
110
100
90
80
70
50
100
150
200
250


Chip 1, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold
10
9
8
7
6
5
4
50
100
150
200
250

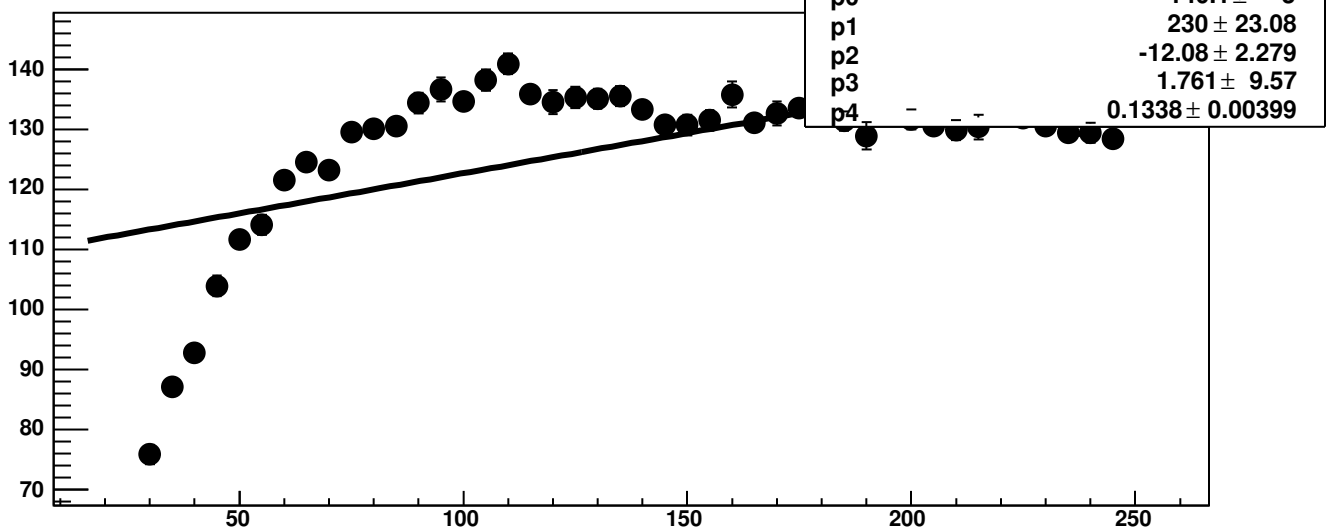

Chip 1, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250


Chip 1, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 179.8 / 39
p0 -757.2 ± 0.6189
p1 4.316 ± 0.01911
p2 1.119e+04 ± 0.9572
p3 31.6 ± 0.00804
10000
8000
6000
4000
2000
0
50
100
150
200
250

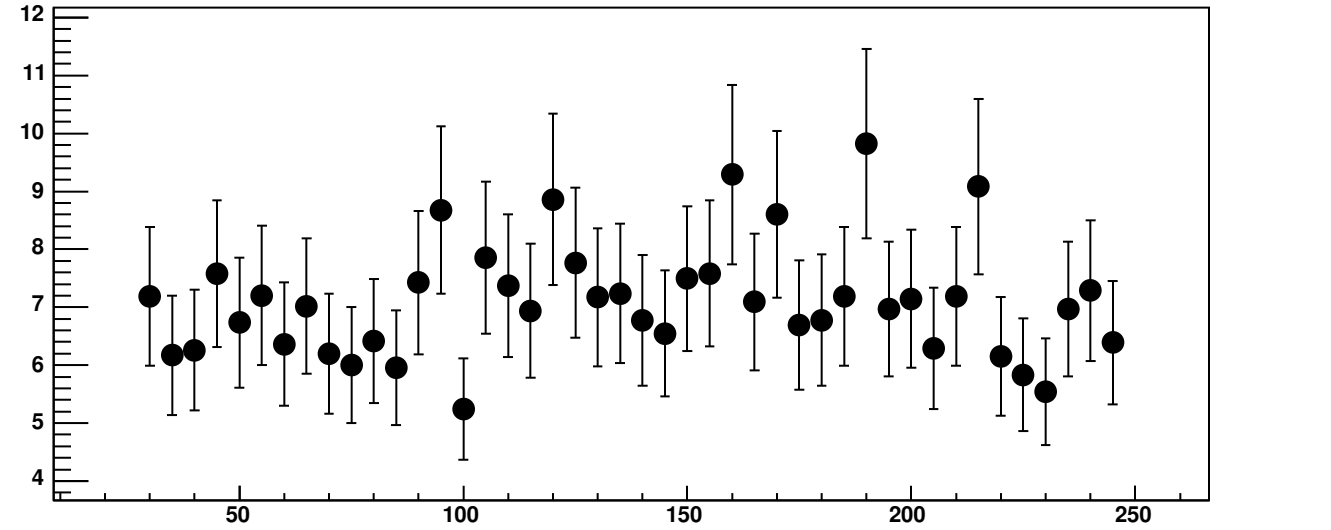

Chip 1, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
0
50
100
150
200
250


Chip 1, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
50
100
150
200
250

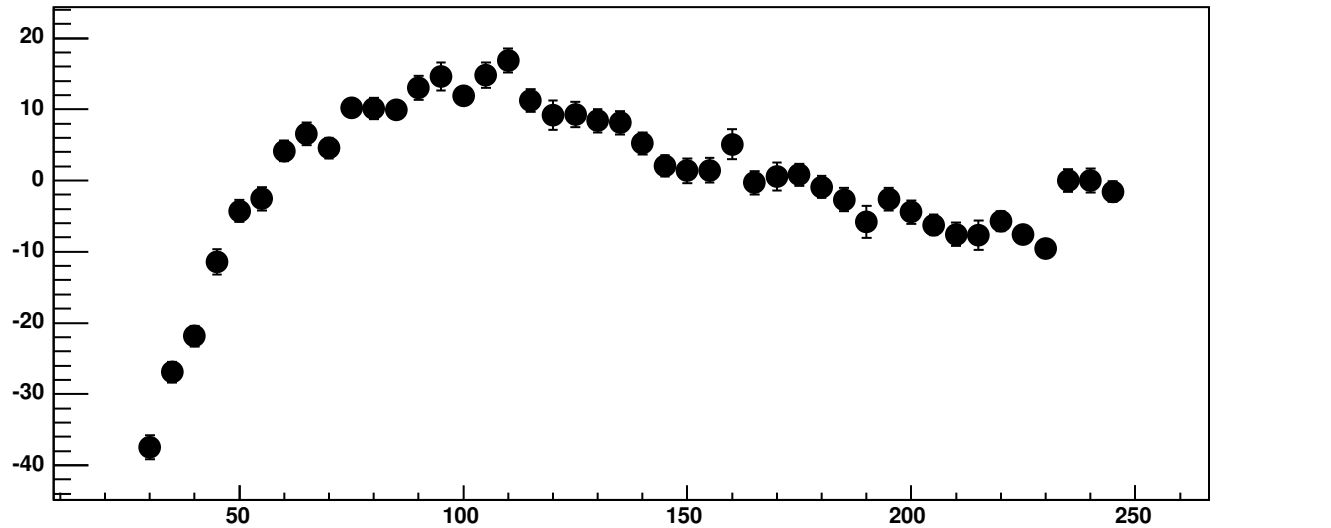
Chip 1, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold



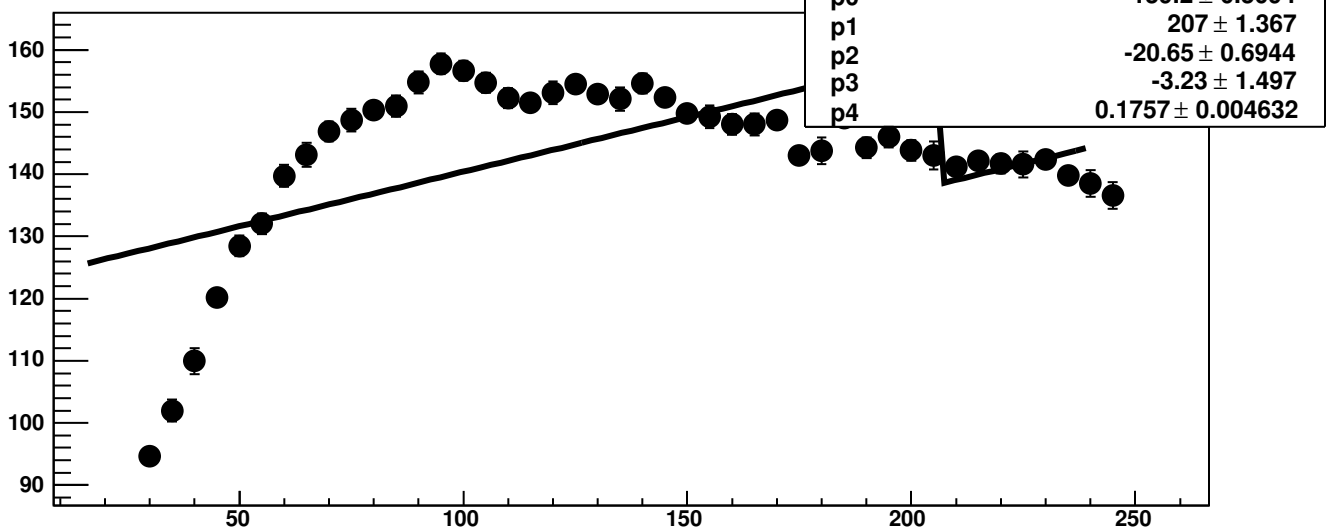
Chip 1, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



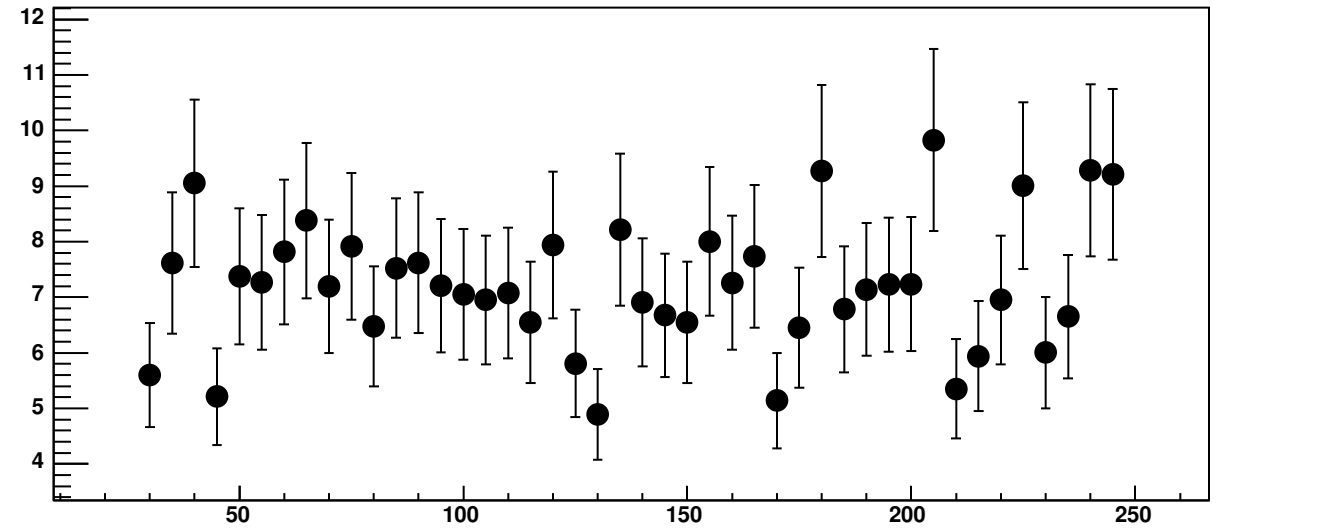
Chip 1, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold



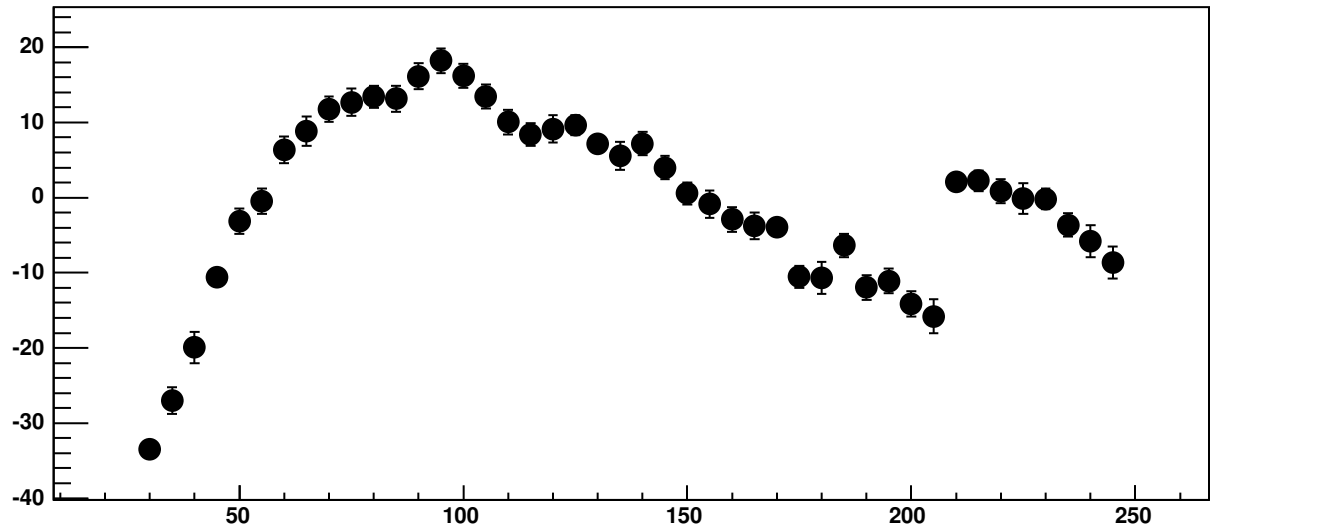
Chip 1, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

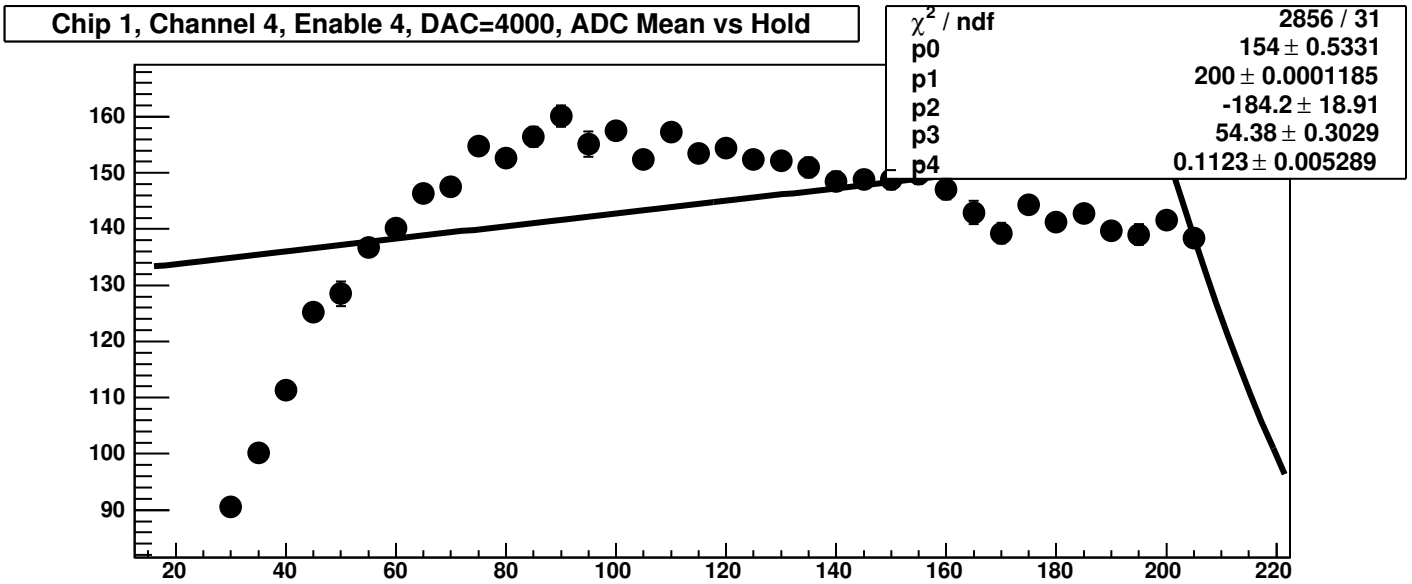


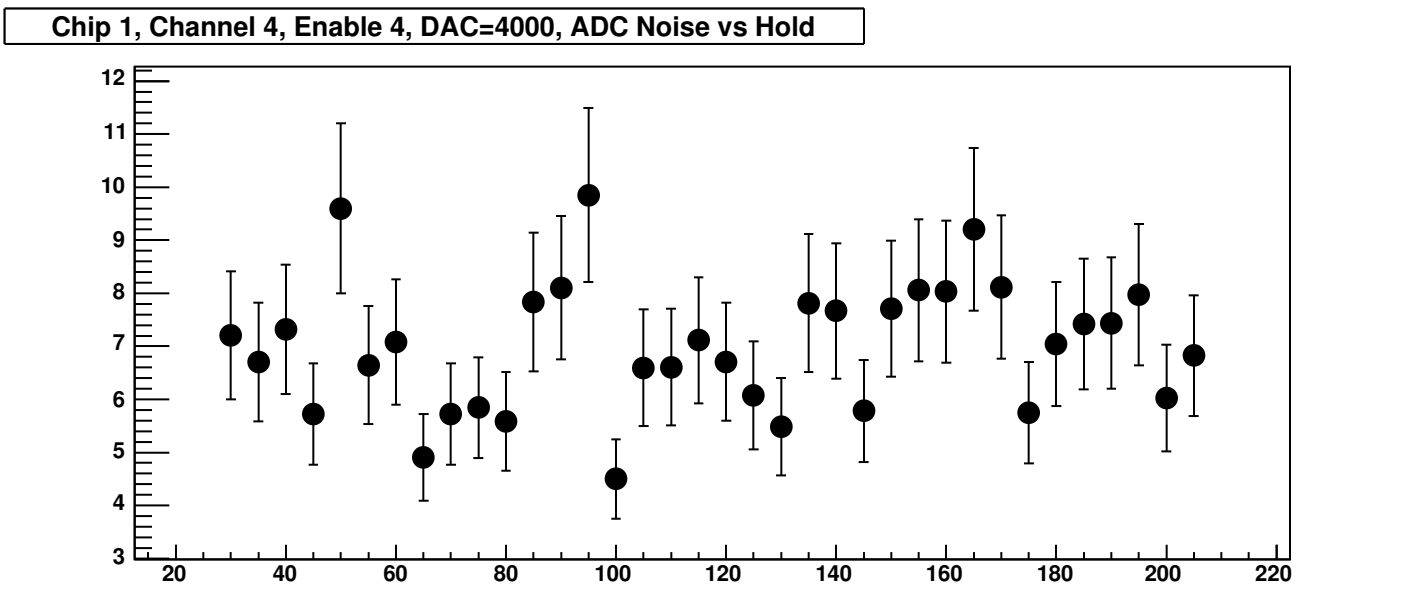
Chip 1, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold

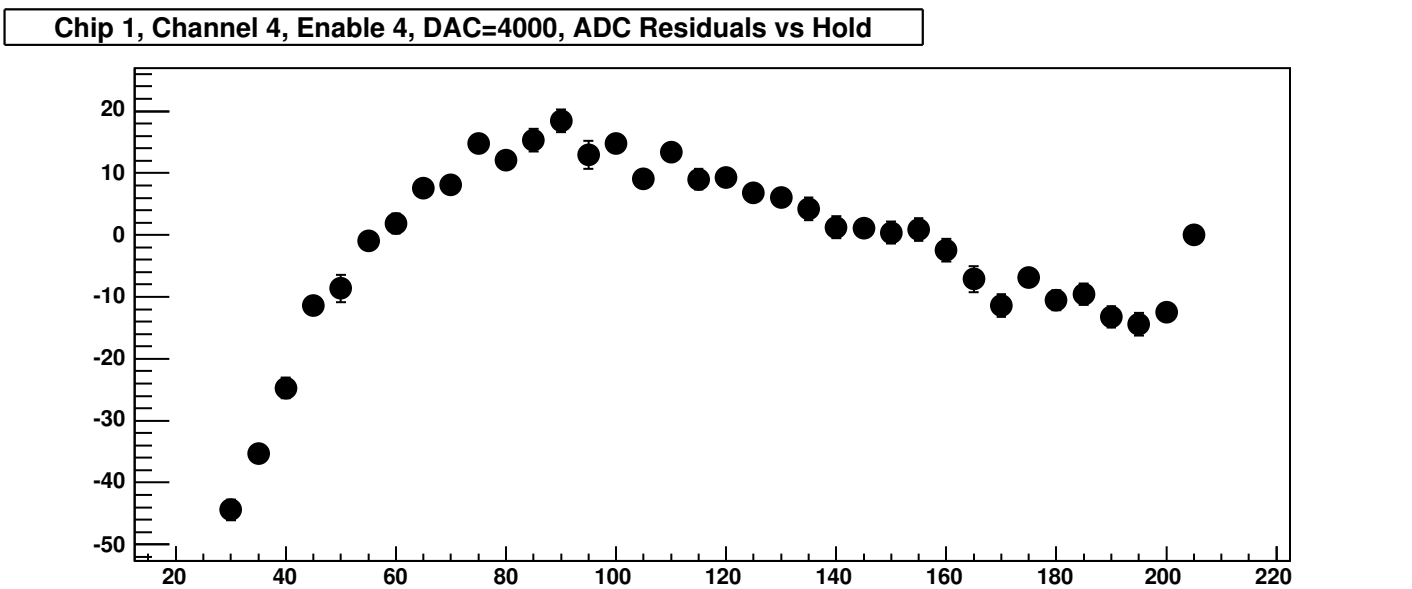


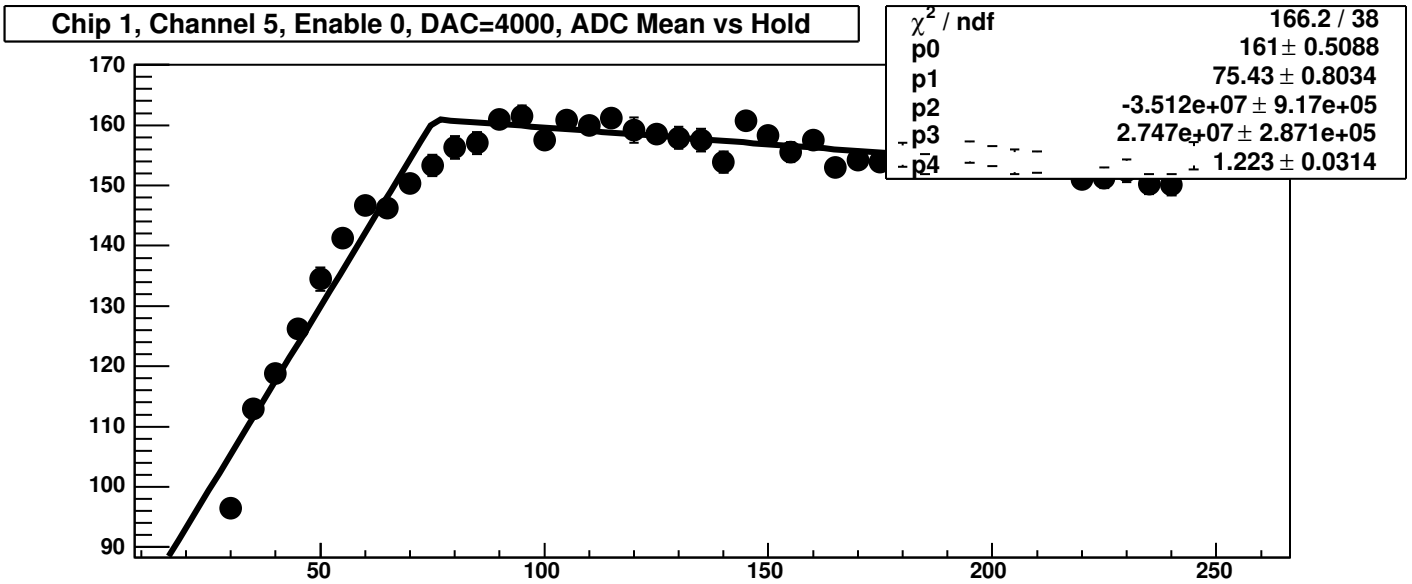
Chip 1, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

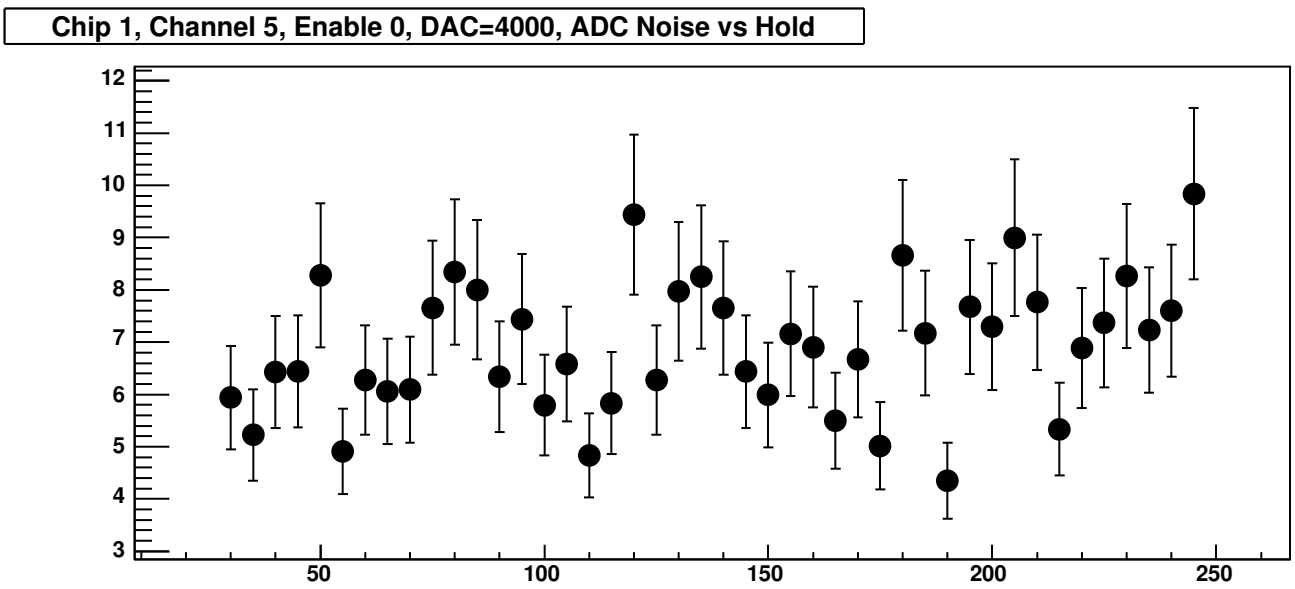


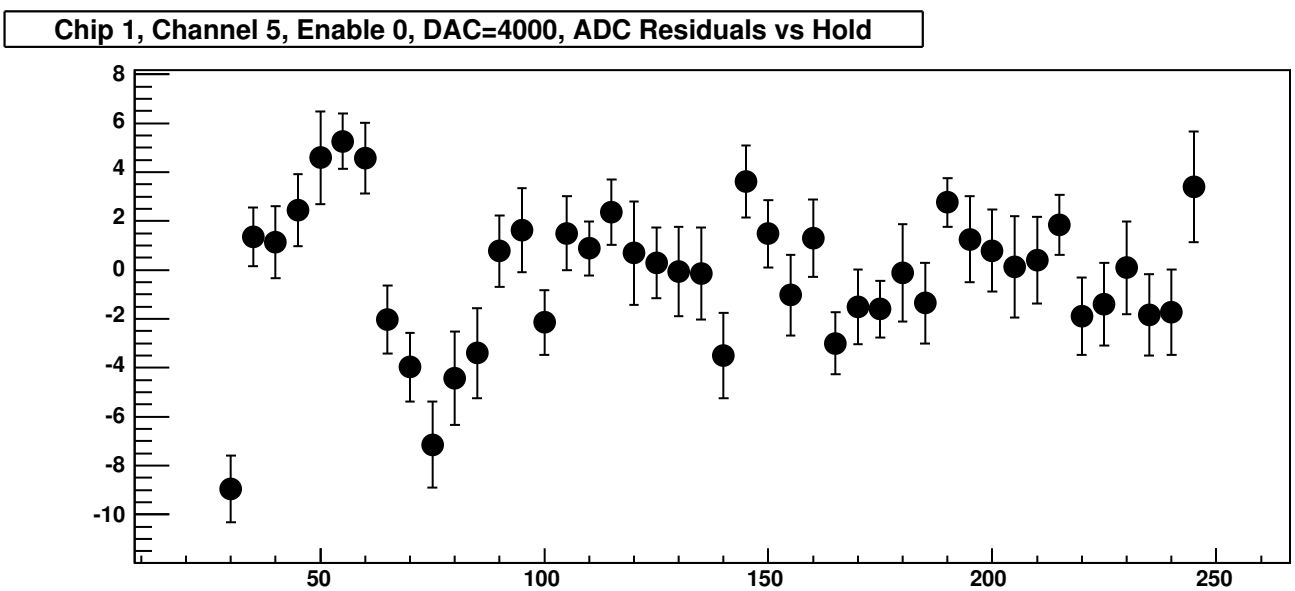

Chip 1, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 2856 / 31
p0 154 ± 0.5331
p1 200 ± 0.0001185
p2 -184.2 ± 18.91
p3 54.38 ± 0.3029
p4 0.1123 ± 0.005289
160
150
140
130
120
110
100
90
20
40
60
80
100
120
140
160
180
200
220

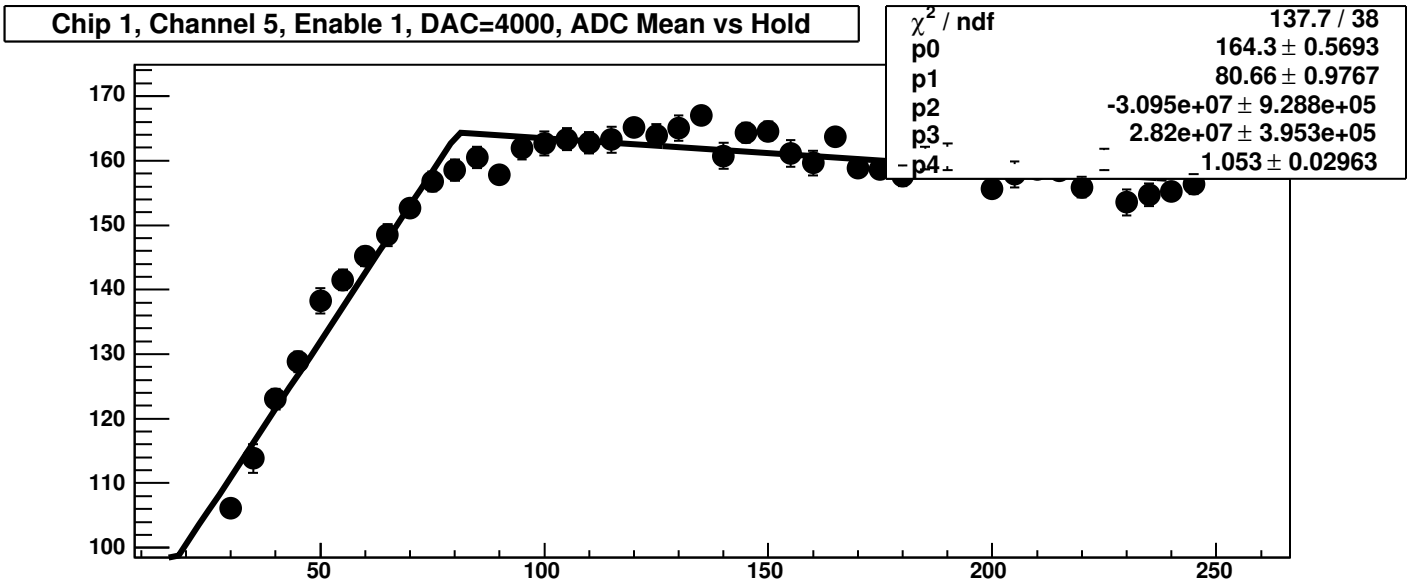

Chip 1, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
20
40
60
80
100
120
140
160
180
200
220

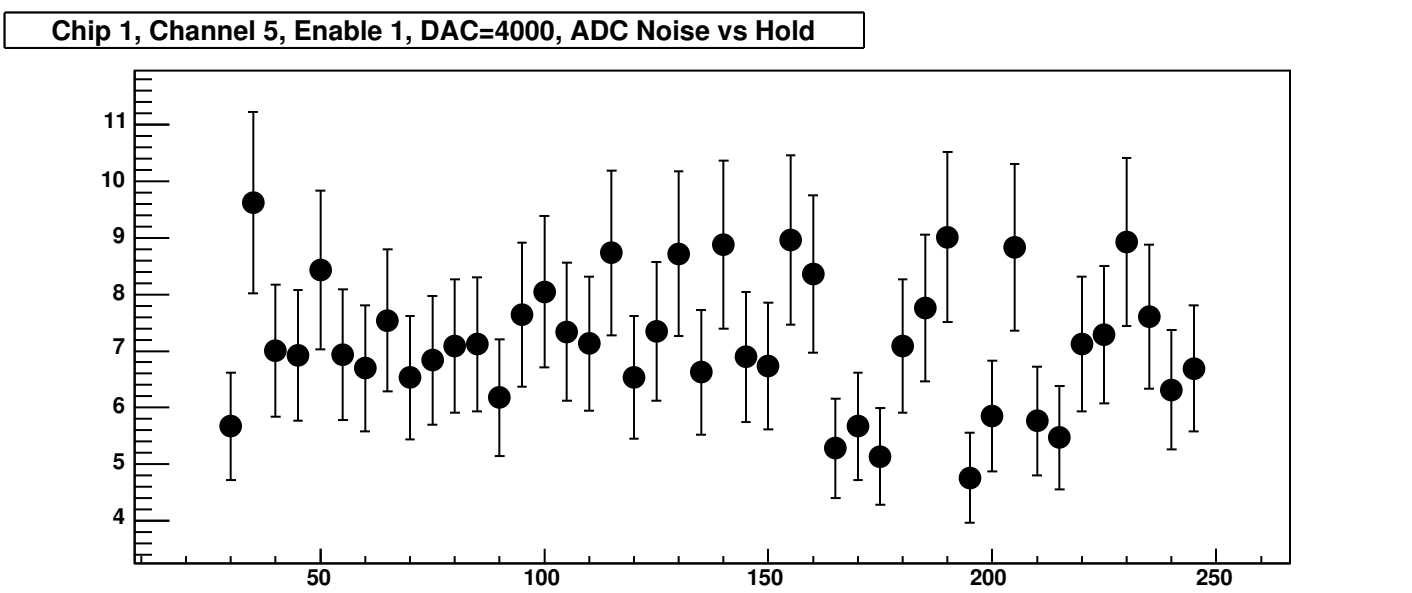

Chip 1, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
-50
20
40
60
80
100
120
140
160
180
200
220

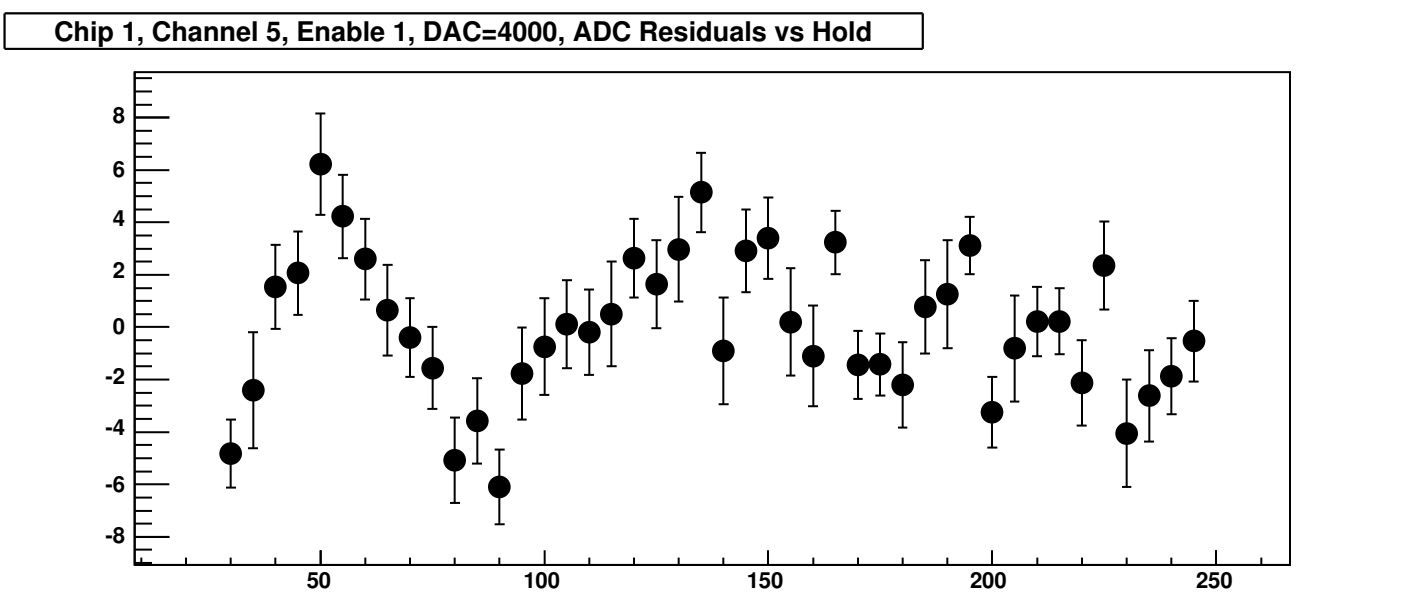

Chip 1, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 166.2 / 38
p0 161 ± 0.5088
p1 75.43 ± 0.8034
p2 -3.512e+07 ± 9.17e+05
p3 2.747e+07 ± 2.871e+05
p4 1.223 ± 0.0314
170
160
150
140
130
120
110
100
90
50
100
150
200
250

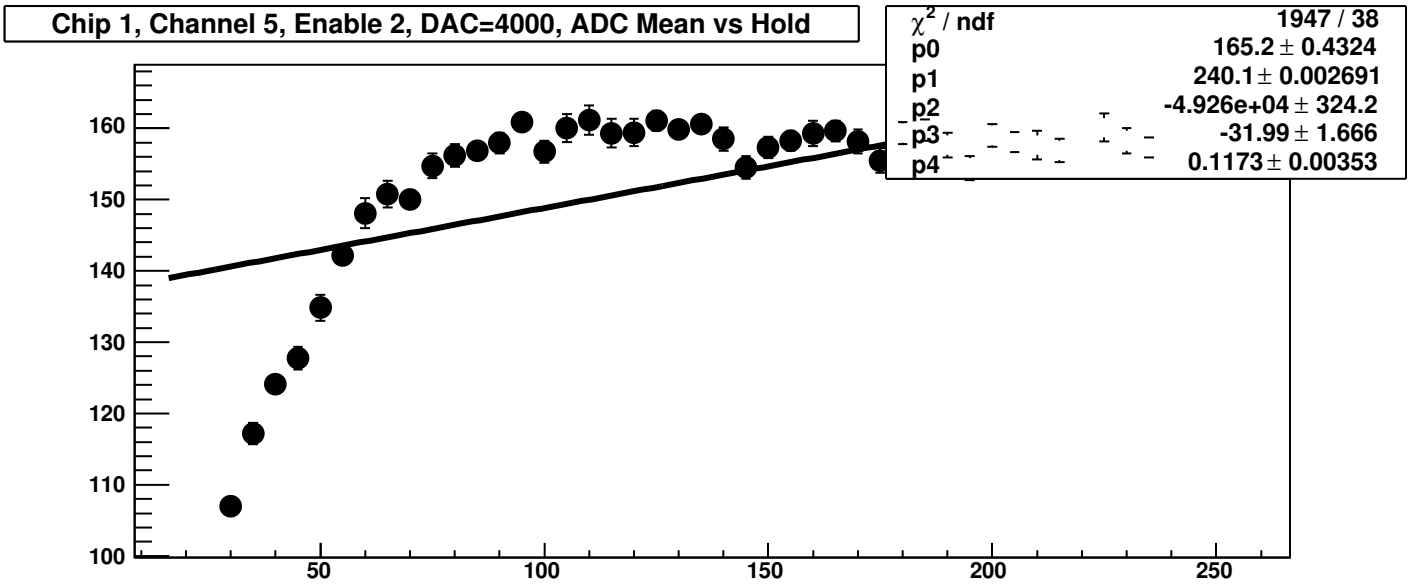

Chip 1, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250

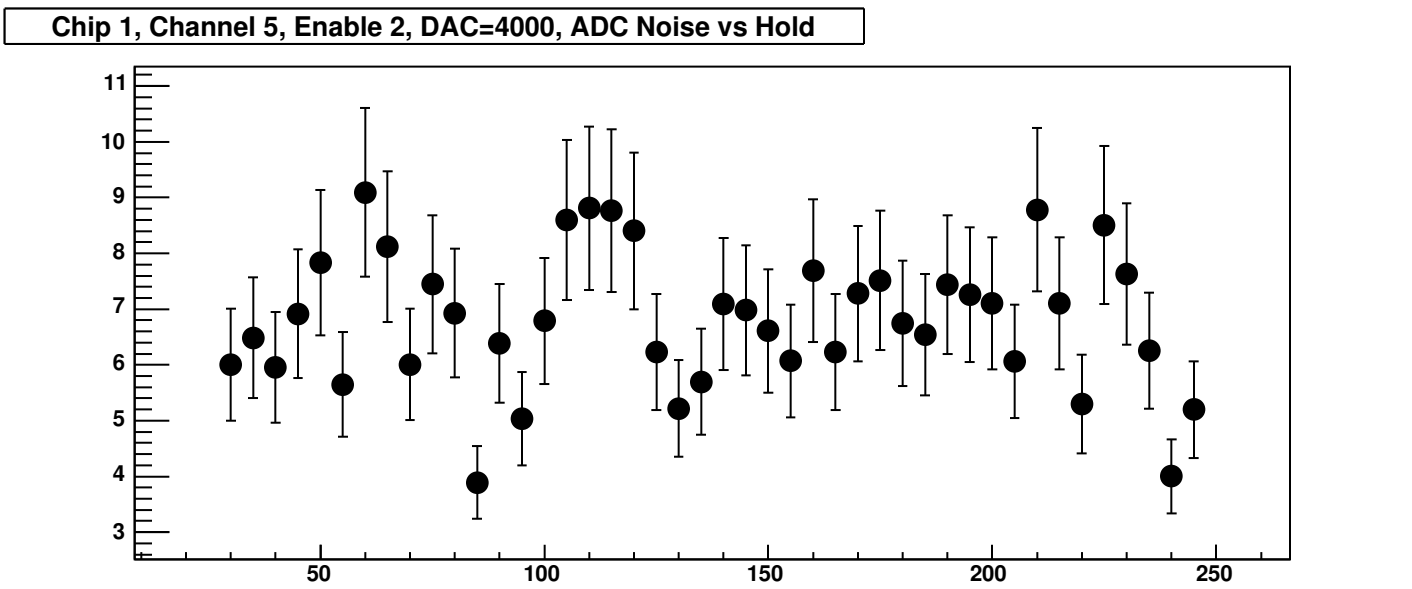

Chip 1, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
50
100
150
200
250

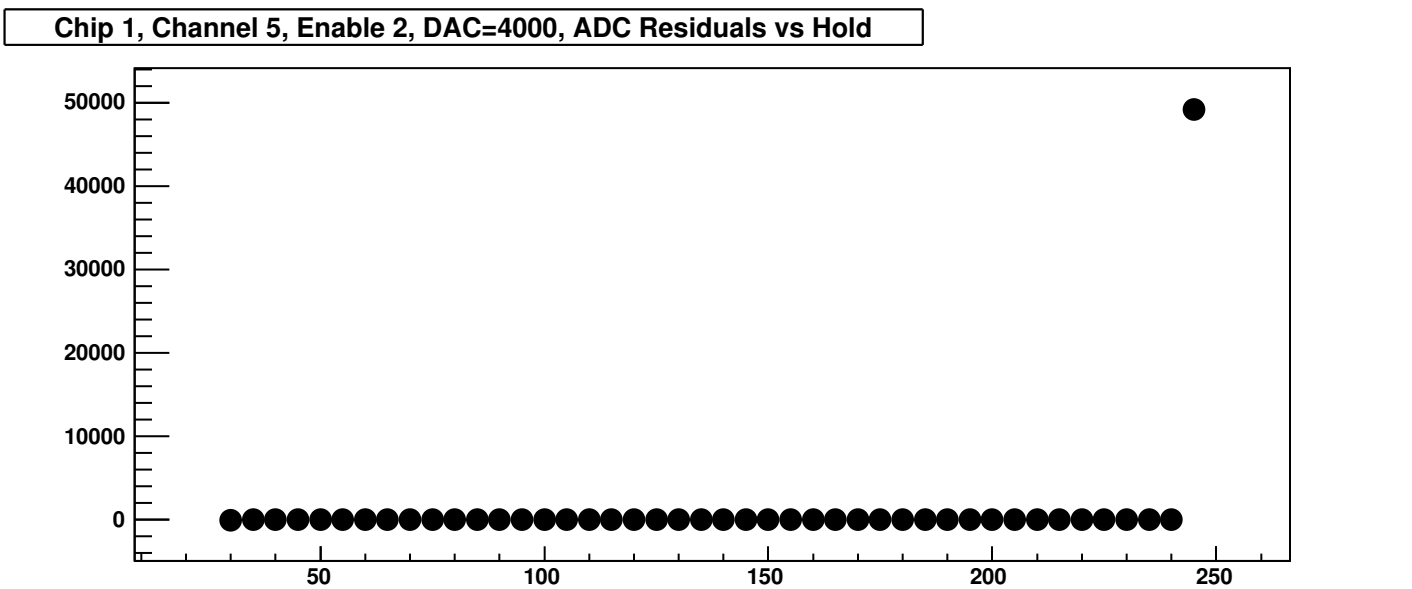

Chip 1, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 137.7 / 38
p0 164.3 ± 0.5693
p1 80.66 ± 0.9767
p2 -3.095e+07 ± 9.288e+05
p3 2.82e+07 ± 3.953e+05
p4 1.053 ± 0.02963
170
160
150
140
130
120
110
100
50
100
150
200
250

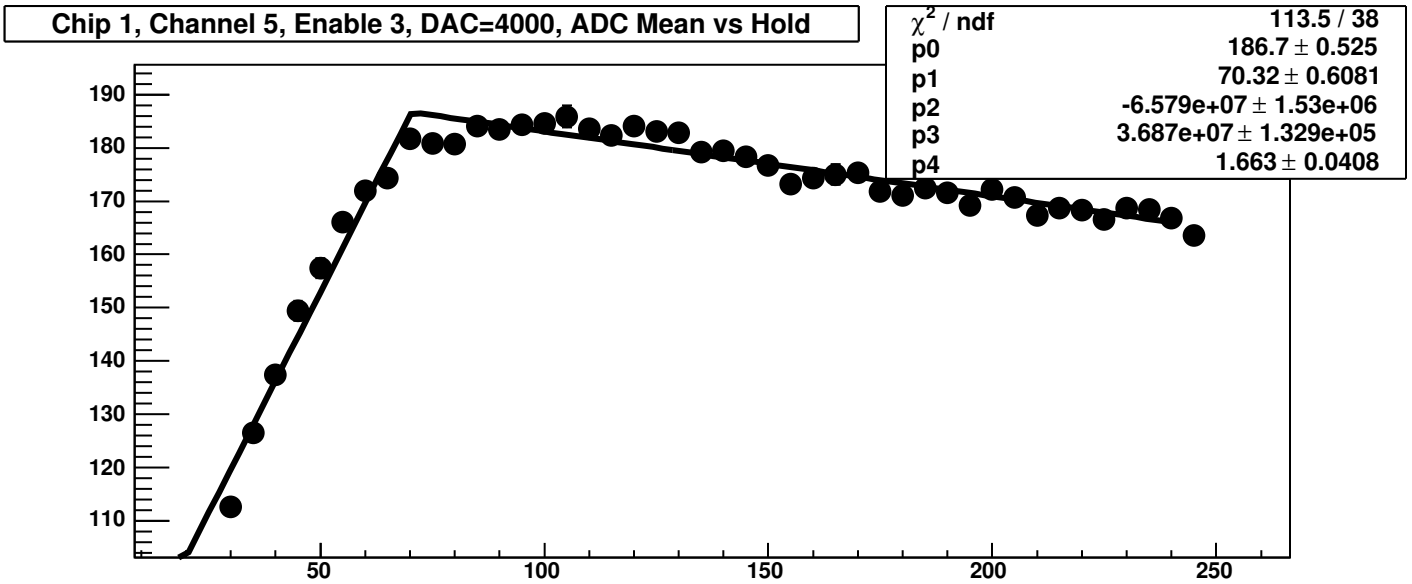

Chip 1, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250

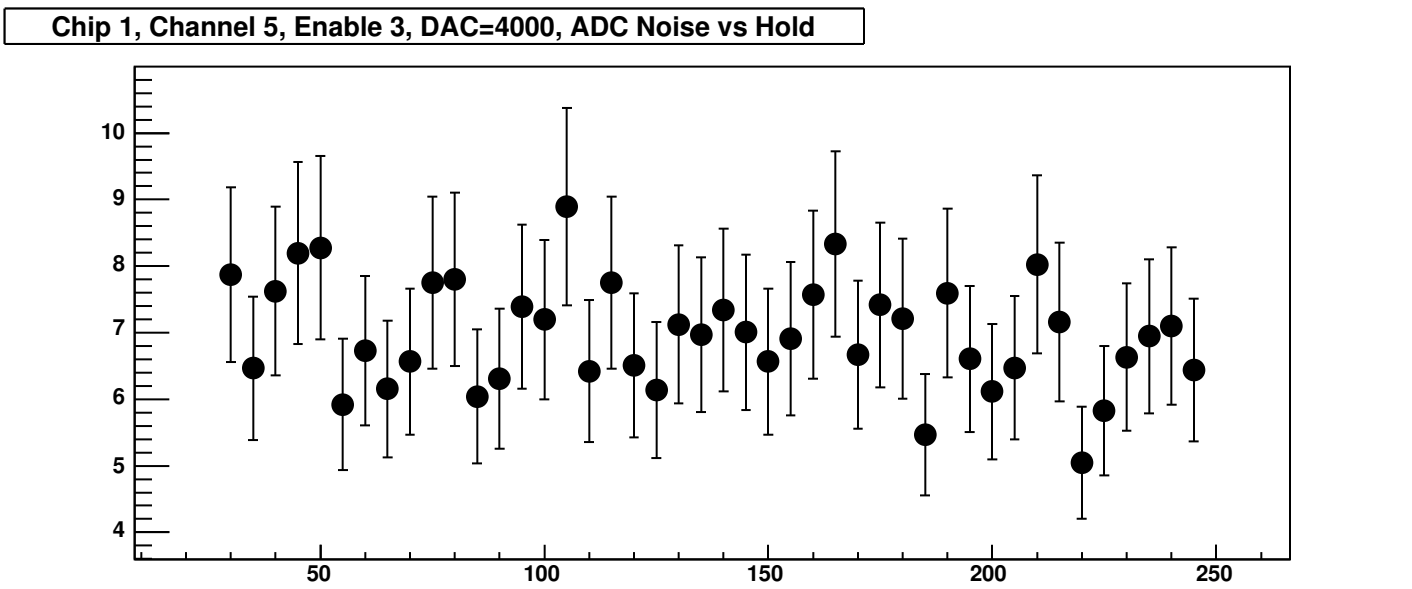

Chip 1, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

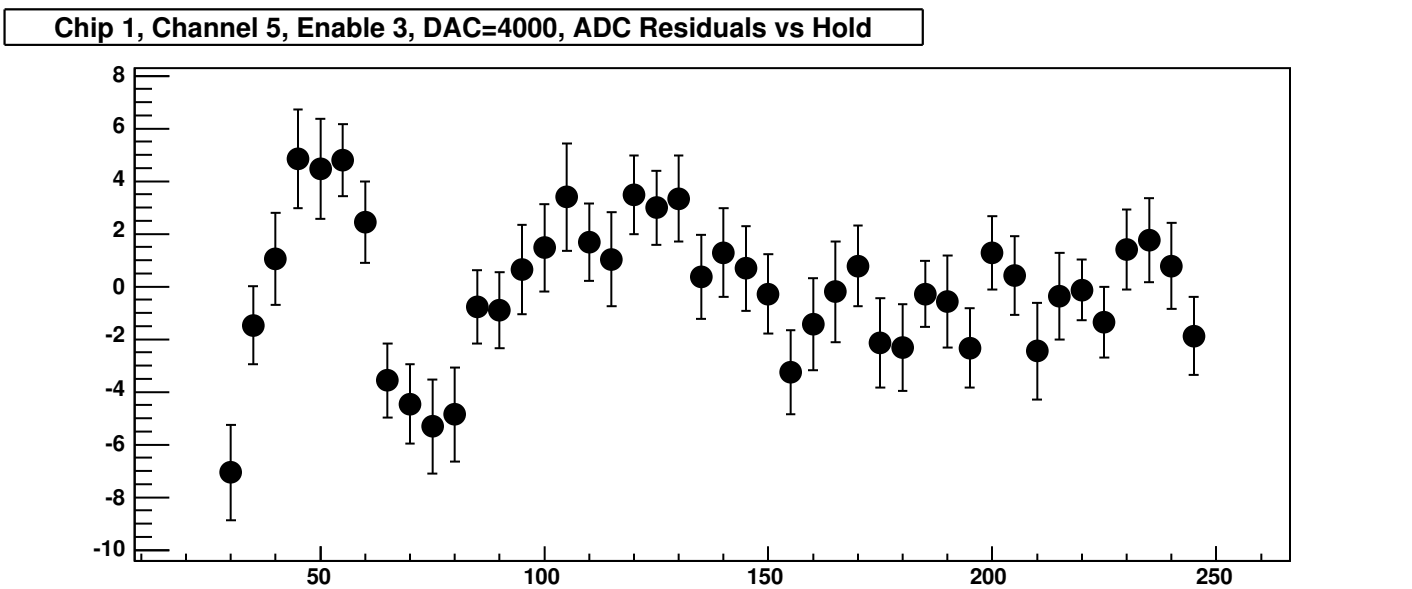

Chip 1, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 1947 / 38
p0 165.2 ± 0.4324
p1 240.1 ± 0.002691
p2 -4.926e+04 ± 324.2
p3 -31.99 ± 1.666
p4 0.1173 ± 0.00353
160
150
140
130
120
110
100
50
100
150
200
250

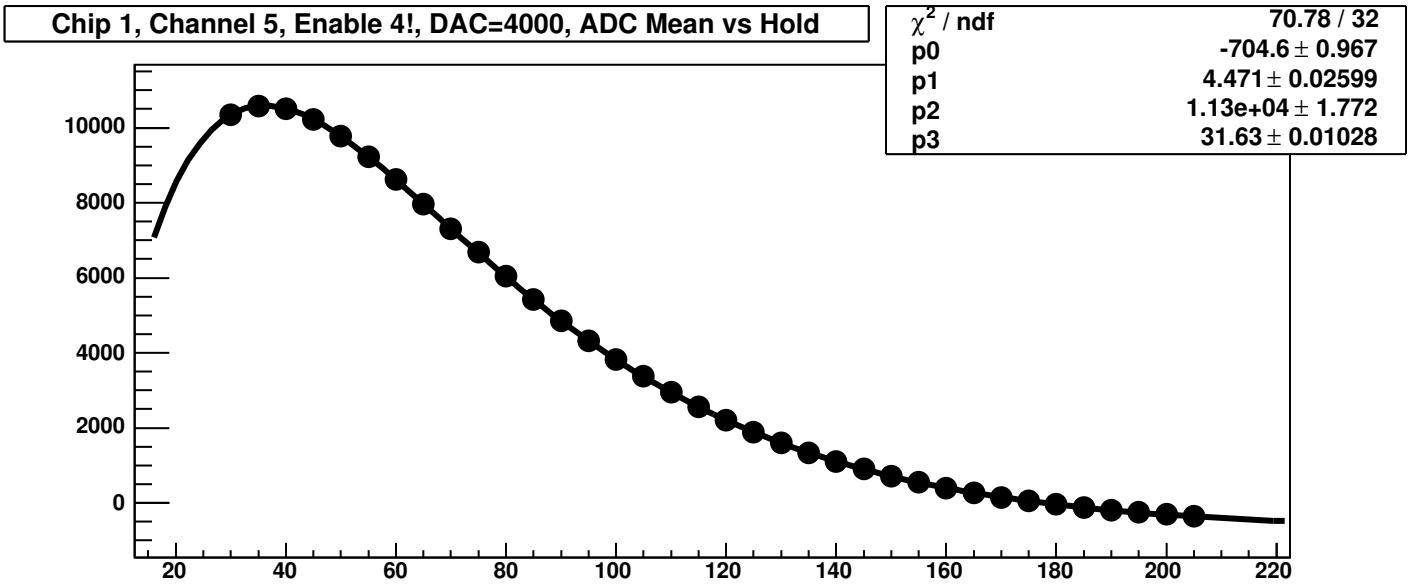

Chip 1, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
50
100
150
200
250

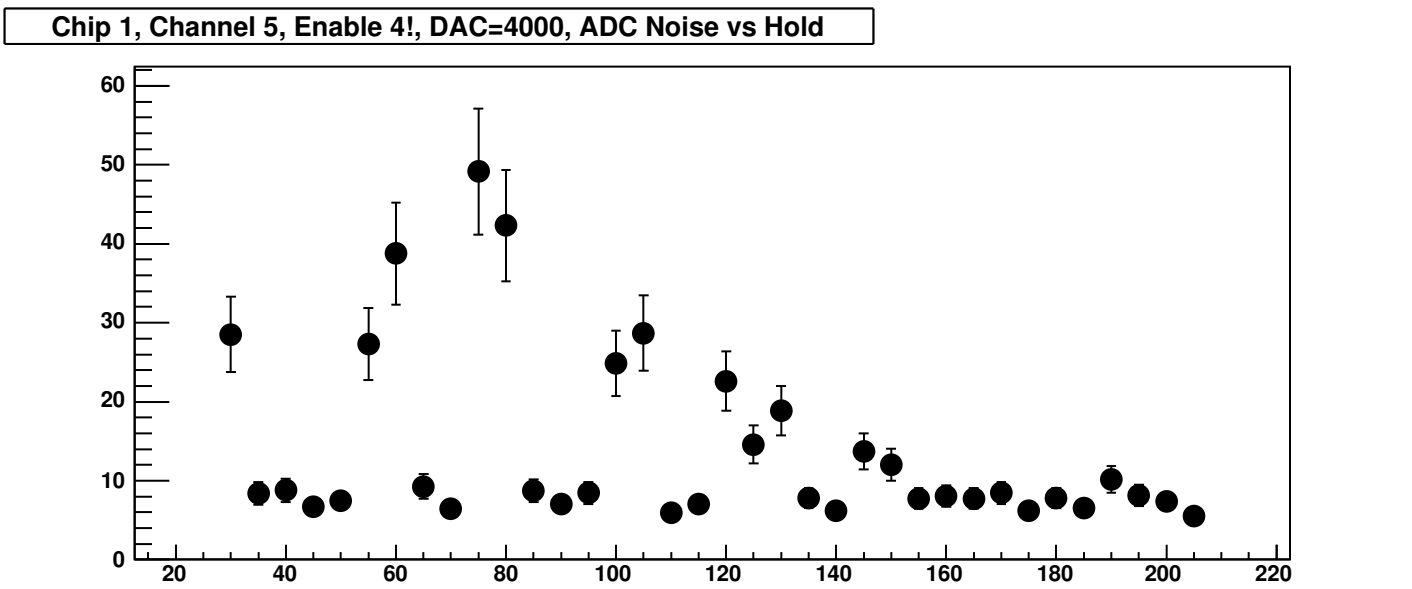

Chip 1, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold
50000
40000
30000
20000
10000
0
50
100
150
200
250

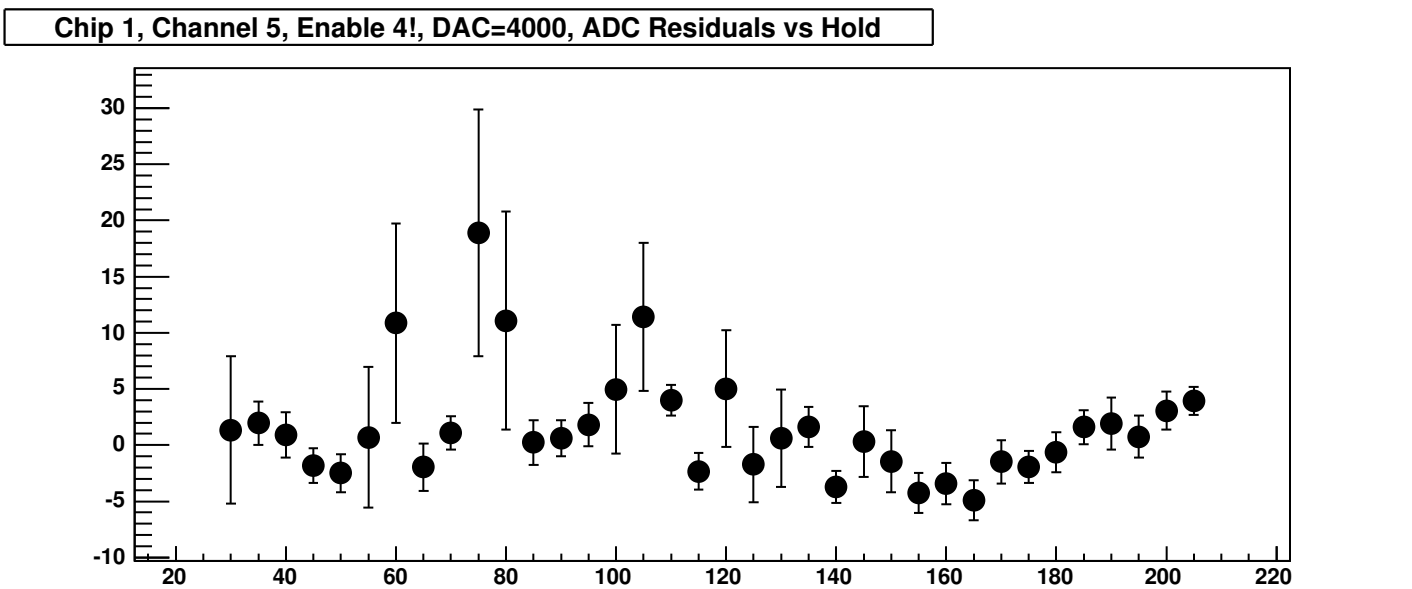

Chip 1, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 113.5 / 38
p0 186.7 ± 0.525
p1 70.32 ± 0.6081
p2 -6.579e+07 ± 1.53e+06
p3 3.687e+07 ± 1.329e+05
p4 1.663 ± 0.0408
190
180
170
160
150
140
130
120
110
50
100
150
200
250

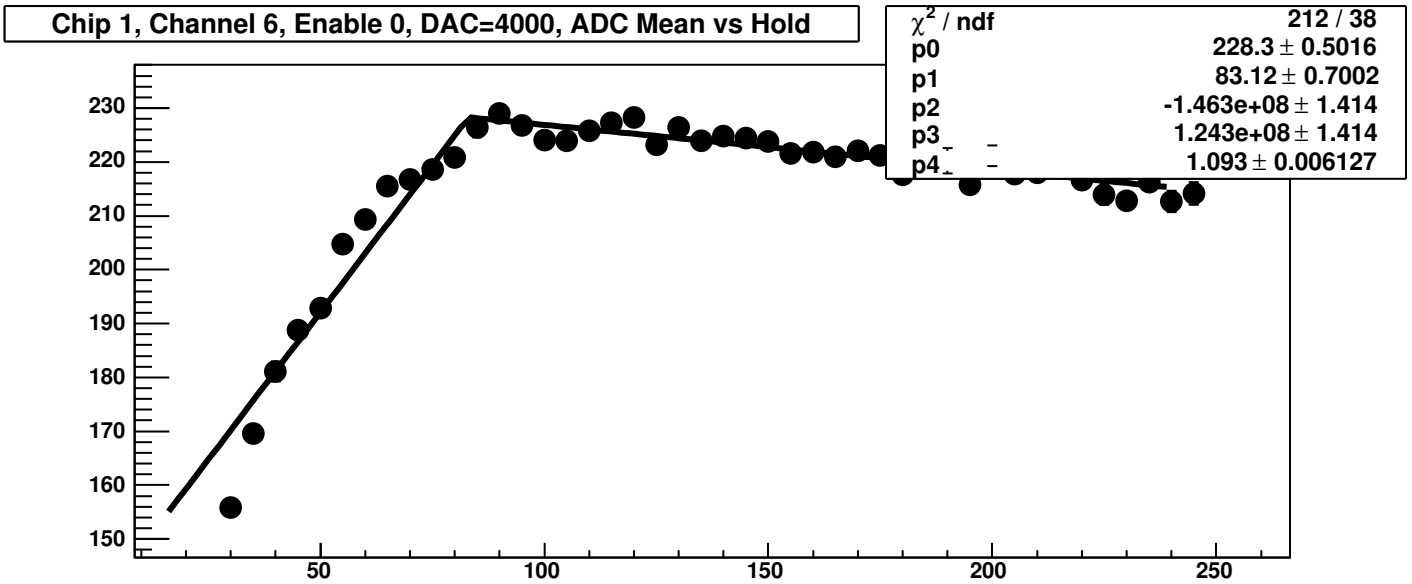

Chip 1, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold
10
9
8
7
6
5
4
50
100
150
200
250

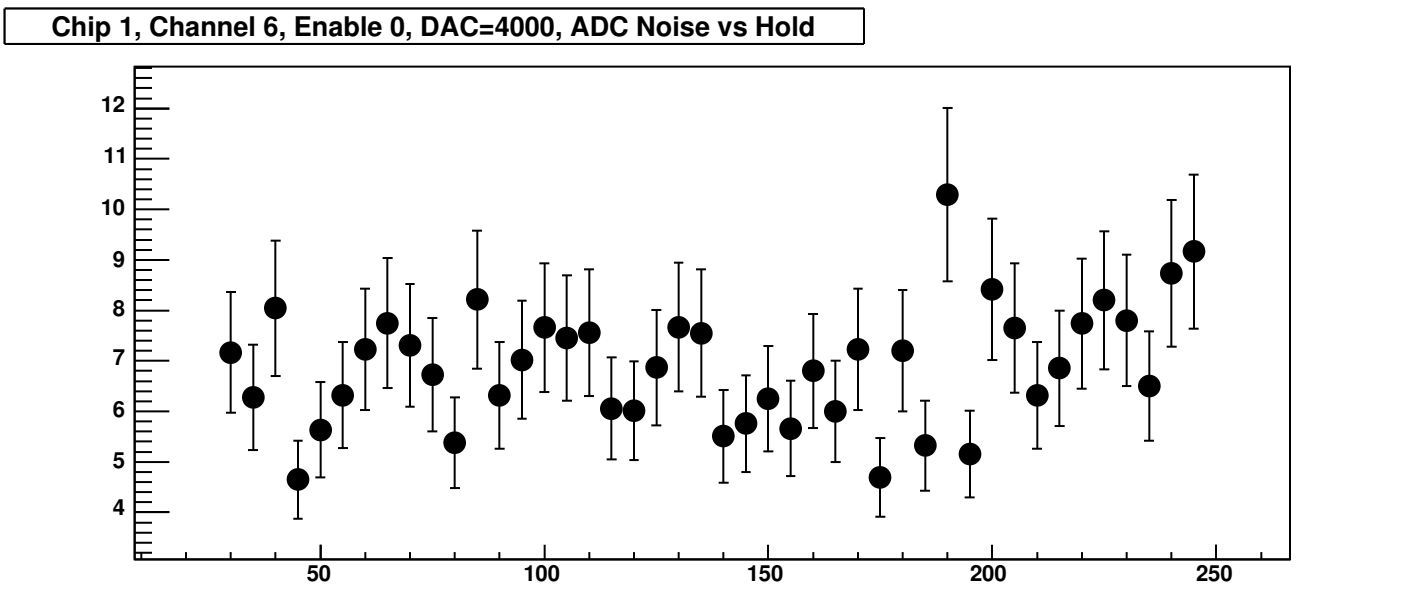

Chip 1, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
50
100
150
200
250

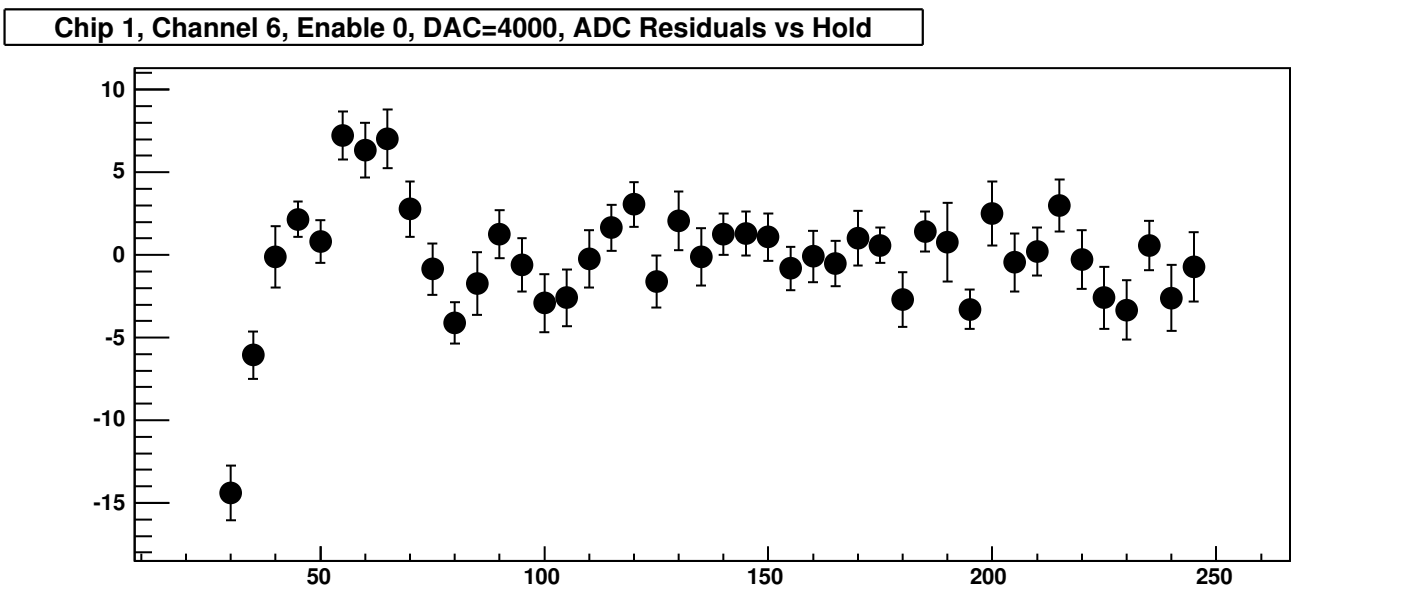

Chip 1, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 70.78 / 32
p0 -704.6 ± 0.967
p1 4.471 ± 0.02599
p2 1.13e+04 ± 1.772
p3 31.63 ± 0.01028
10000
8000
6000
4000
2000
0
20
40
60
80
100
120
140
160
180
200
220

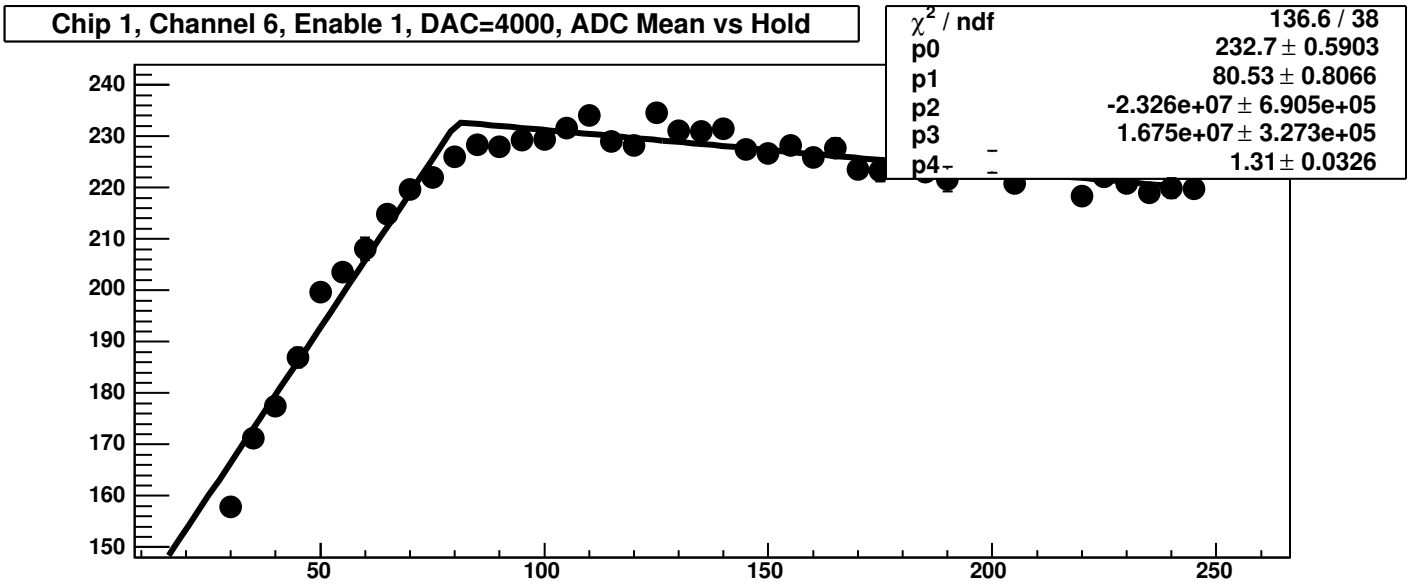

Chip 1, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold
60
50
40
30
20
10
0
20
40
60
80
100
120
140
160
180
200
220

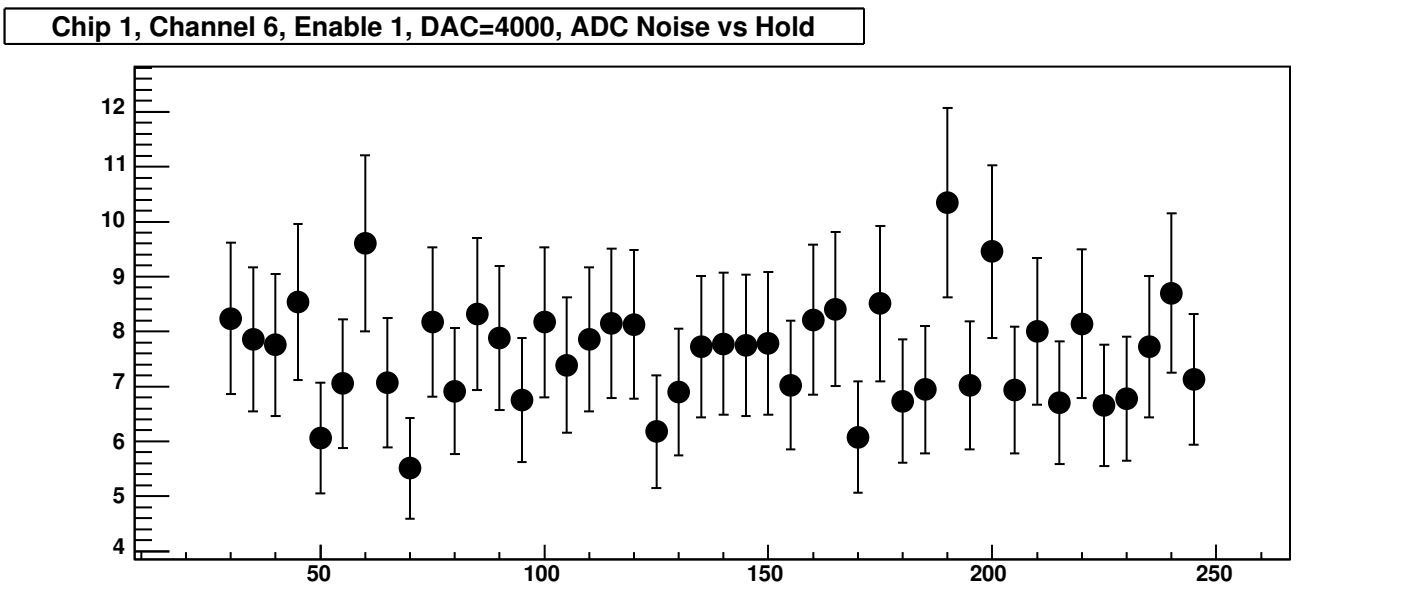

Chip 1, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
20
40
60
80
100
120
140
160
180
200
220

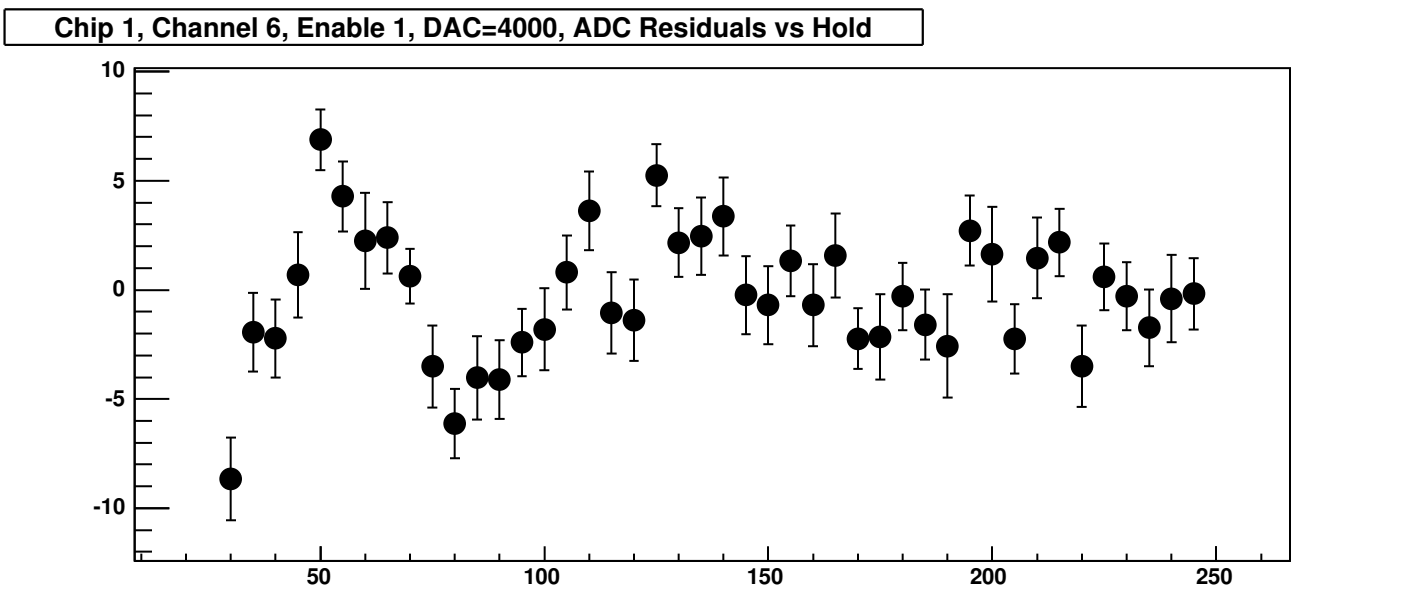

Chip 1, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 212 / 38
p0 228.3 ± 0.5016
p1 83.12 ± 0.7002
p2 -1.463e+08 ± 1.414
p3 1.243e+08 ± 1.414
p4 1.093 ± 0.006127
230
220
210
200
190
180
170
160
150
50
100
150
200
250

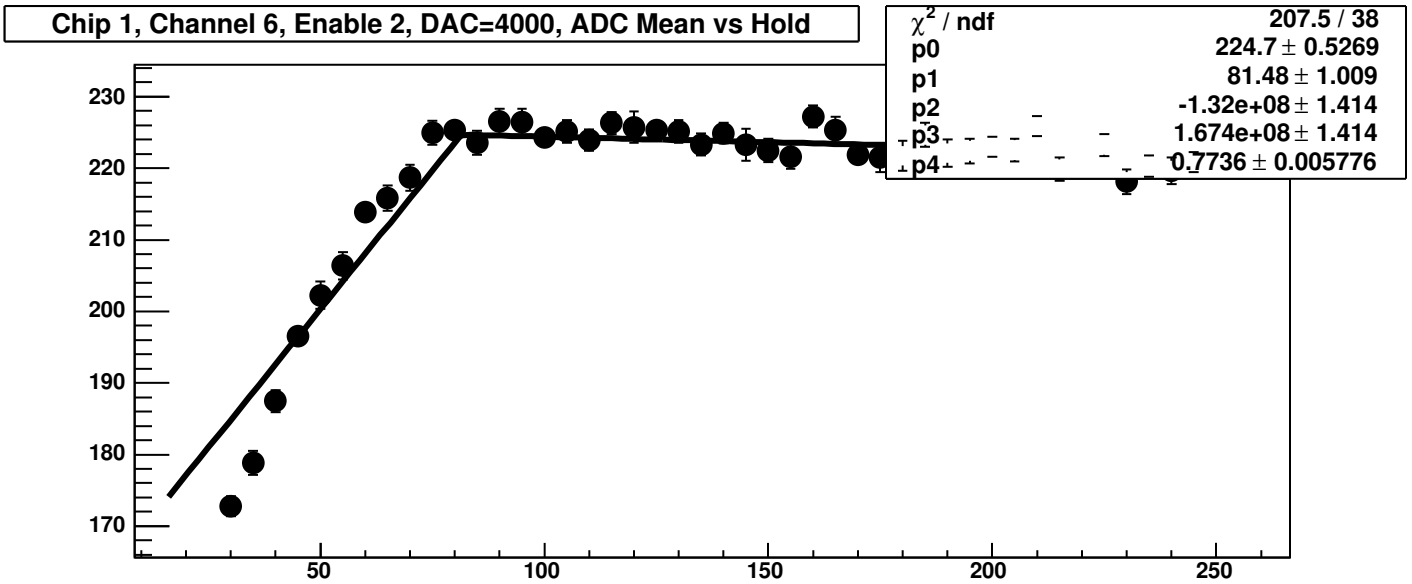

Chip 1, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

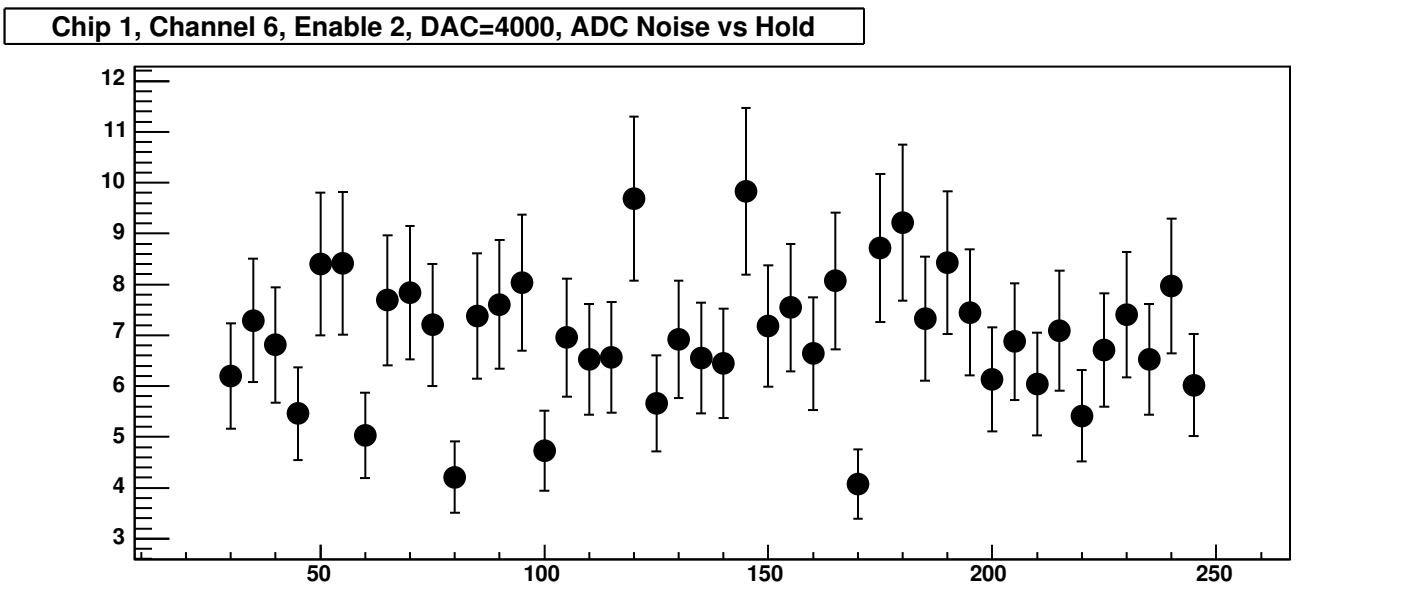

Chip 1, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
50
100
150
200
250

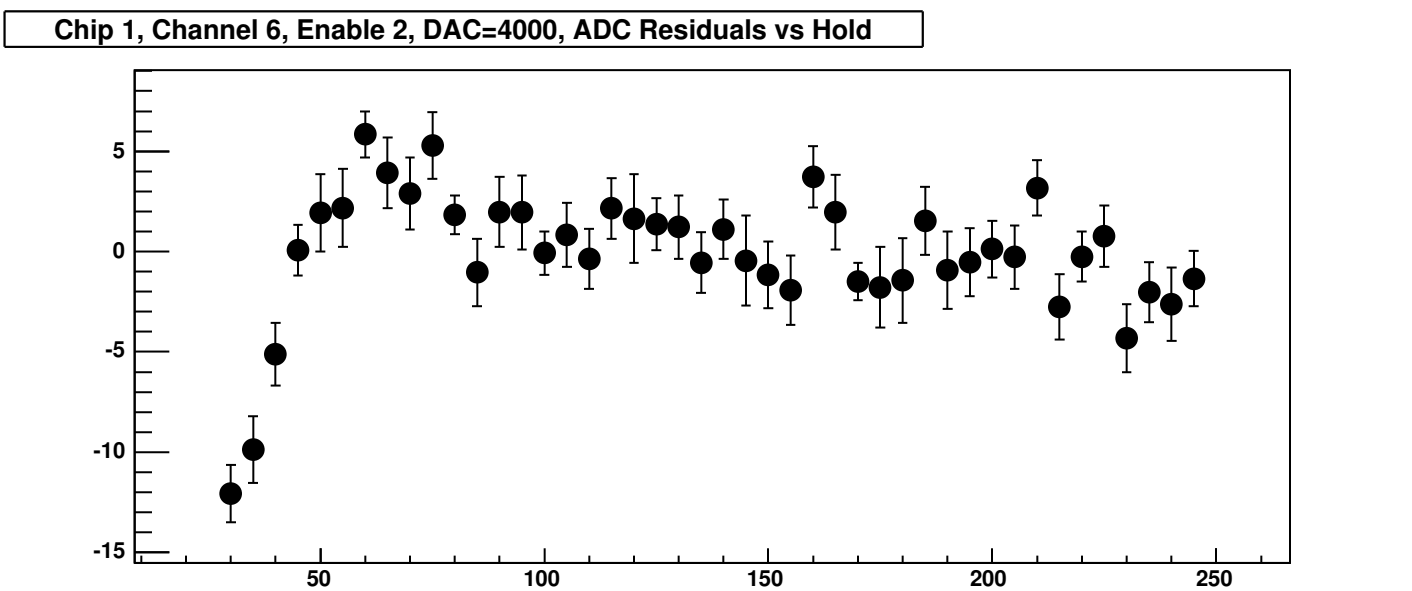

Chip 1, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 136.6 / 38
p0 232.7 ± 0.5903
p1 80.53 ± 0.8066
p2 -2.326e+07 ± 6.905e+05
p3 1.675e+07 ± 3.273e+05
p4 1.31 ± 0.0326
240
230
220
210
200
190
180
170
160
150
50
100
150
200
250

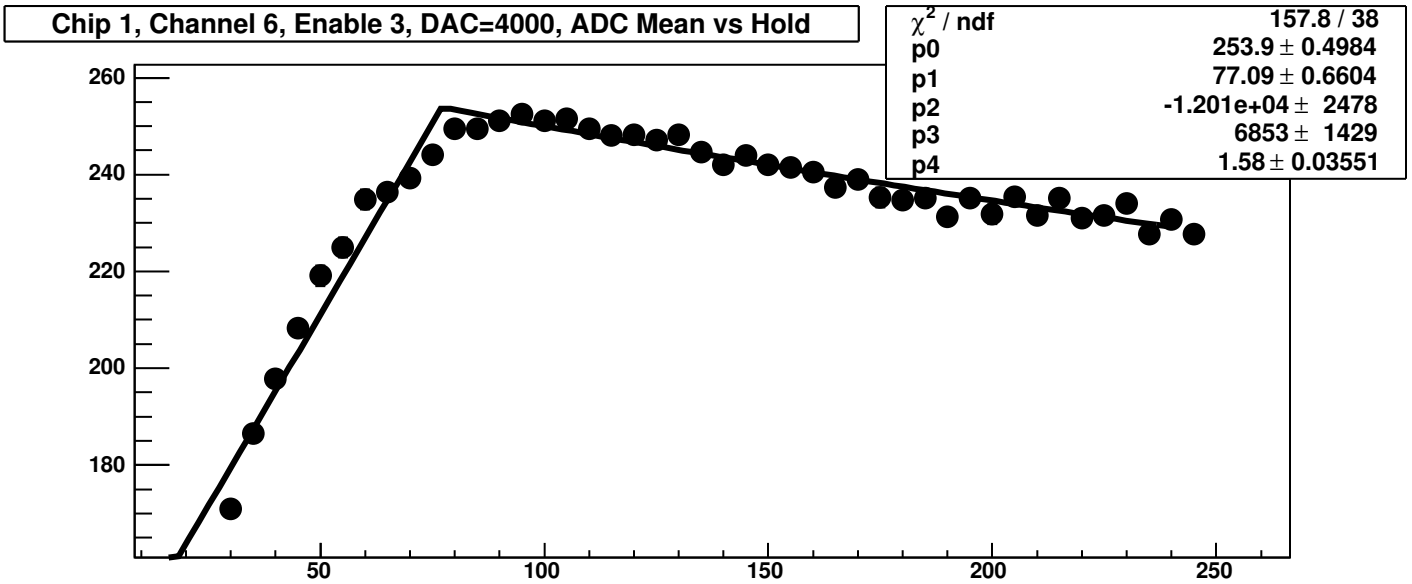

Chip 1, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

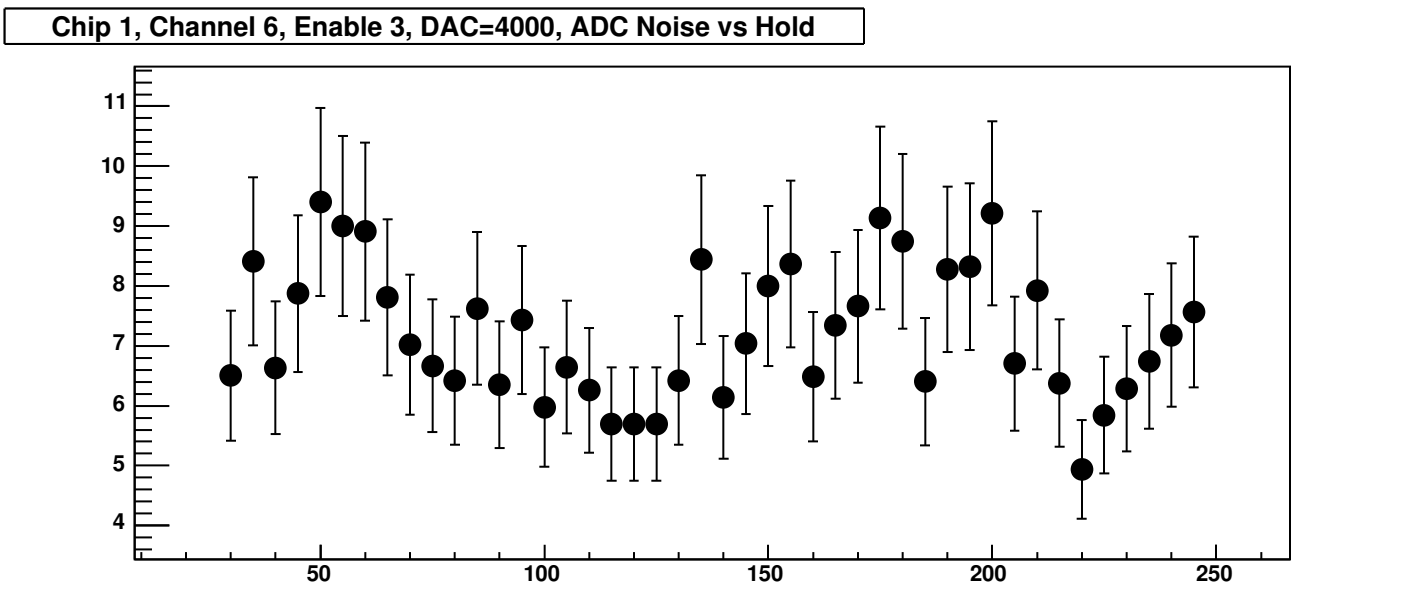

Chip 1, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

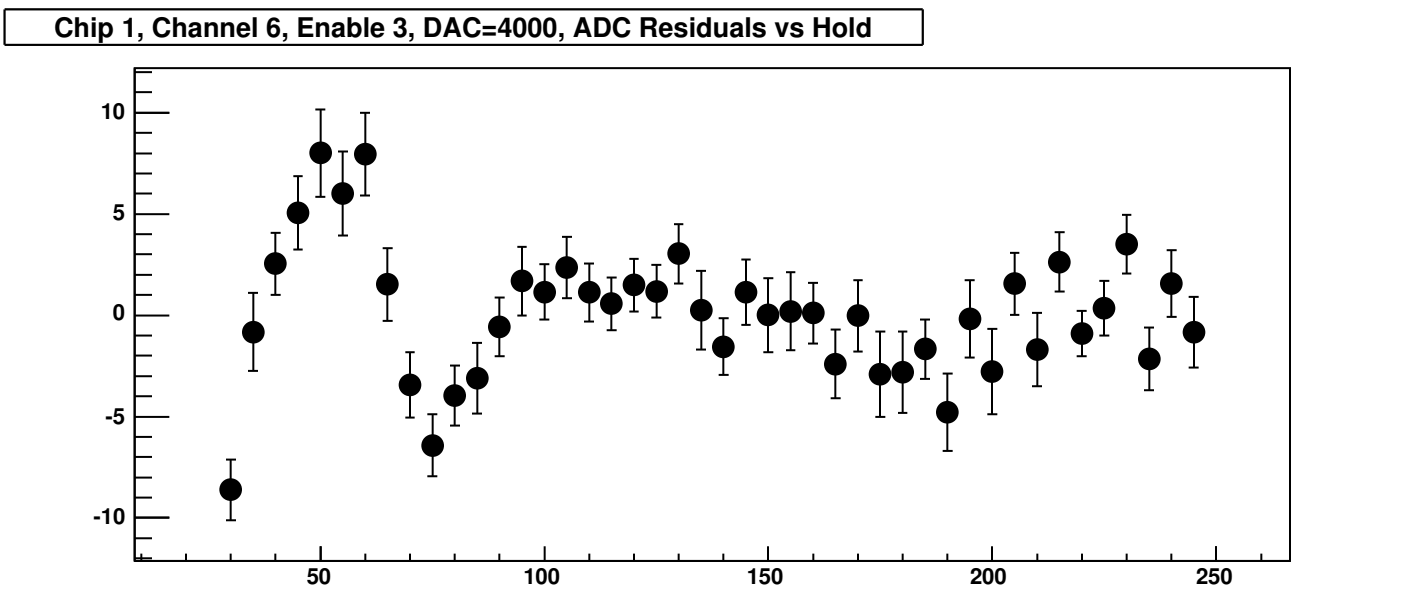

Chip 1, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 207.5 / 38
p0 224.7 ± 0.5269
p1 81.48 ± 1.009
p2 -1.32e+08 ± 1.414
p3 1.674e+08 ± 1.414
p4 0.7736 ± 0.005776
230
220
210
200
190
180
170
50
100
150
200
250


Chip 1, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250

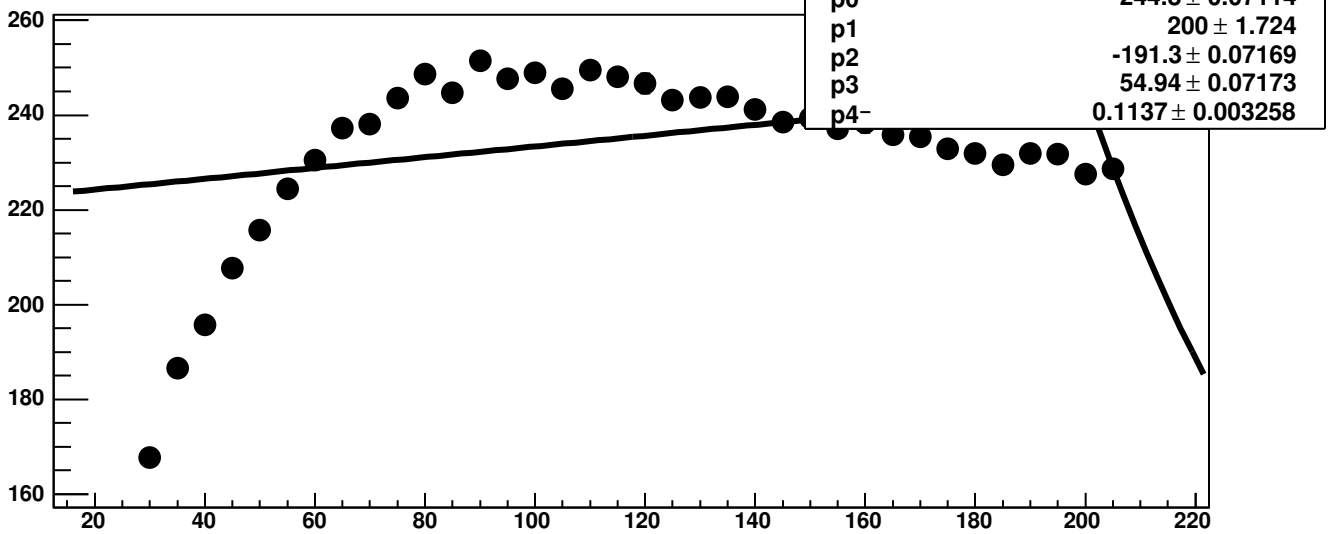

Chip 1, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold
5
0
-5
-10
-15
50
100
150
200
250


Chip 1, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 157.8 / 38
p0 253.9 ± 0.4984
p1 77.09 ± 0.6604
p2 -1.201e+04 ± 2478
p3 6853 ± 1429
p4 1.58 ± 0.03551
260
240
220
200
180
50
100
150
200
250

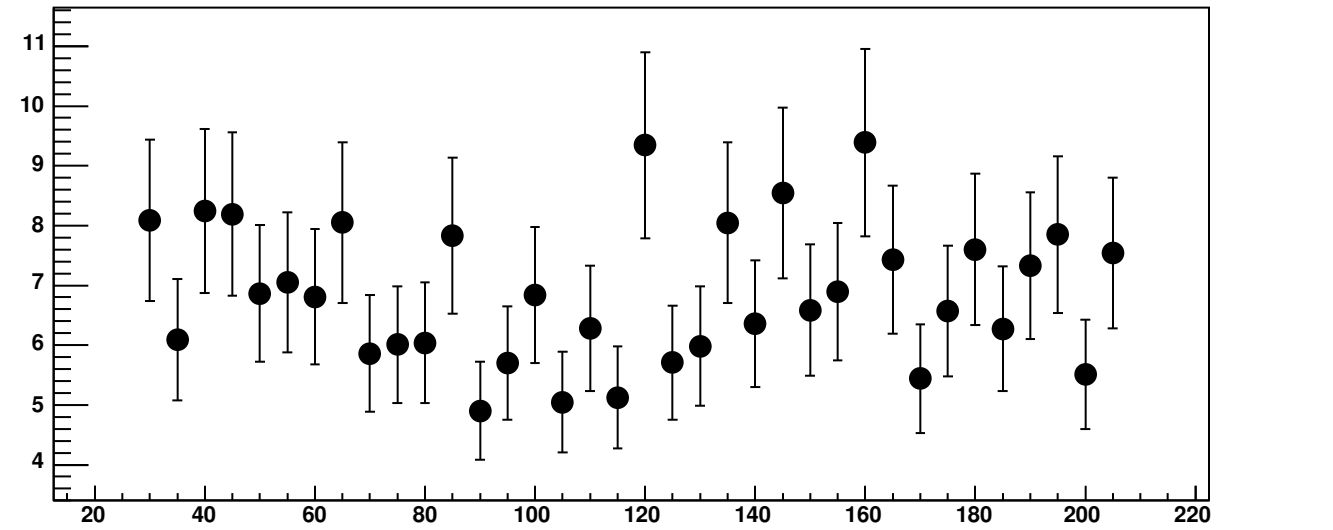

Chip 1, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 1, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

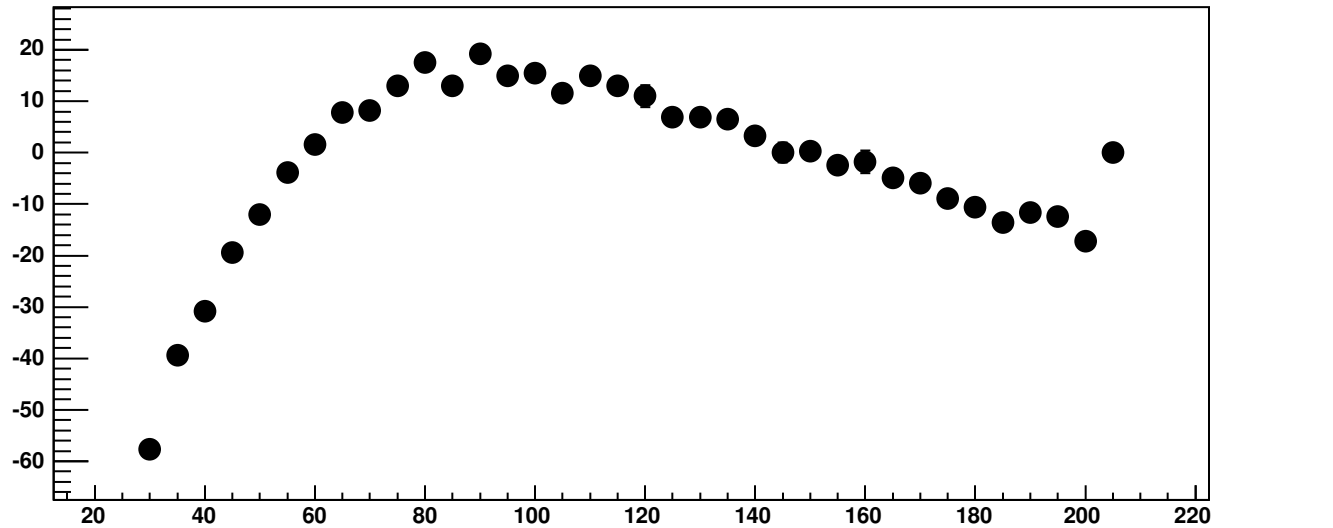
Chip 1, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

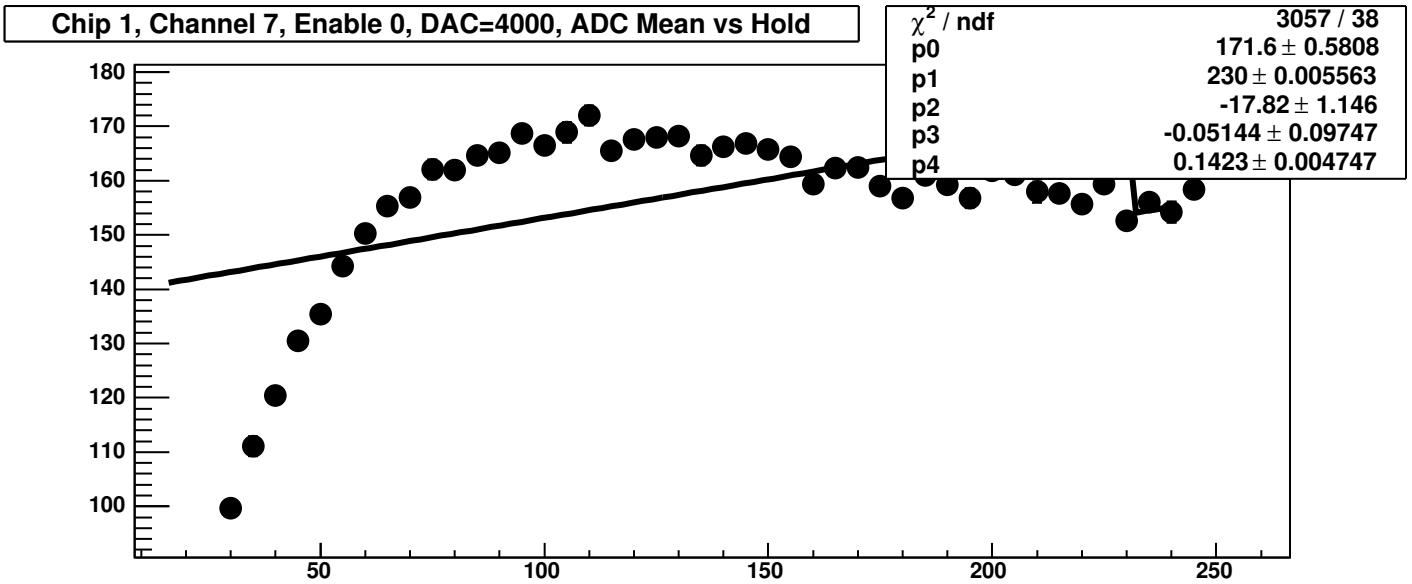


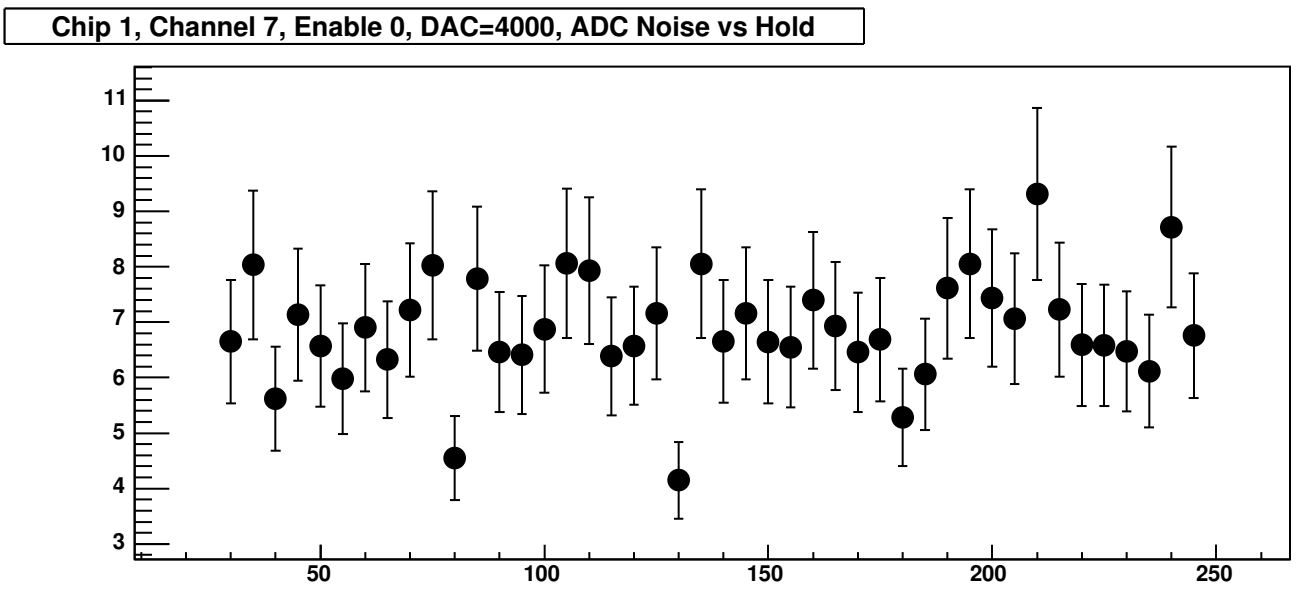
Chip 1, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold

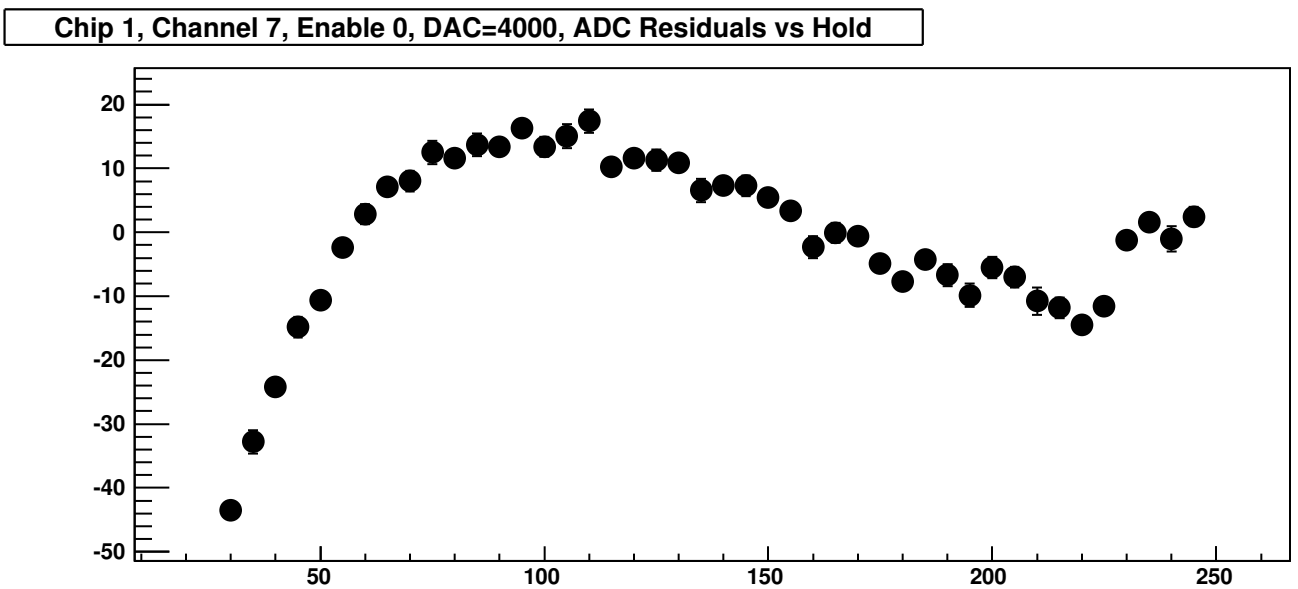


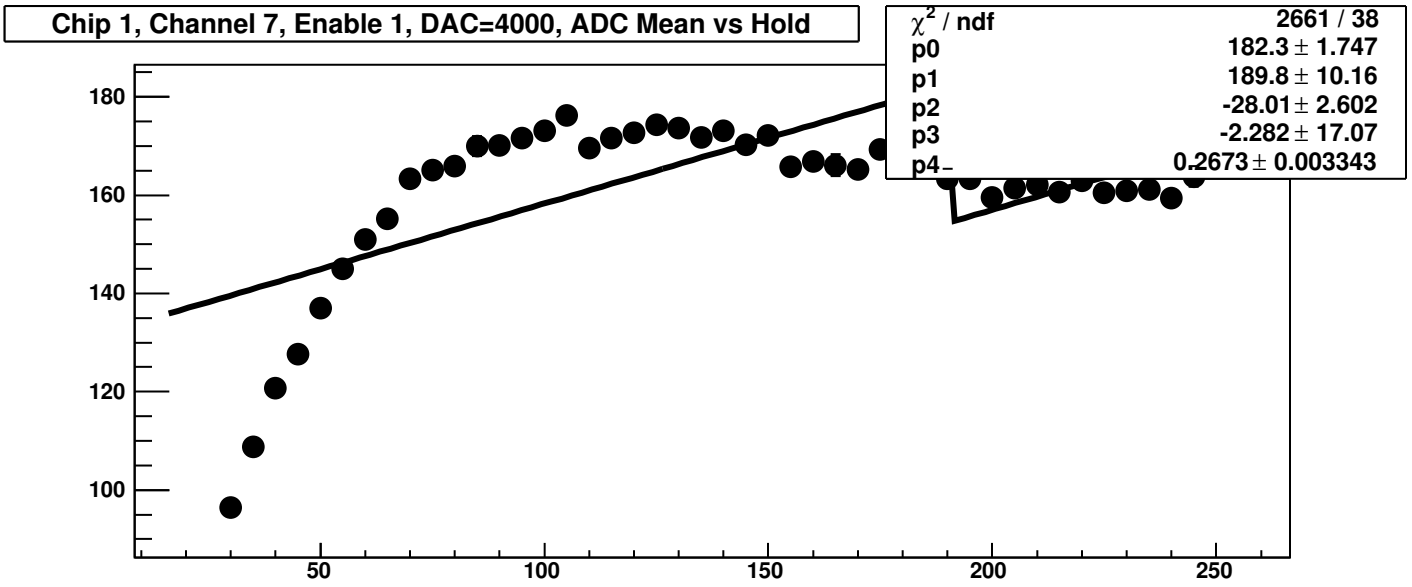
Chip 1, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

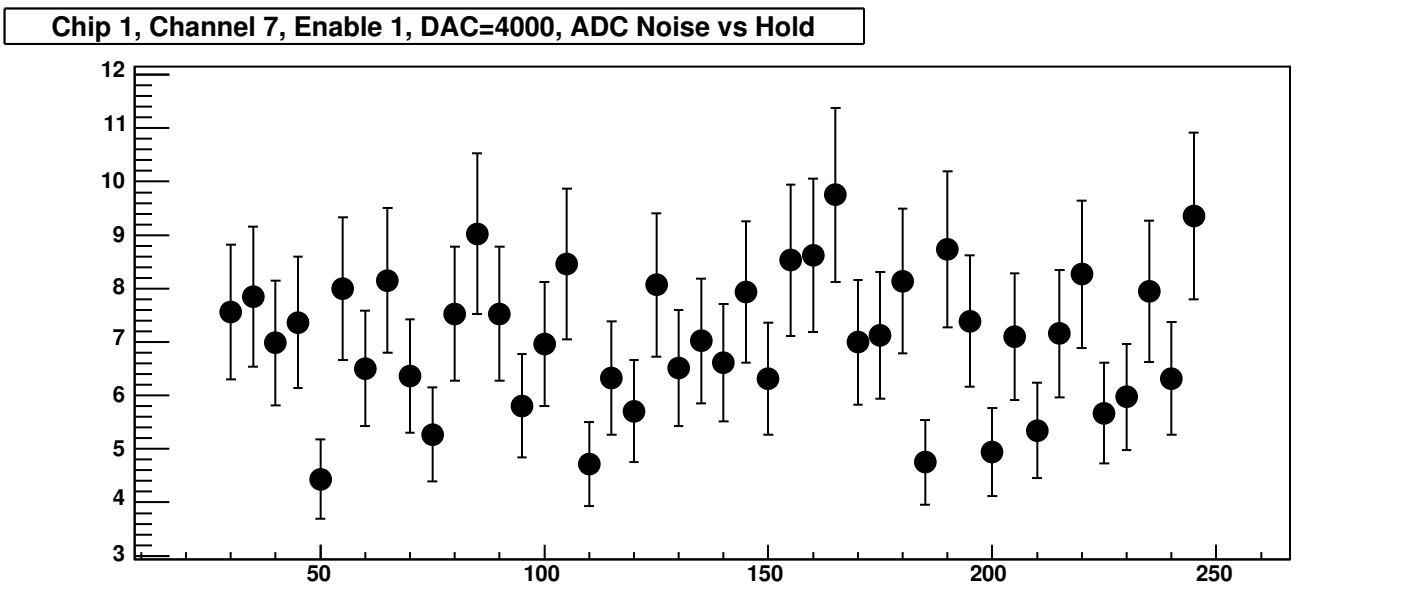


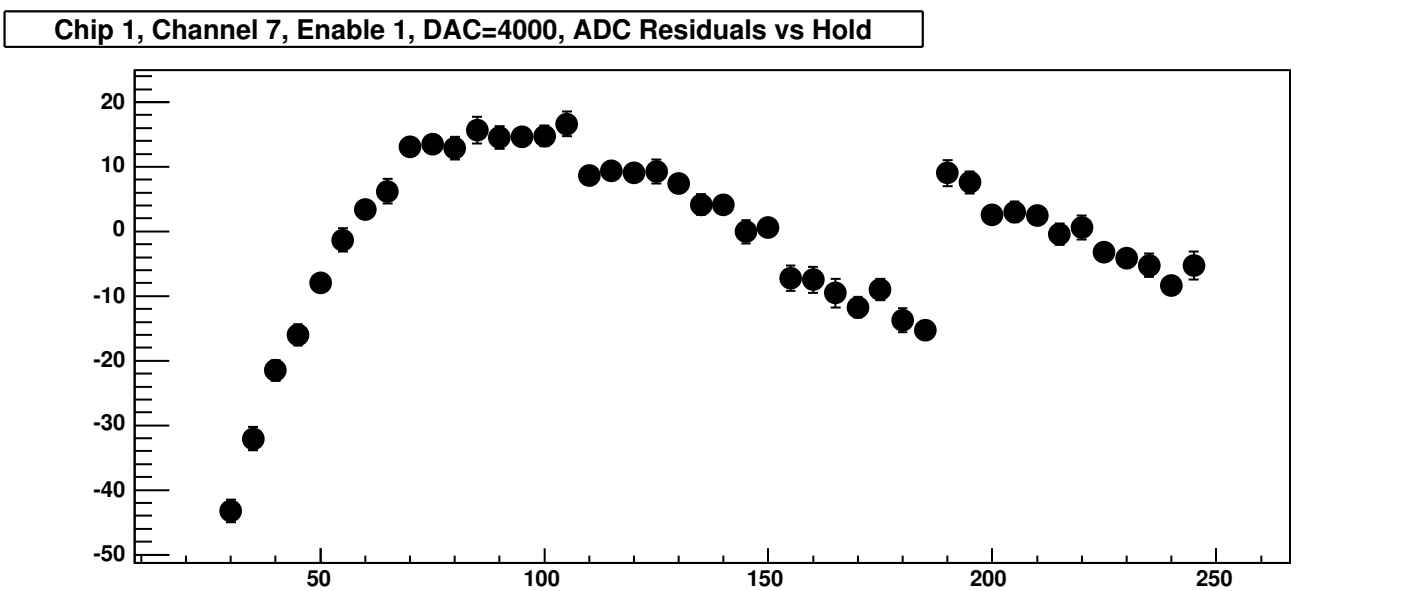

Chip 1, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3057 / 38
p0 171.6 ± 0.5808
p1 230 ± 0.005563
p2 -17.82 ± 1.146
p3 -0.05144 ± 0.09747
p4 0.1423 ± 0.004747
180
170
160
150
140
130
120
110
100
50
100
150
200
250


Chip 1, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
50
100
150
200
250

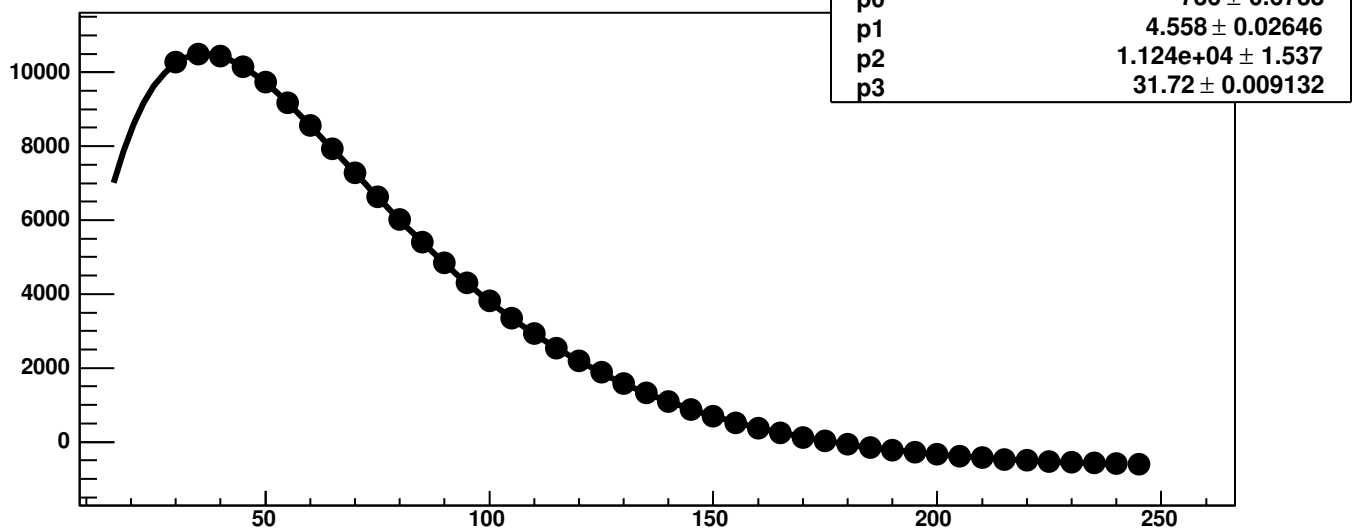

Chip 1, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
-50
50
100
150
200
250


Chip 1, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 2661 / 38
p0 182.3 ± 1.747
p1 189.8 ± 10.16
p2 -28.01 ± 2.602
p3 -2.282 ± 17.07
p4 0.2673 ± 0.003343
180
160
140
120
100
50
100
150
200
250

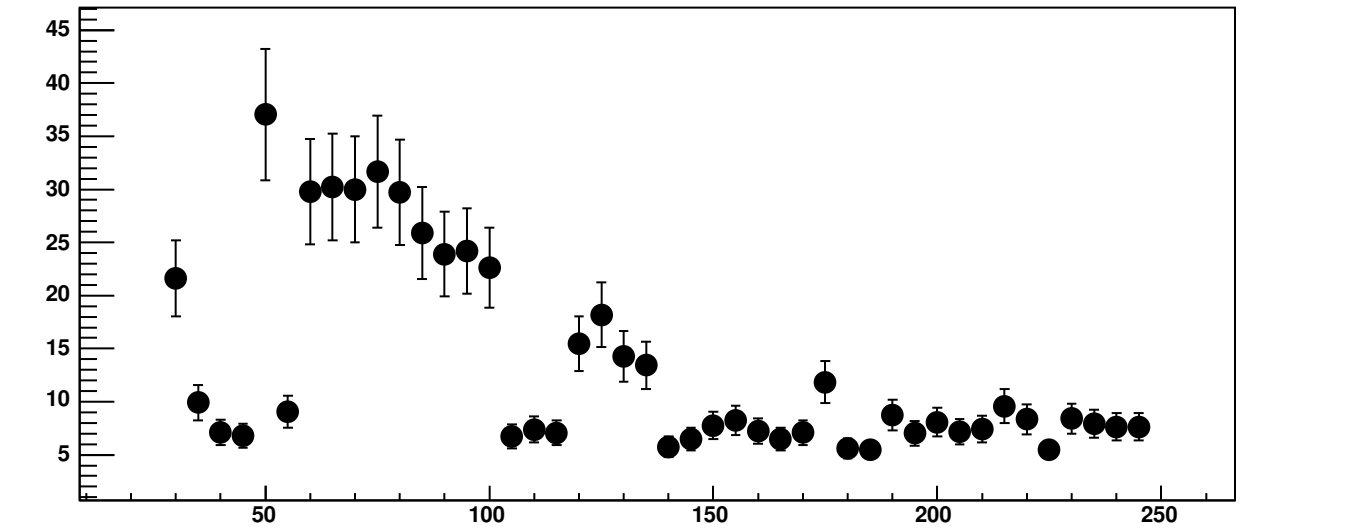

Chip 1, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250


Chip 1, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
-50
50
100
150
200
250

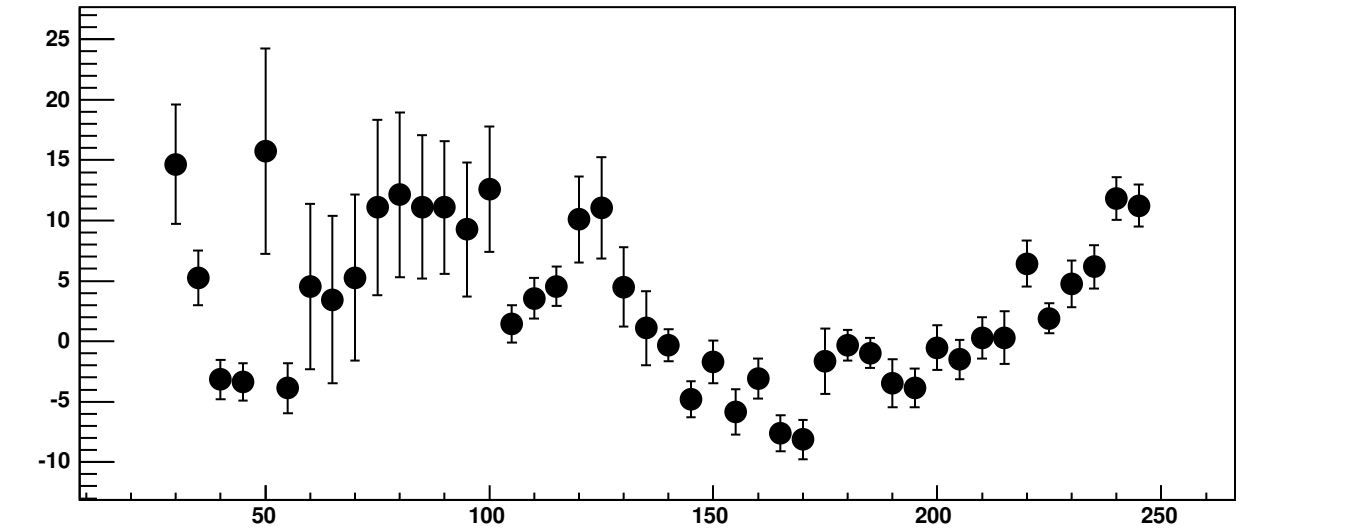
Chip 1, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold

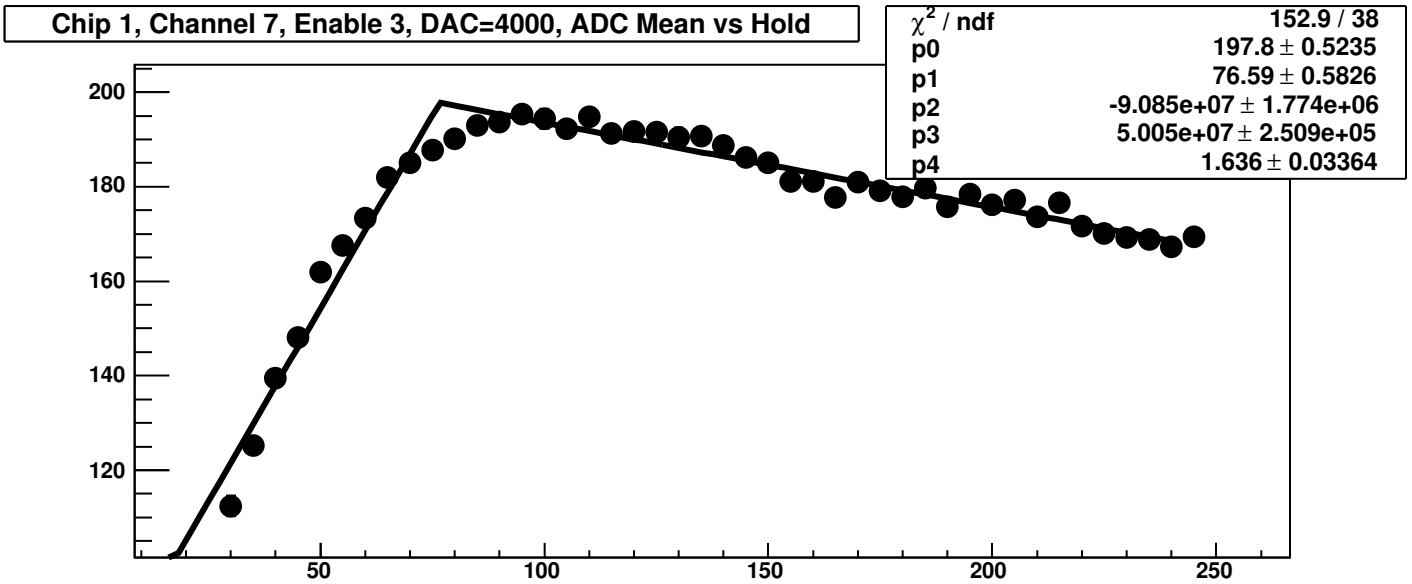


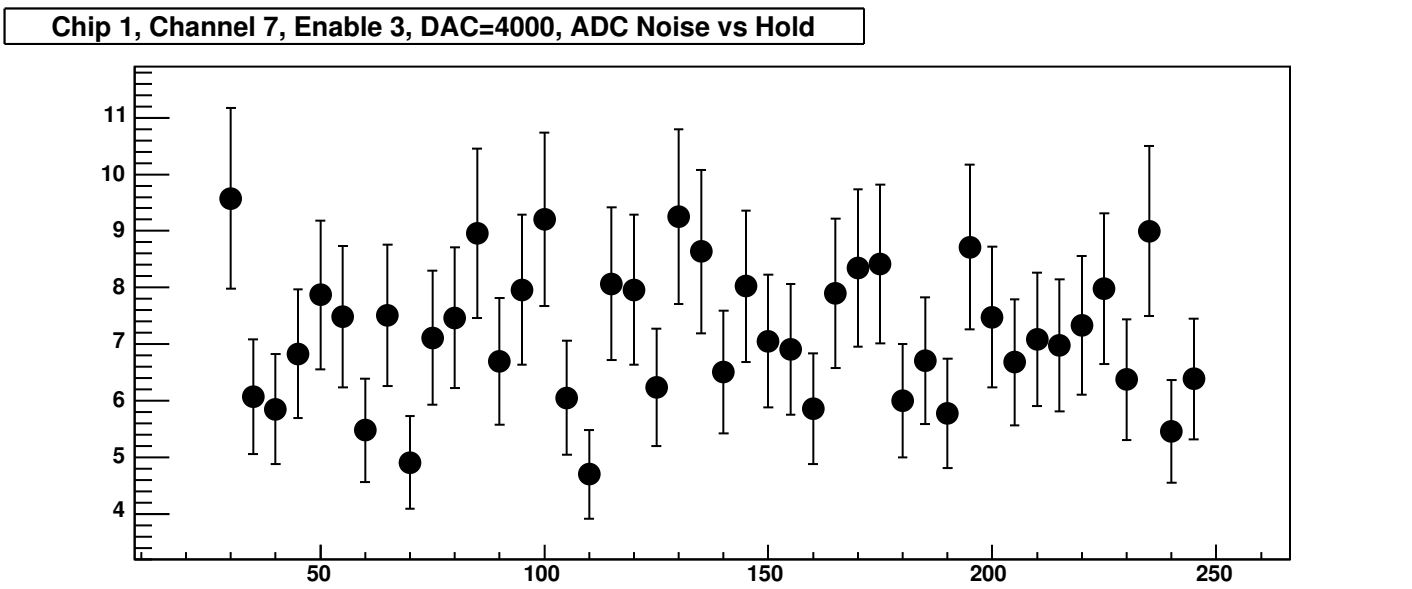
Chip 1, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold

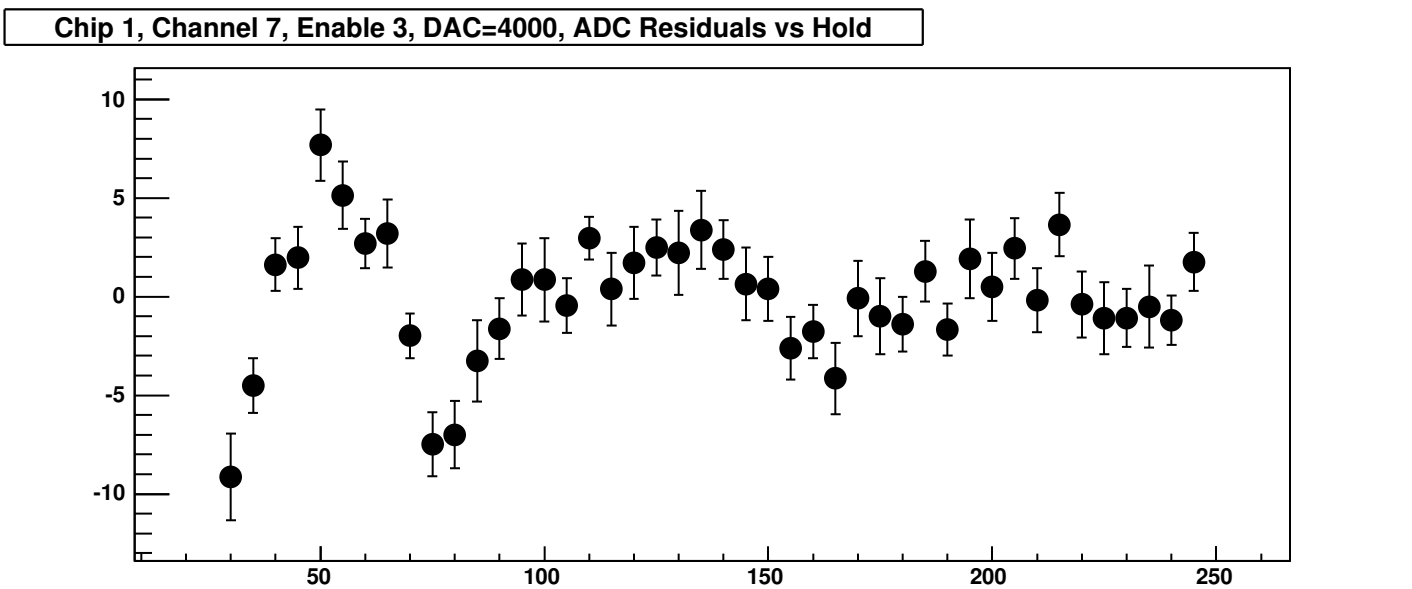


Chip 1, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold

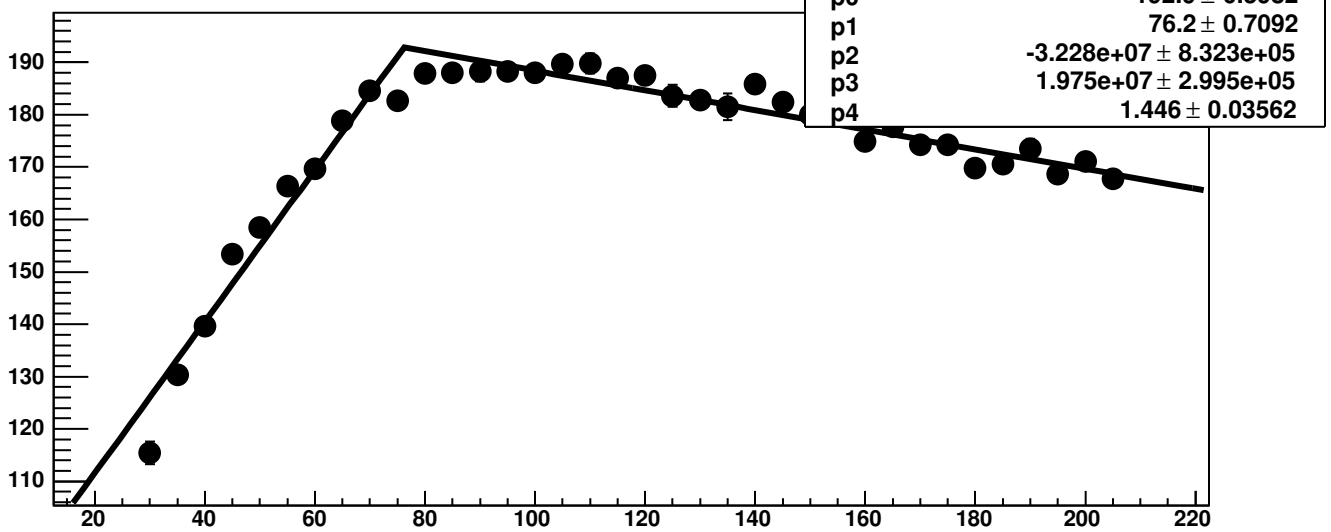



Chip 1, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 152.9 / 38
p0 197.8 ± 0.5235
p1 76.59 ± 0.5826
p2 -9.085e+07 ± 1.774e+06
p3 5.005e+07 ± 2.509e+05
p4 1.636 ± 0.03364
200
180
160
140
120
50
100
150
200
250

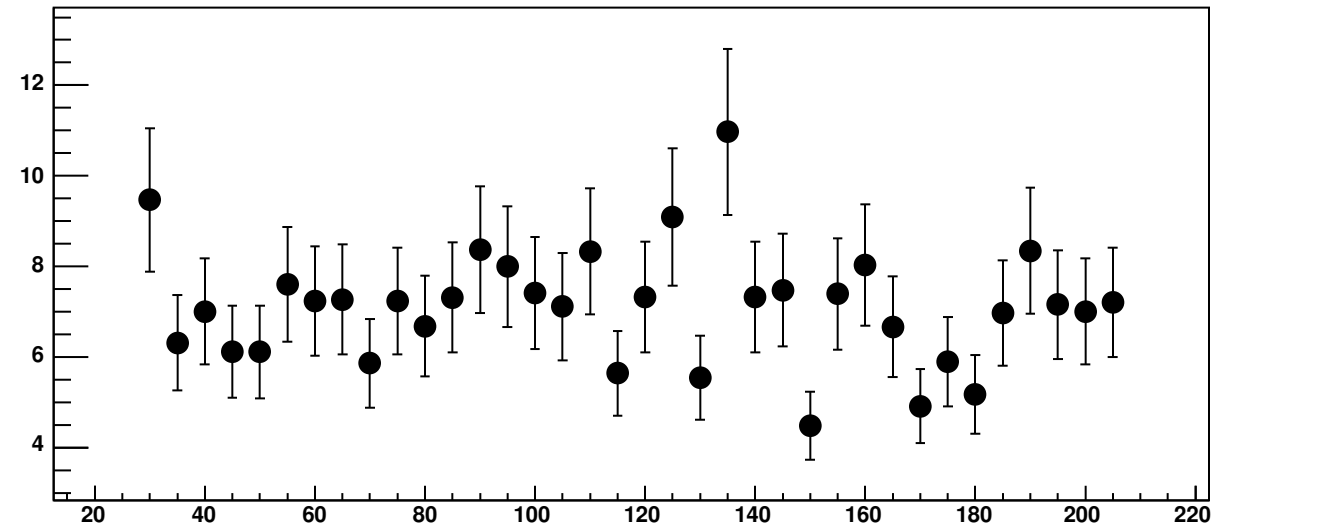

Chip 1, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 1, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

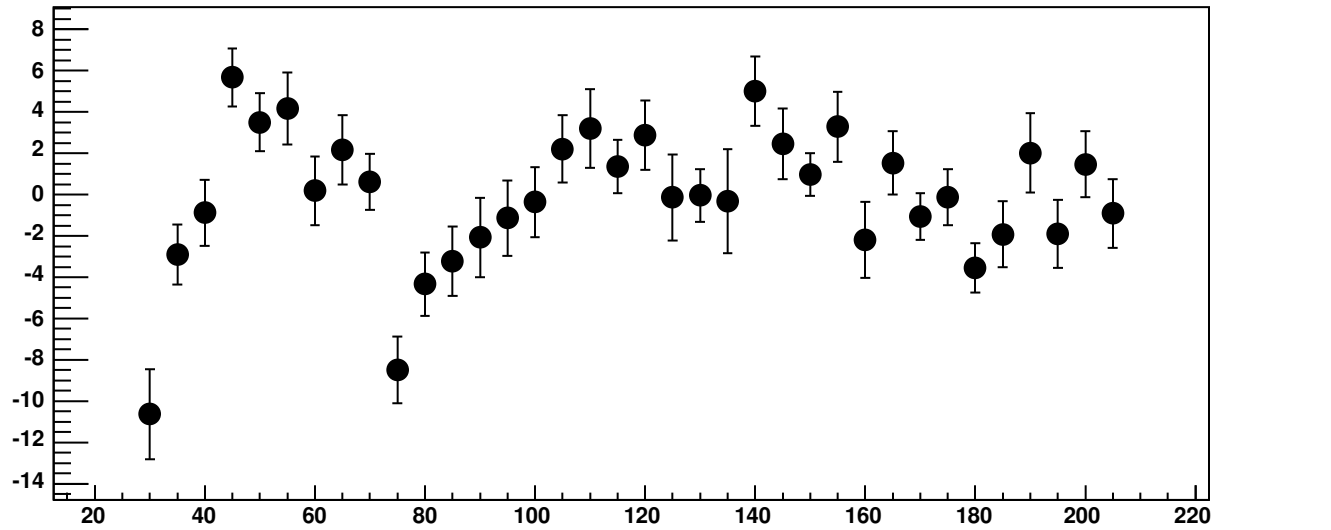
Chip 1, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

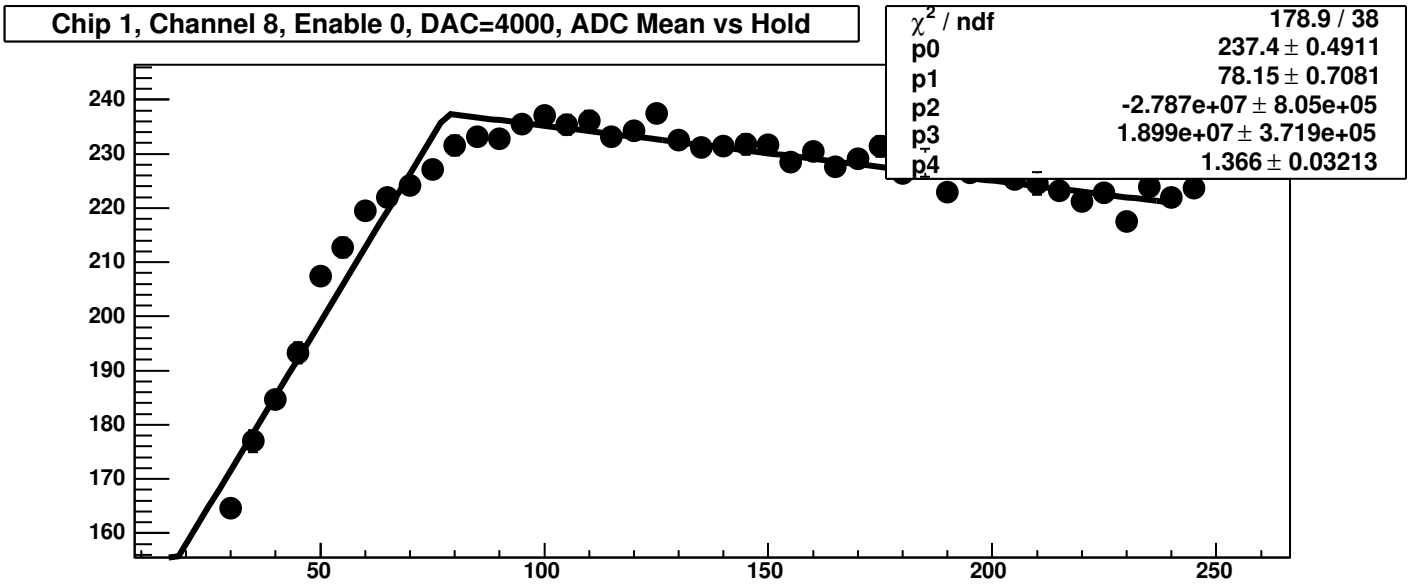


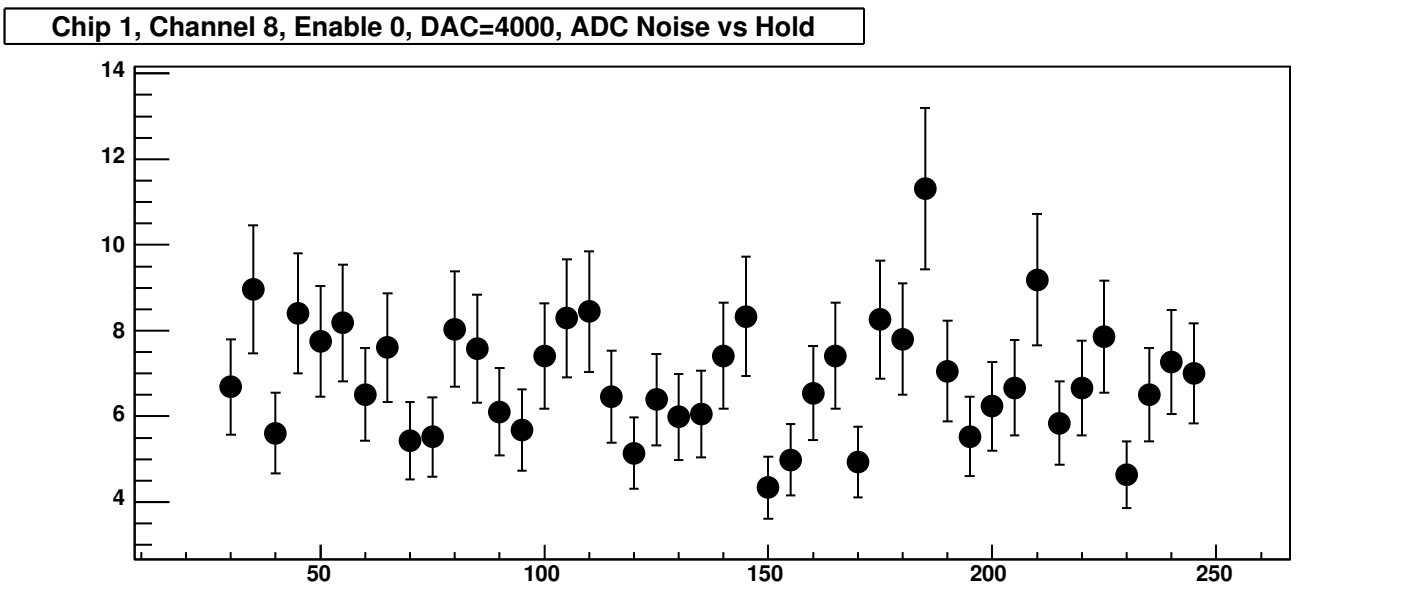
Chip 1, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold

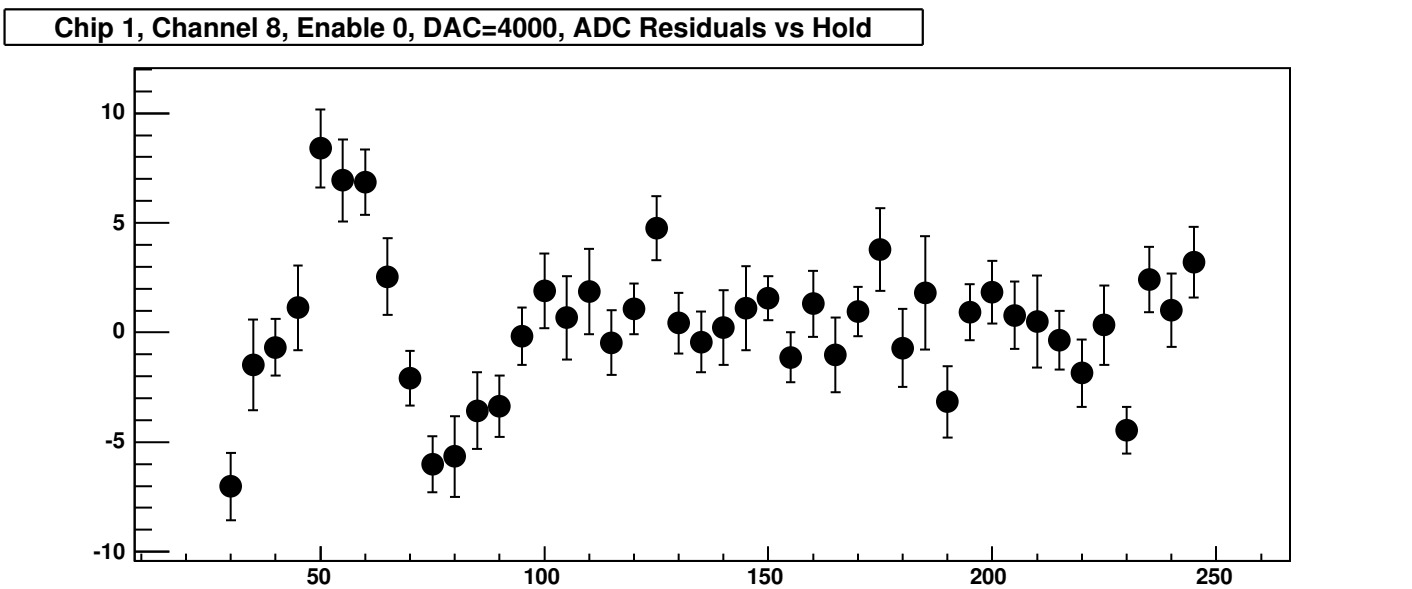


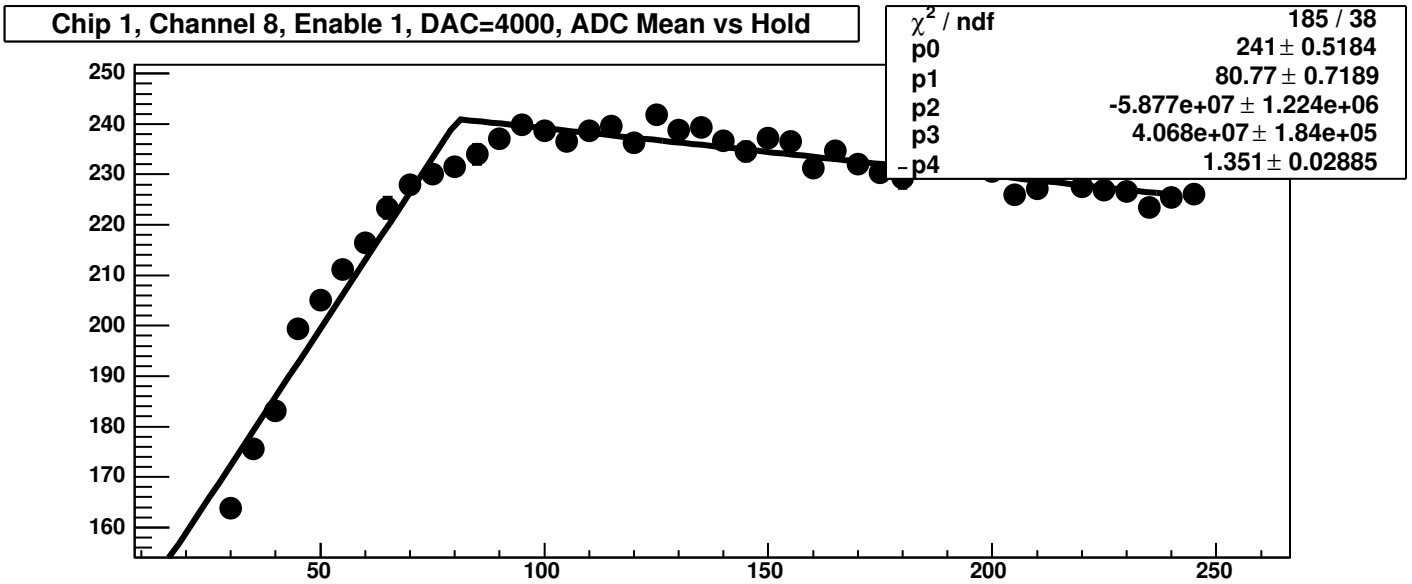
Chip 1, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

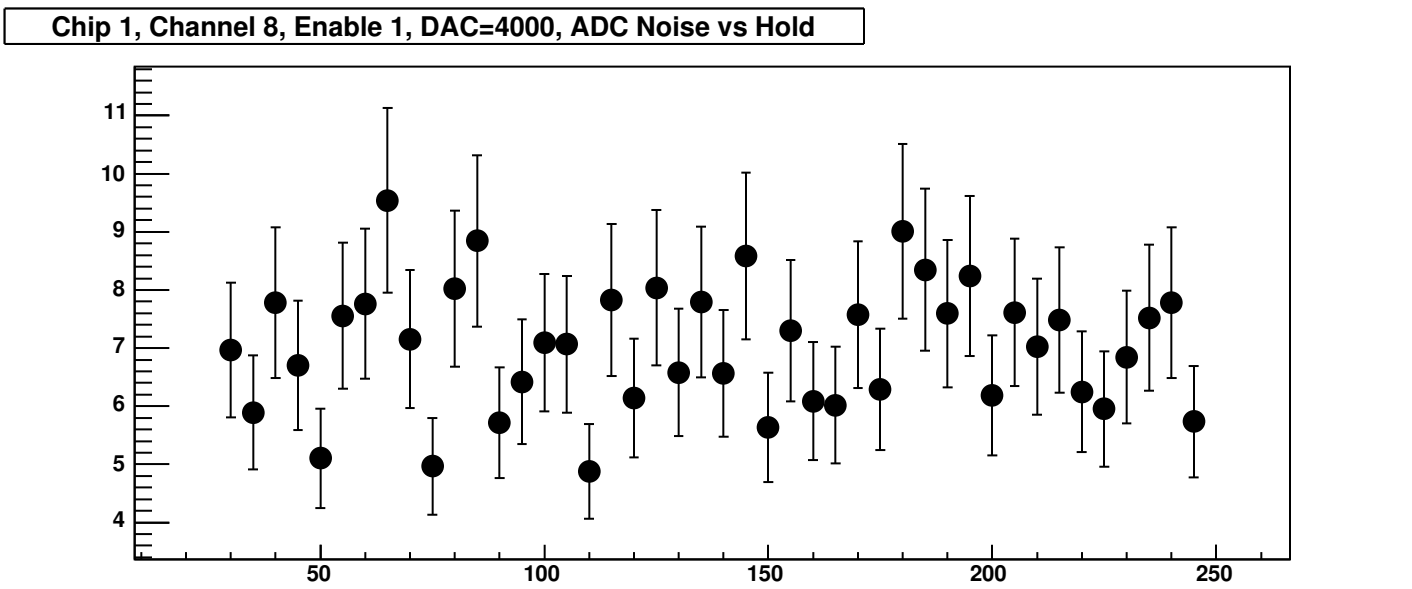


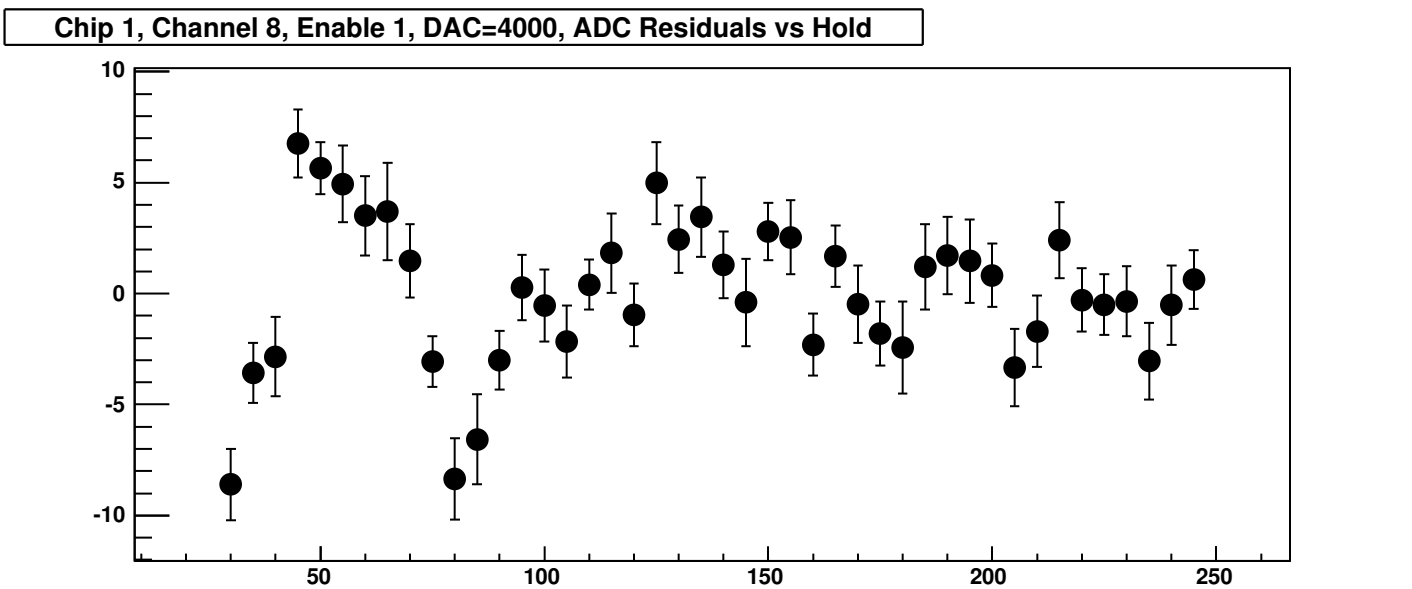

Chip 1, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 178.9 / 38
p0 237.4 ± 0.4911
p1 78.15 ± 0.7081
p2 -2.787e+07 ± 8.05e+05
p3 1.899e+07 ± 3.719e+05
p4 1.366 ± 0.03213
240
230
220
210
200
190
180
170
160
50
100
150
200
250

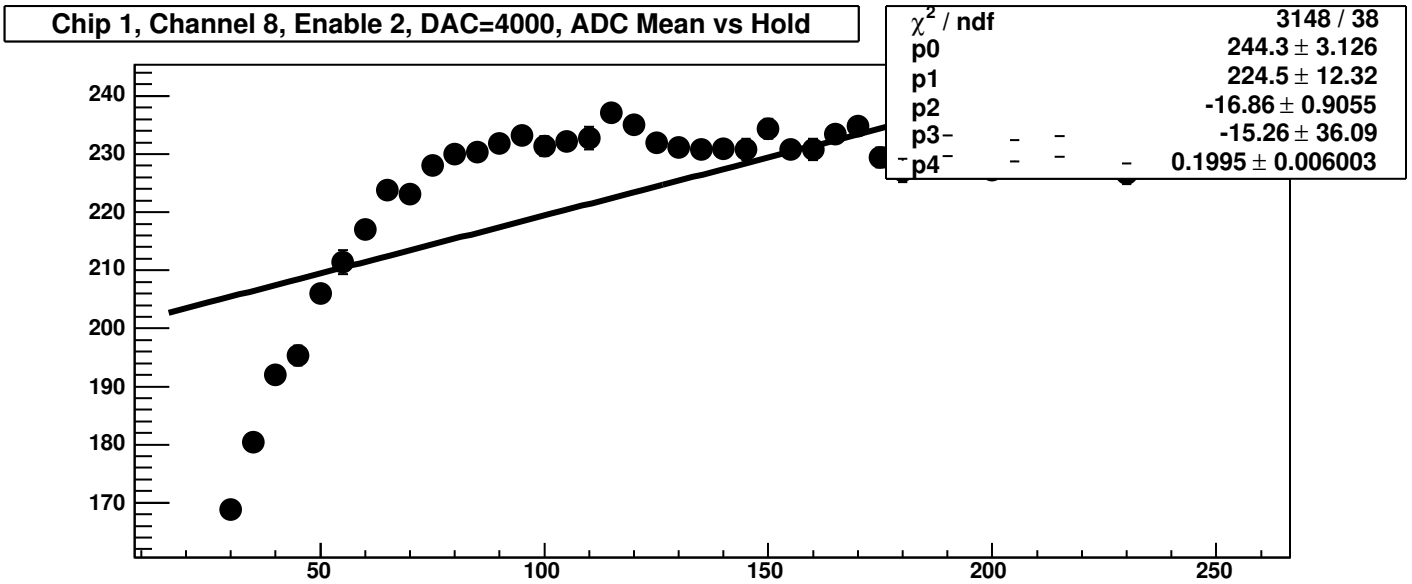

Chip 1, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250

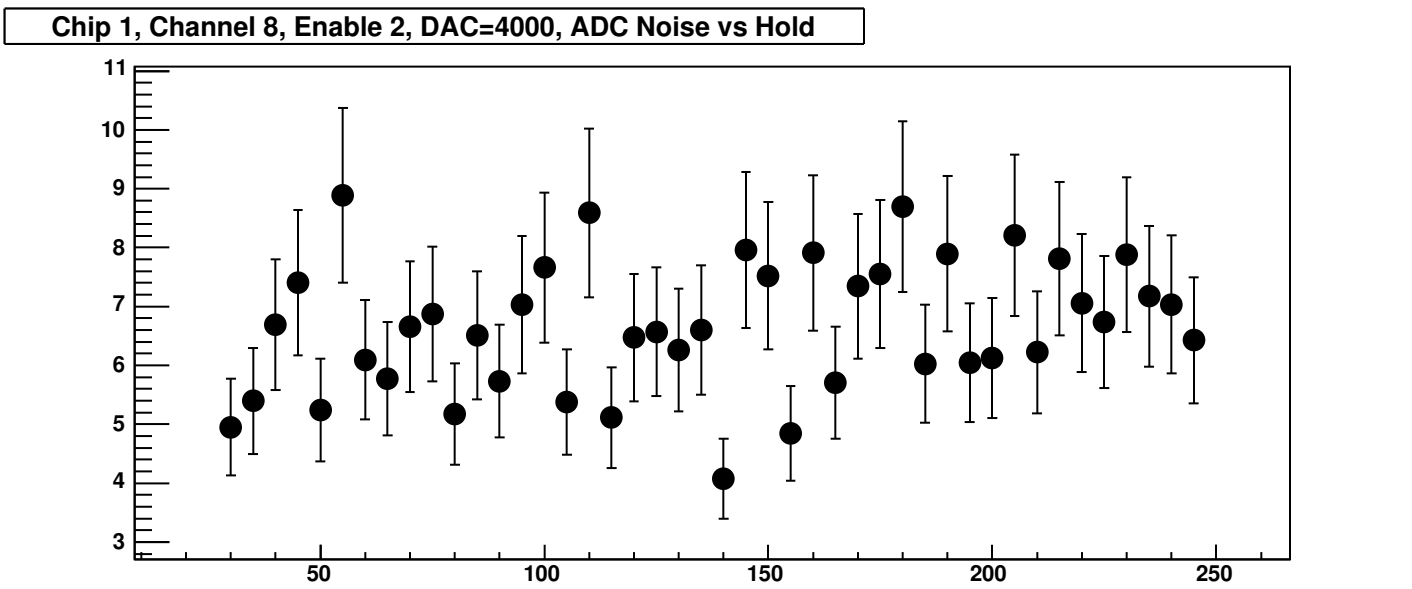

Chip 1, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

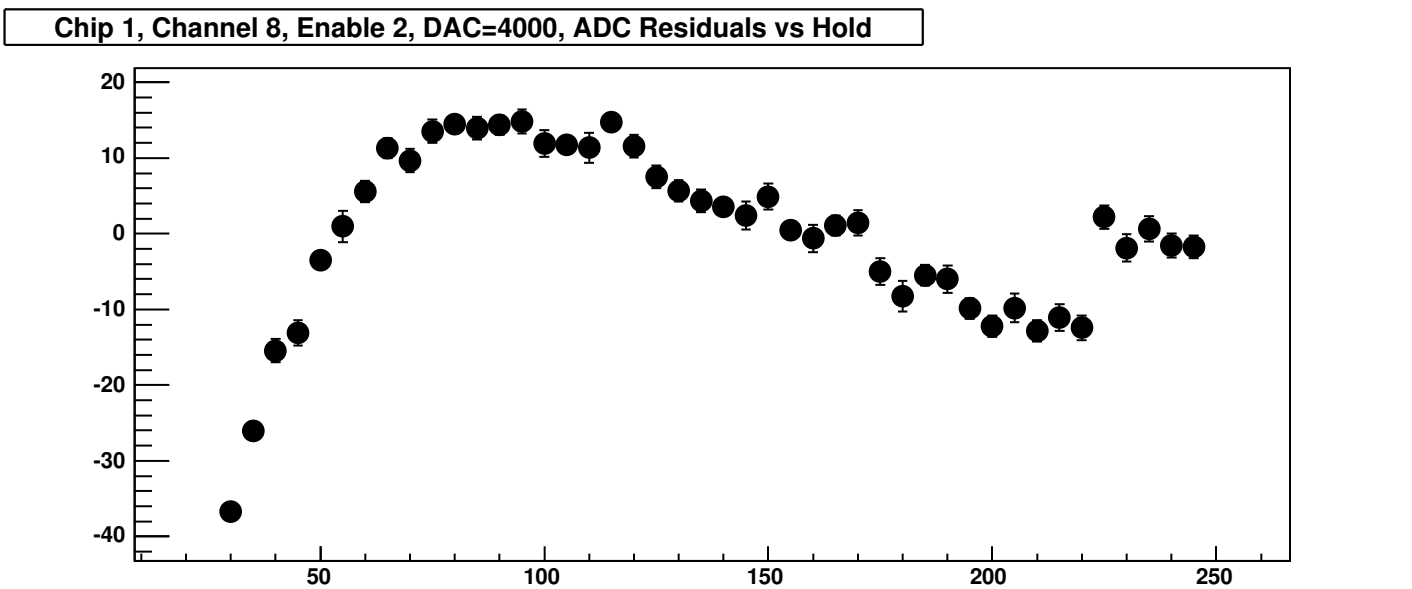

Chip 1, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 185 / 38
p0 241 ± 0.5184
p1 80.77 ± 0.7189
p2 -5.877e+07 ± 1.224e+06
p3 4.068e+07 ± 1.84e+05
p4 1.351 ± 0.02885
250
240
230
220
210
200
190
180
170
160
50
100
150
200
250

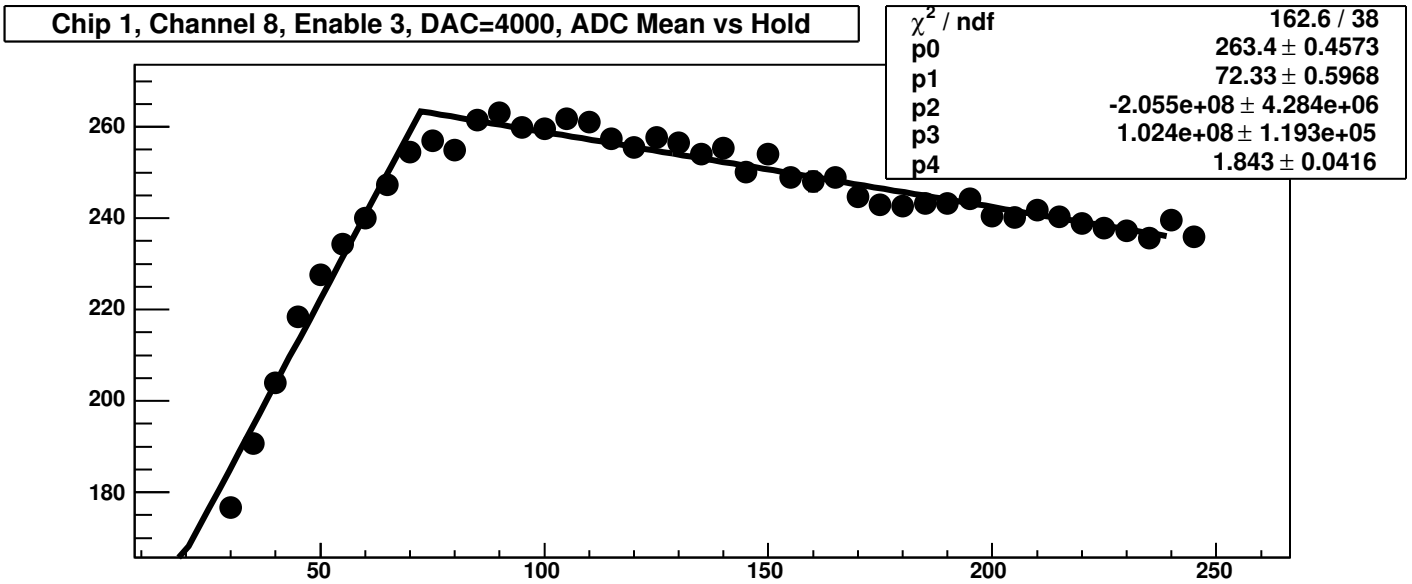

Chip 1, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250

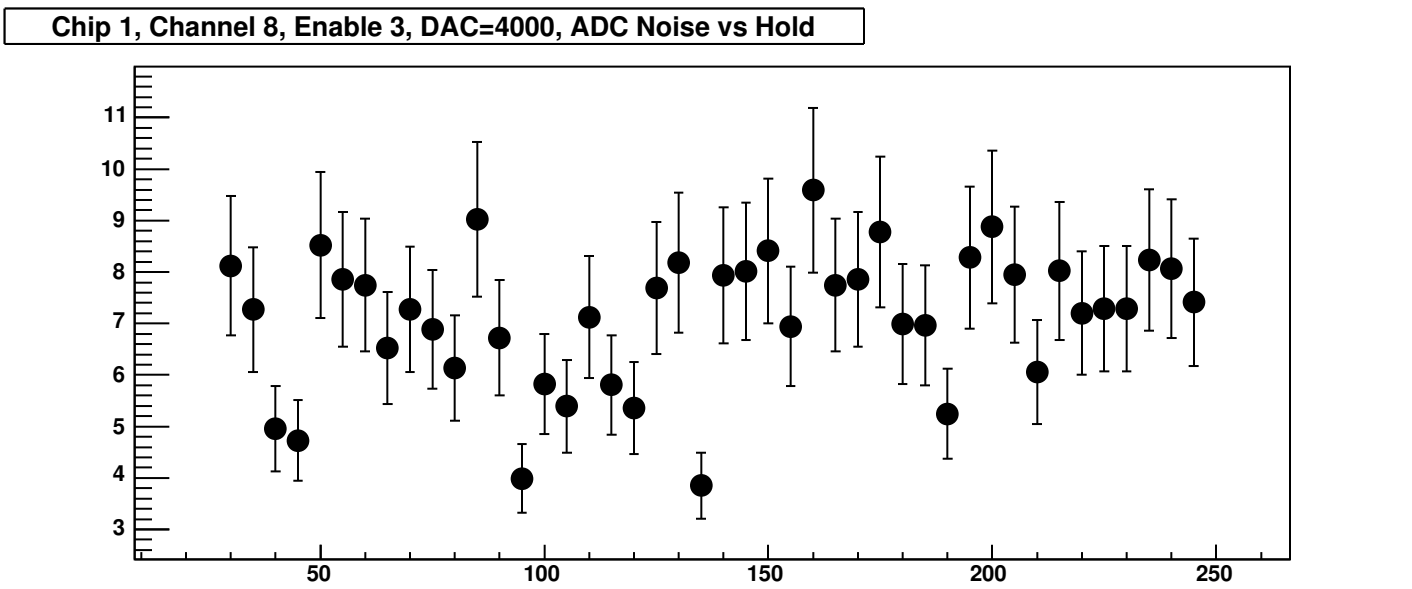

Chip 1, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

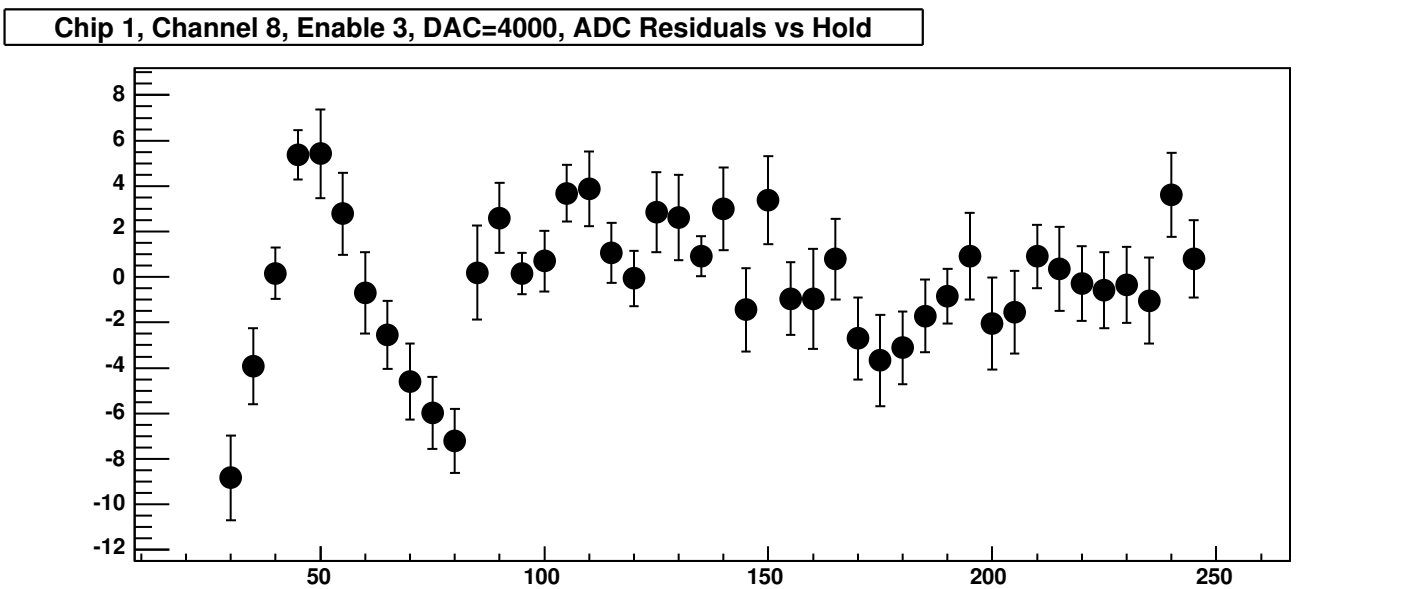

Chip 1, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 3148 / 38
p0 244.3 ± 3.126
p1 224.5 ± 12.32
p2 -16.86 ± 0.9055
p3 -15.26 ± 36.09
p4 0.1995 ± 0.006003
240
230
220
210
200
190
180
170
50
100
150
200
250

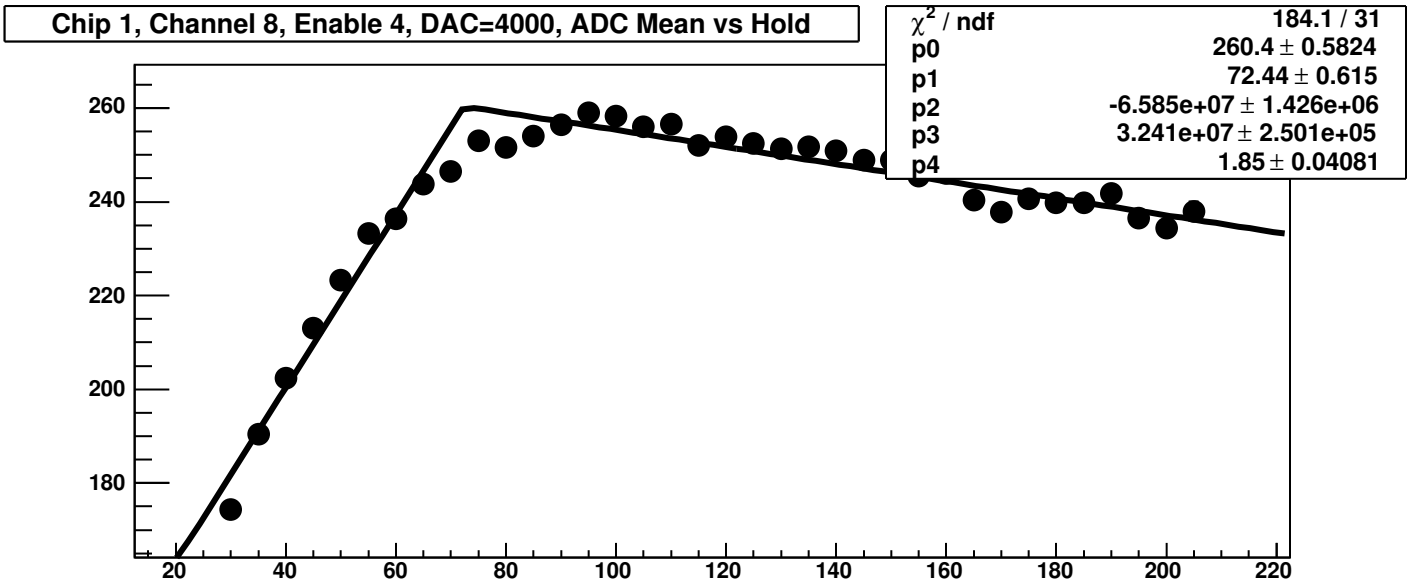

Chip 1, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
50
100
150
200
250

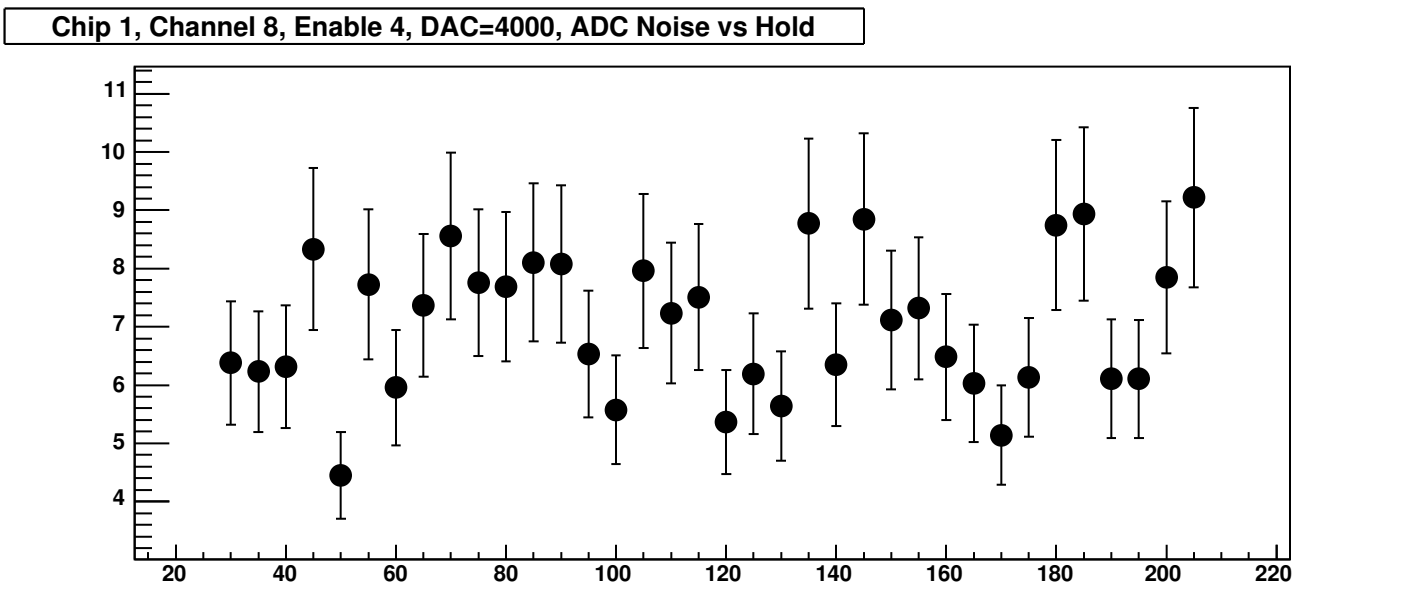

Chip 1, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
50
100
150
200
250

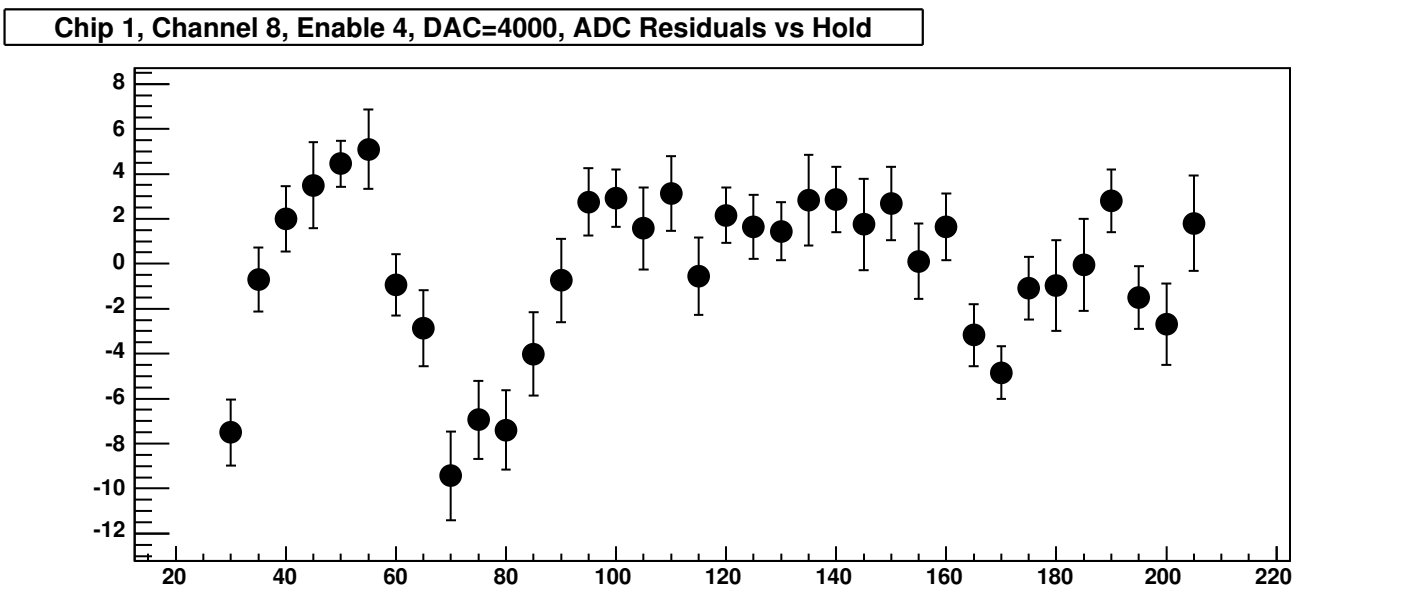

Chip 1, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 162.6 / 38
p0 263.4 ± 0.4573
p1 72.33 ± 0.5968
p2 -2.055e+08 ± 4.284e+06
p3 1.024e+08 ± 1.193e+05
p4 1.843 ± 0.0416
260
240
220
200
180
50
100
150
200
250


Chip 1, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
50
100
150
200
250

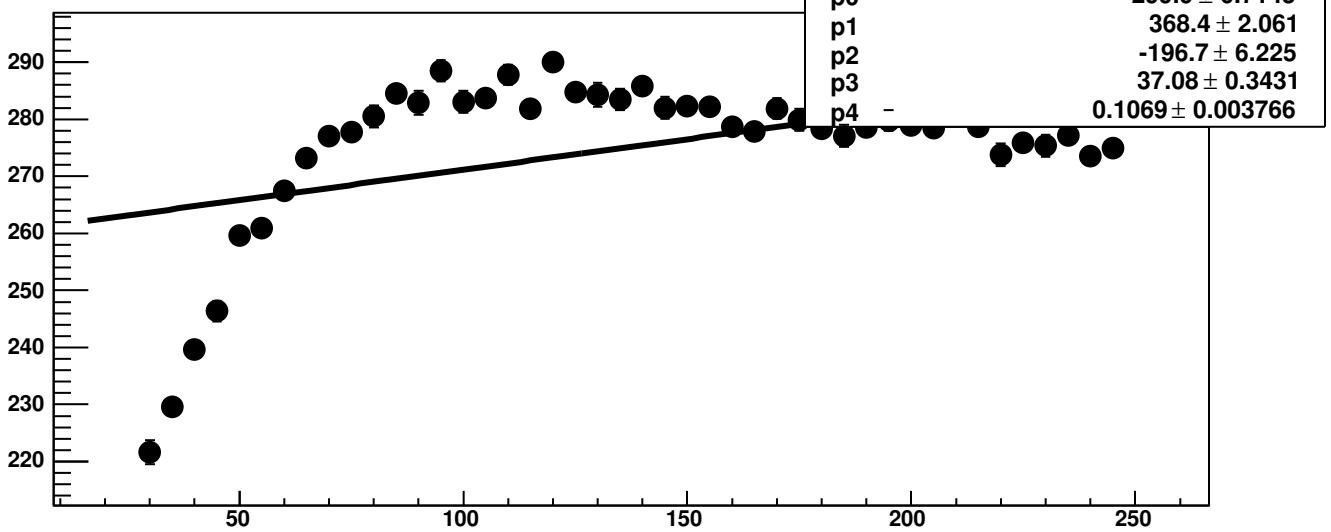

Chip 1, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
-12
50
100
150
200
250


Chip 1, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 184.1 / 31
p0 260.4 ± 0.5824
p1 72.44 ± 0.615
p2 -6.585e+07 ± 1.426e+06
p3 3.241e+07 ± 2.501e+05
p4 1.85 ± 0.04081
260
240
220
200
180
20
40
60
80
100
120
140
160
180
200
220

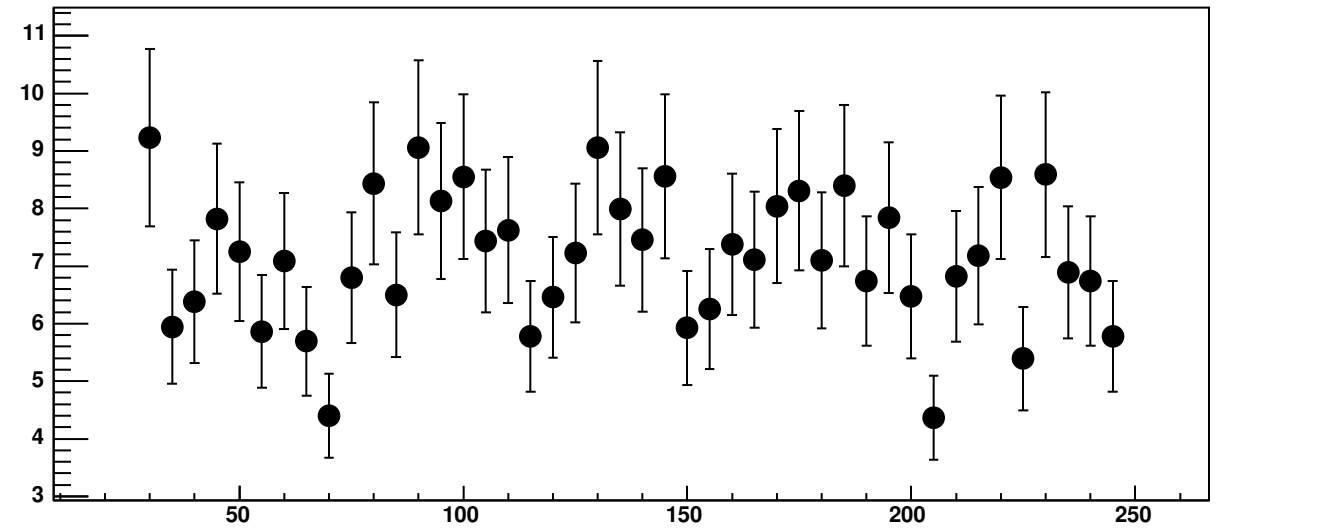

Chip 1, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
20
40
60
80
100
120
140
160
180
200
220


Chip 1, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
-12
20
40
60
80
100
120
140
160
180
200
220

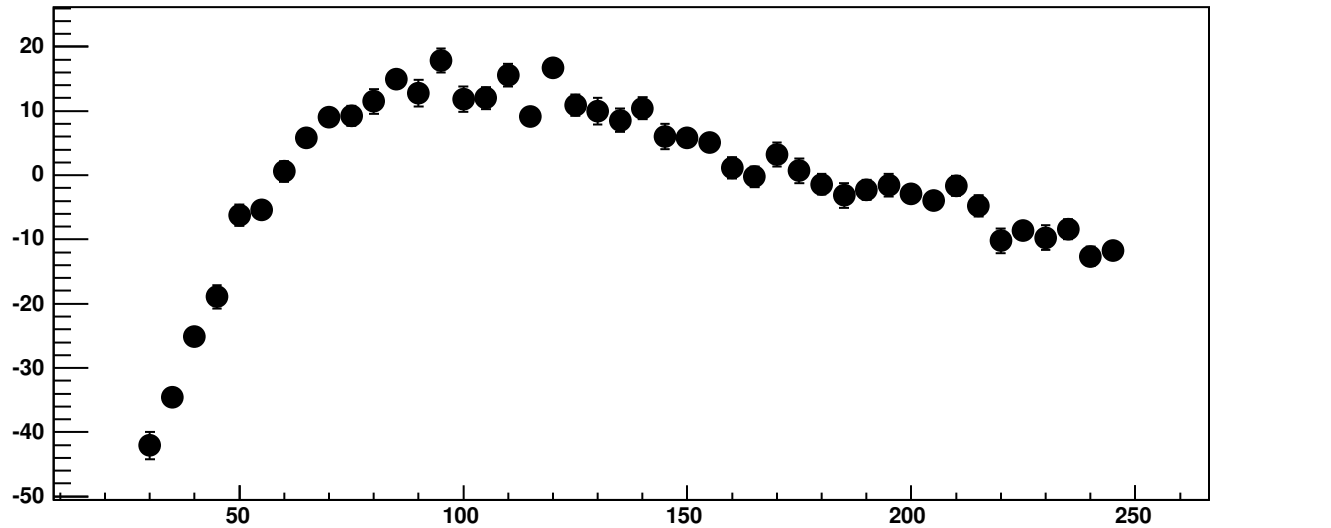
Chip 1, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold

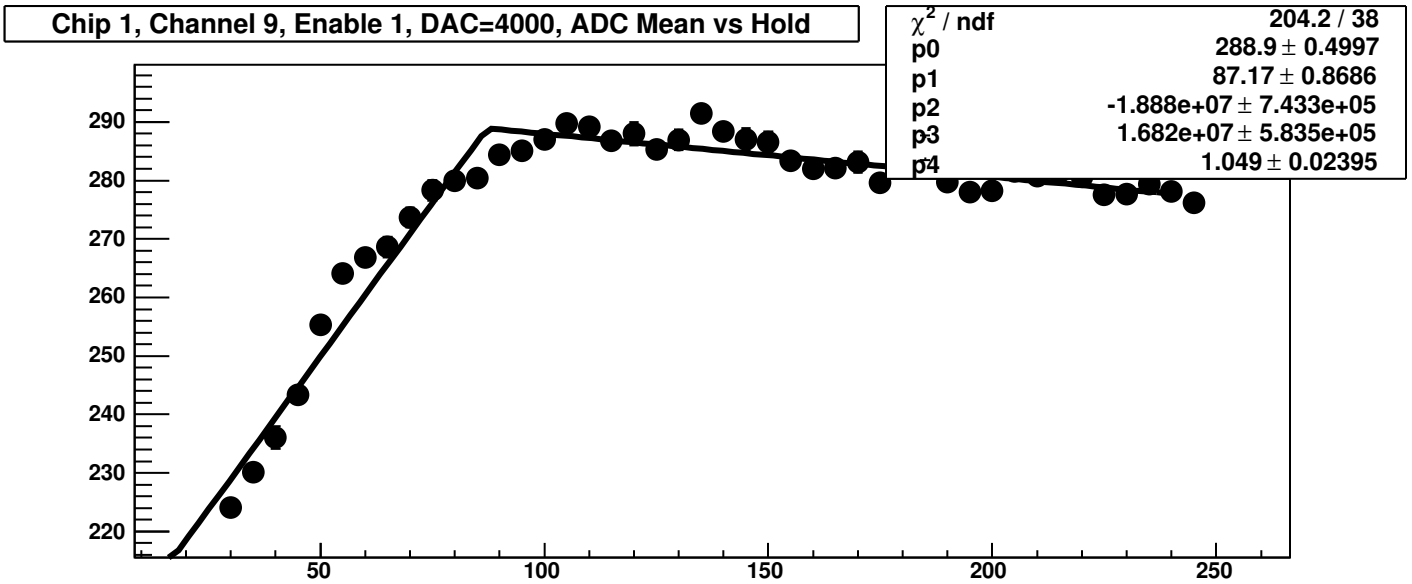


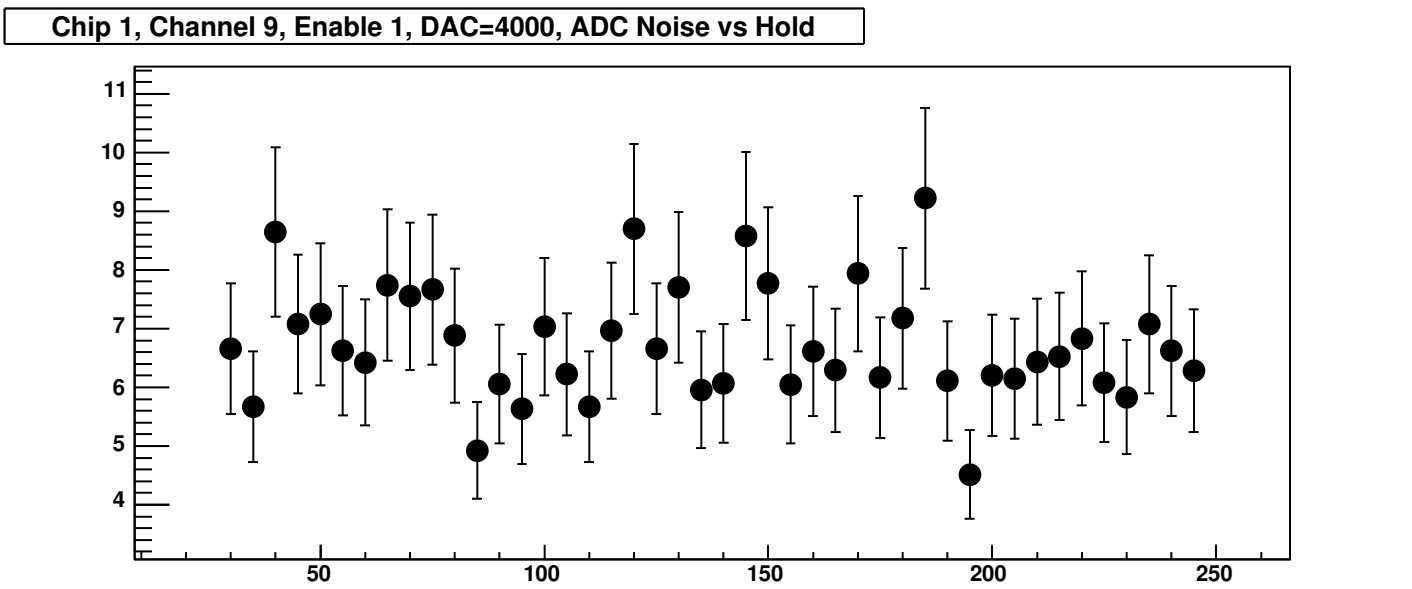
Chip 1, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold

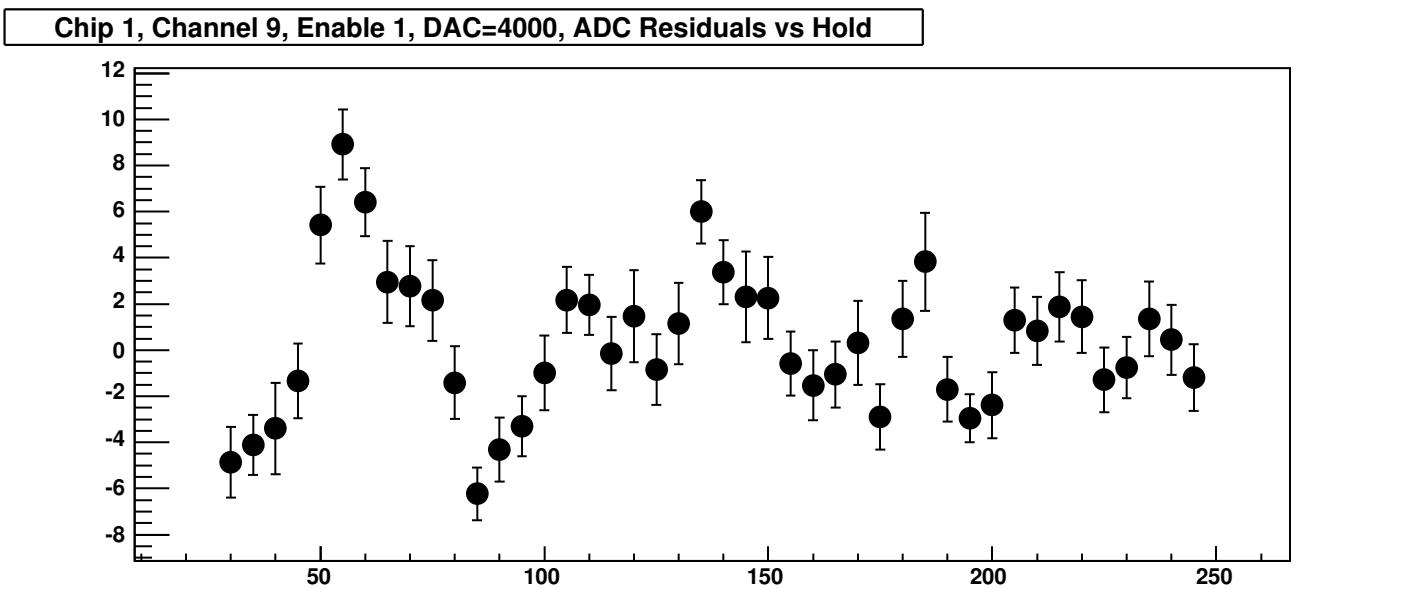


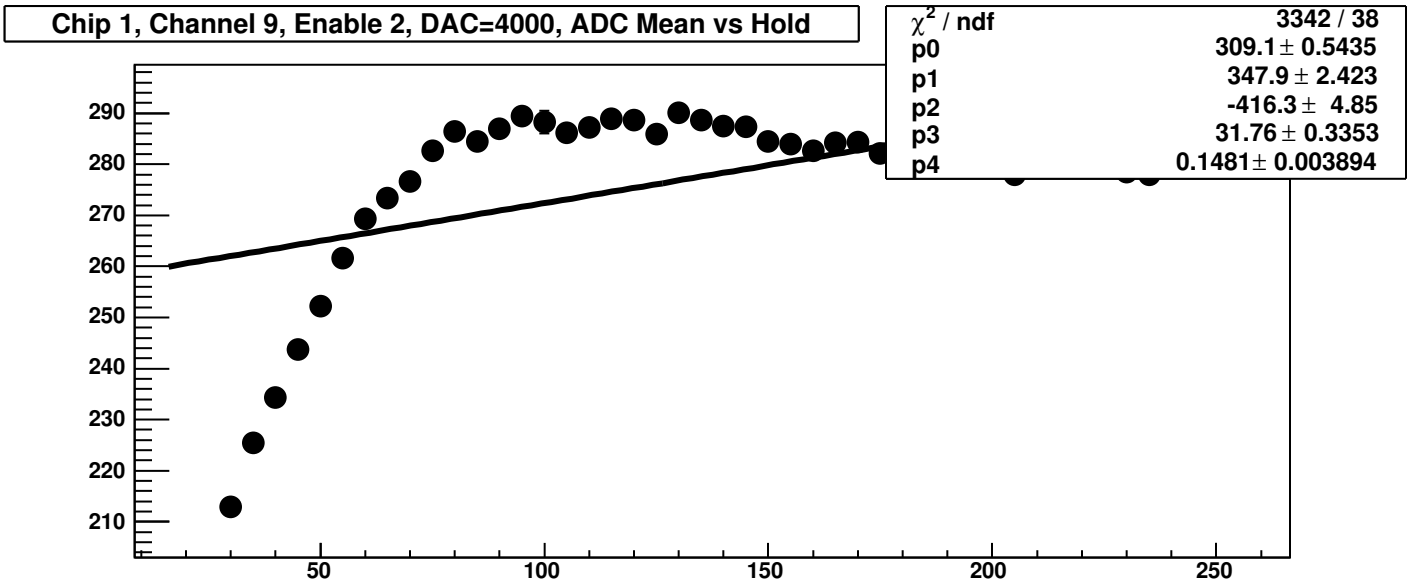
Chip 1, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold

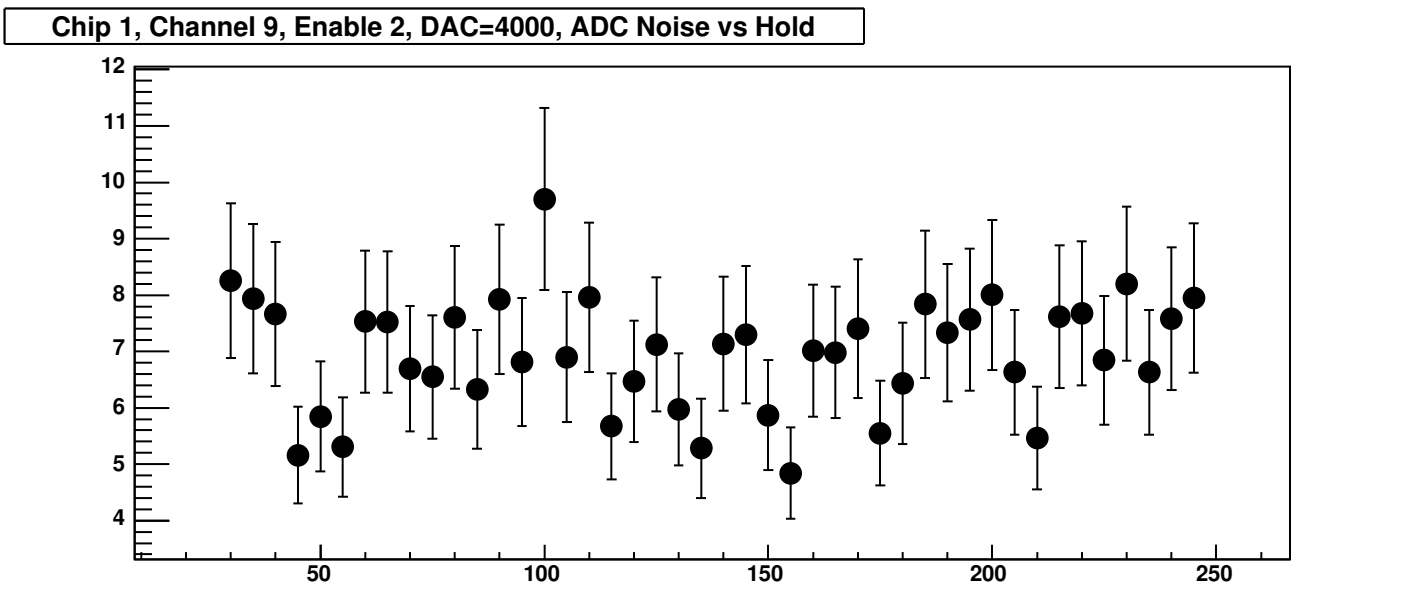


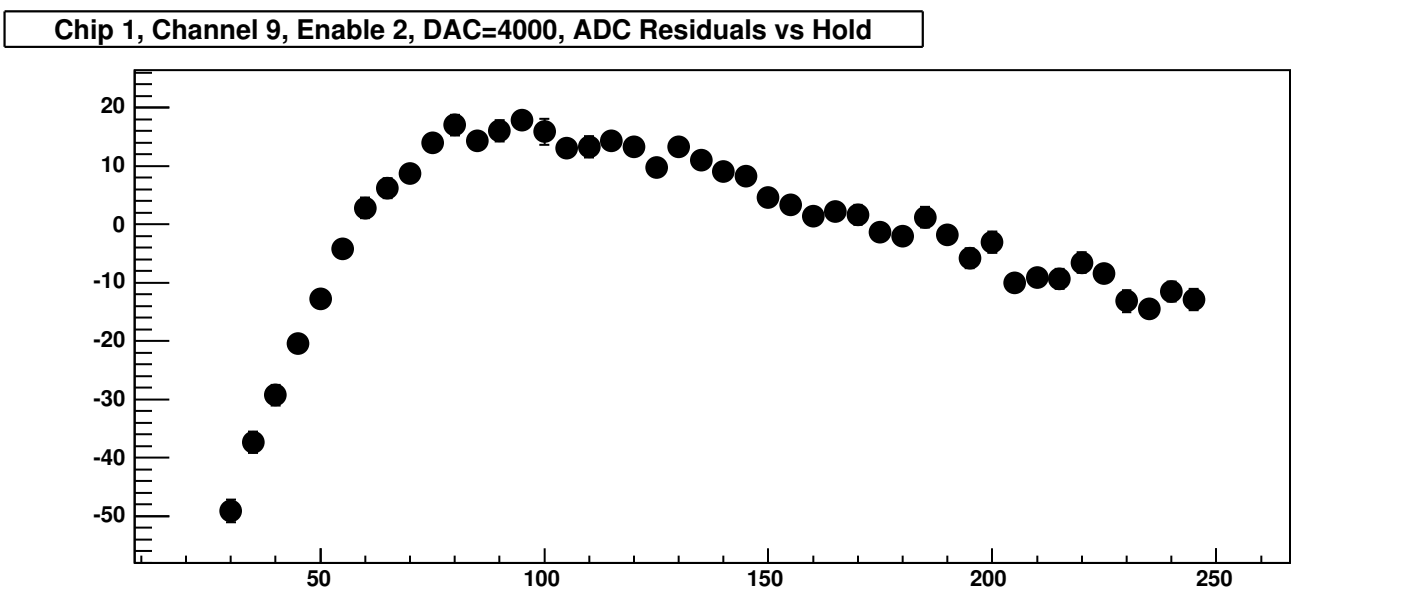

Chip 1, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 204.2 / 38
p0 288.9 ± 0.4997
p1 87.17 ± 0.8686
p2 -1.888e+07 ± 7.433e+05
p3 1.682e+07 ± 5.835e+05
p4 1.049 ± 0.02395
290
280
270
260
250
240
230
220
50
100
150
200
250


Chip 1, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250

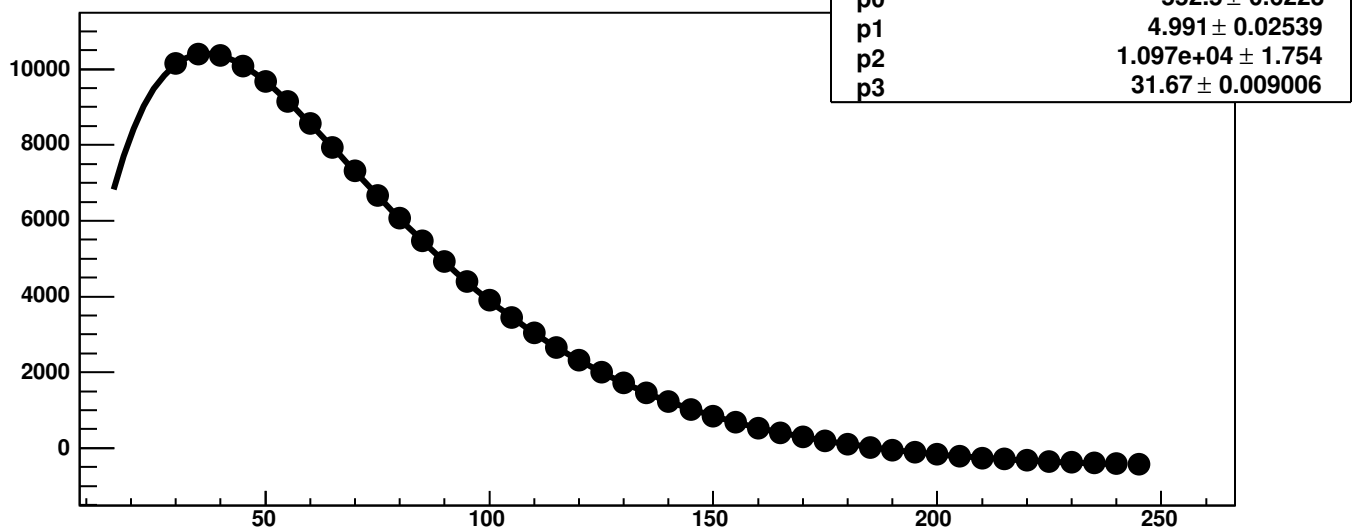

Chip 1, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold
12
10
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250


Chip 1, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 3342 / 38
p0 309.1 ± 0.5435
p1 347.9 ± 2.423
p2 -416.3 ± 4.85
p3 31.76 ± 0.3353
p4 0.1481 ± 0.003894
290
280
270
260
250
240
230
220
210
50
100
150
200
250

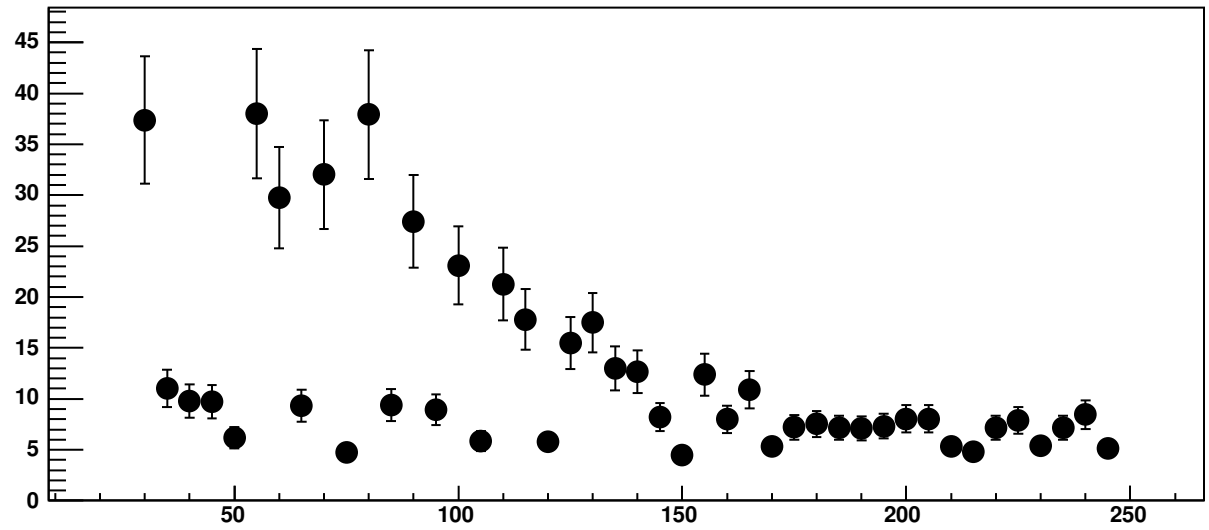

Chip 1, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 1, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
-50
50
100
150
200
250

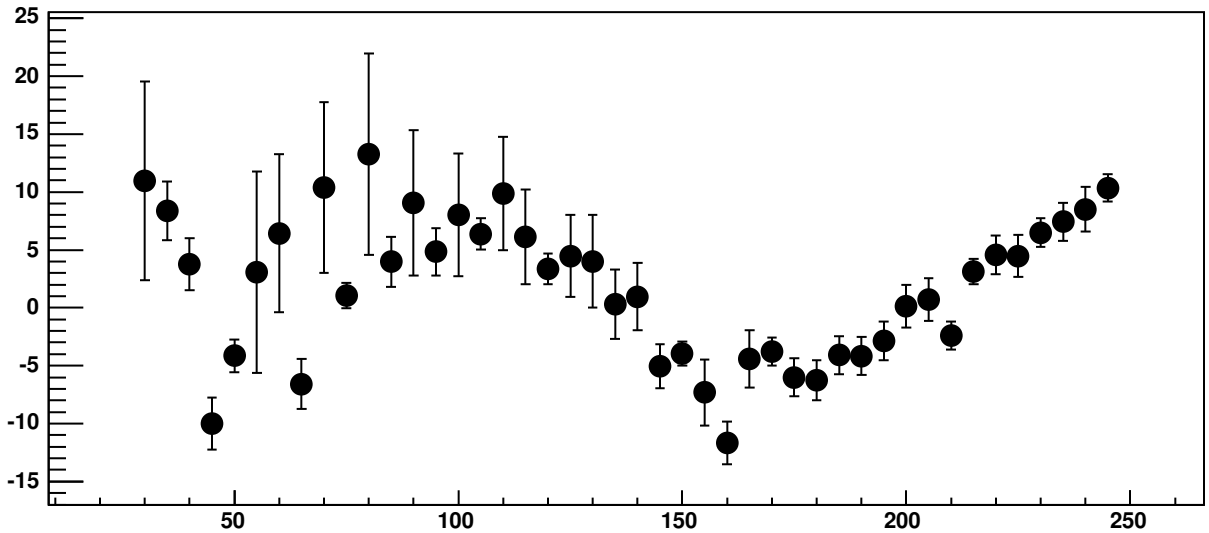
Chip 1, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold

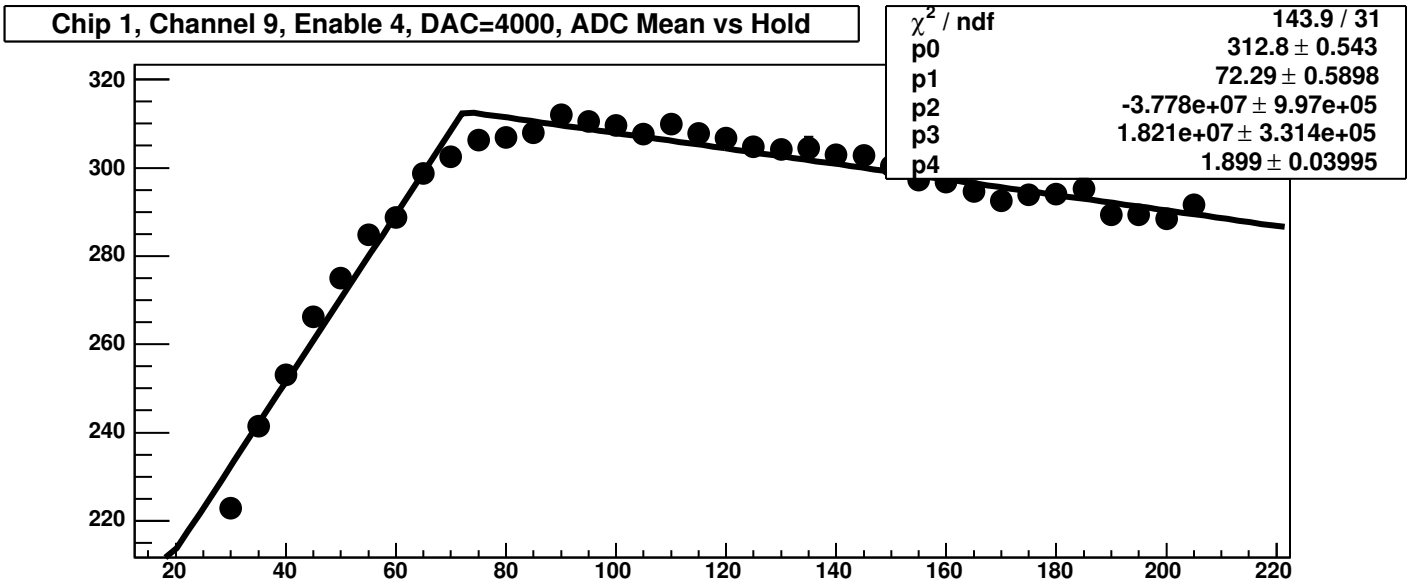


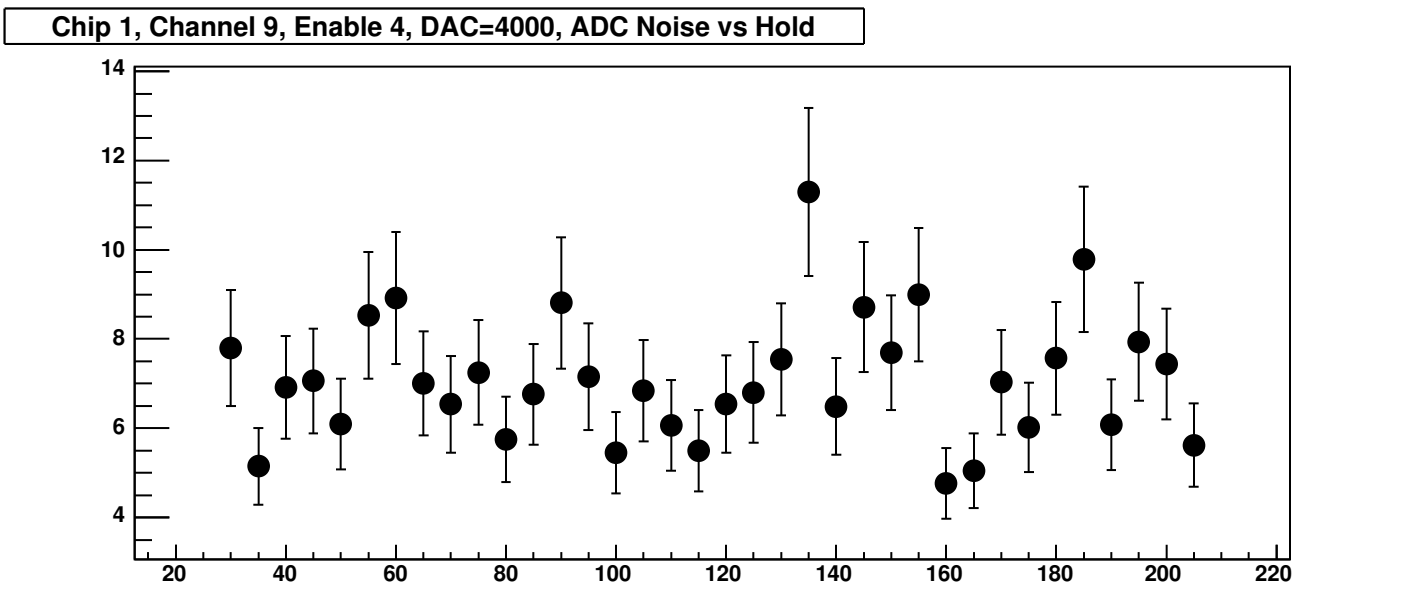
Chip 1, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold

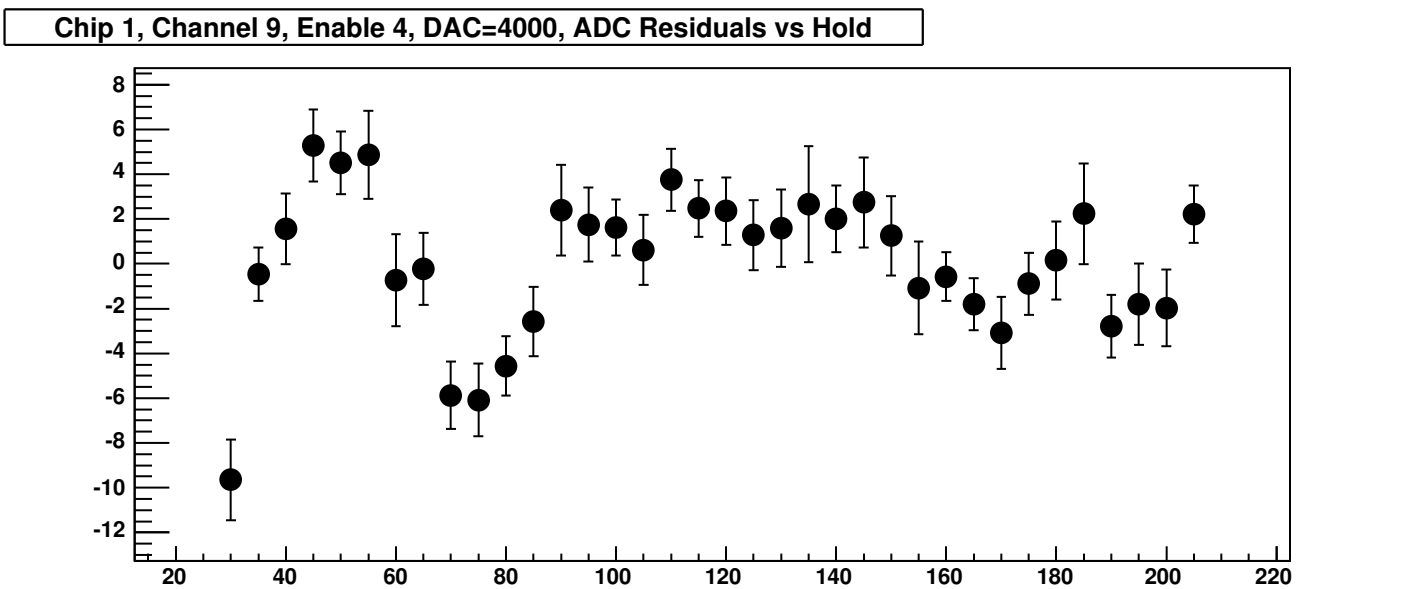


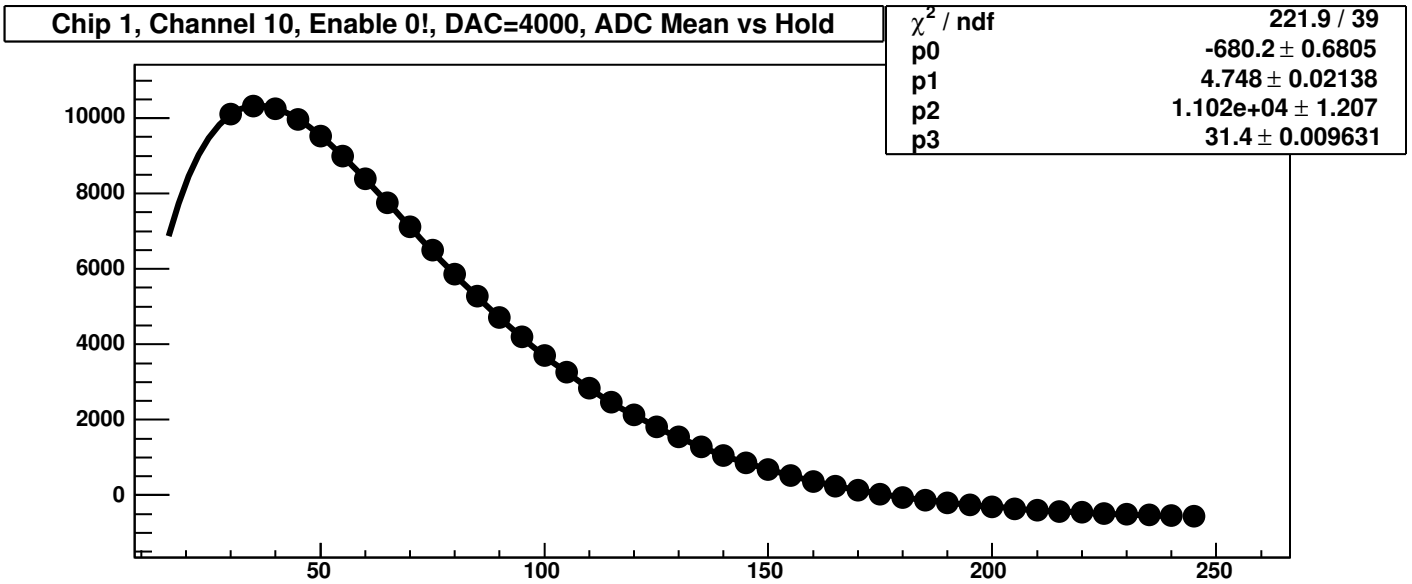
Chip 1, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold

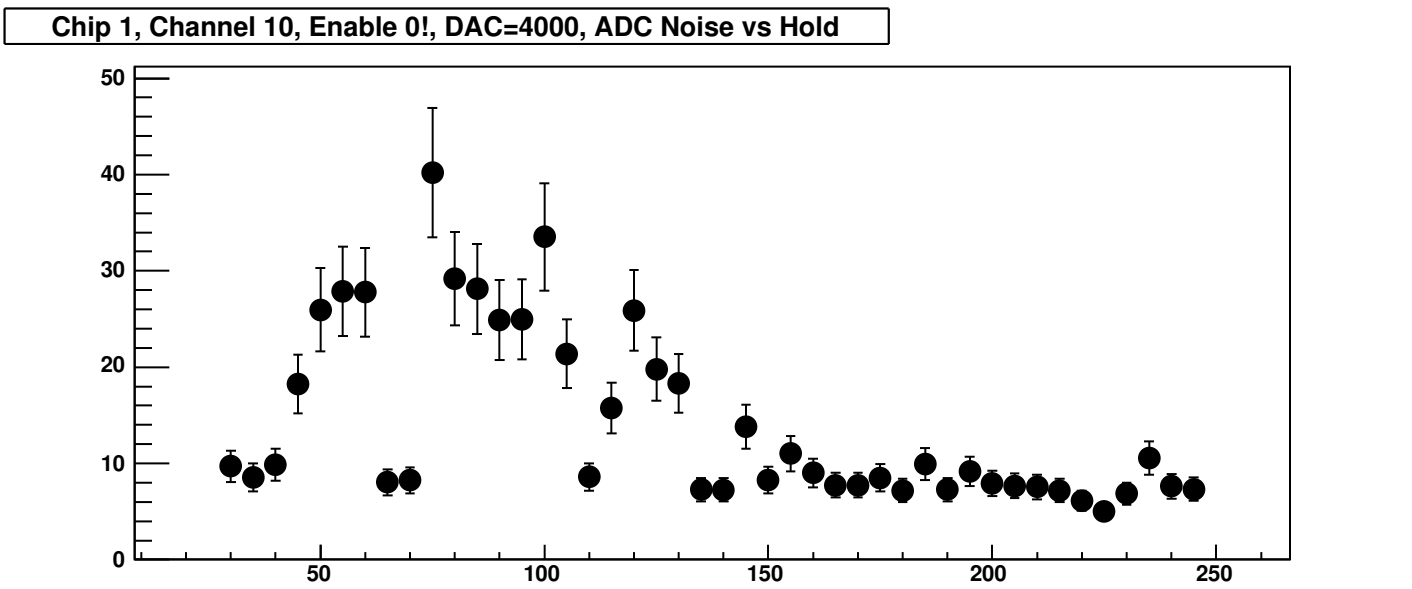


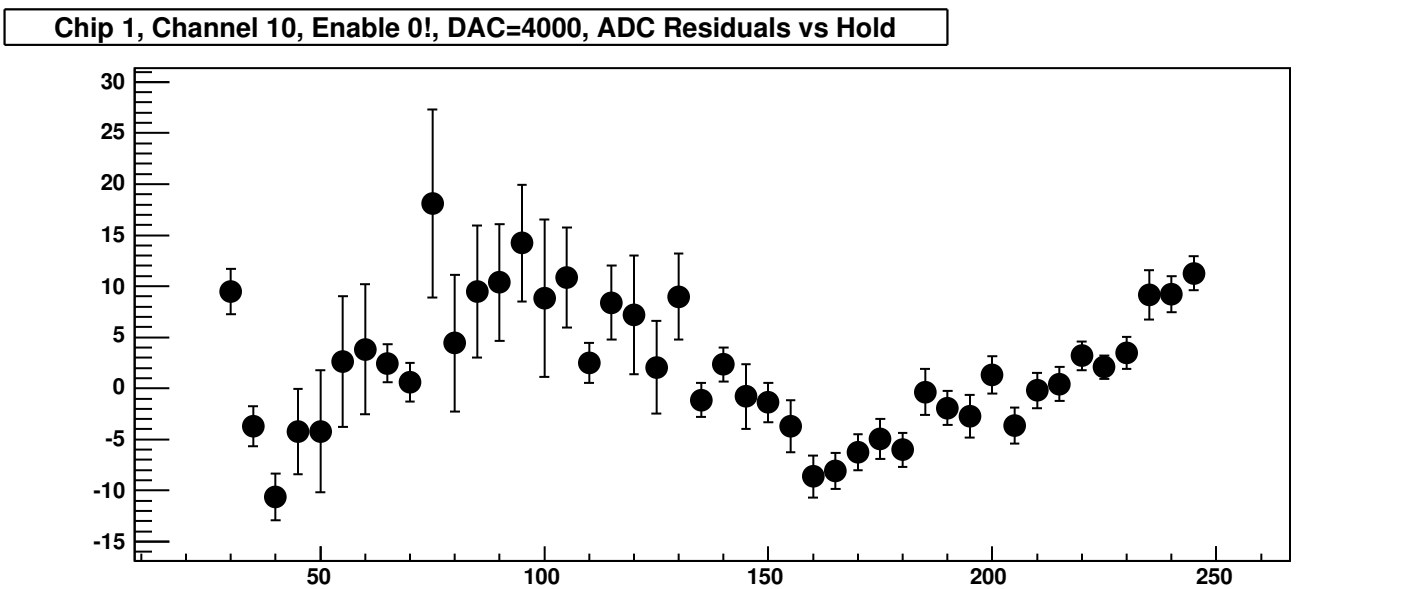

Chip 1, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 143.9 / 31
p0 312.8 ± 0.543
p1 72.29 ± 0.5898
p2 -3.778e+07 ± 9.97e+05
p3 1.821e+07 ± 3.314e+05
p4 1.899 ± 0.03995
320
300
280
260
240
220
20
40
60
80
100
120
140
160
180
200
220

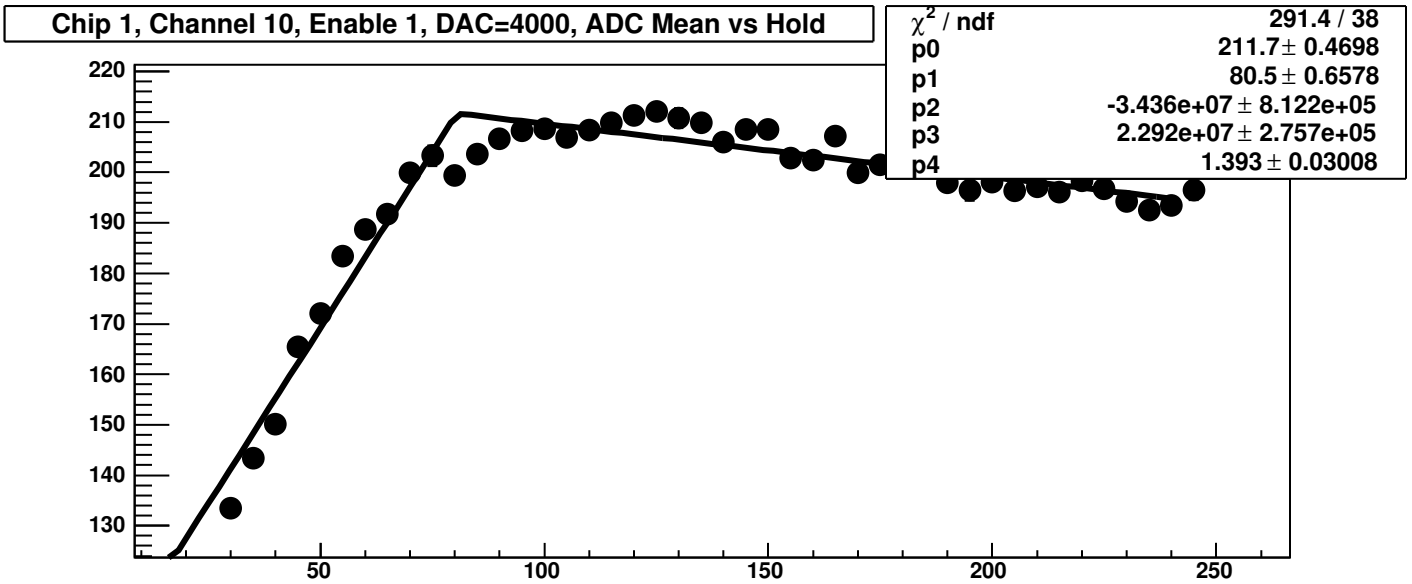

Chip 1, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
20
40
60
80
100
120
140
160
180
200
220

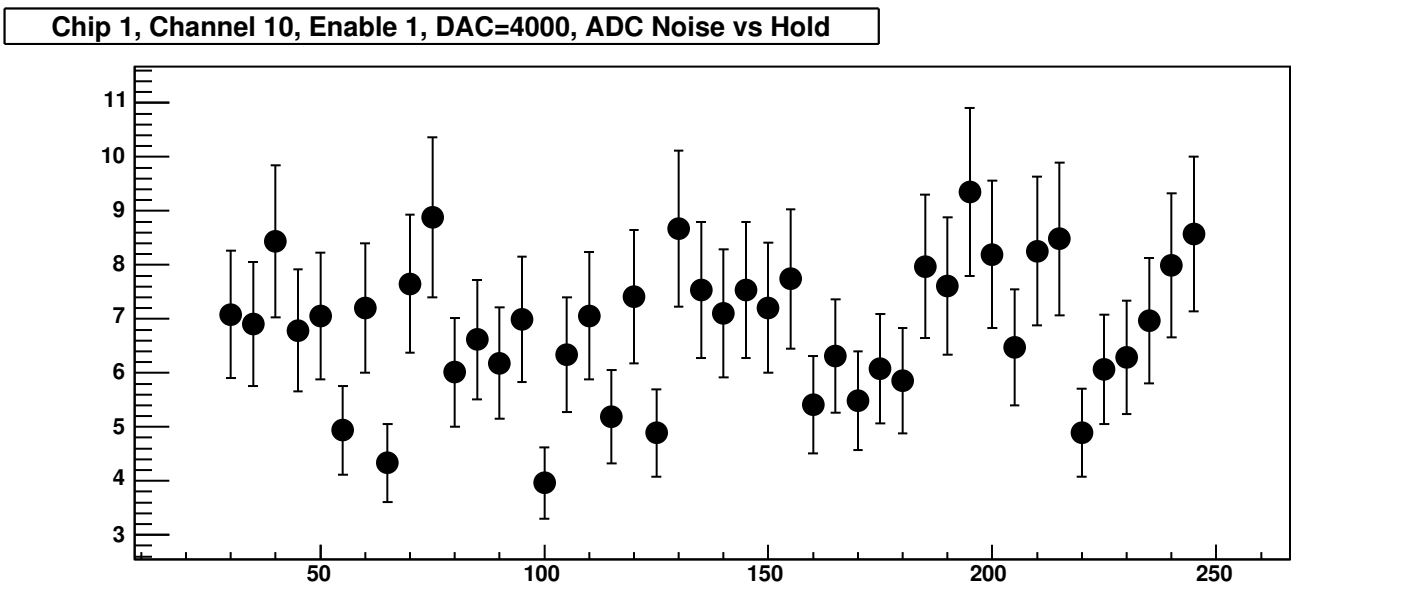

Chip 1, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
-12
20
40
60
80
100
120
140
160
180
200
220

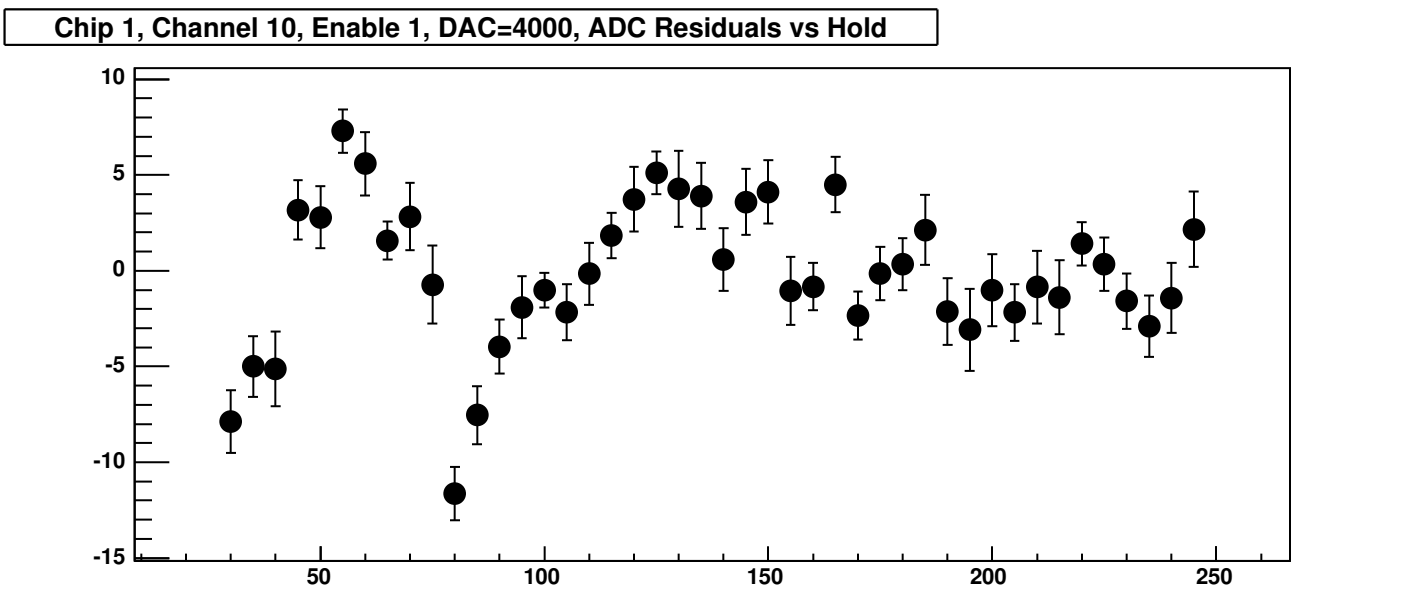

Chip 1, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 221.9 / 39
p0 -680.2 ± 0.6805
p1 4.748 ± 0.02138
p2 1.102e+04 ± 1.207
p3 31.4 ± 0.009631
10000
8000
6000
4000
2000
0
50
100
150
200
250

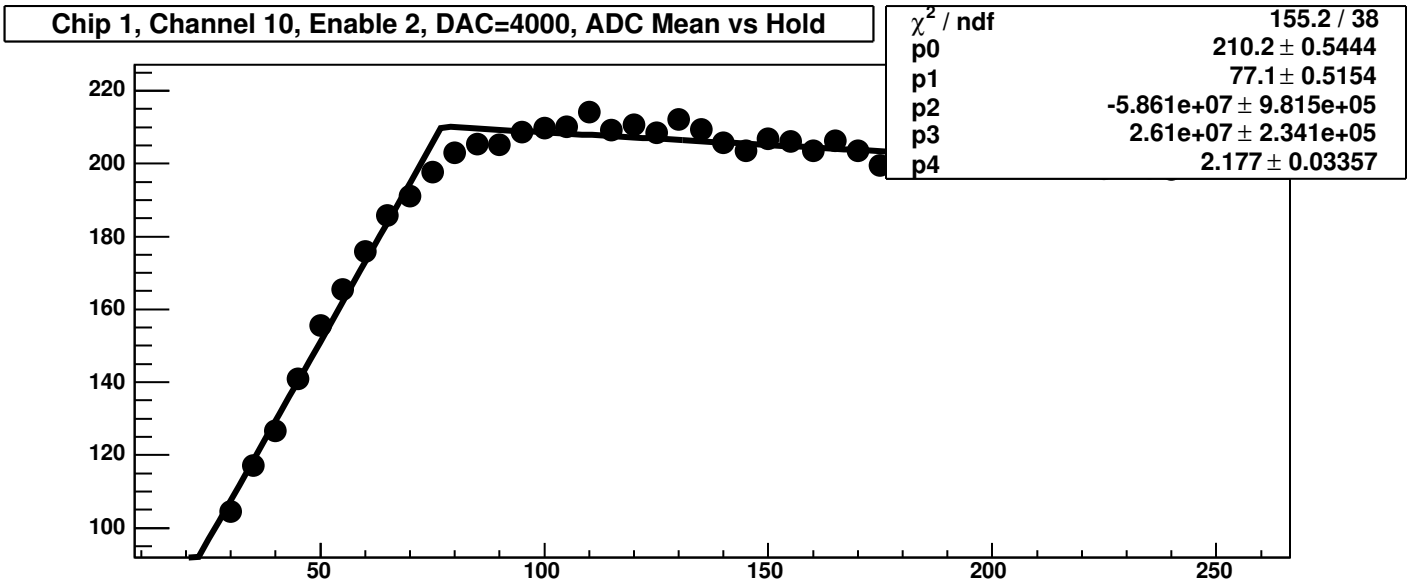

Chip 1, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
0
50
100
150
200
250

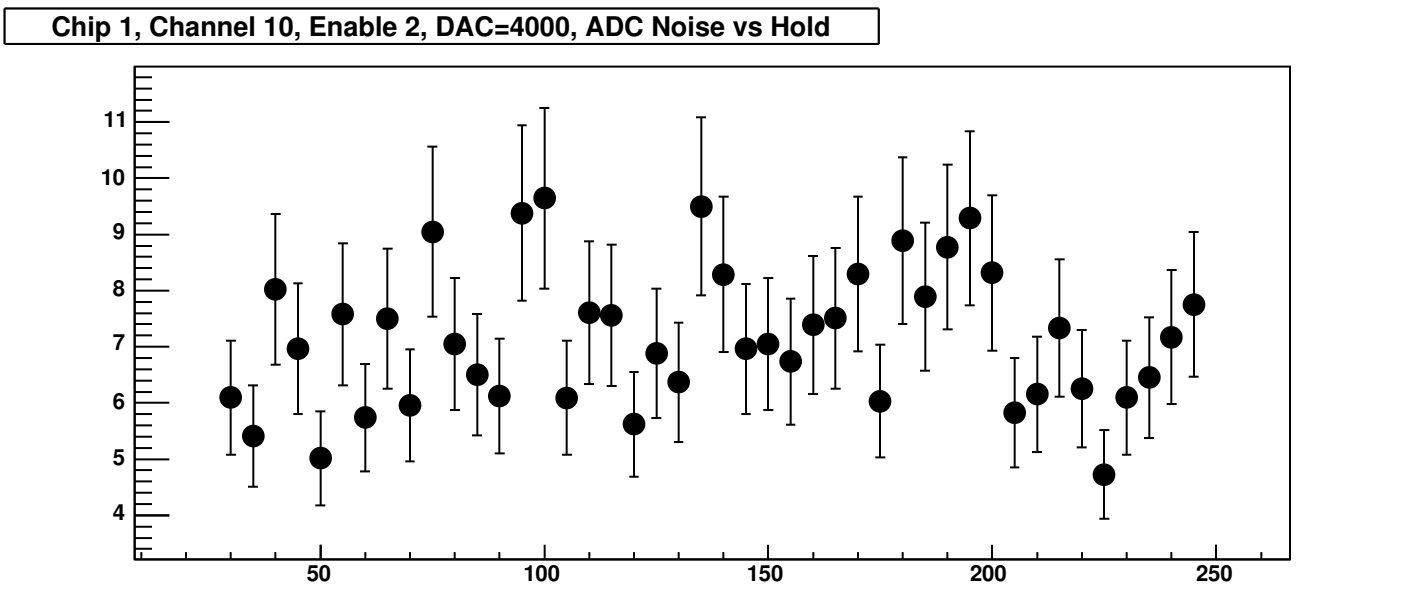

Chip 1, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
-15
50
100
150
200
250

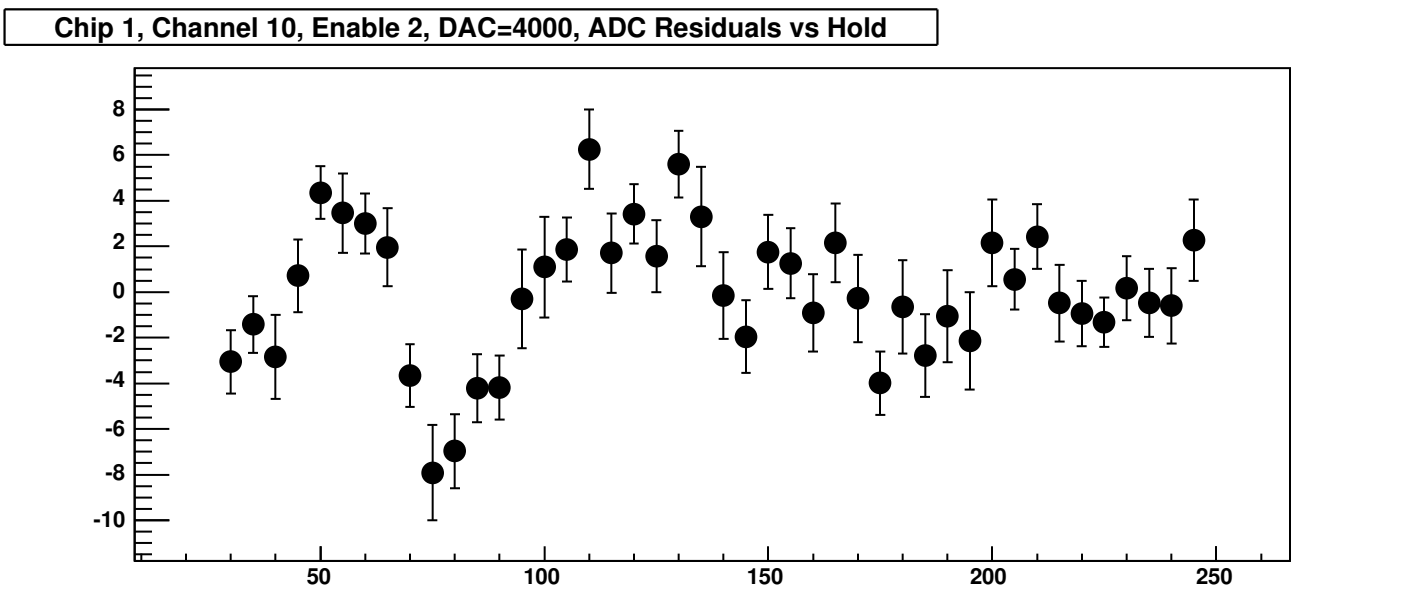

Chip 1, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 291.4 / 38
p0 211.7 ± 0.4698
p1 80.5 ± 0.6578
p2 -3.436e+07 ± 8.122e+05
p3 2.292e+07 ± 2.757e+05
p4 1.393 ± 0.03008
220
210
200
190
180
170
160
150
140
130
50
100
150
200
250


Chip 1, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
50
100
150
200
250

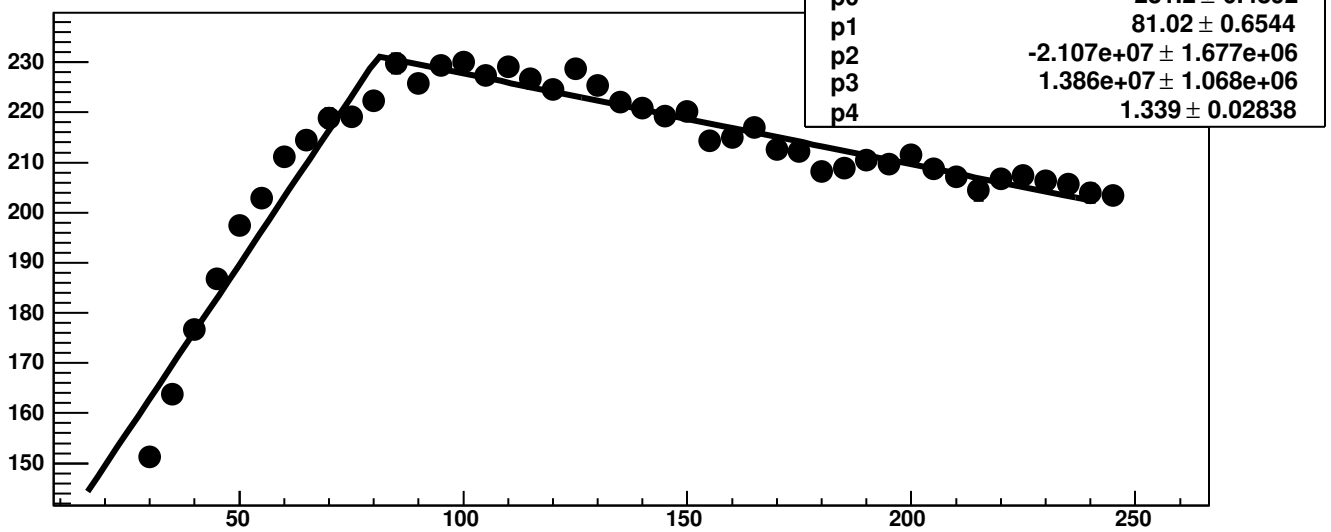

Chip 1, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
50
100
150
200
250


Chip 1, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 155.2 / 38
p0 210.2 ± 0.5444
p1 77.1 ± 0.5154
p2 -5.861e+07 ± 9.815e+05
p3 2.61e+07 ± 2.341e+05
p4 2.177 ± 0.03357
220
200
180
160
140
120
100
50
100
150
200
250

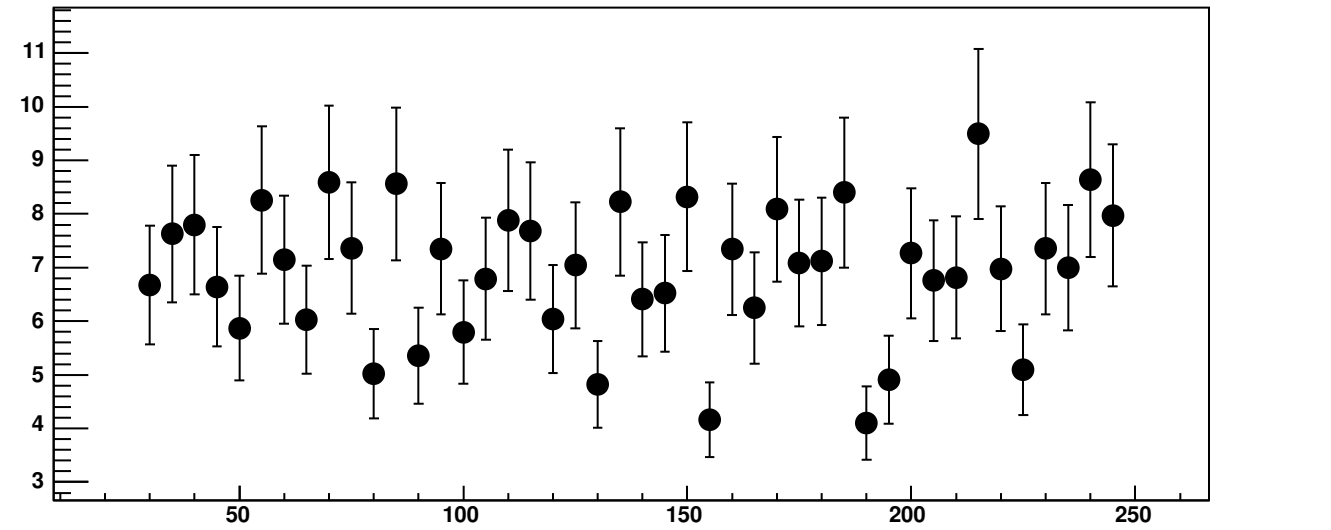

Chip 1, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 1, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
50
100
150
200
250

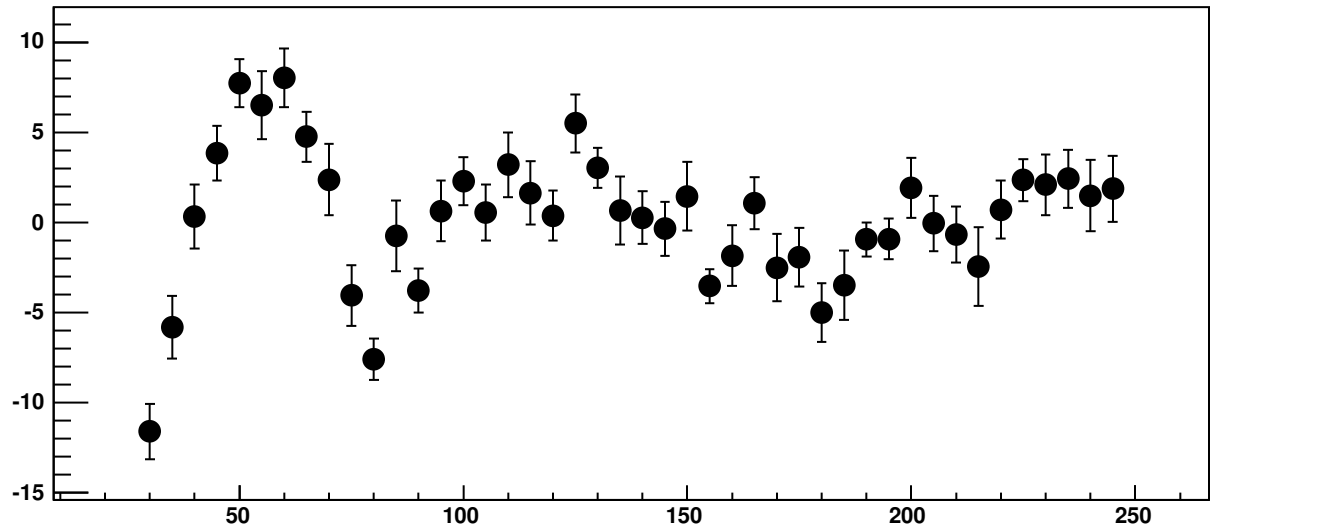
Chip 1, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold



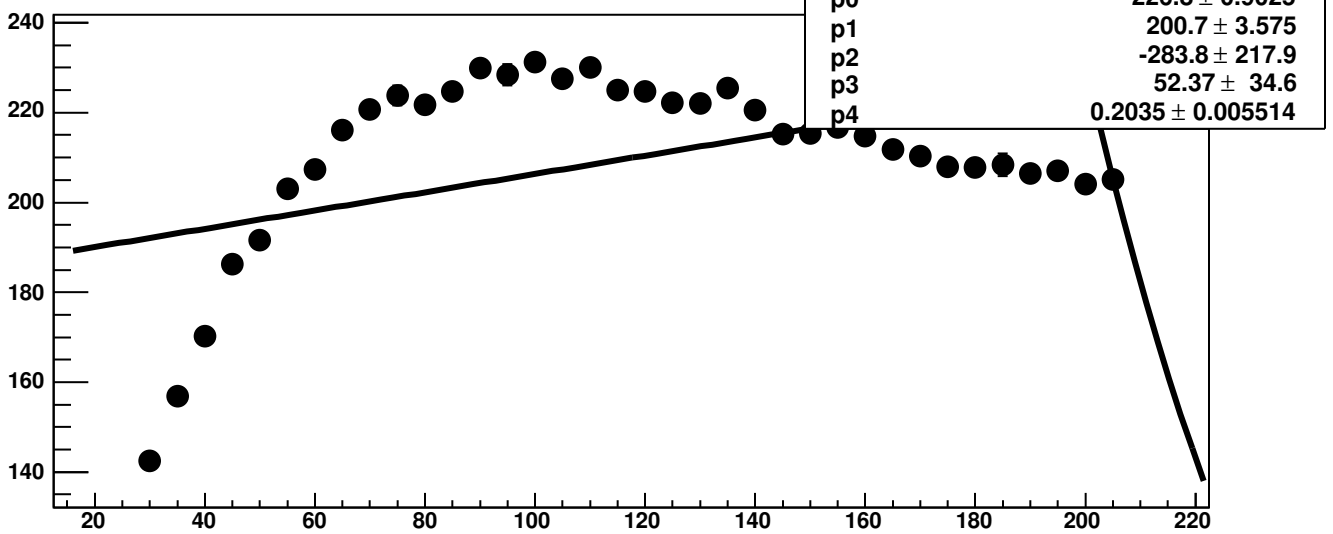
Chip 1, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold



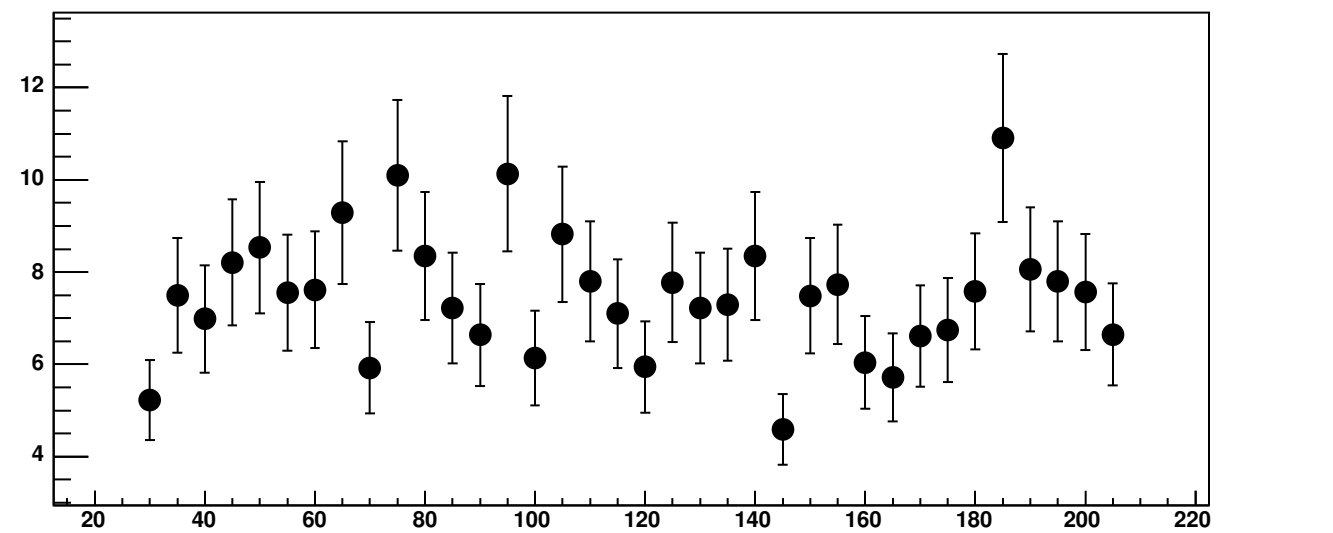
Chip 1, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold



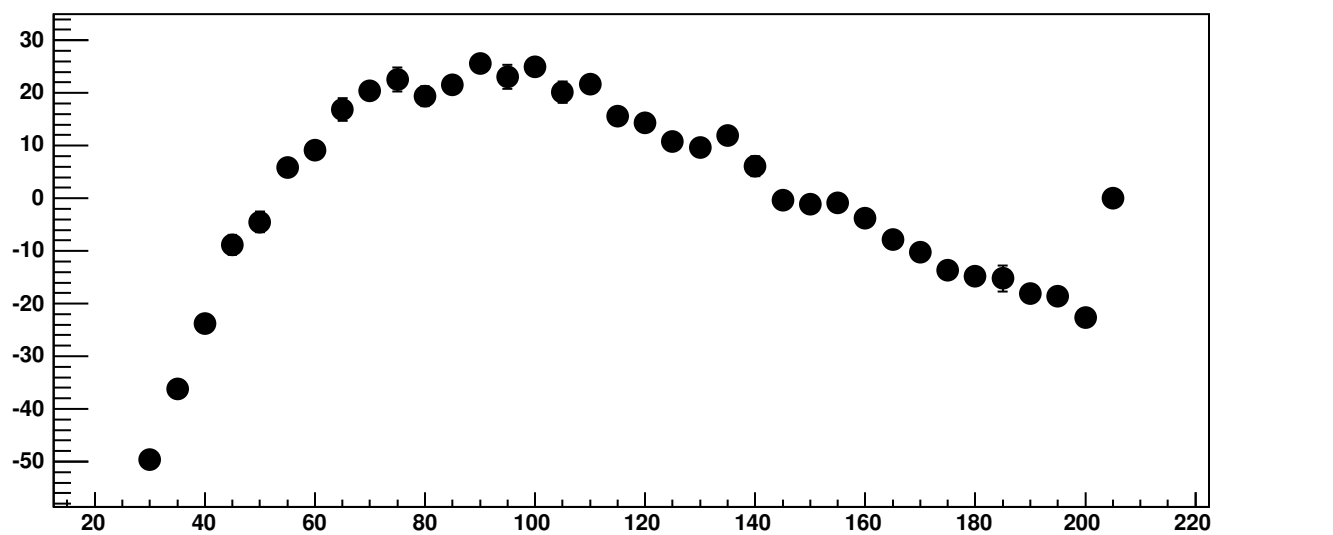
Chip 1, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold



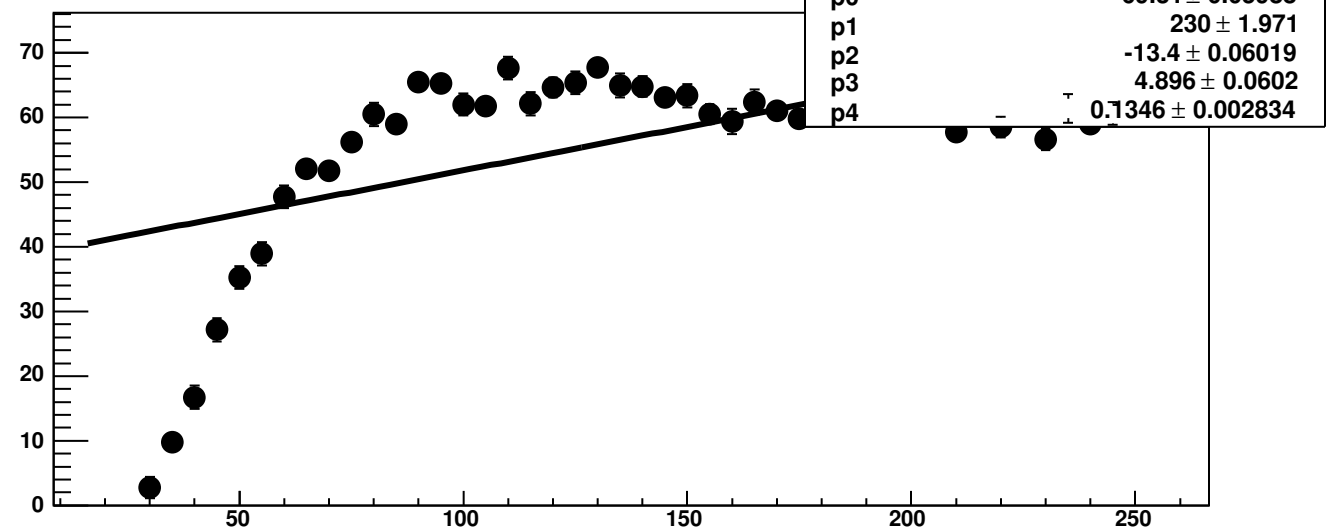
Chip 1, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



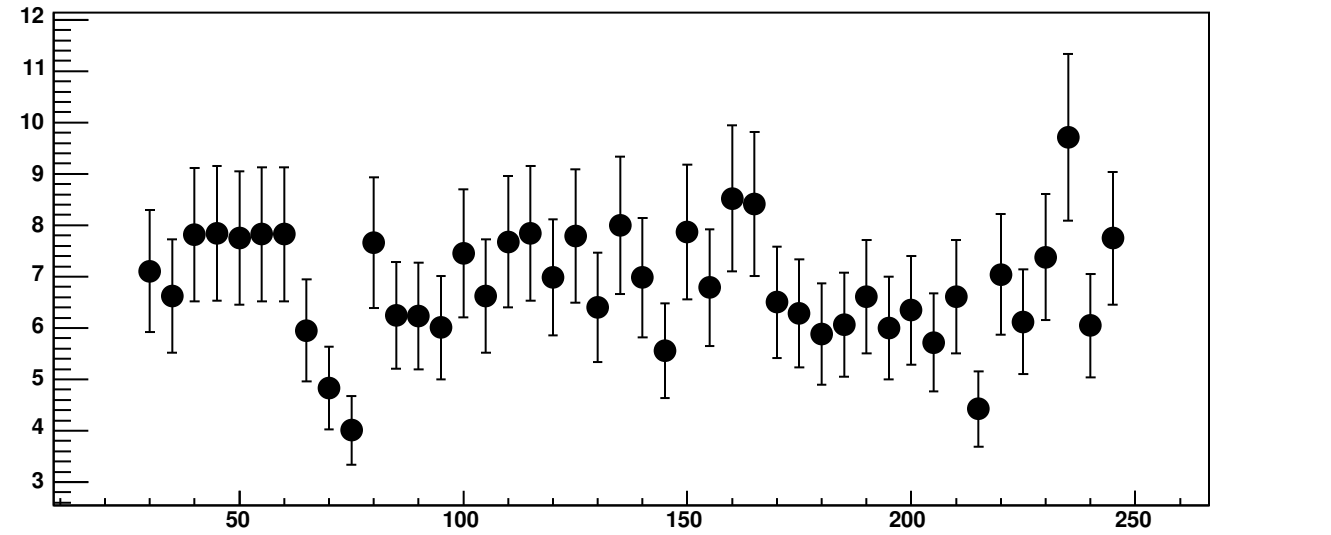
Chip 1, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold



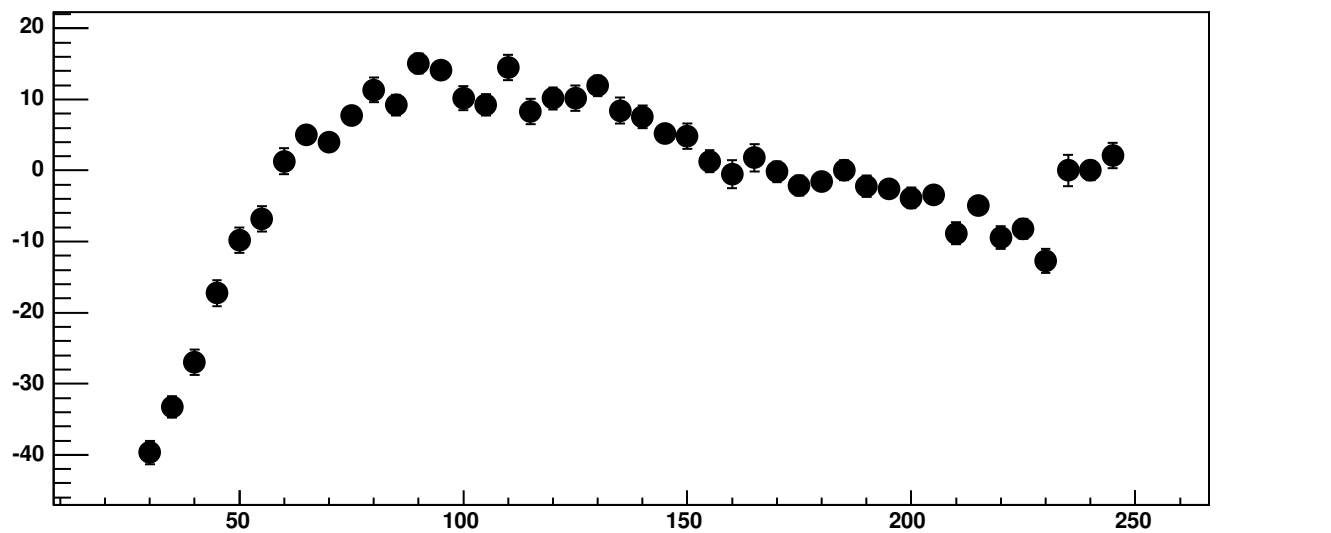
Chip 1, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold



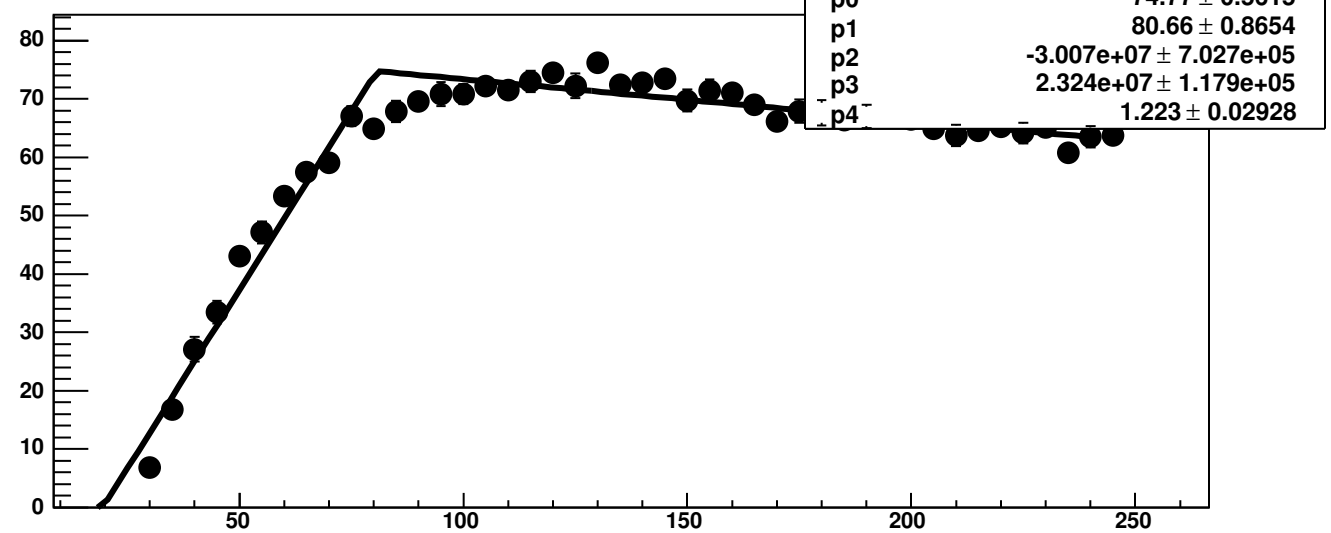
Chip 1, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold



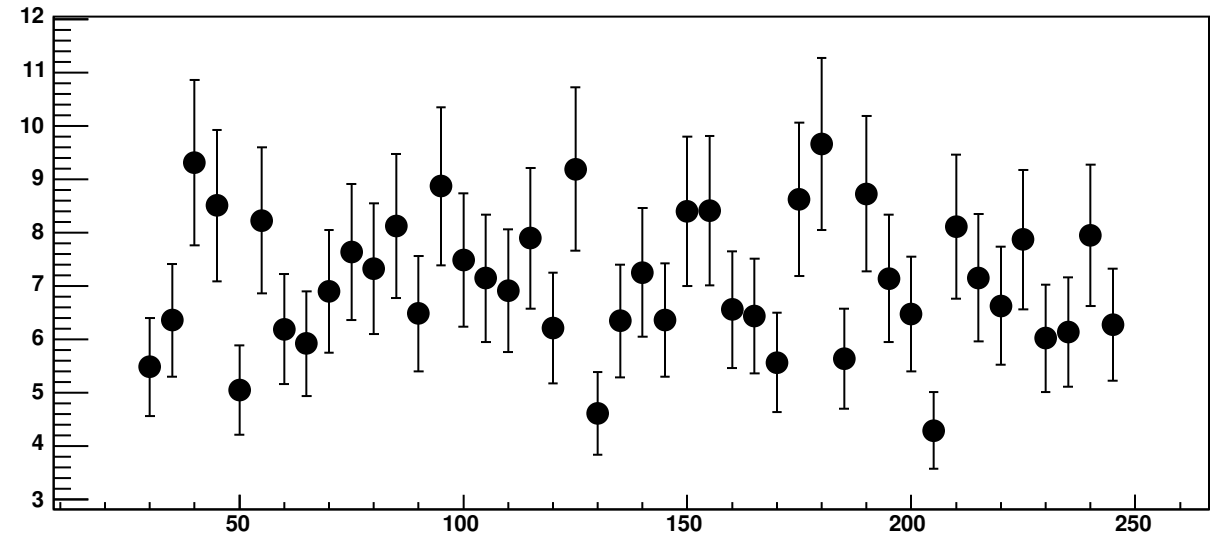
Chip 1, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold



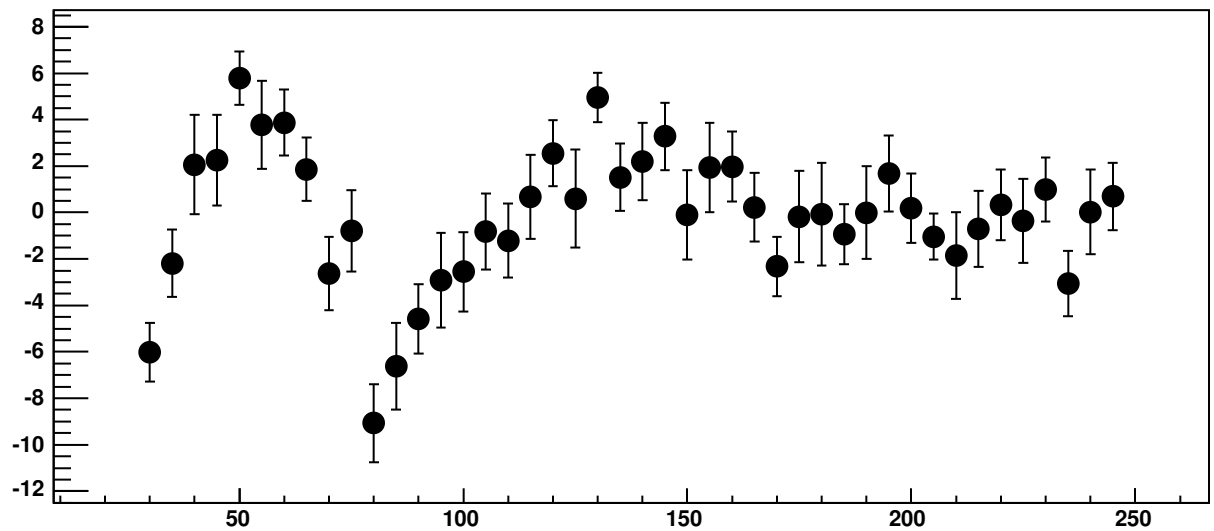
Chip 1, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold



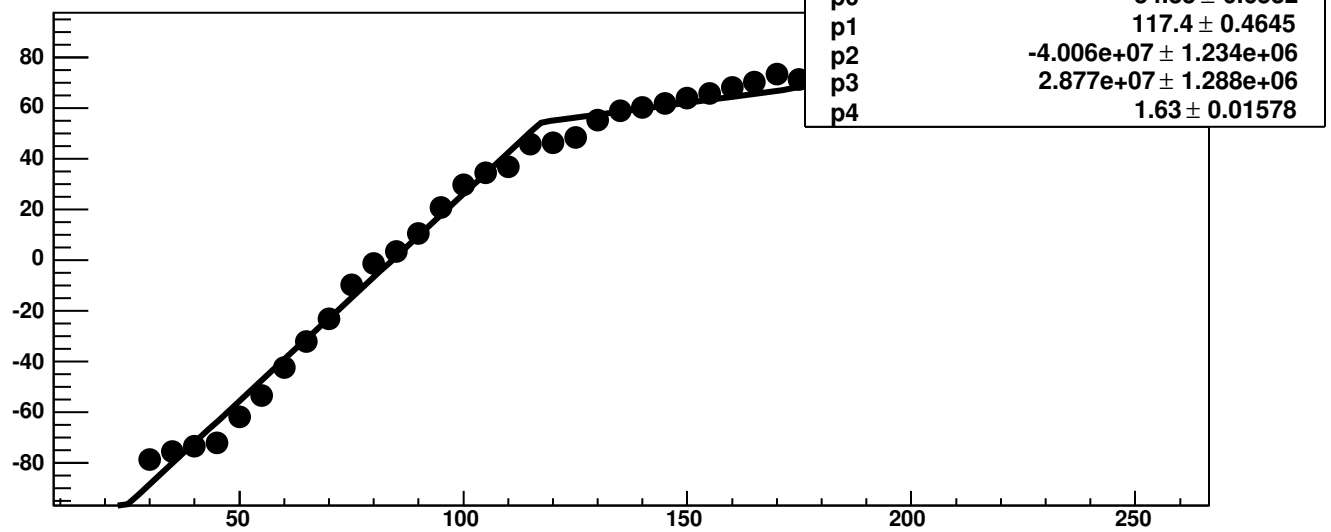
Chip 1, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



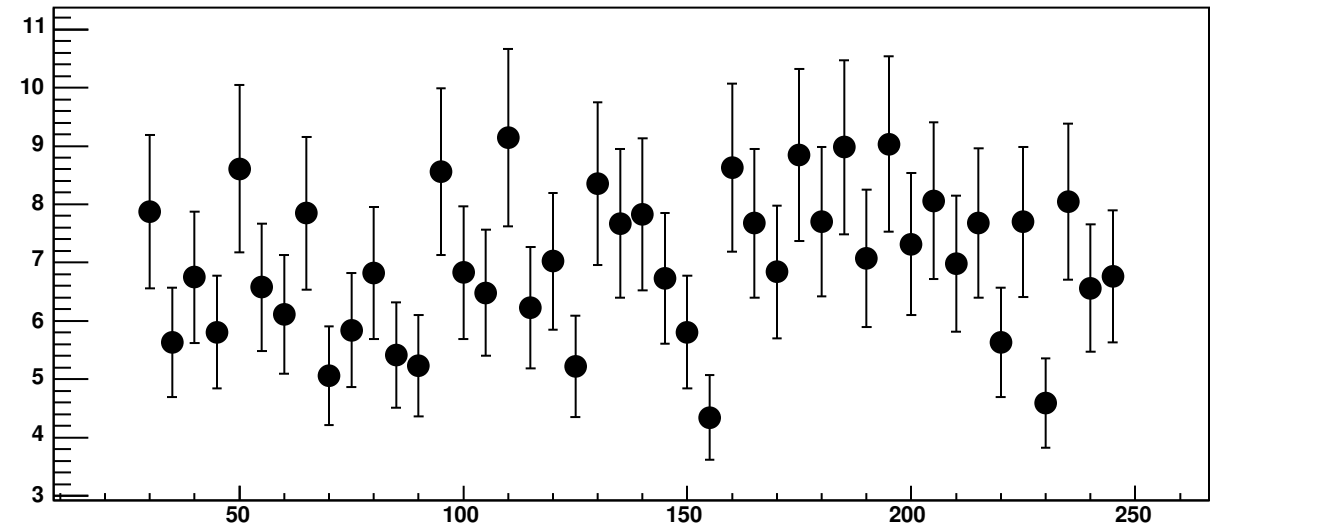
Chip 1, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold



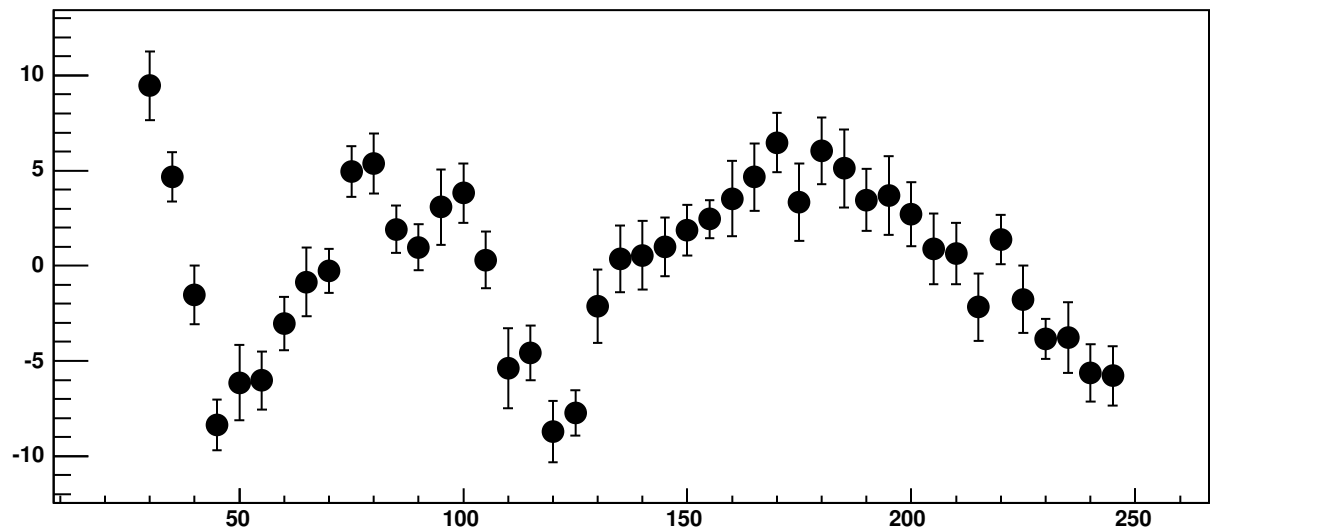
Chip 1, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

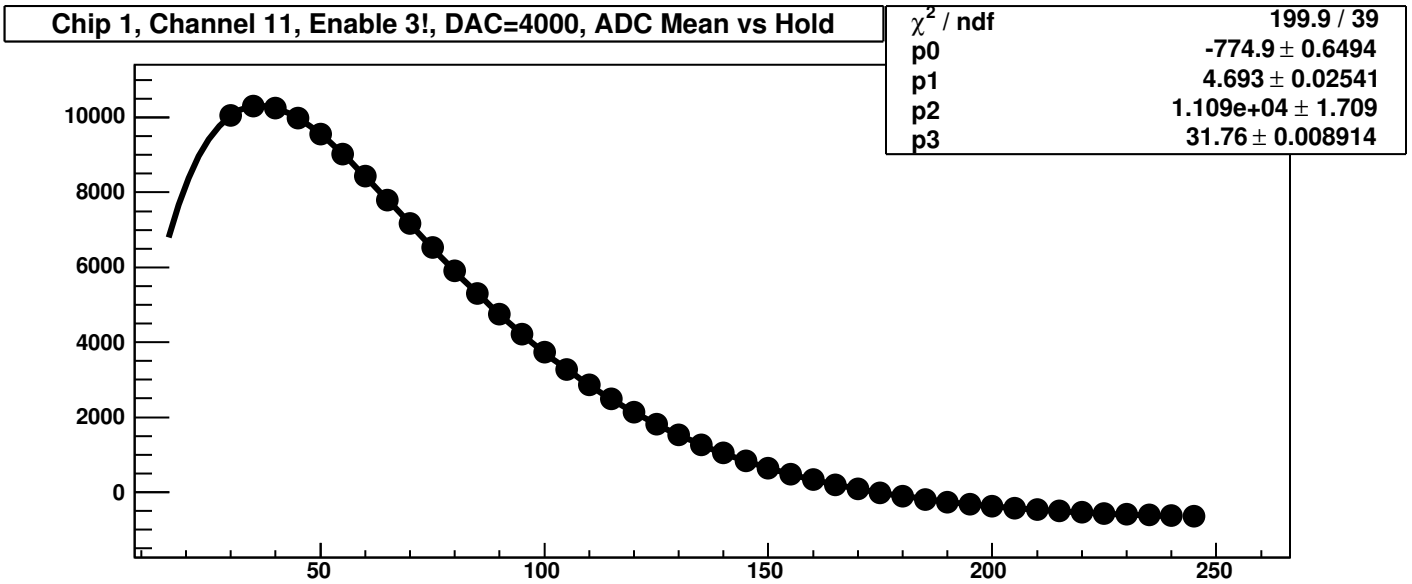


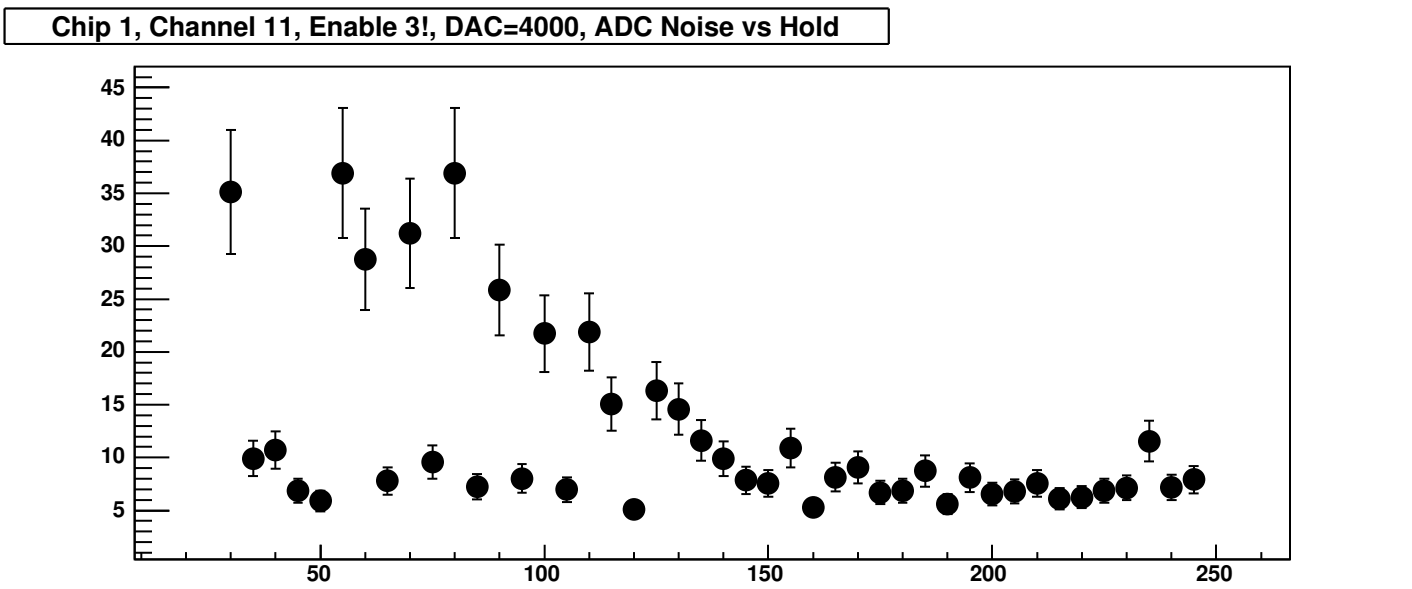
Chip 1, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold

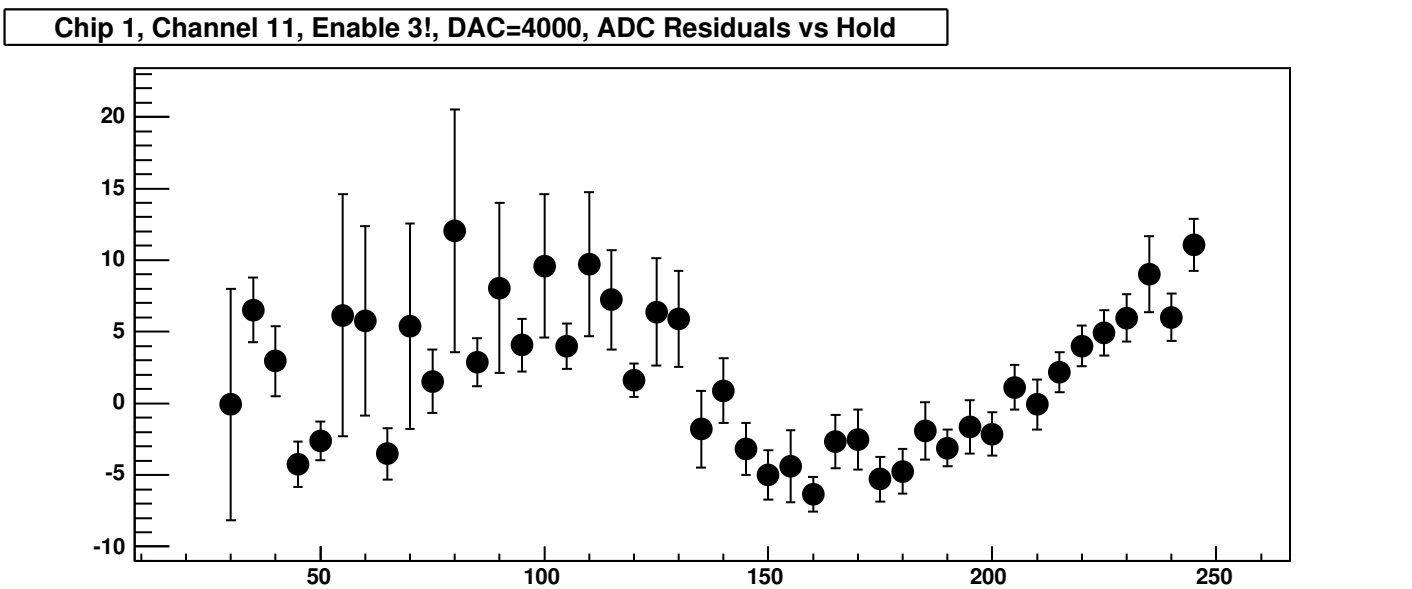


Chip 1, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

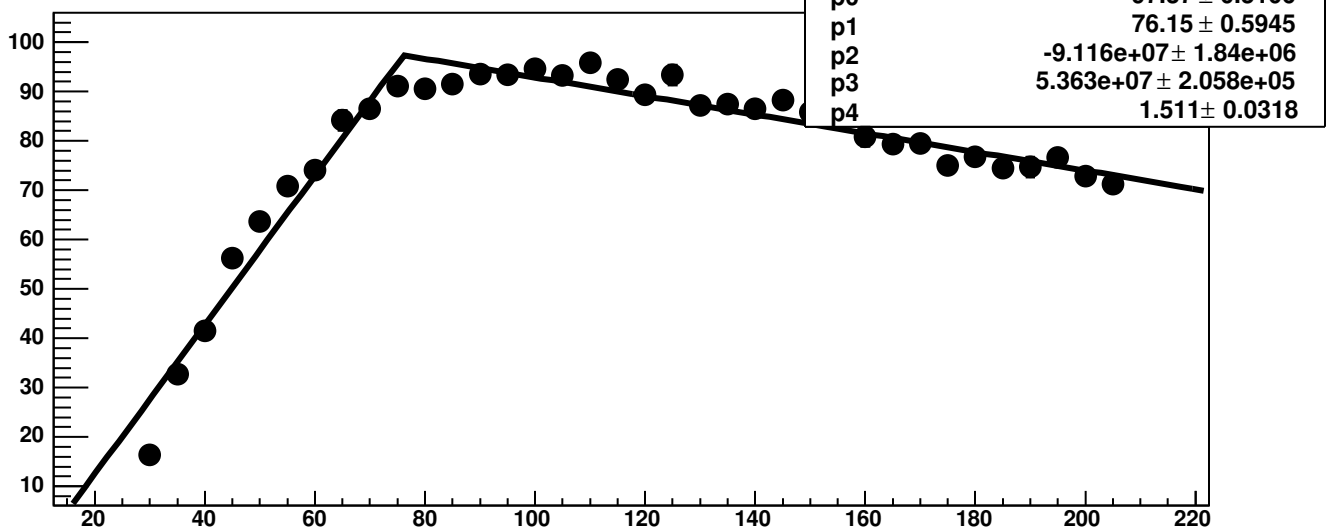



Chip 1, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 199.9 / 39
p0 -774.9 ± 0.6494
p1 4.693 ± 0.02541
p2 1.109e+04 ± 1.709
p3 31.76 ± 0.008914
10000
8000
6000
4000
2000
0
50
100
150
200
250

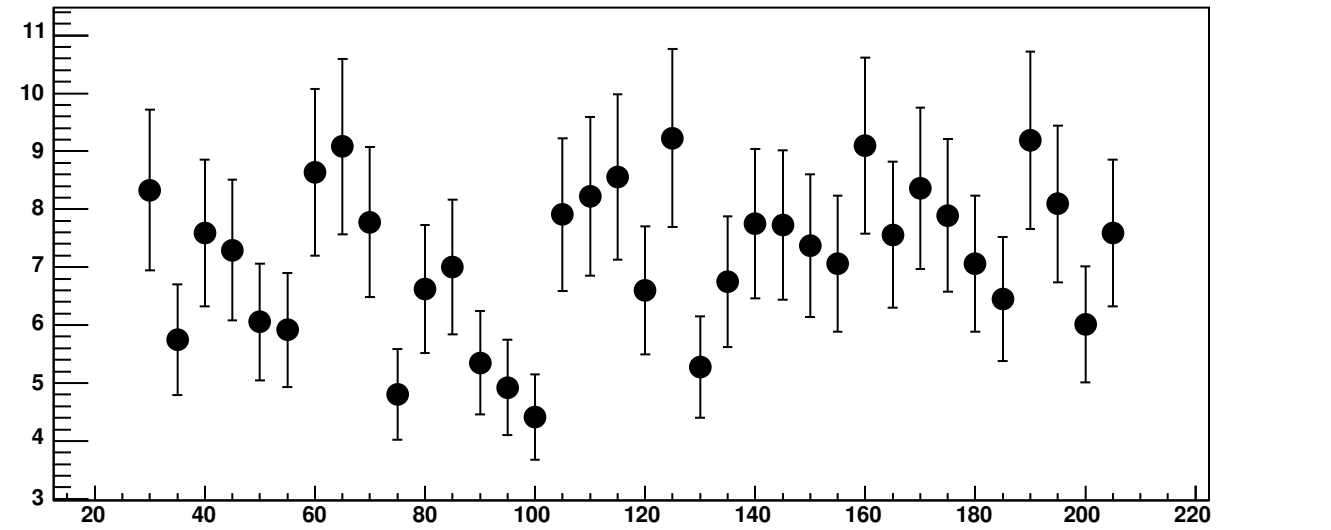

Chip 1, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold
45
40
35
30
25
20
15
10
5
50
100
150
200
250


Chip 1, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
50
100
150
200
250

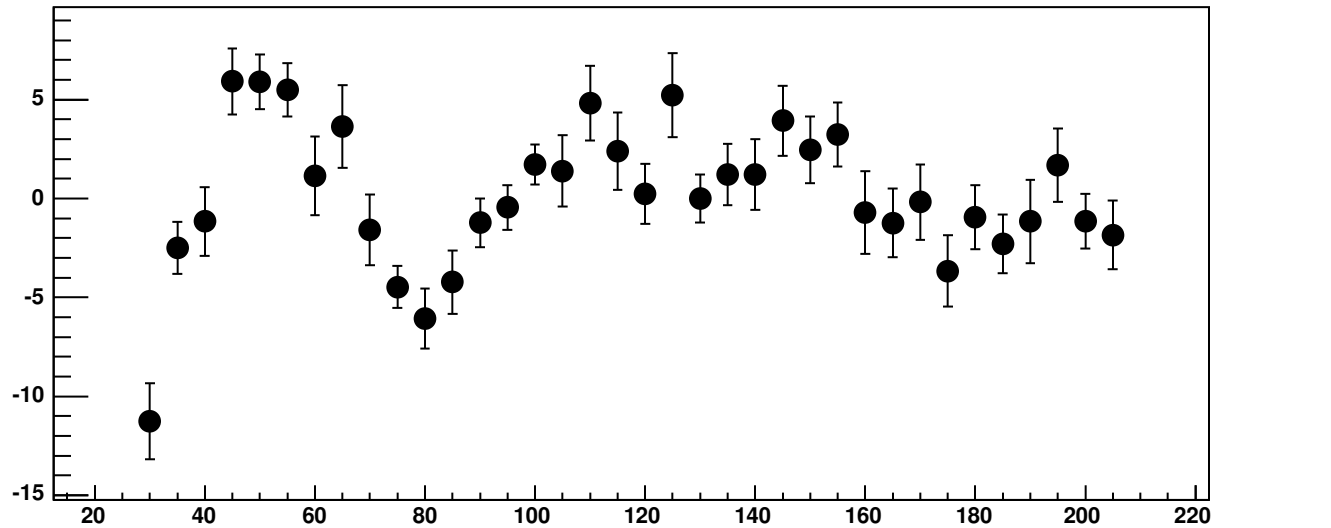
Chip 1, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

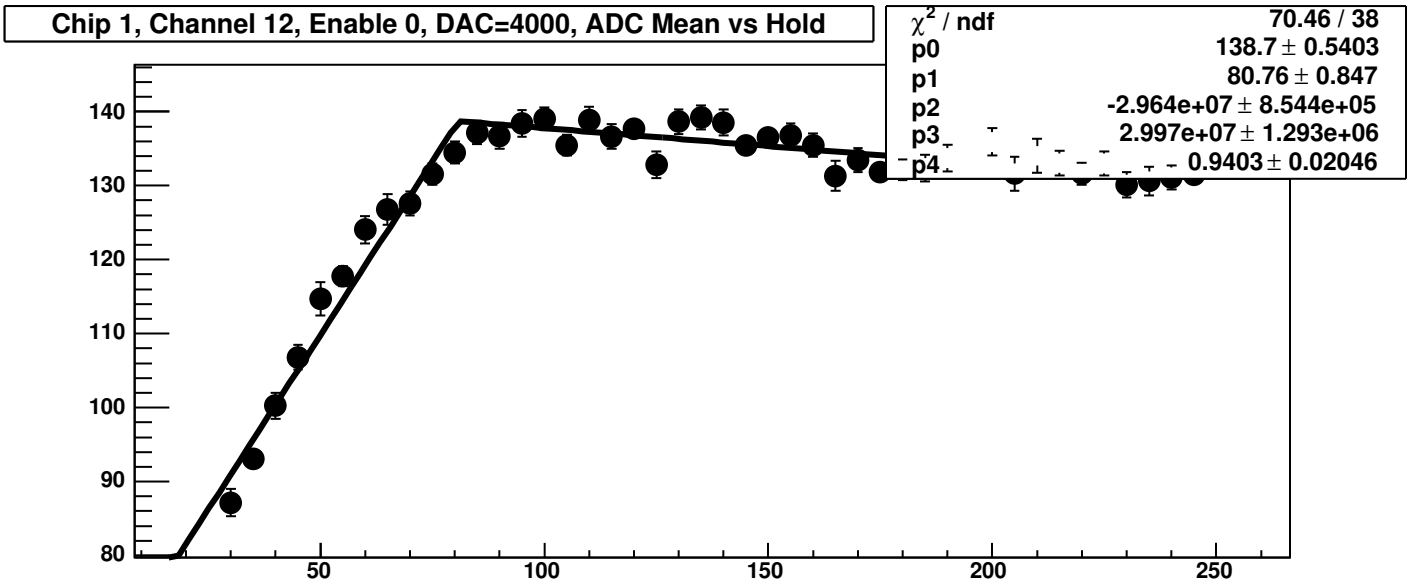


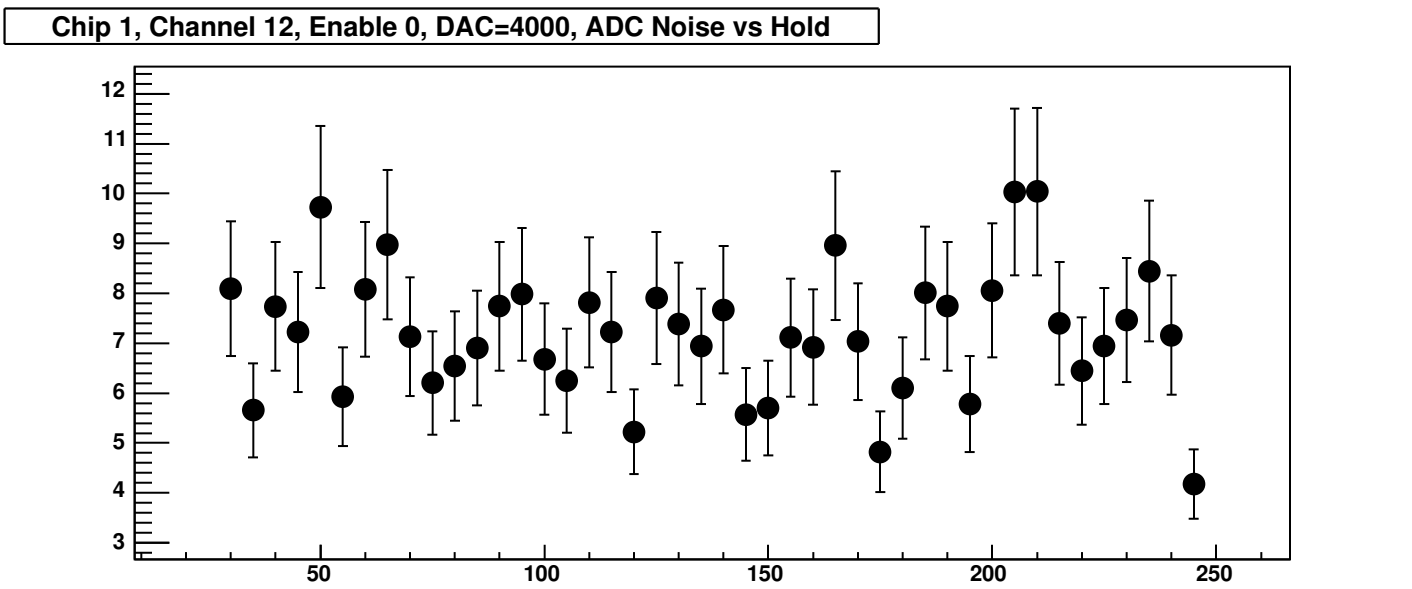
Chip 1, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold

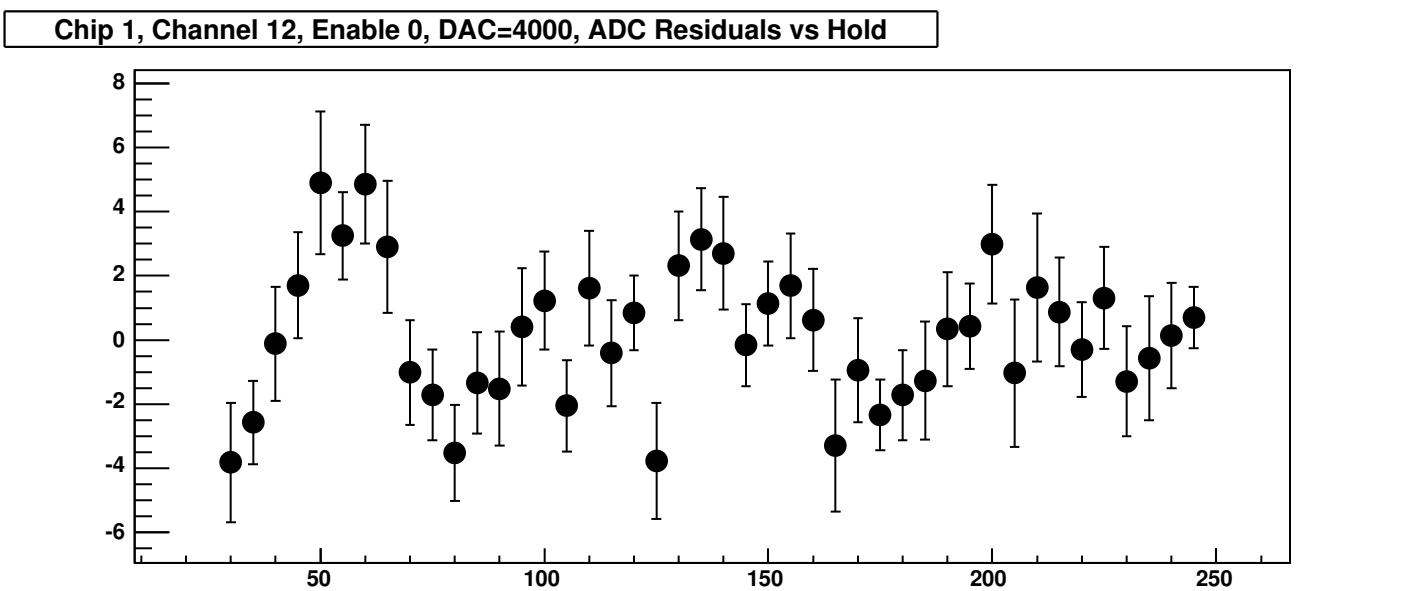


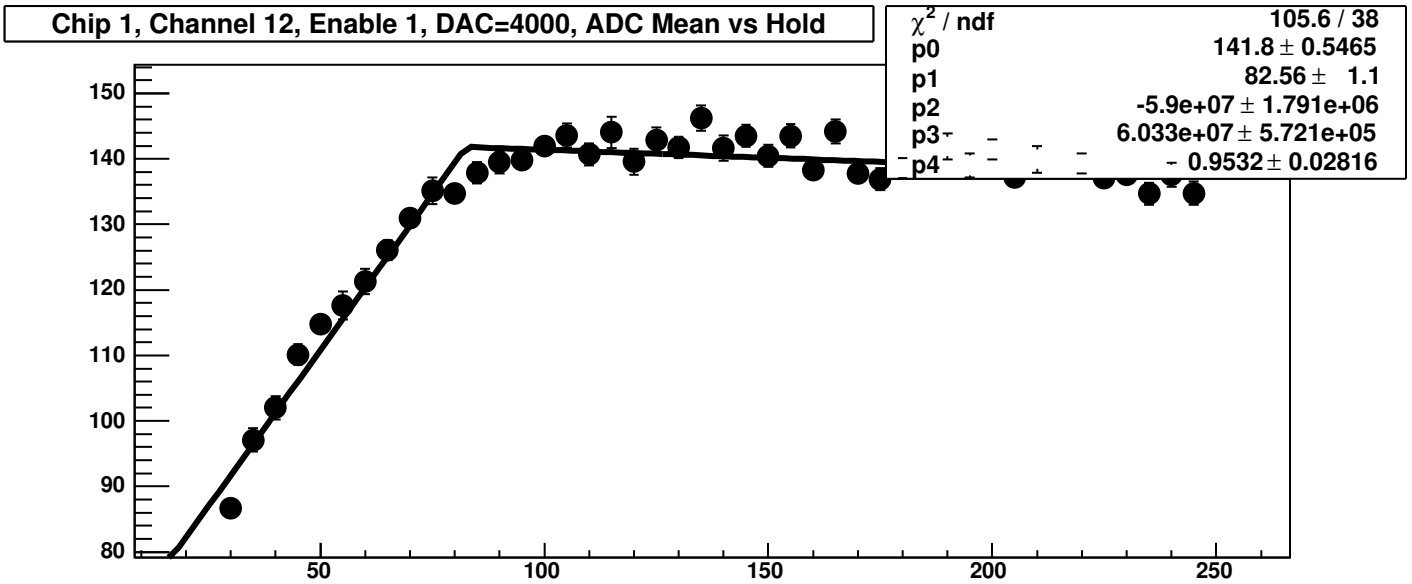
Chip 1, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

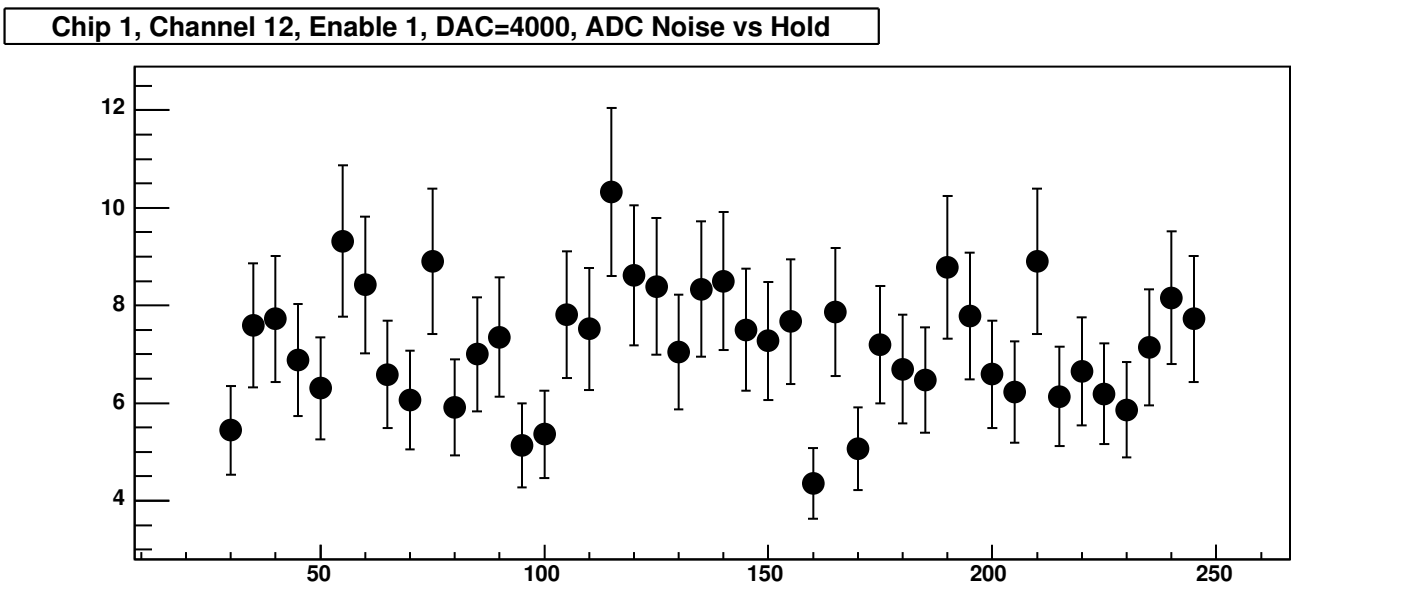


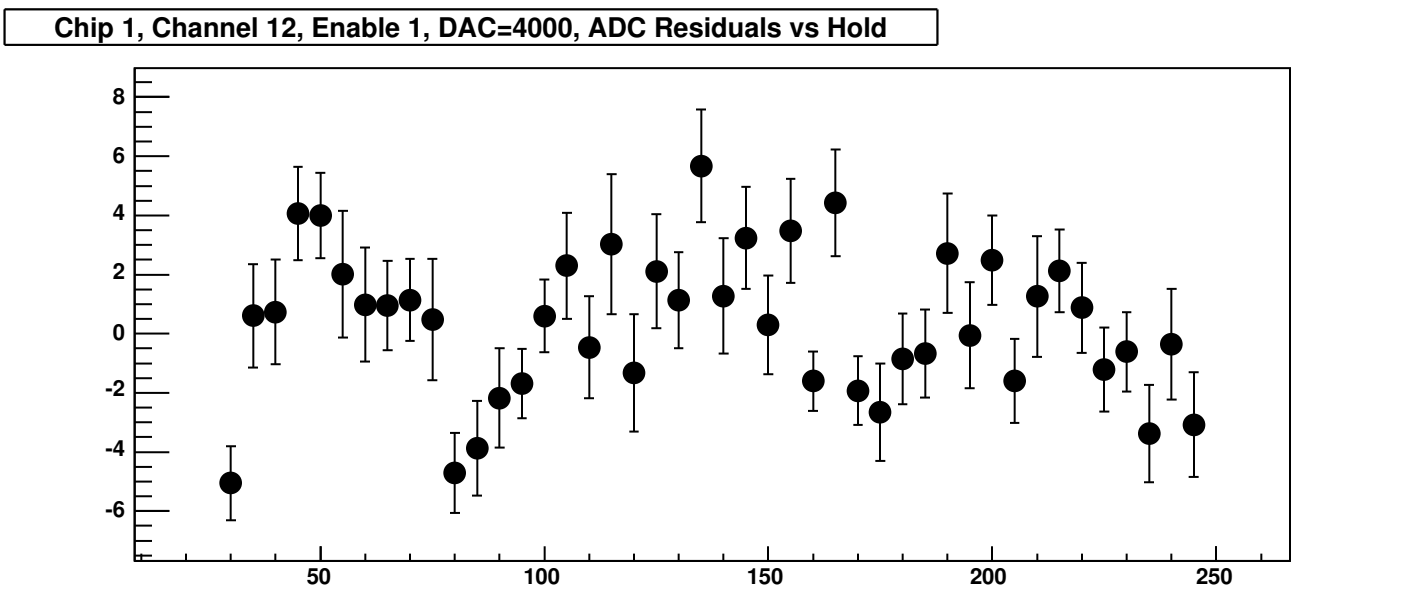

Chip 1, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 70.46 / 38
p0 138.7 ± 0.5403
p1 80.76 ± 0.847
p2 -2.964e+07 ± 8.544e+05
p3 2.997e+07 ± 1.293e+06
p4 0.9403 ± 0.02046
140
130
120
110
100
90
80
50
100
150
200
250

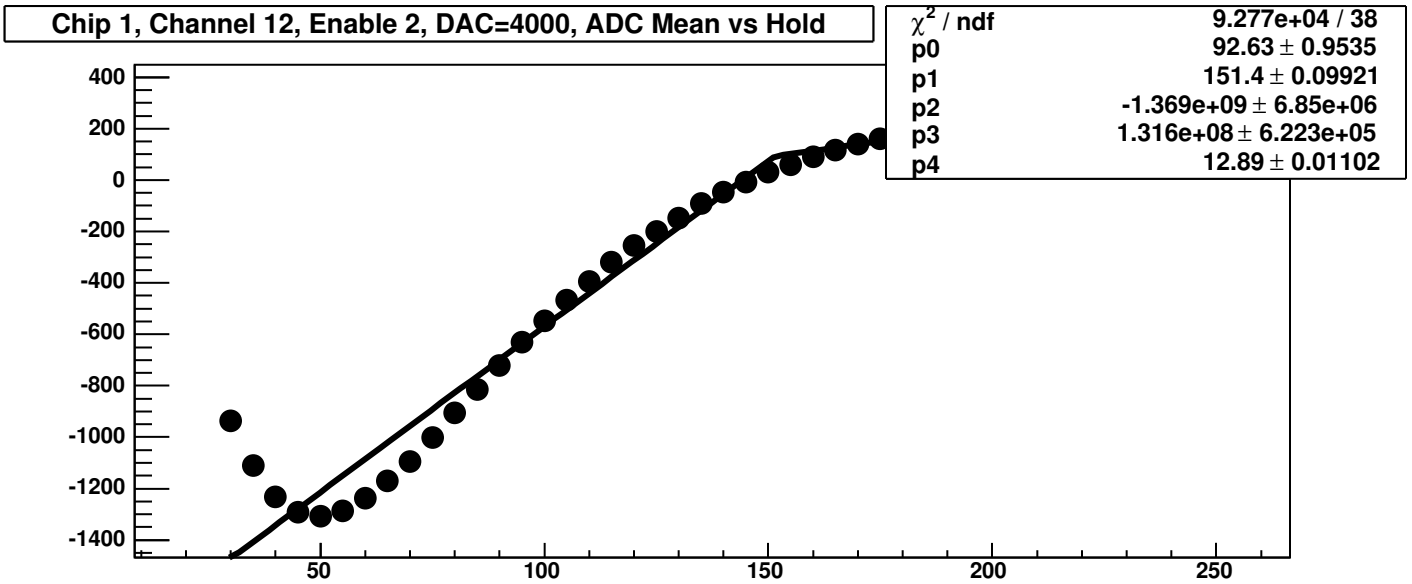

Chip 1, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250

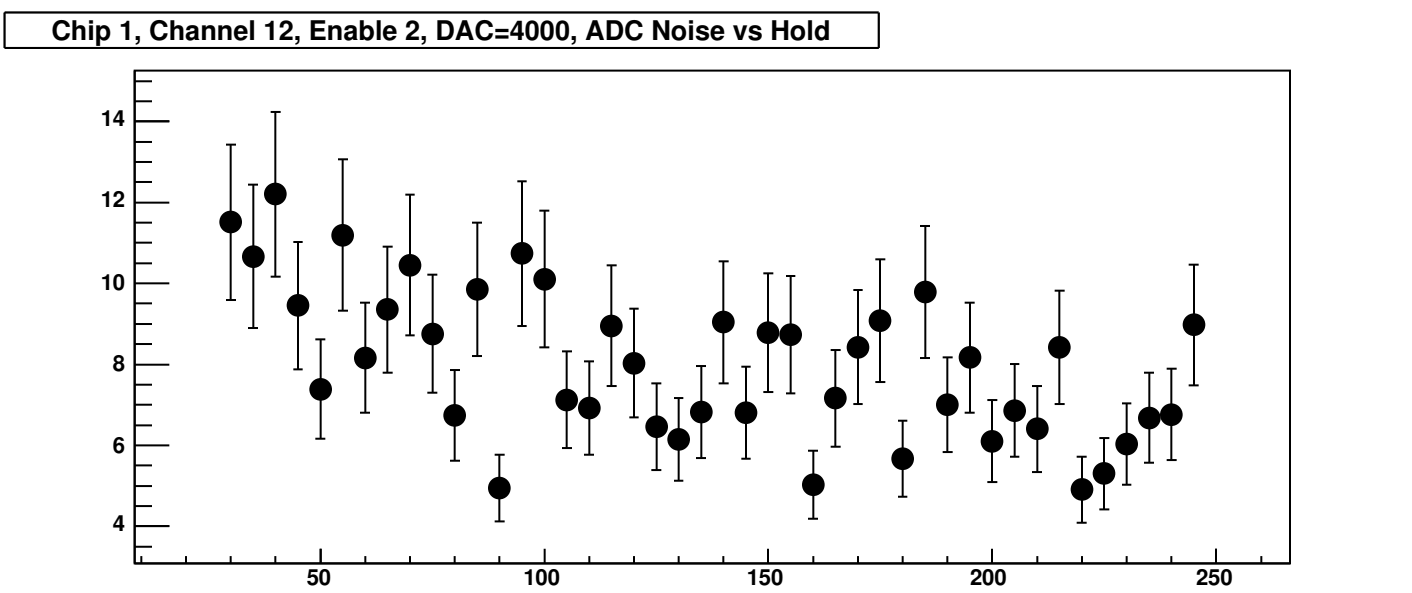

Chip 1, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
50
100
150
200
250

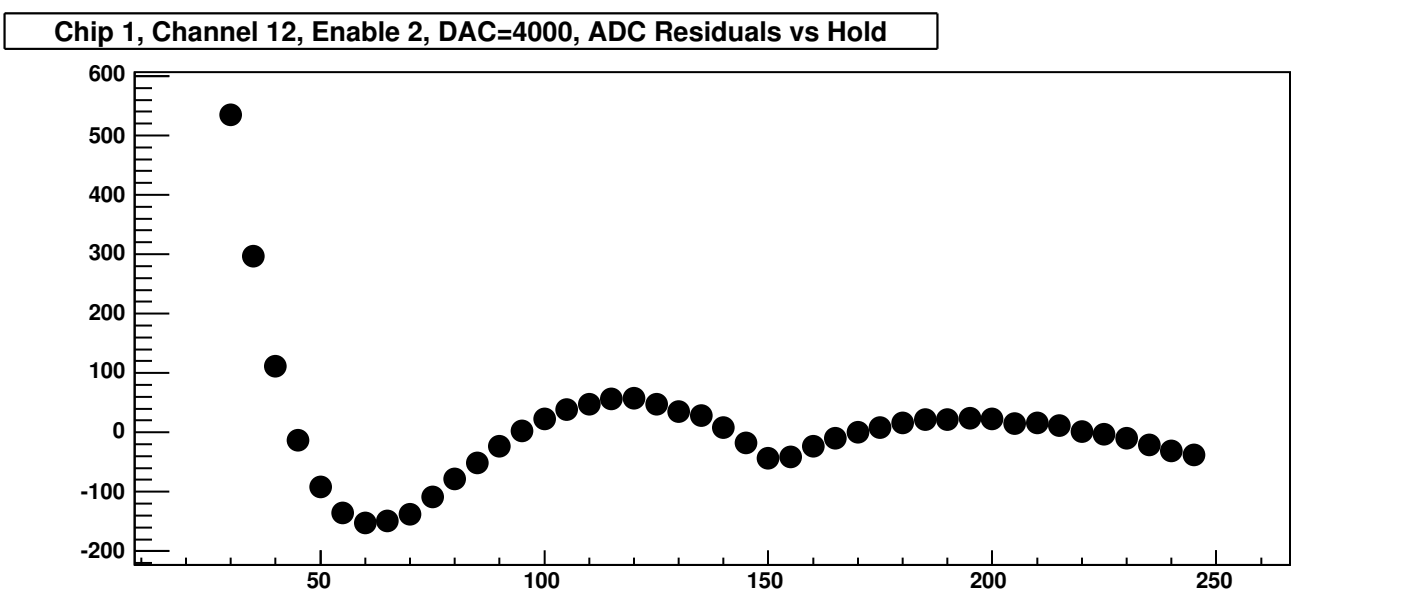

Chip 1, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 105.6 / 38
p0 141.8 ± 0.5465
p1 82.56 ± 1.1
p2 -5.9e+07 ± 1.791e+06
p3 6.033e+07 ± 5.721e+05
p4 - 0.9532 ± 0.02816
150
140
130
120
110
100
90
80
50
100
150
200
250

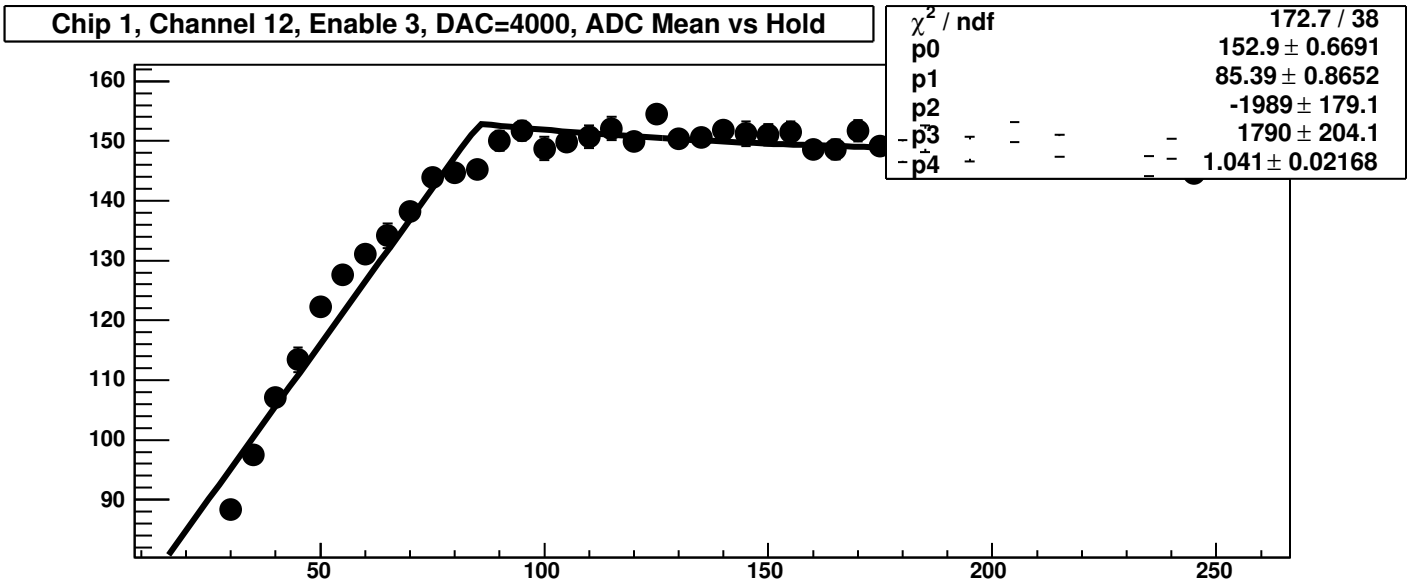

Chip 1, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250

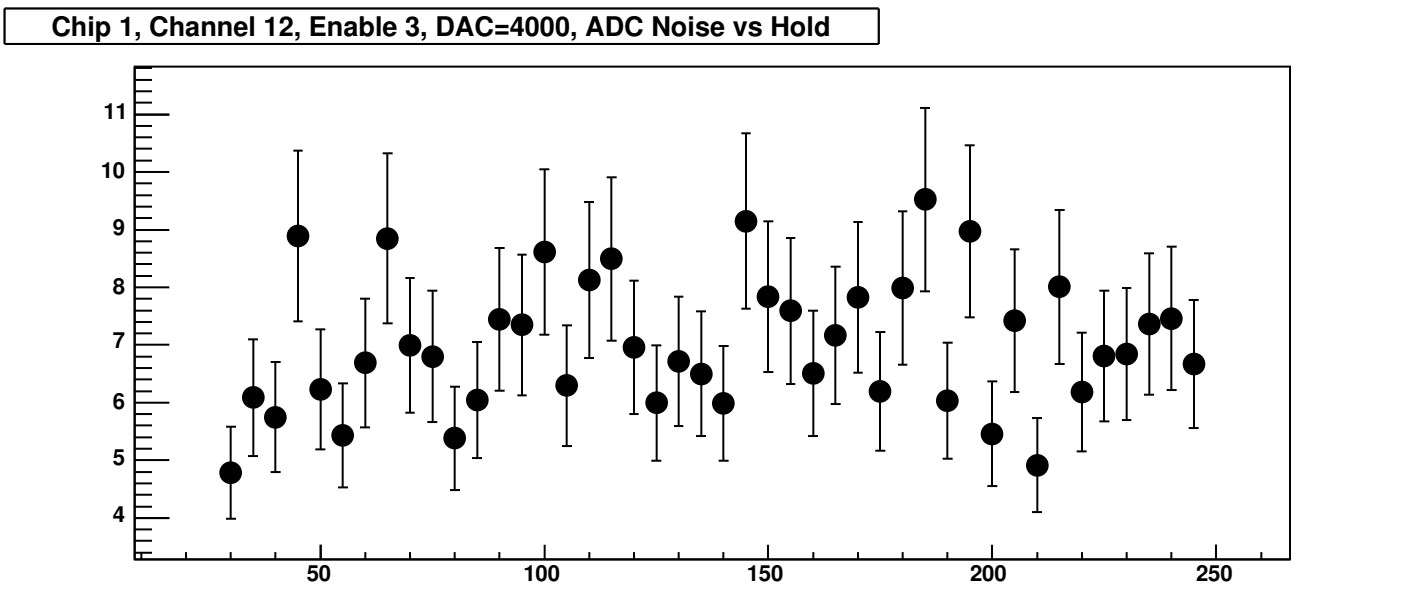

Chip 1, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
50
100
150
200
250

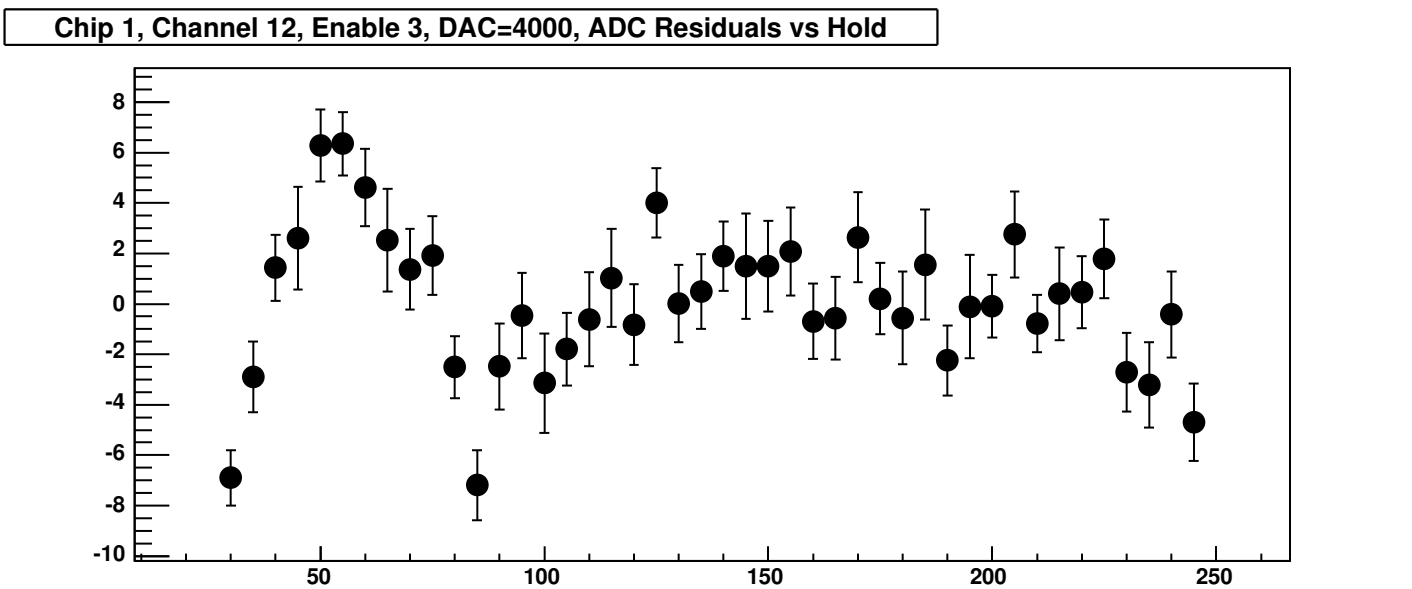

Chip 1, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 9.277e+04 / 38
p0 92.63 ± 0.9535
p1 151.4 ± 0.09921
p2 -1.369e+09 ± 6.85e+06
p3 1.316e+08 ± 6.223e+05
p4 12.89 ± 0.01102
400
200
0
-200
-400
-600
-800
-1000
-1200
-1400
50
100
150
200
250

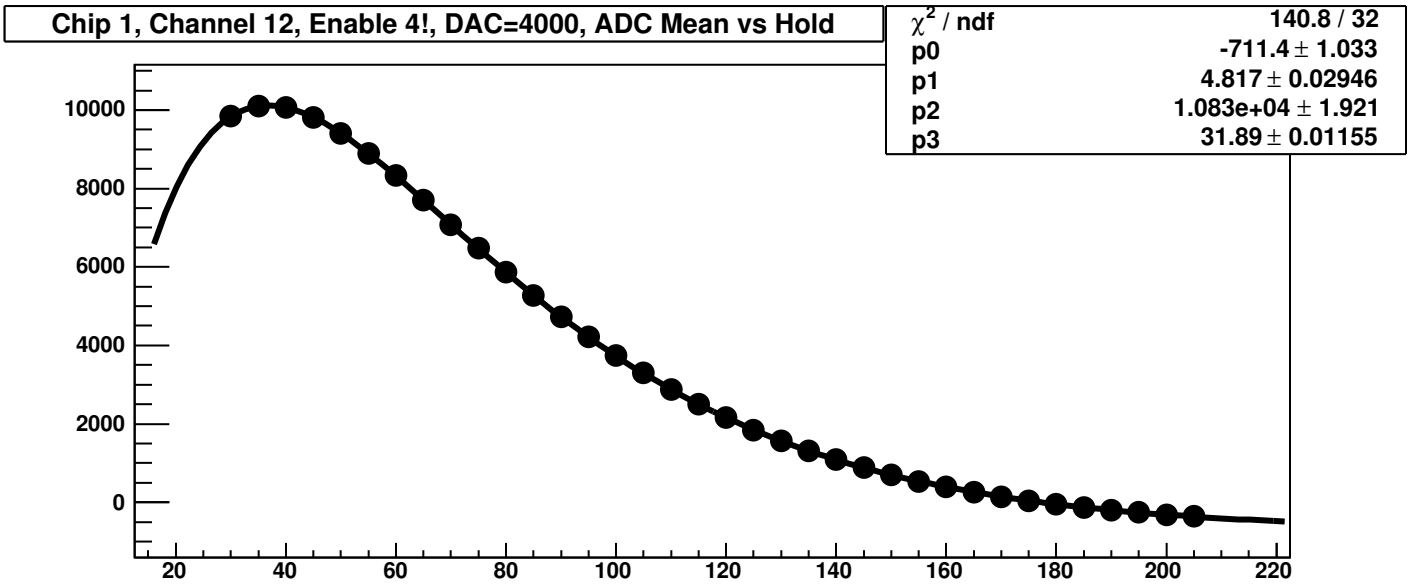

Chip 1, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250

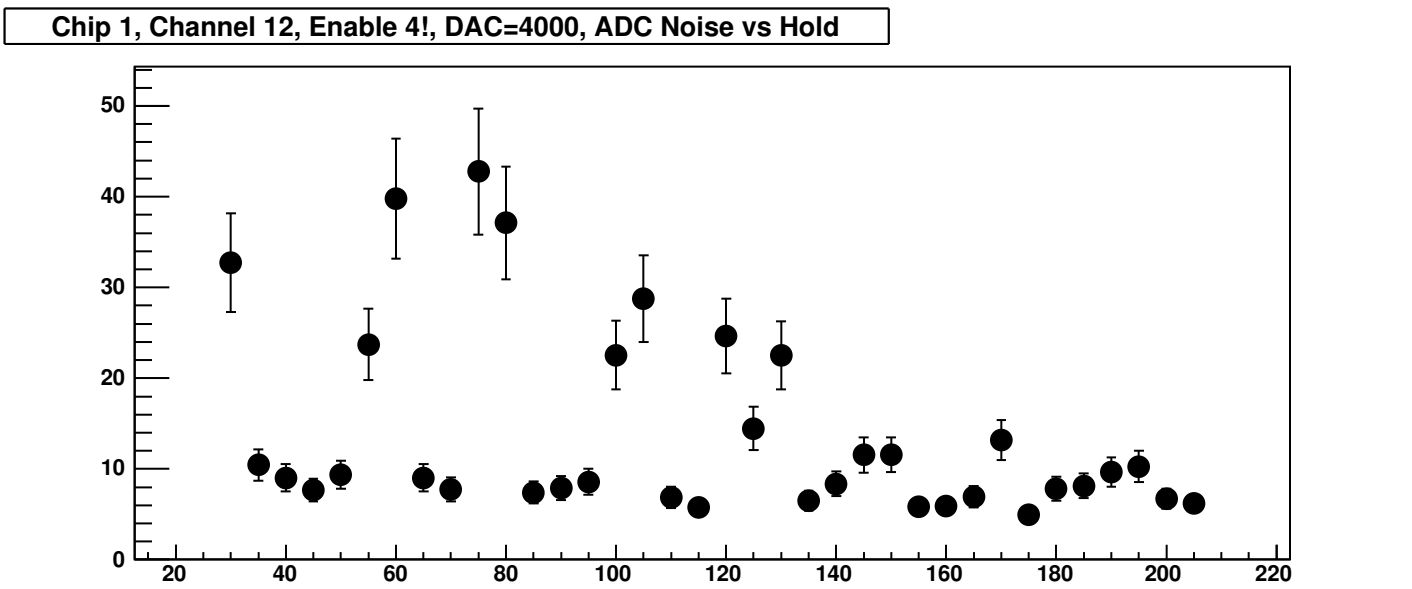

Chip 1, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold
600
500
400
300
200
100
0
-100
-200
50
100
150
200
250

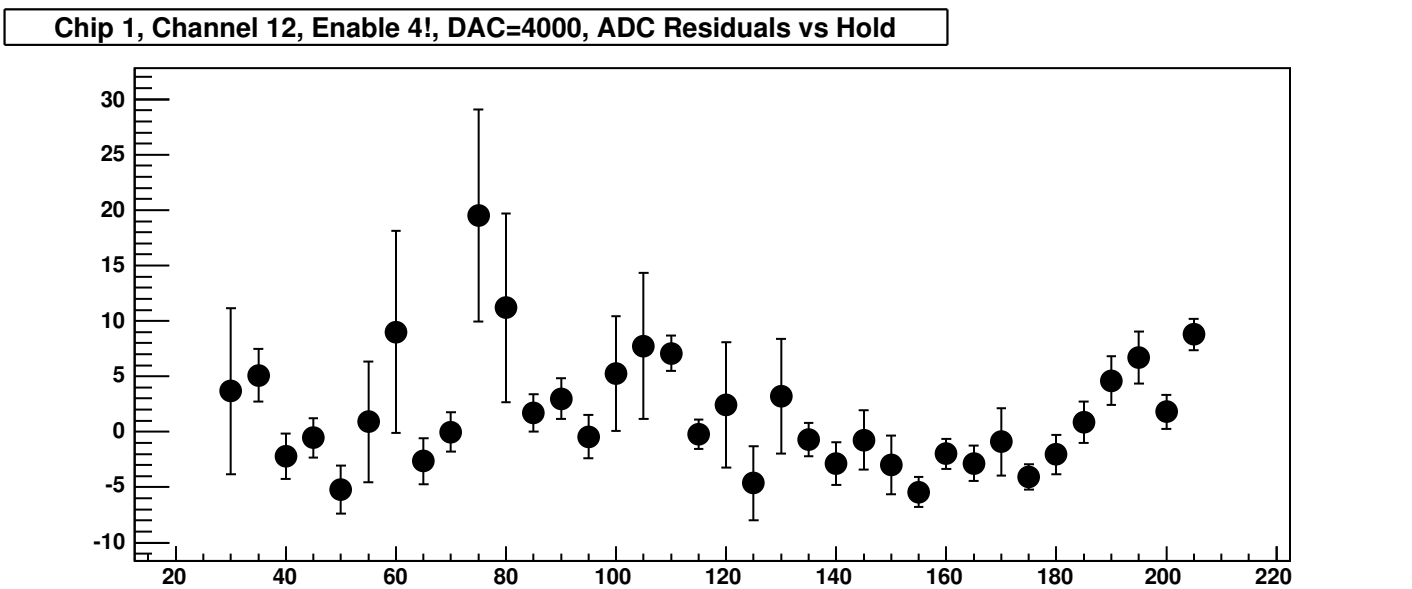

Chip 1, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 172.7 / 38
p0 152.9 ± 0.6691
p1 85.39 ± 0.8652
p2 -1989 ± 179.1
p3 1790 ± 204.1
p4 1.041 ± 0.02168
160
150
140
130
120
110
100
90
50
100
150
200
250

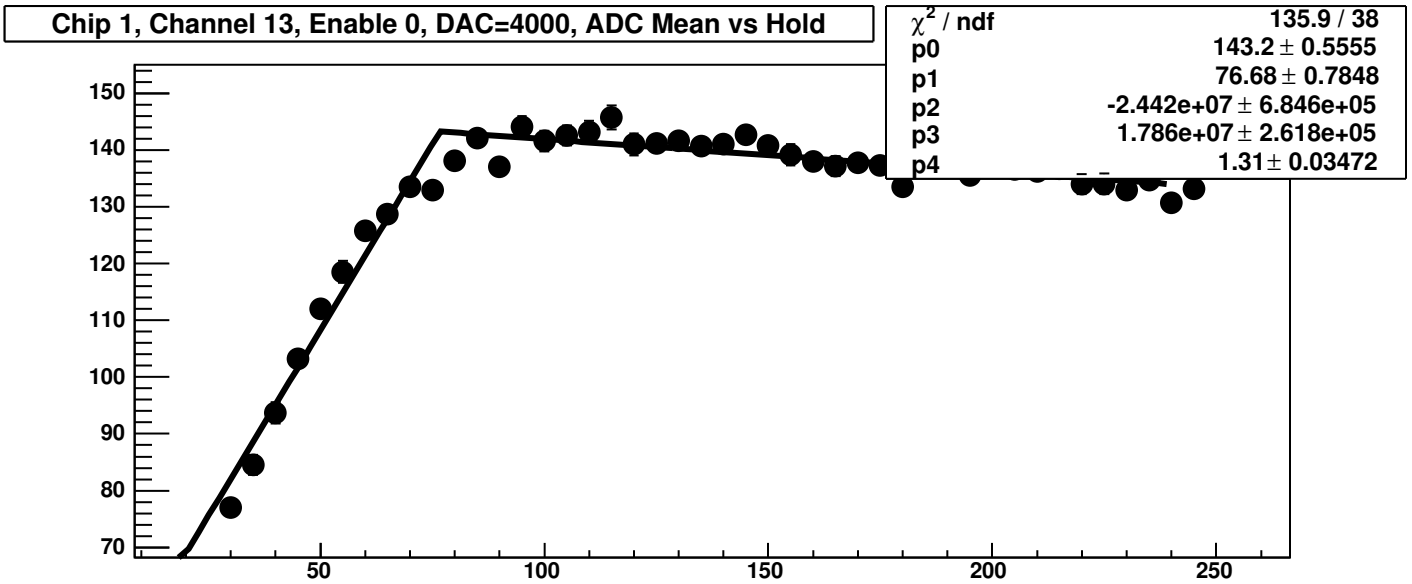

Chip 1, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250

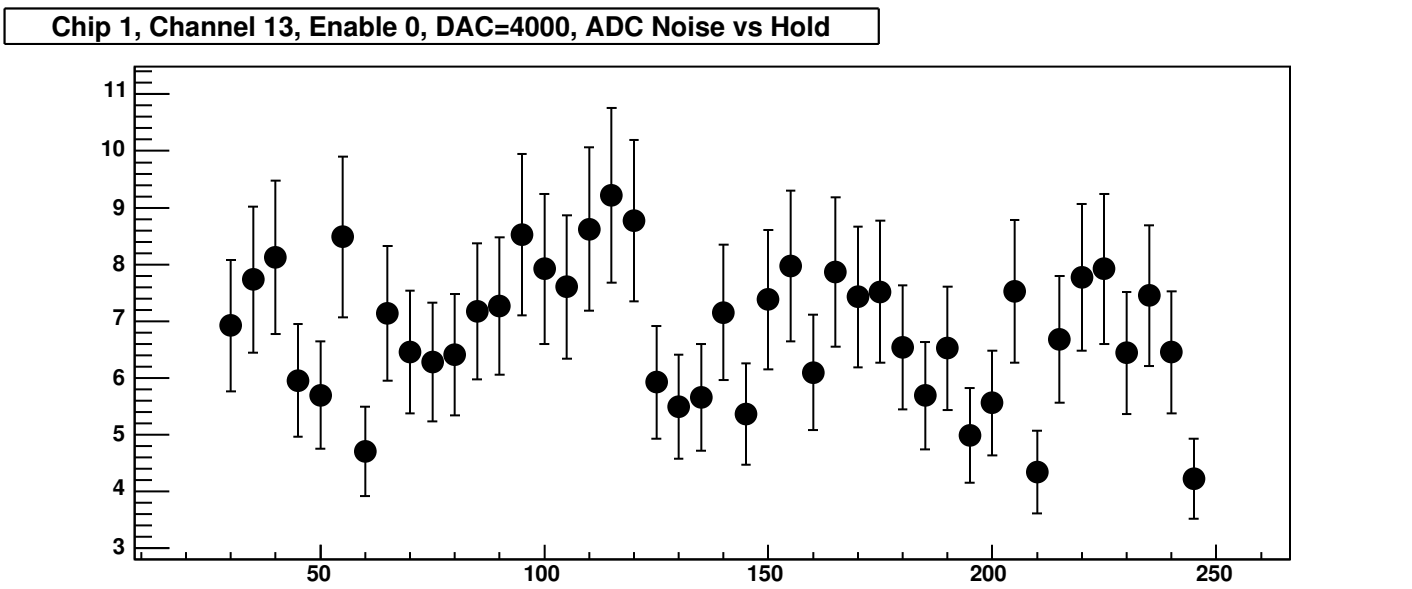

Chip 1, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
50
100
150
200
250

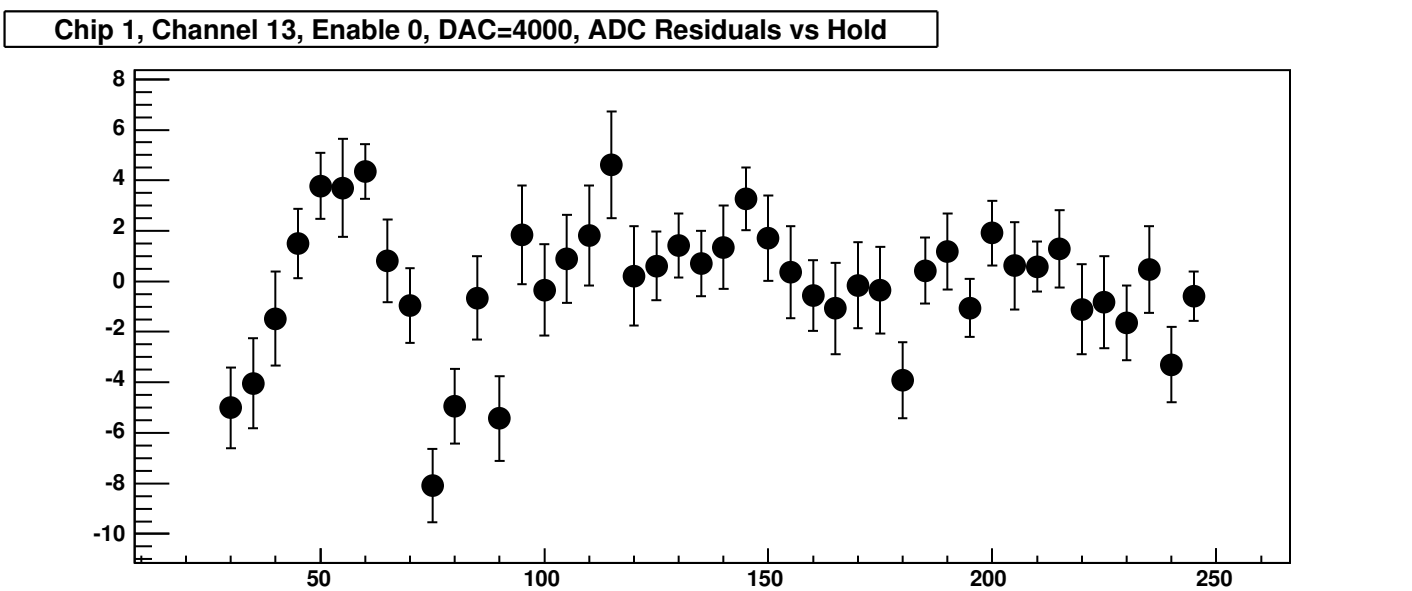

Chip 1, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 140.8 / 32
p0 -711.4 ± 1.033
p1 4.817 ± 0.02946
p2 1.083e+04 ± 1.921
p3 31.89 ± 0.01155
10000
8000
6000
4000
2000
0
20
40
60
80
100
120
140
160
180
200
220

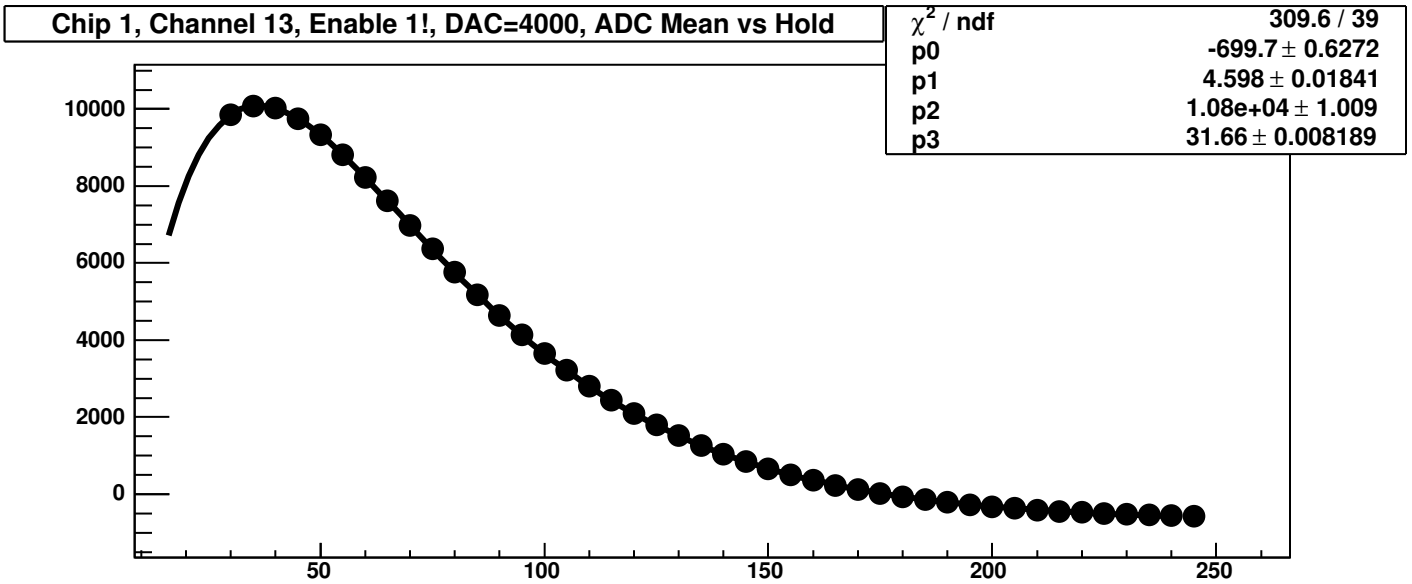

Chip 1, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
0
20
40
60
80
100
120
140
160
180
200
220

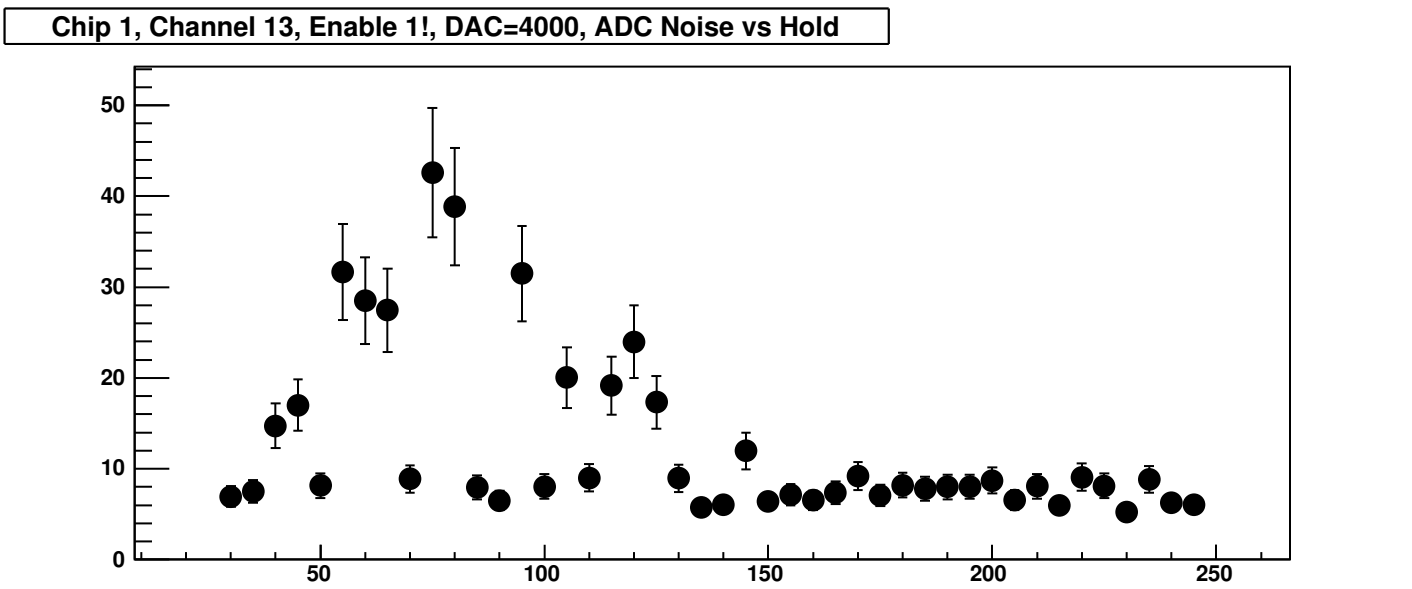

Chip 1, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
20
40
60
80
100
120
140
160
180
200
220

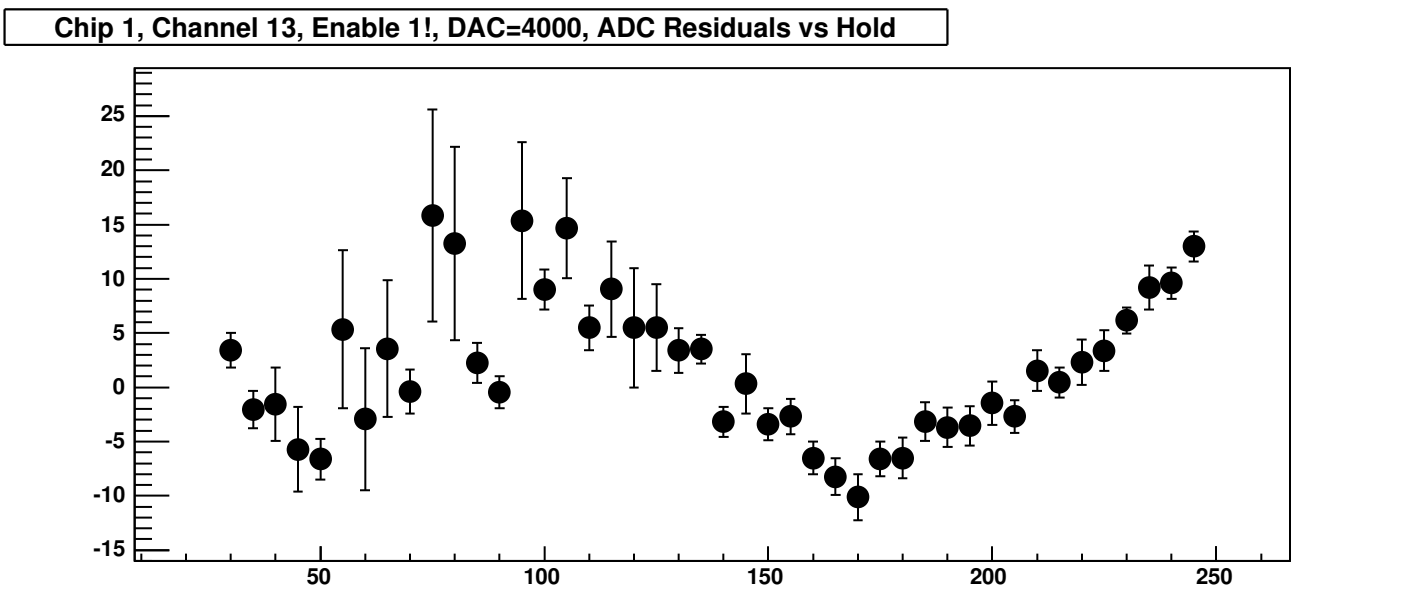

Chip 1, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 135.9 / 38
p0 143.2 ± 0.5555
p1 76.68 ± 0.7848
p2 -2.442e+07 ± 6.846e+05
p3 1.786e+07 ± 2.618e+05
p4 1.31 ± 0.03472
150
140
130
120
110
100
90
80
70
50
100
150
200
250


Chip 1, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
50
100
150
200
250

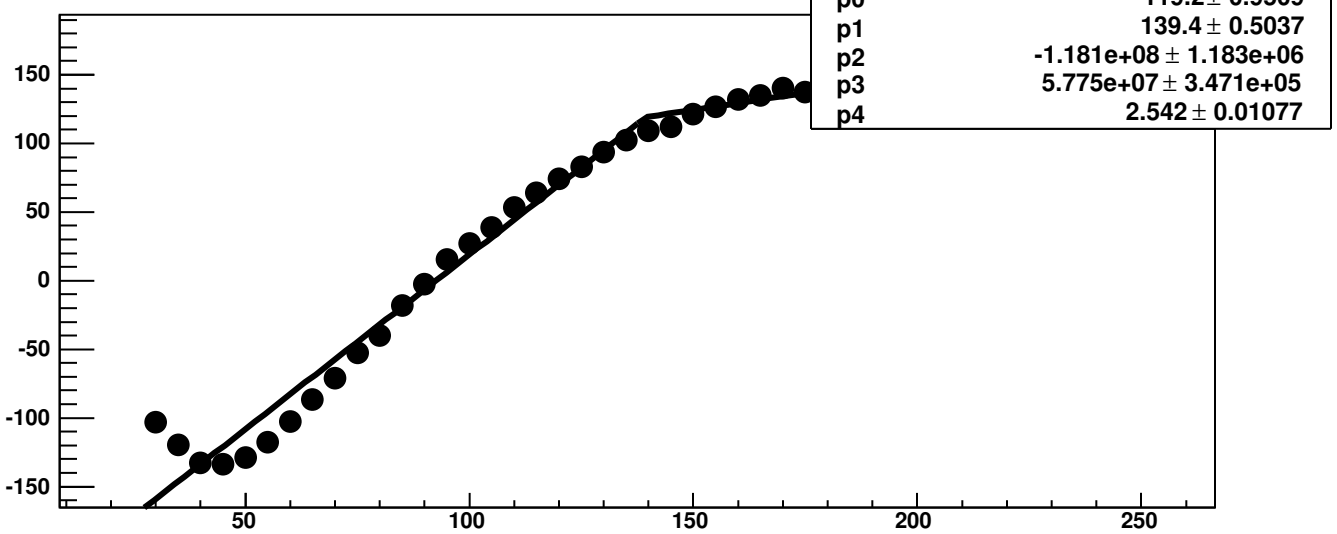

Chip 1, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
50
100
150
200
250


Chip 1, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 309.6 / 39
p0 -699.7 ± 0.6272
p1 4.598 ± 0.01841
p2 1.08e+04 ± 1.009
p3 31.66 ± 0.008189
10000
8000
6000
4000
2000
0
50
100
150
200
250

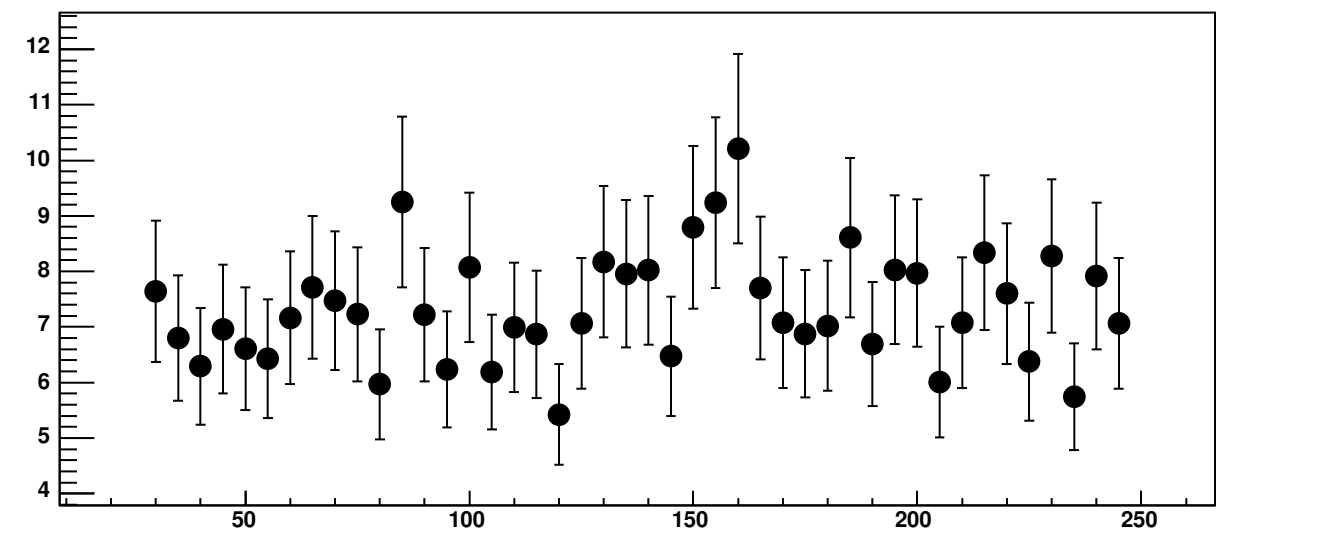

Chip 1, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
0
50
100
150
200
250


Chip 1, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
-15
50
100
150
200
250

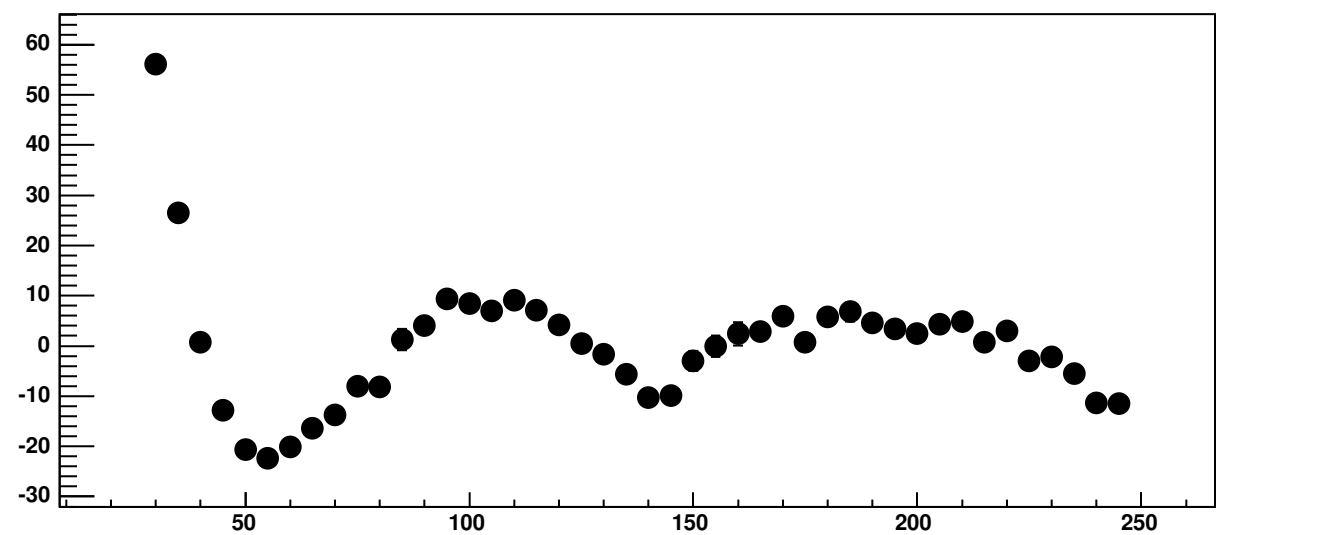
Chip 1, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

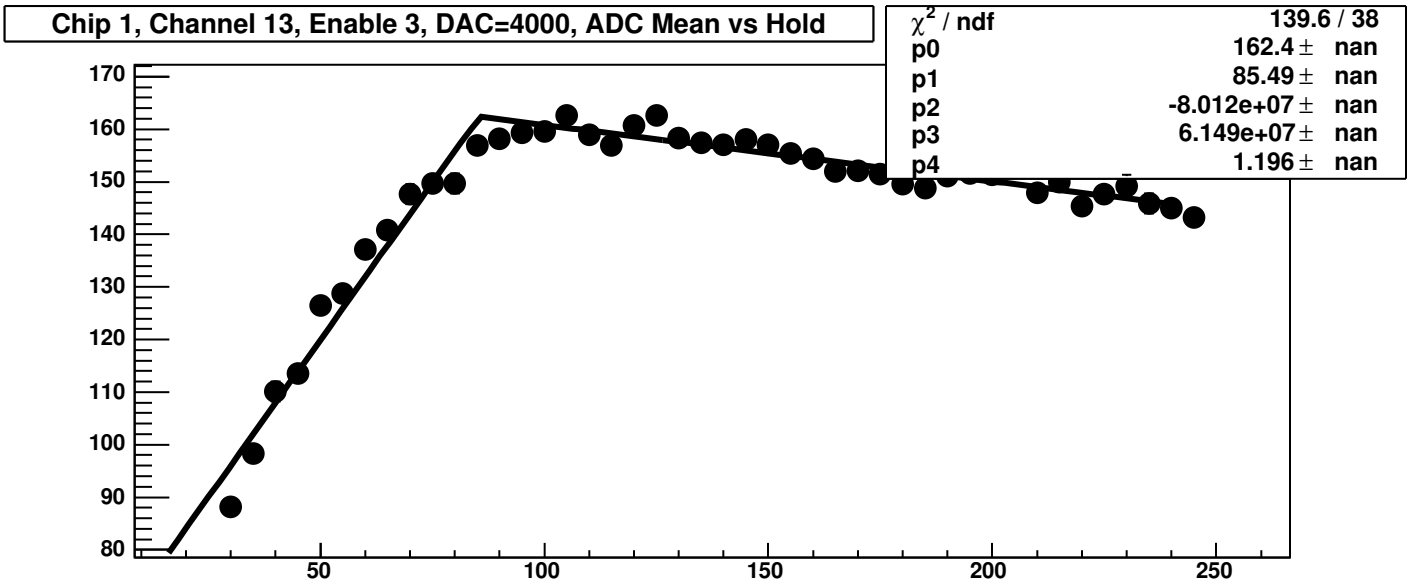


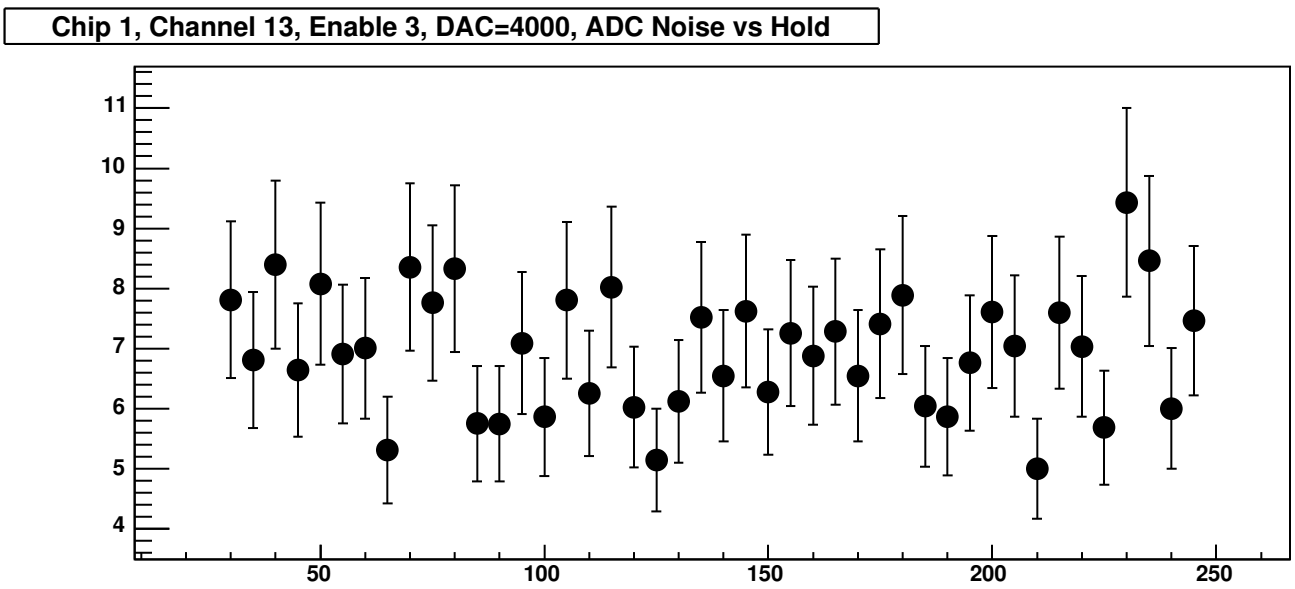
Chip 1, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold

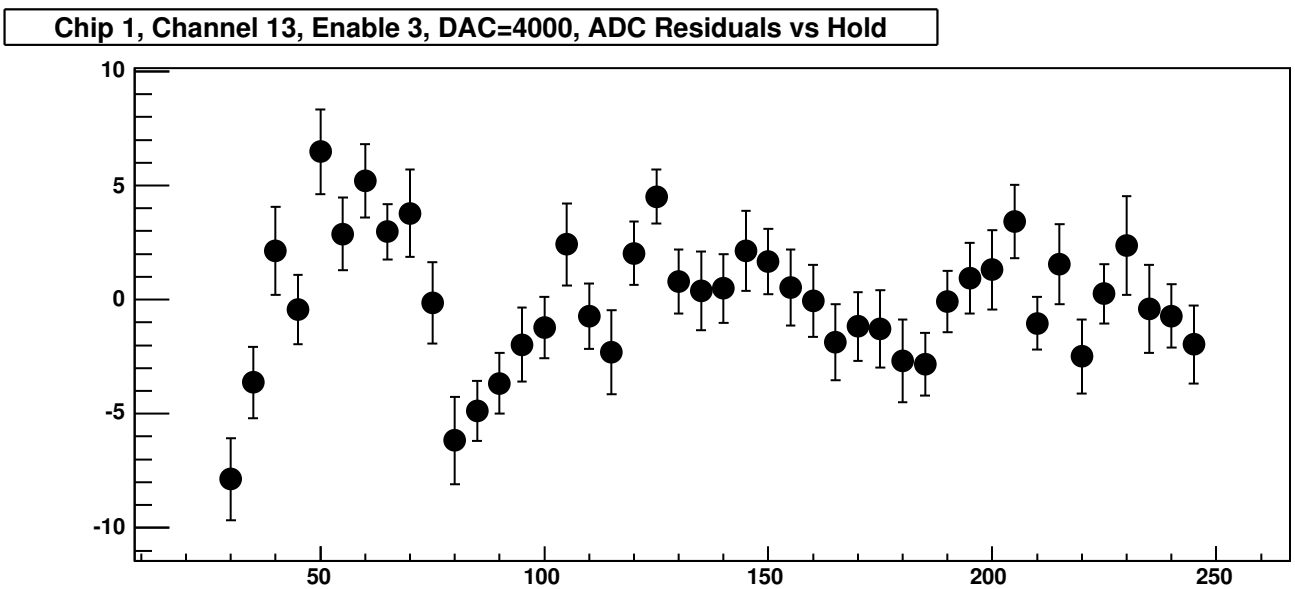


Chip 1, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

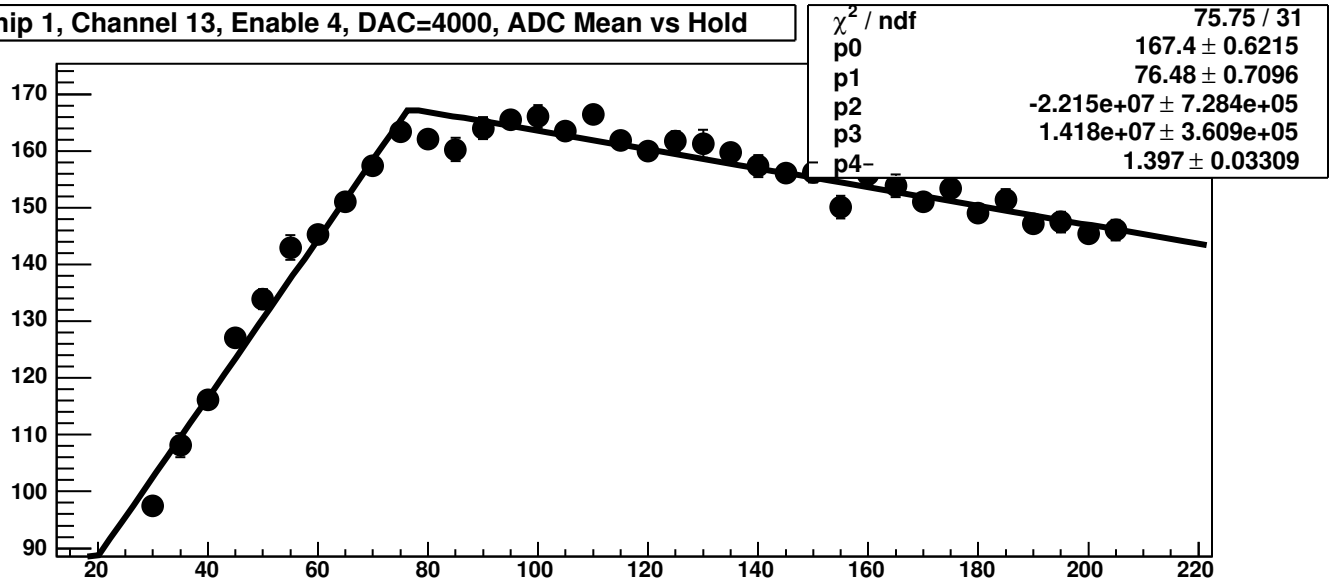



Chip 1, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 139.6 / 38
p0 162.4 ± nan
p1 85.49 ± nan
p2 -8.012e+07 ± nan
p3 6.149e+07 ± nan
p4 1.196 ± nan
170
160
150
140
130
120
110
100
90
80
50
100
150
200
250

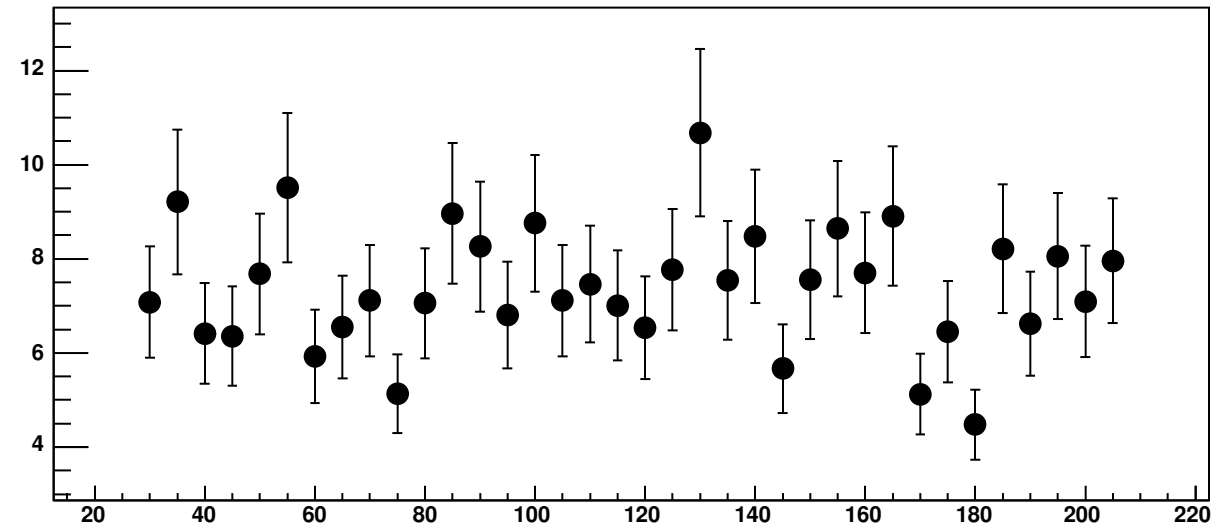

Chip 1, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 1, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

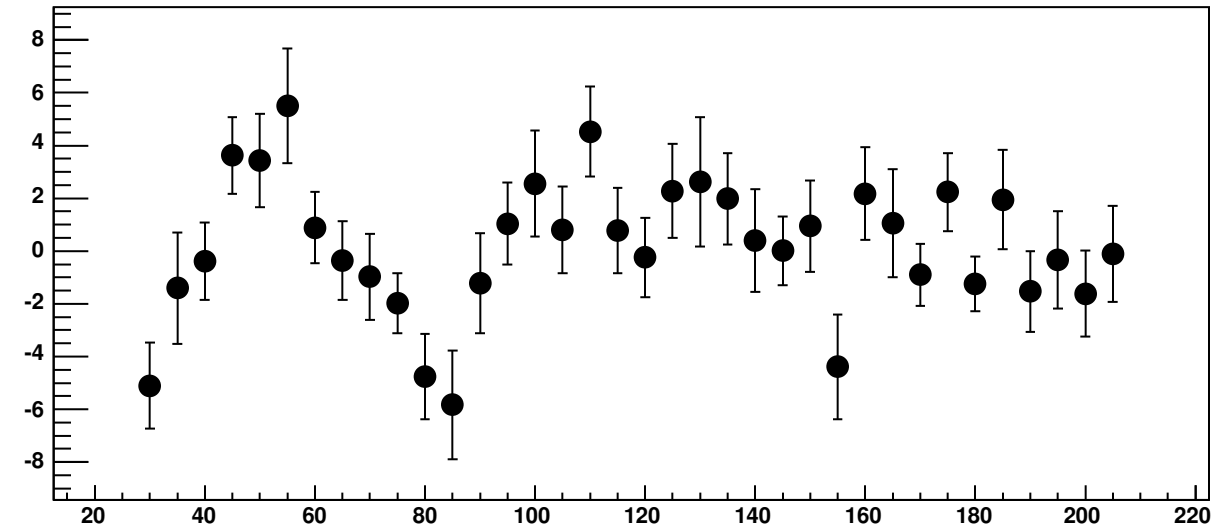
Chip 1, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold



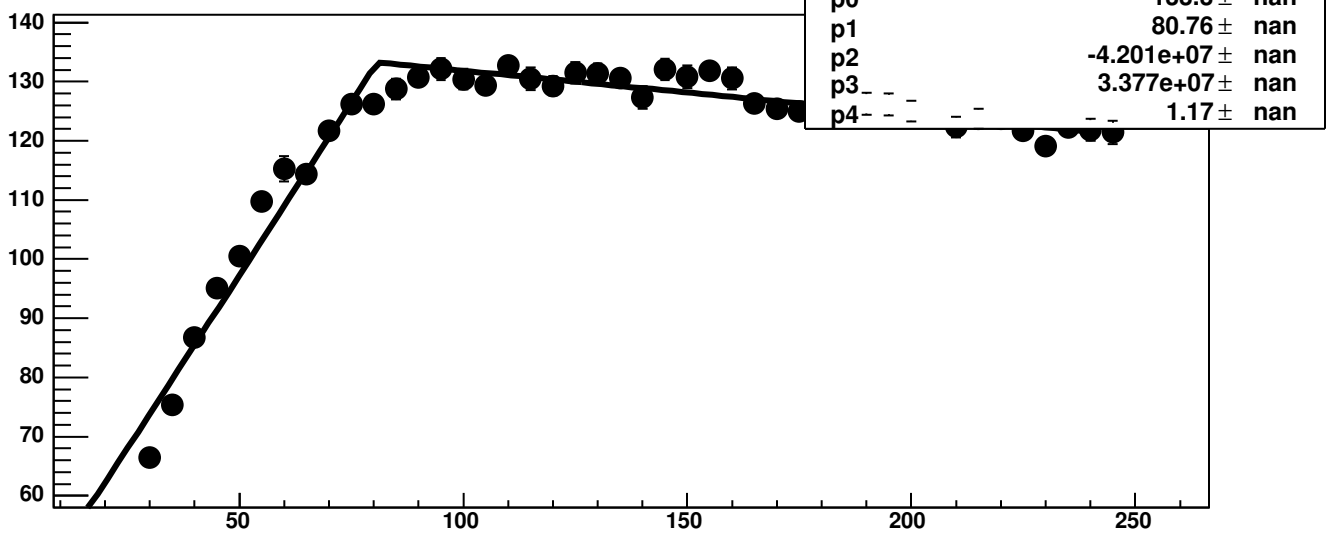
Chip 1, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold



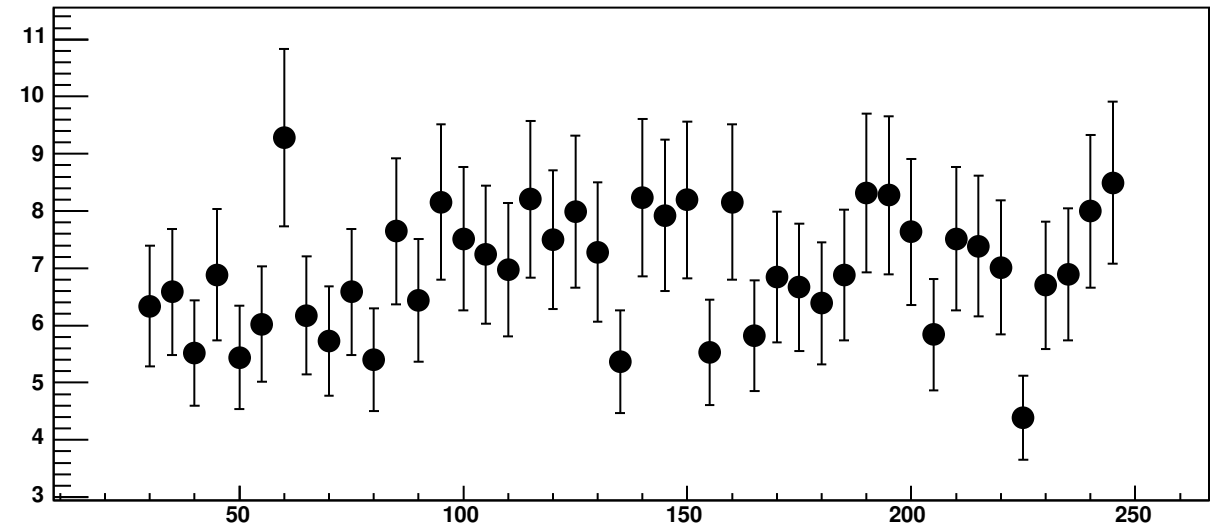
Chip 1, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold



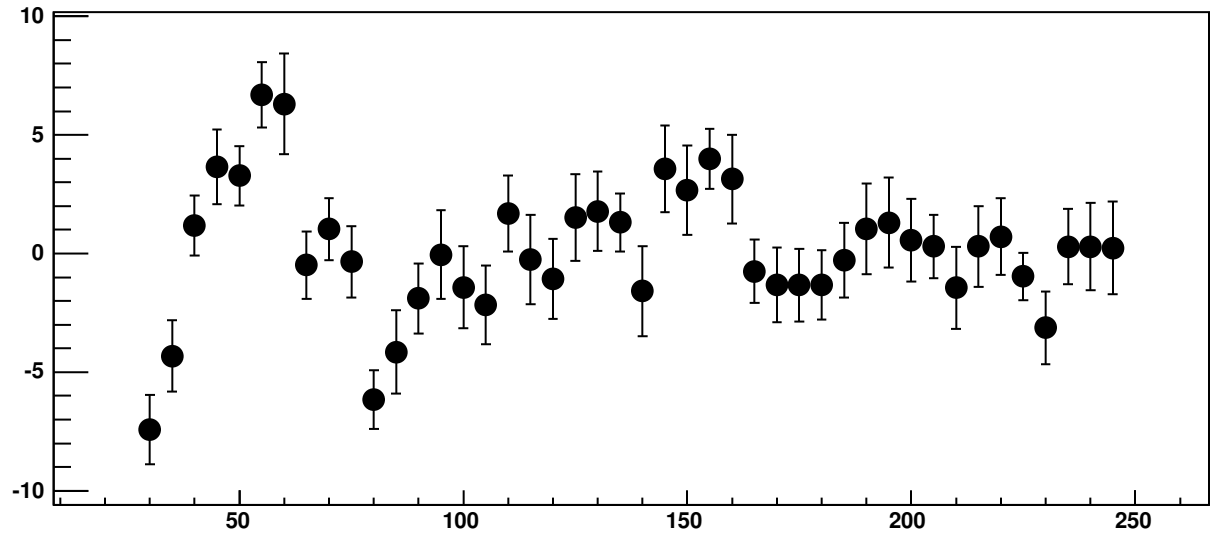
Chip 1, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold



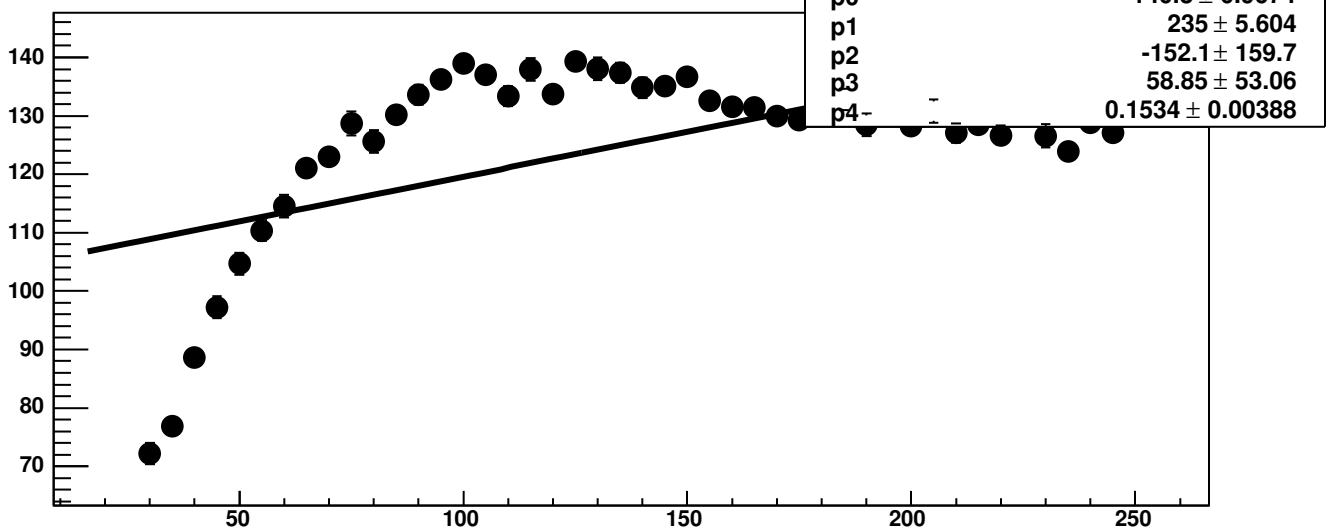
Chip 1, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



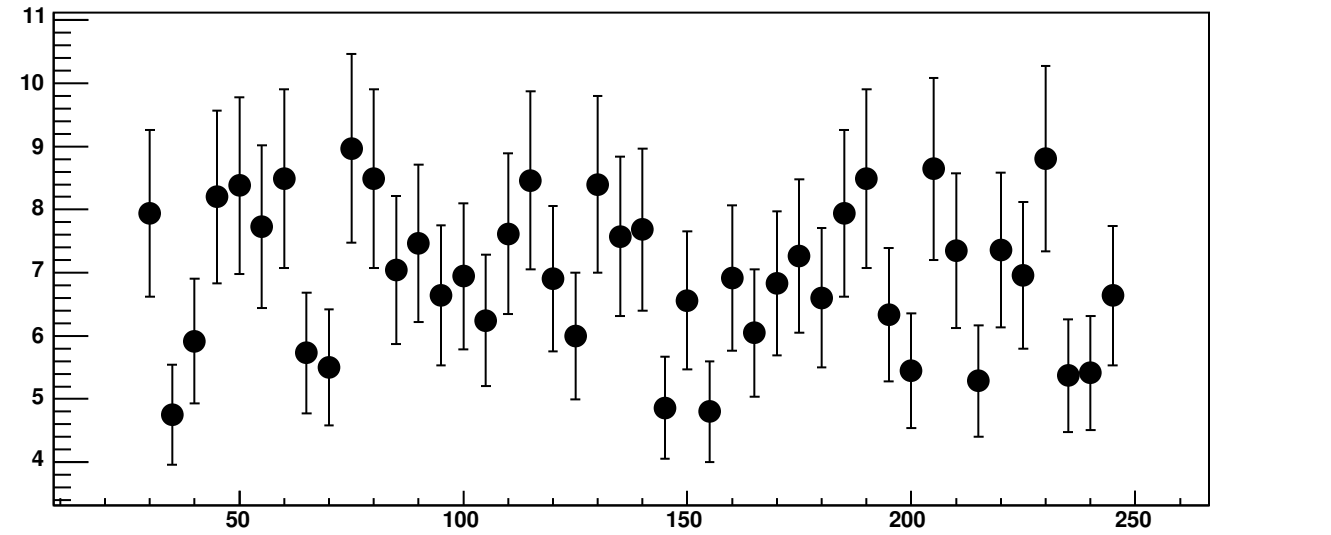
Chip 1, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold



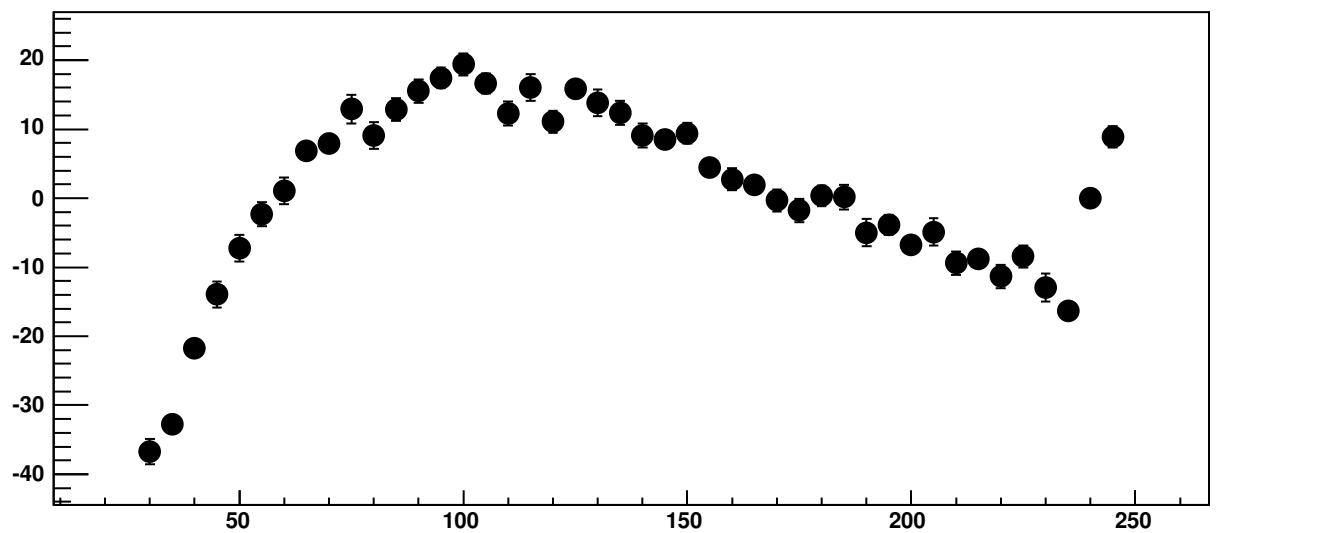
Chip 1, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold



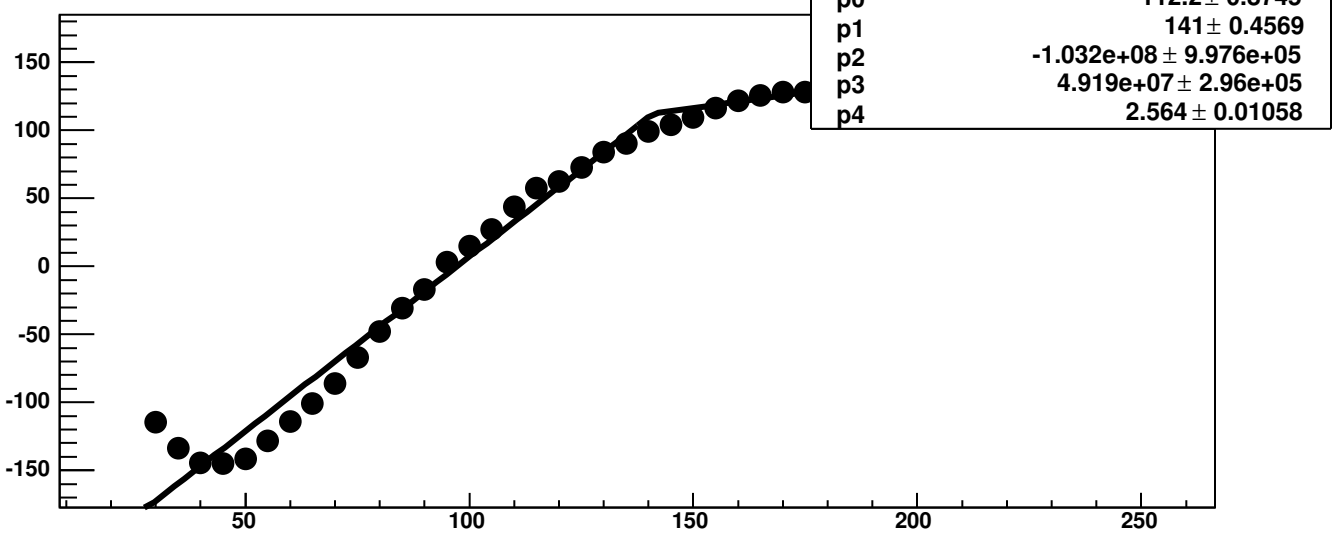
Chip 1, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold



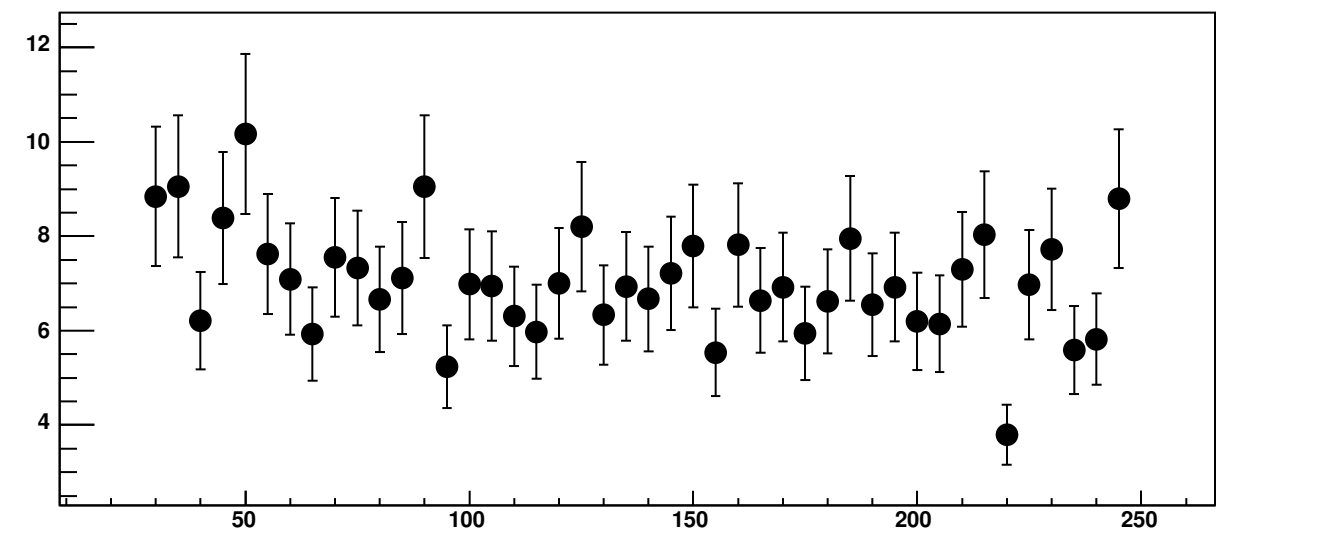
Chip 1, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold



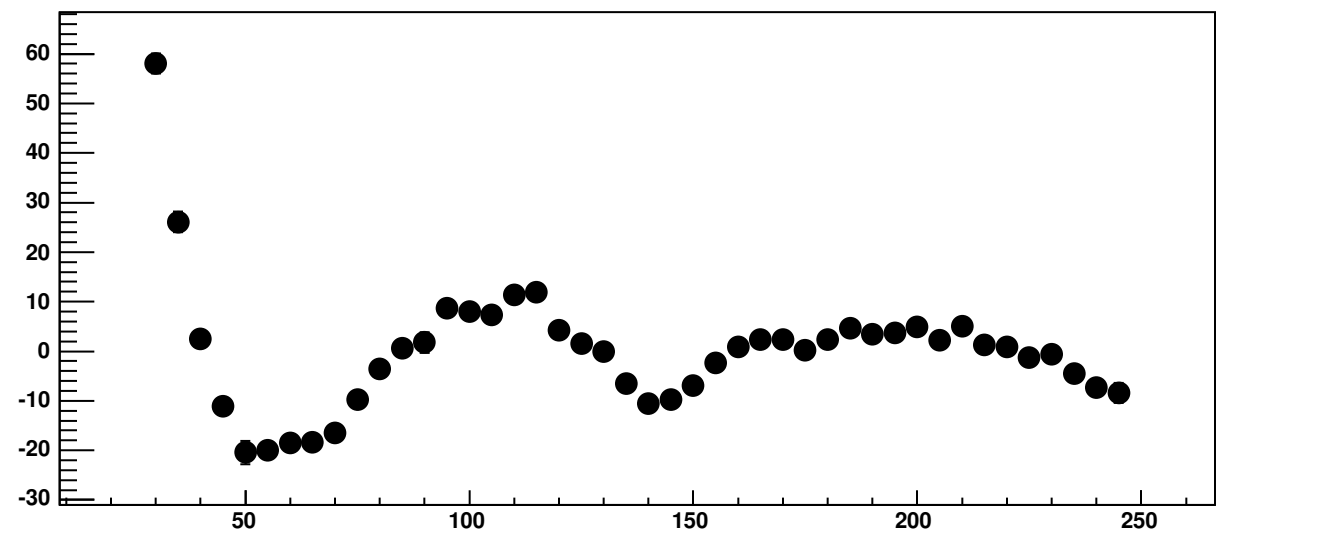
Chip 1, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold



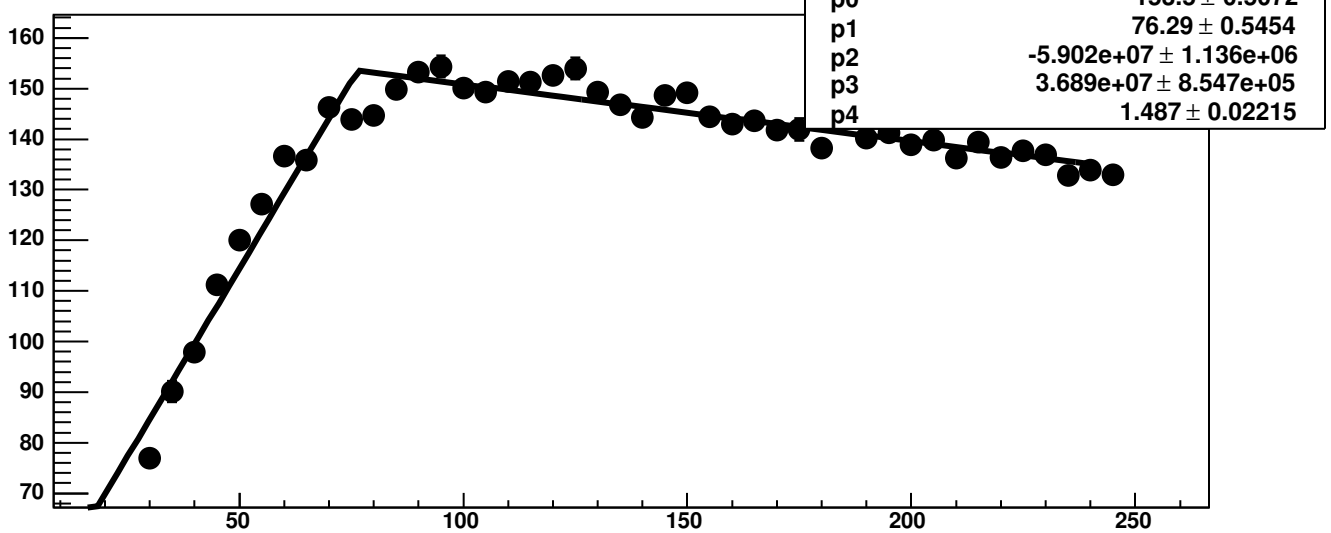
Chip 1, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



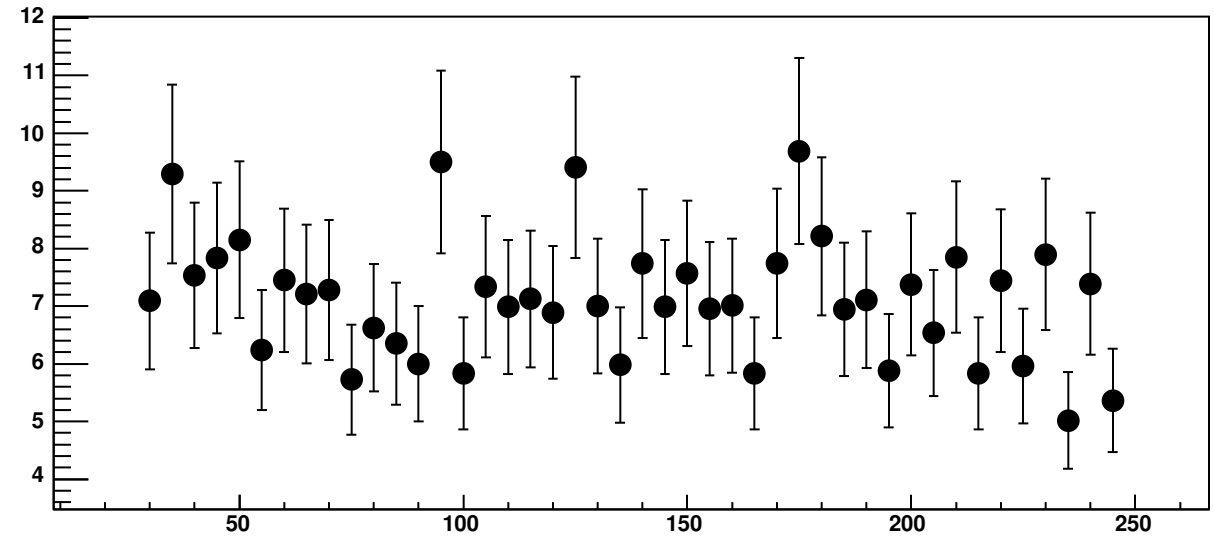
Chip 1, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold



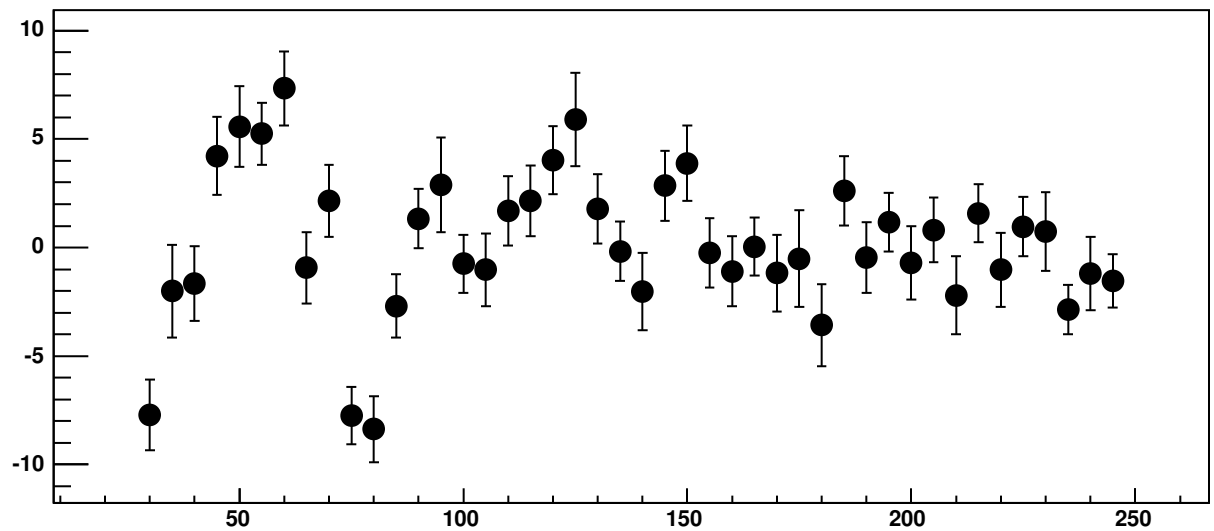
Chip 1, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

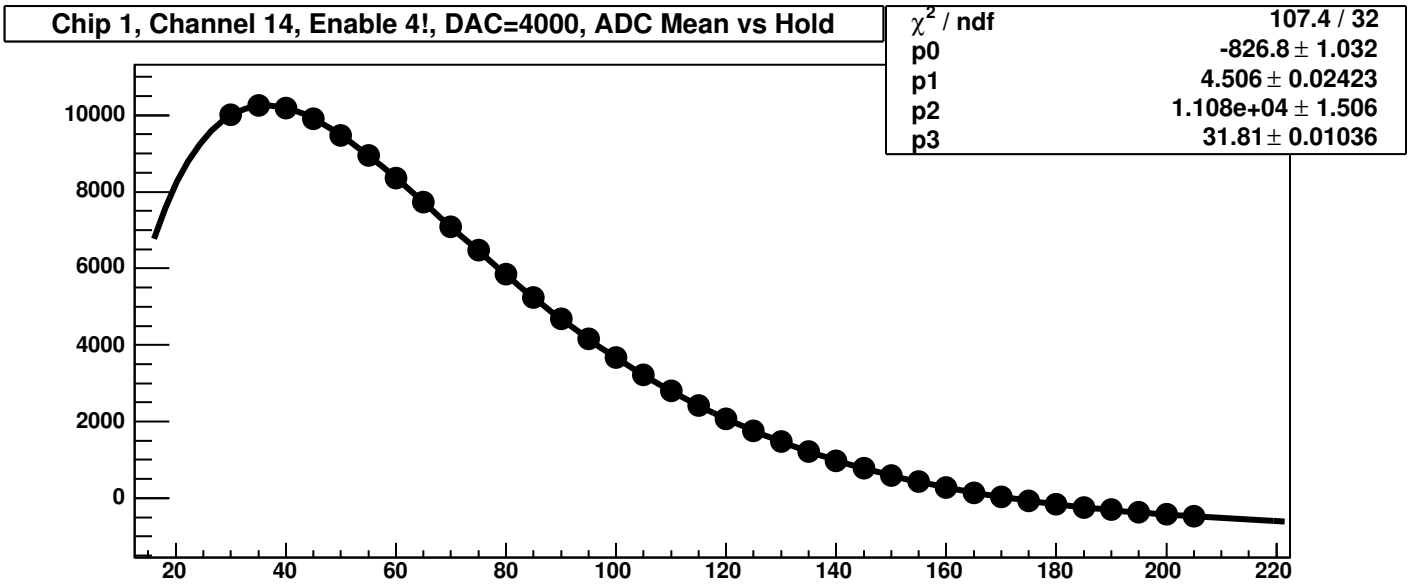


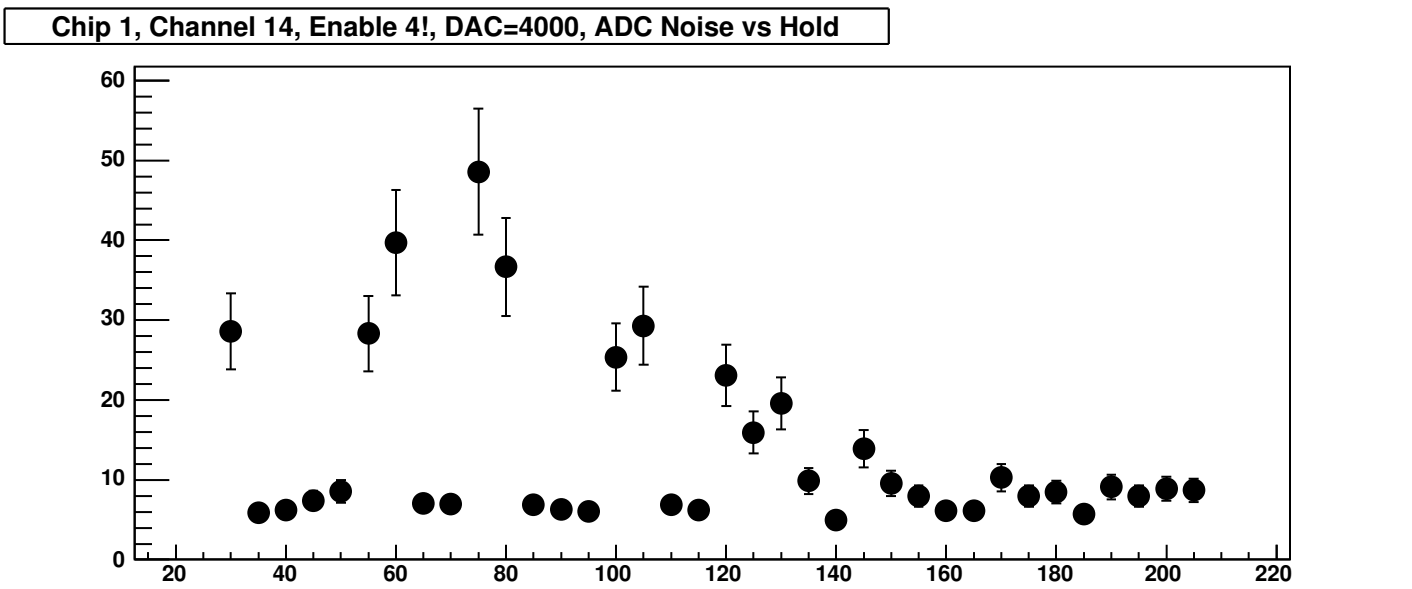
Chip 1, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold

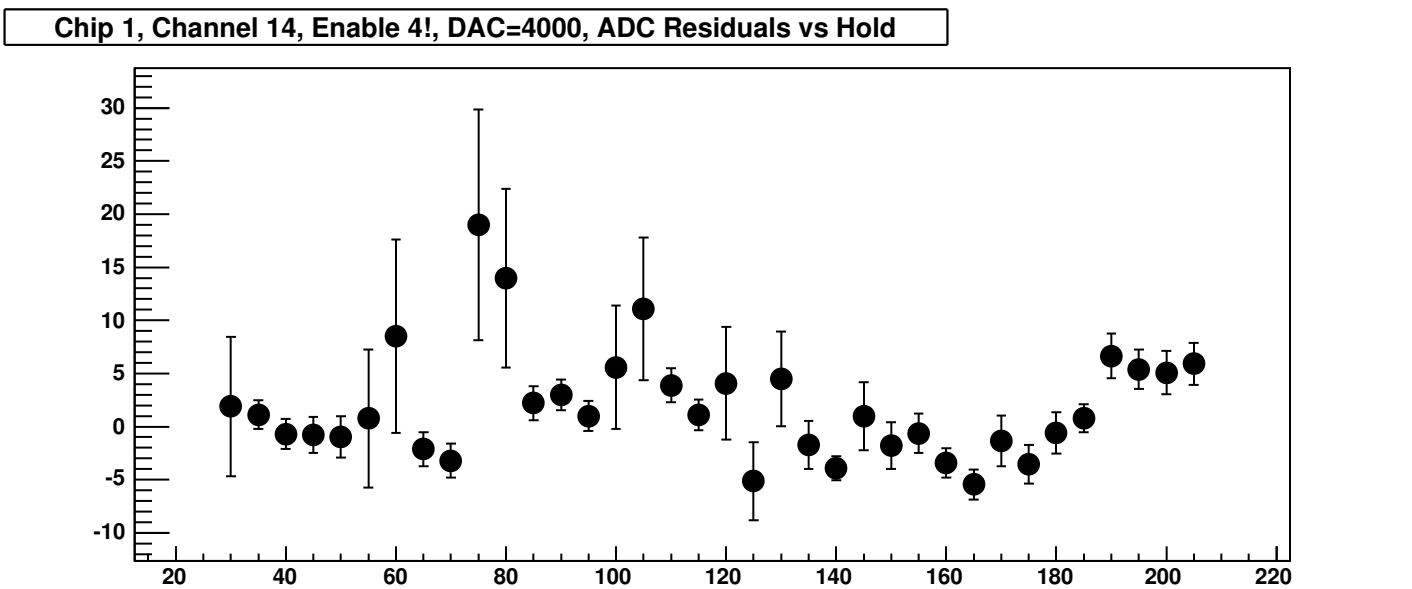


Chip 1, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

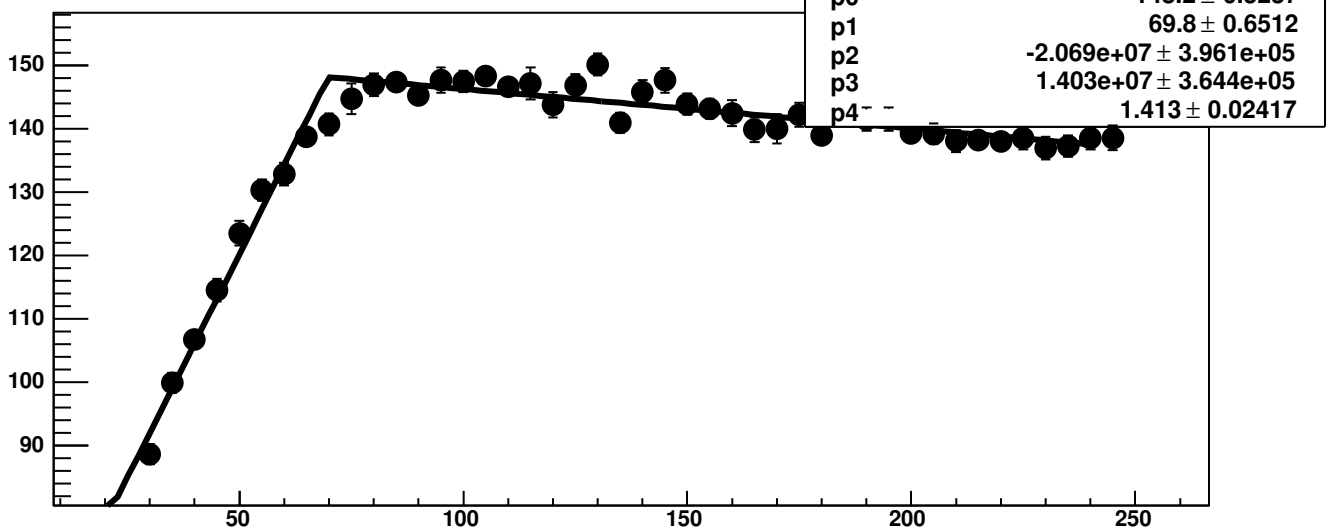



Chip 1, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 107.4 / 32
p0 -826.8 ± 1.032
p1 4.506 ± 0.02423
p2 1.108e+04 ± 1.506
p3 31.81 ± 0.01036
10000
8000
6000
4000
2000
0
20
40
60
80
100
120
140
160
180
200
220

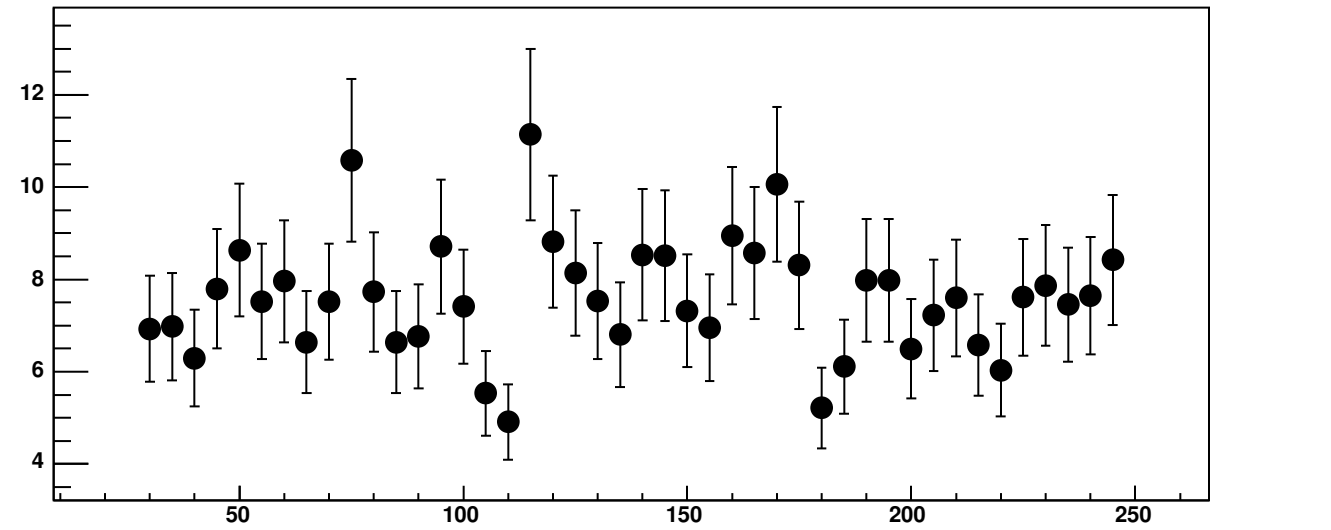

Chip 1, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold
60
50
40
30
20
10
0
20
40
60
80
100
120
140
160
180
200
220


Chip 1, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
20
40
60
80
100
120
140
160
180
200
220

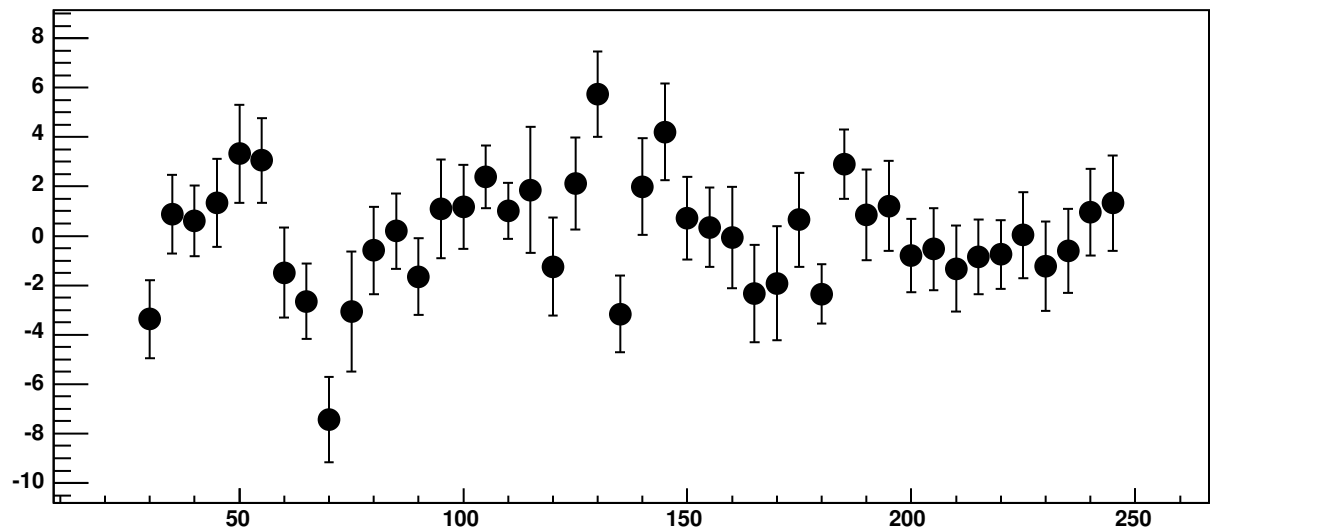
Chip 1, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold



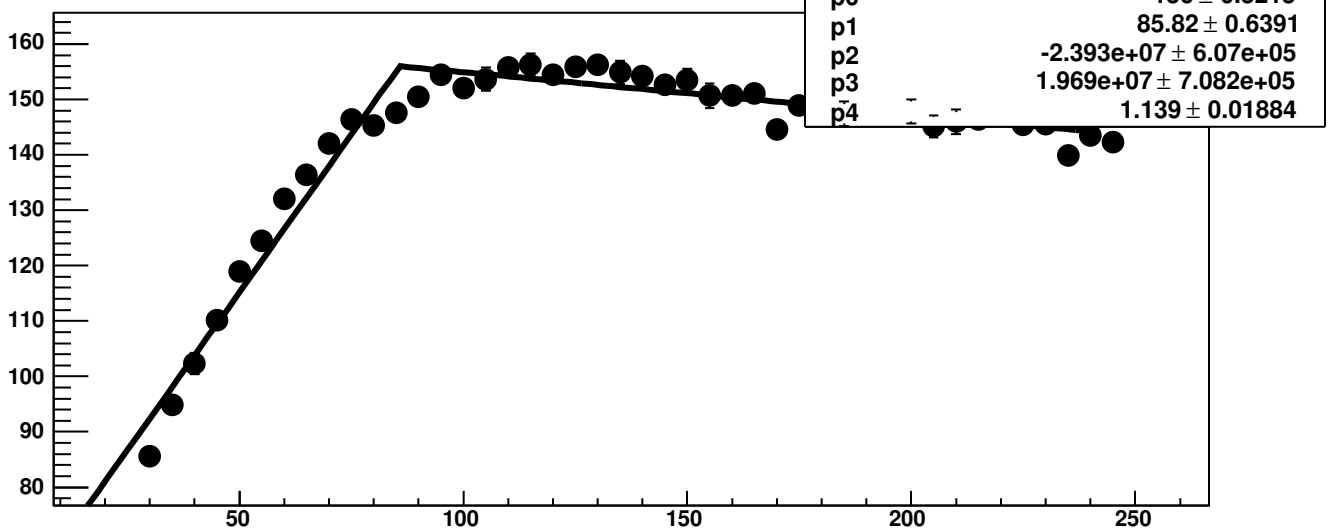
Chip 1, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



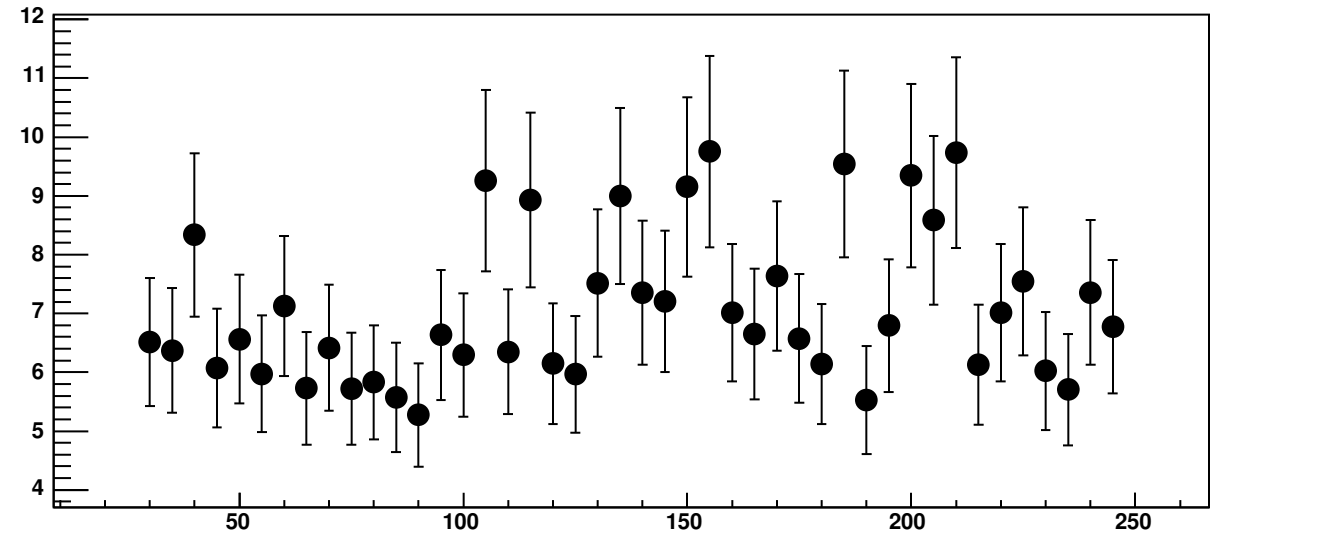
Chip 1, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold



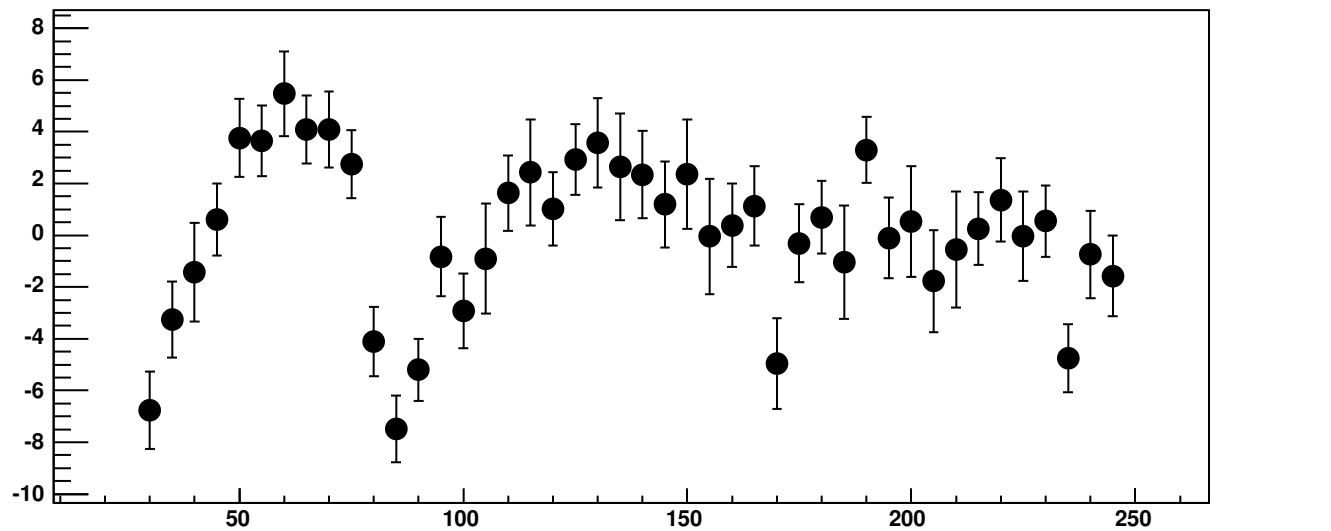
Chip 1, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

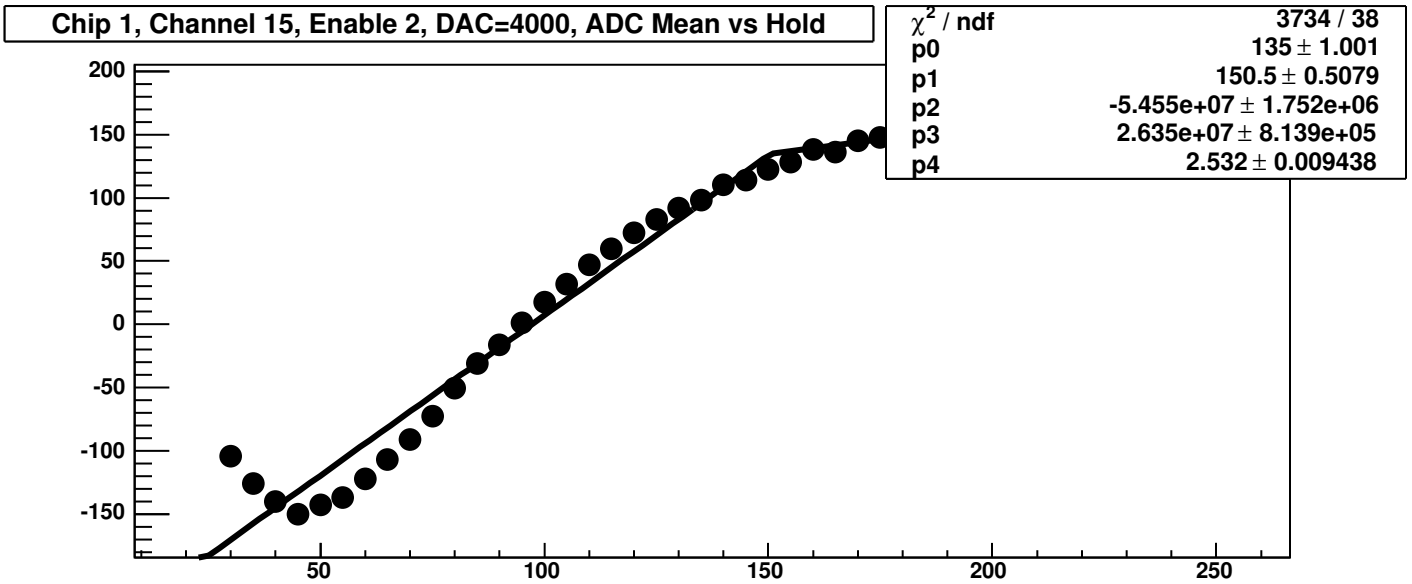


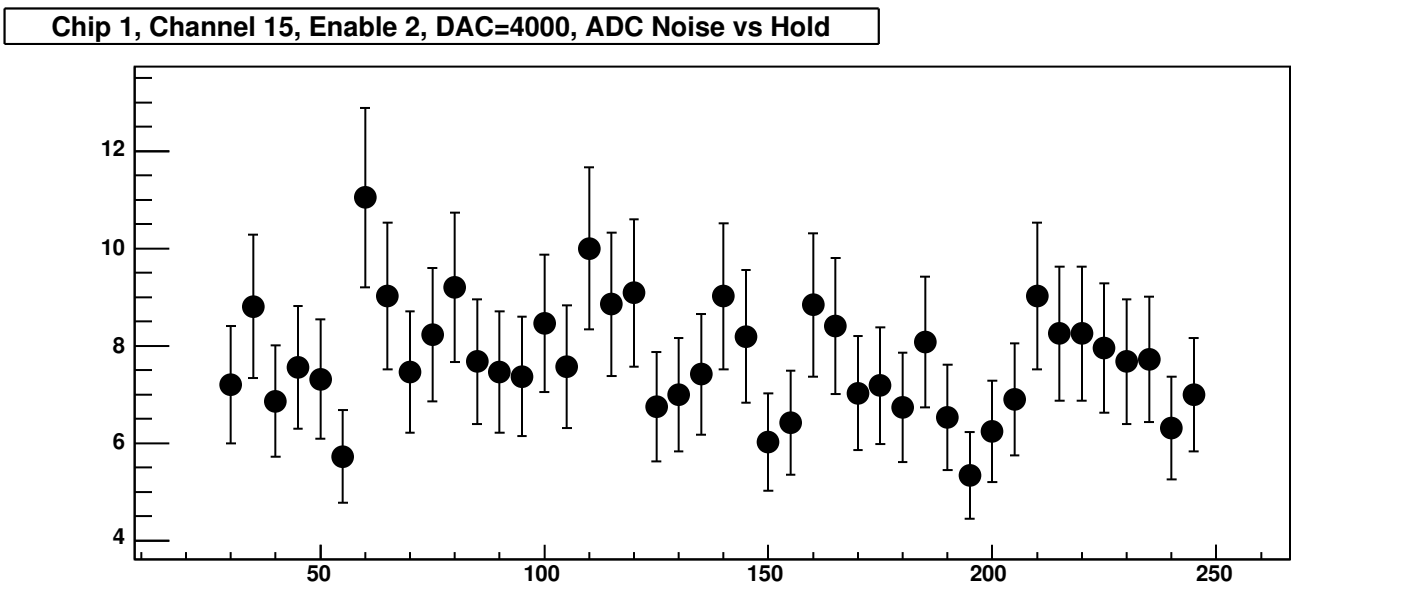
Chip 1, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold

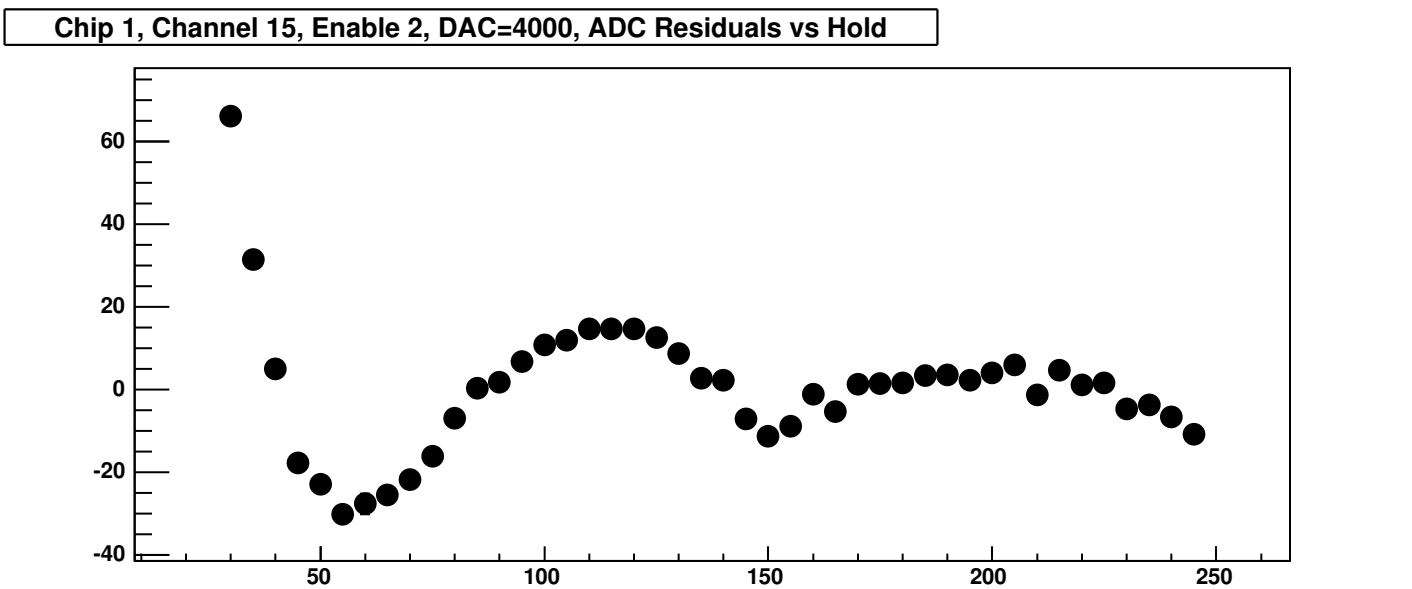


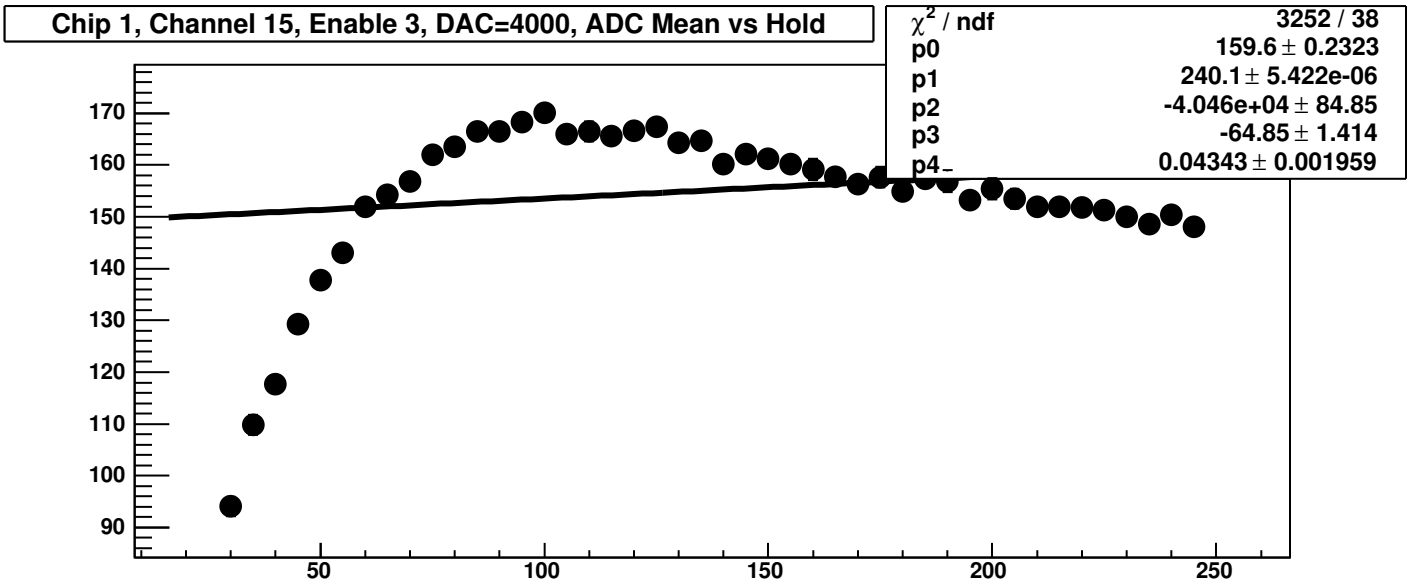
Chip 1, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

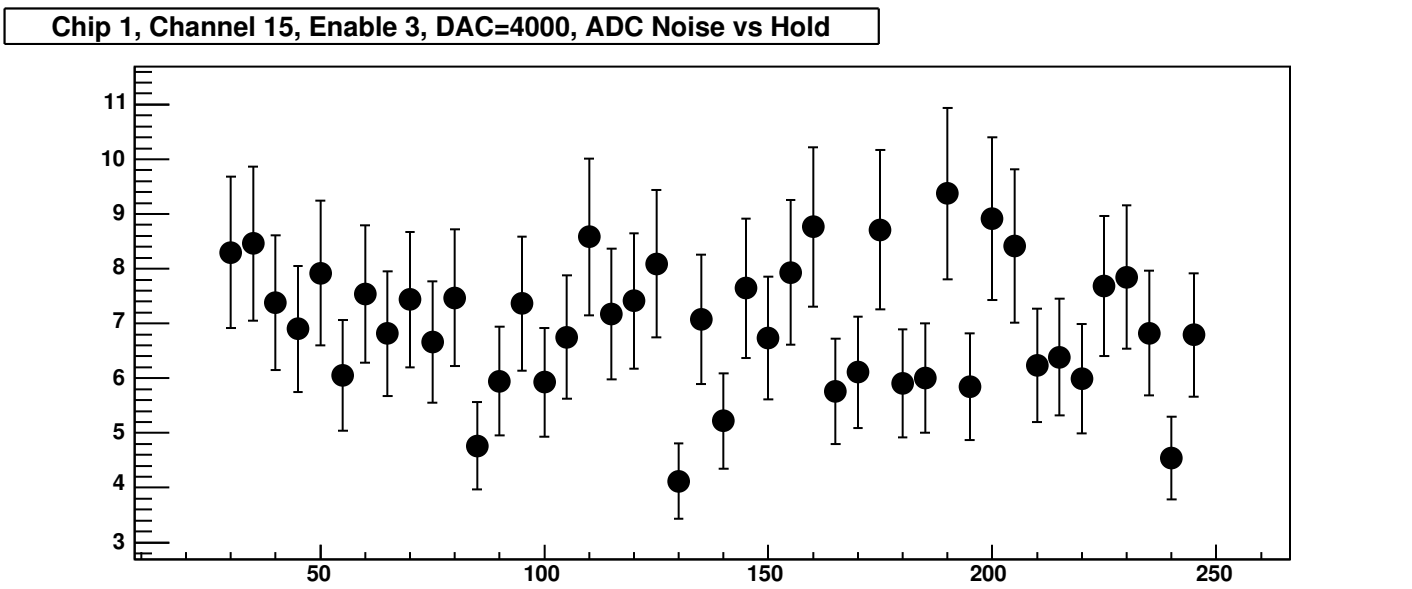


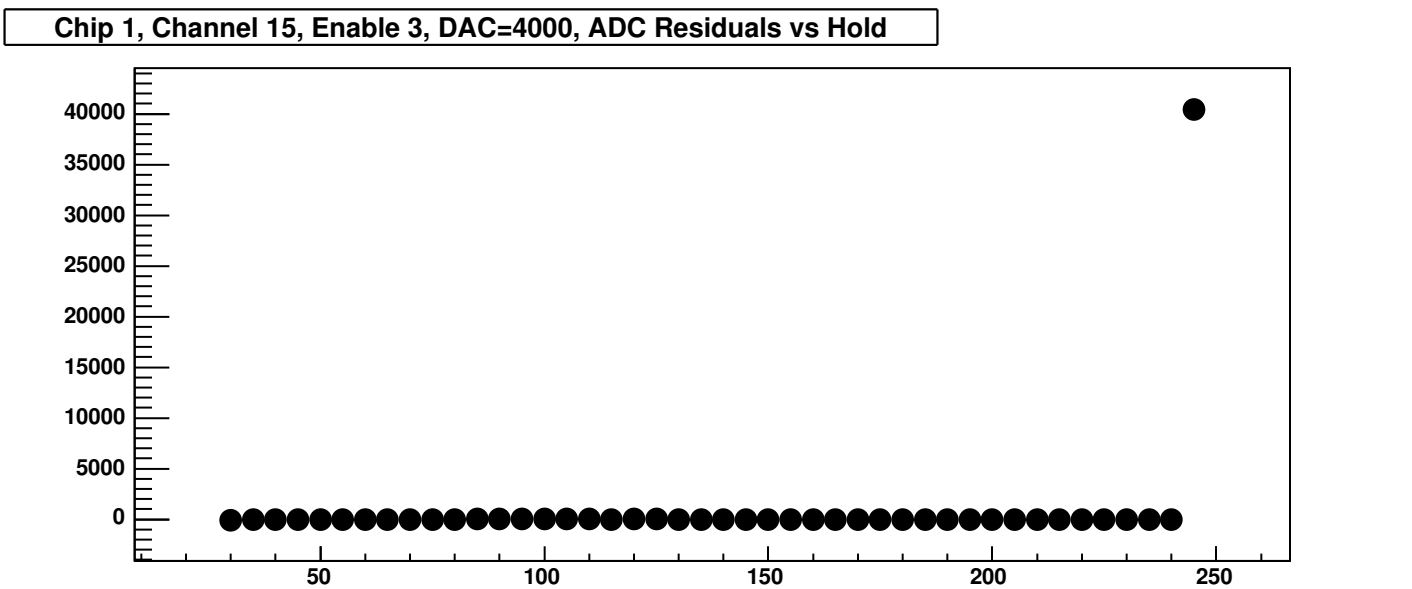

Chip 1, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 3734 / 38
p0 135 ± 1.001
p1 150.5 ± 0.5079
p2 -5.455e+07 ± 1.752e+06
p3 2.635e+07 ± 8.139e+05
p4 2.532 ± 0.009438
200
150
100
50
0
-50
-100
-150
50
100
150
200
250


Chip 1, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250

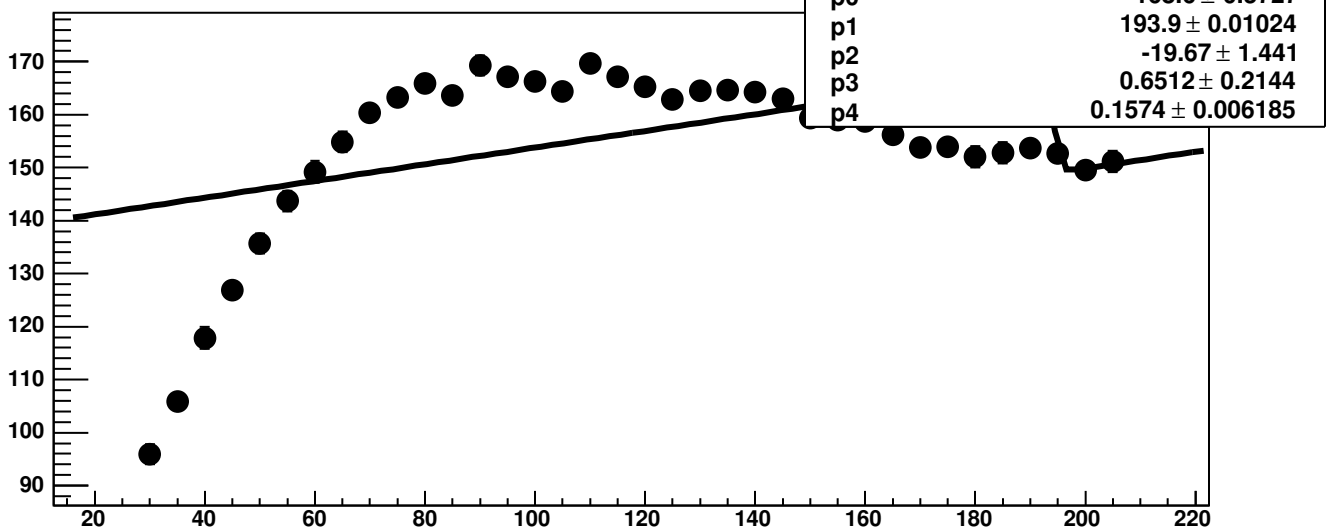

Chip 1, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold
60
40
20
0
-20
-40
50
100
150
200
250


Chip 1, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 3252 / 38
p0 159.6 ± 0.2323
p1 240.1 ± 5.422e-06
p2 -4.046e+04 ± 84.85
p3 -64.85 ± 1.414
p4 0.04343 ± 0.001959
170
160
150
140
130
120
110
100
90
50
100
150
200
250

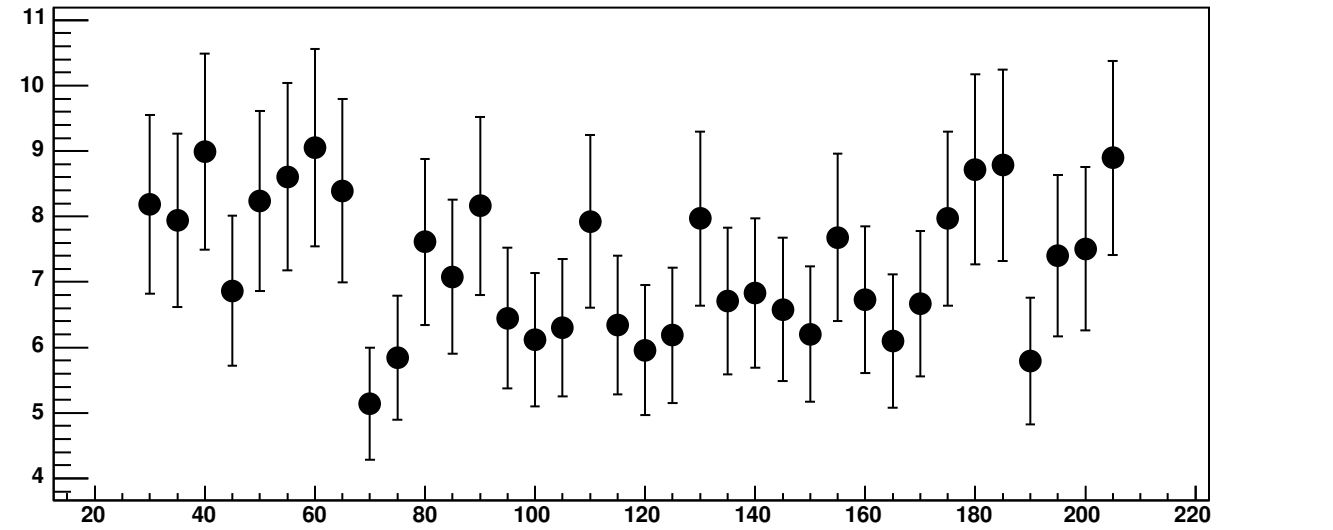

Chip 1, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
50
100
150
200
250


Chip 1, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold
40000
35000
30000
25000
20000
15000
10000
5000
0
50
100
150
200
250

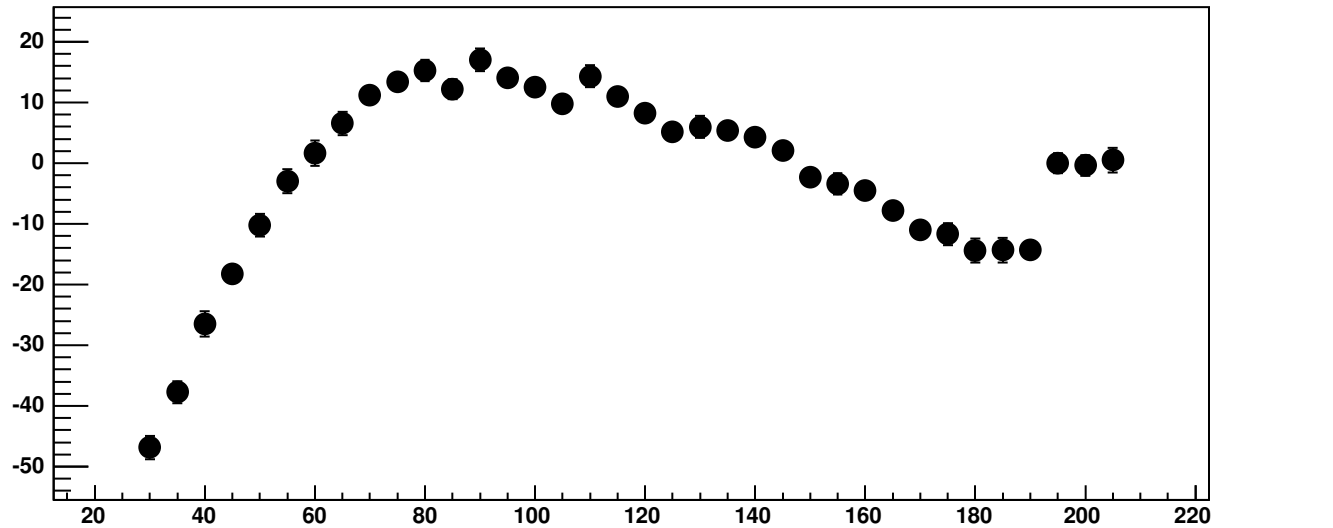
Chip 1, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold



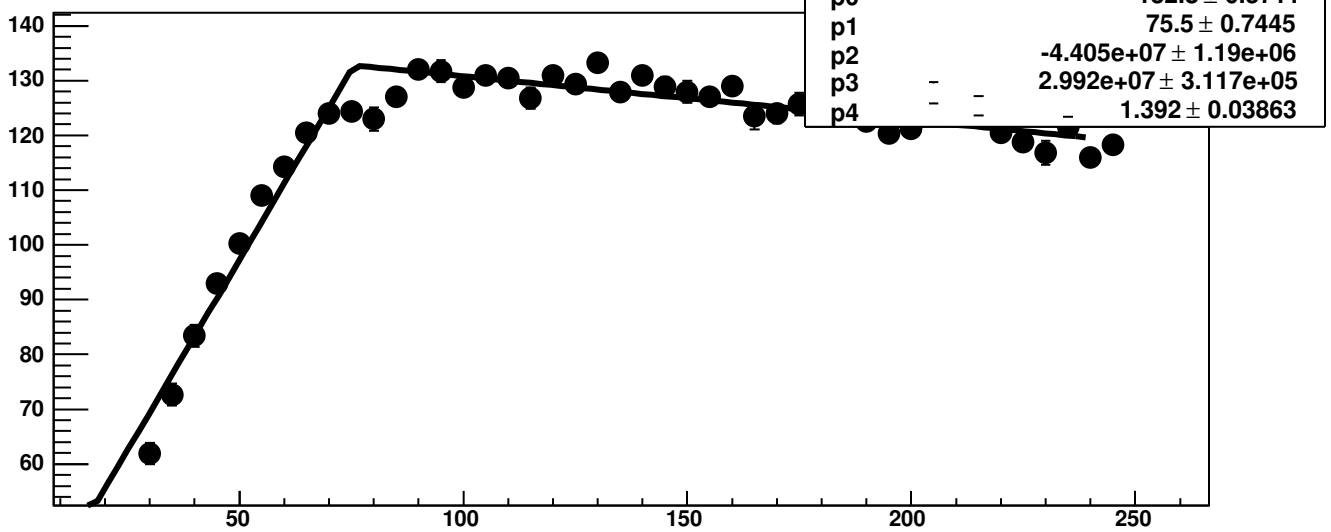
Chip 1, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



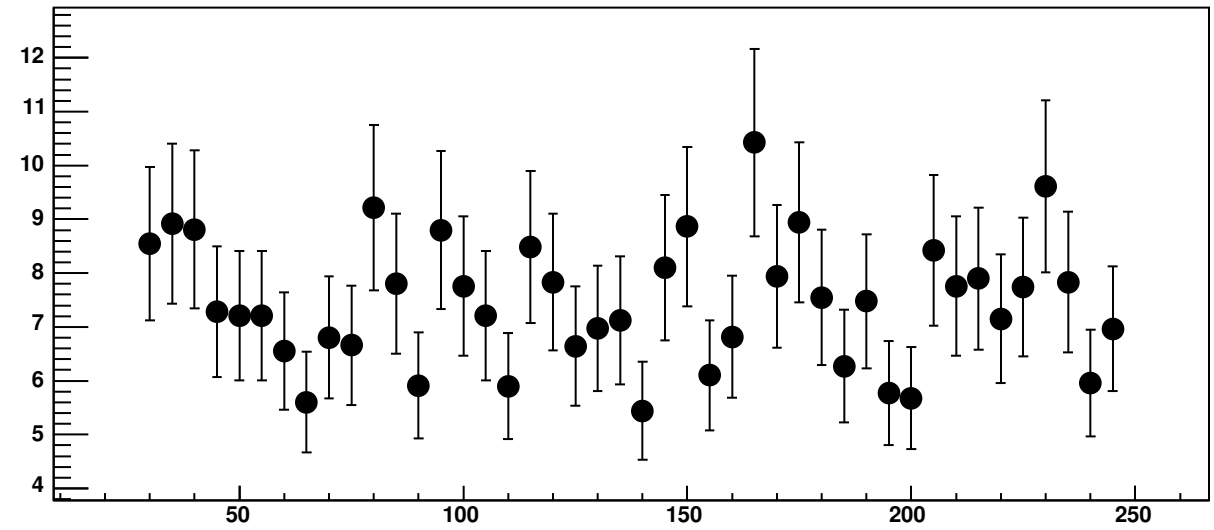
Chip 1, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold



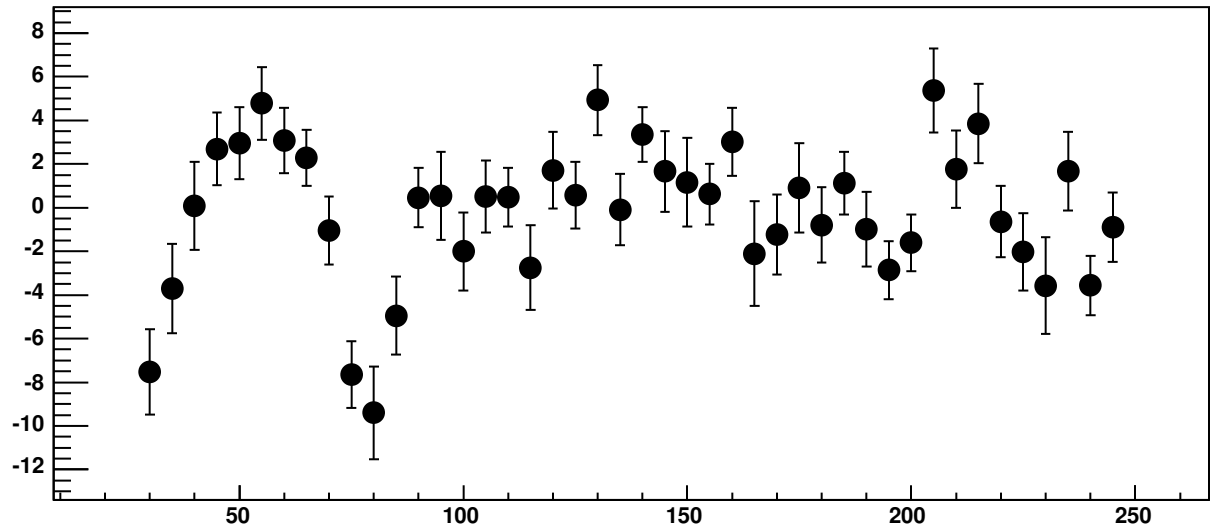
Chip 1, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

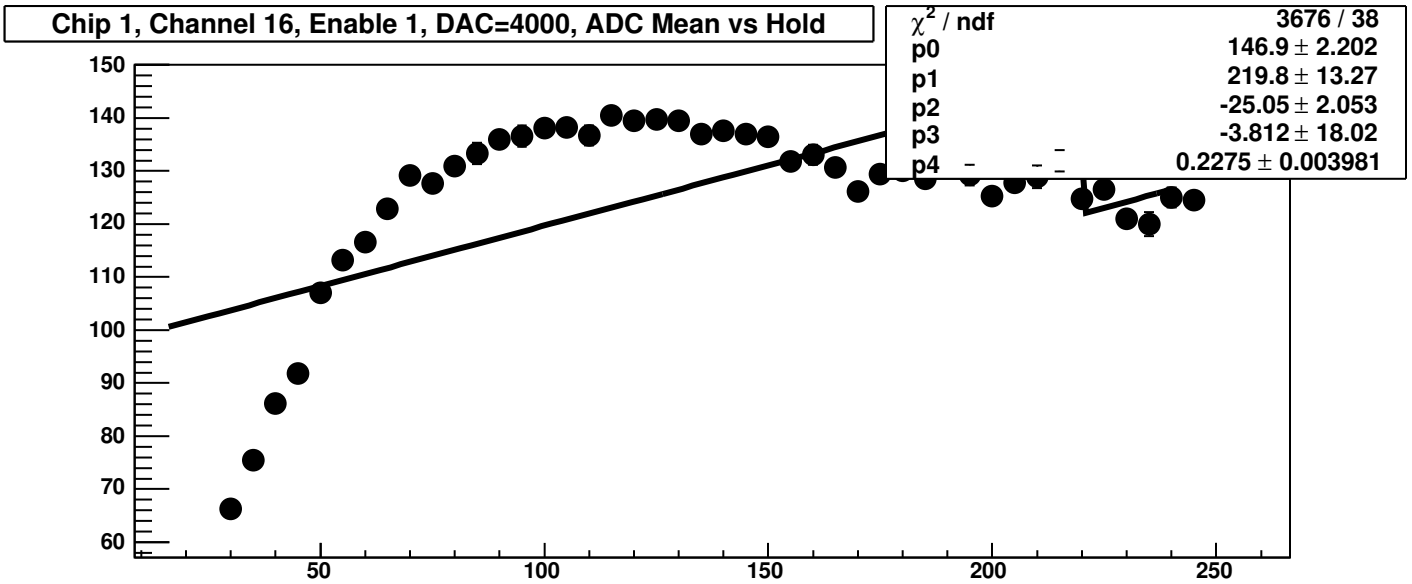


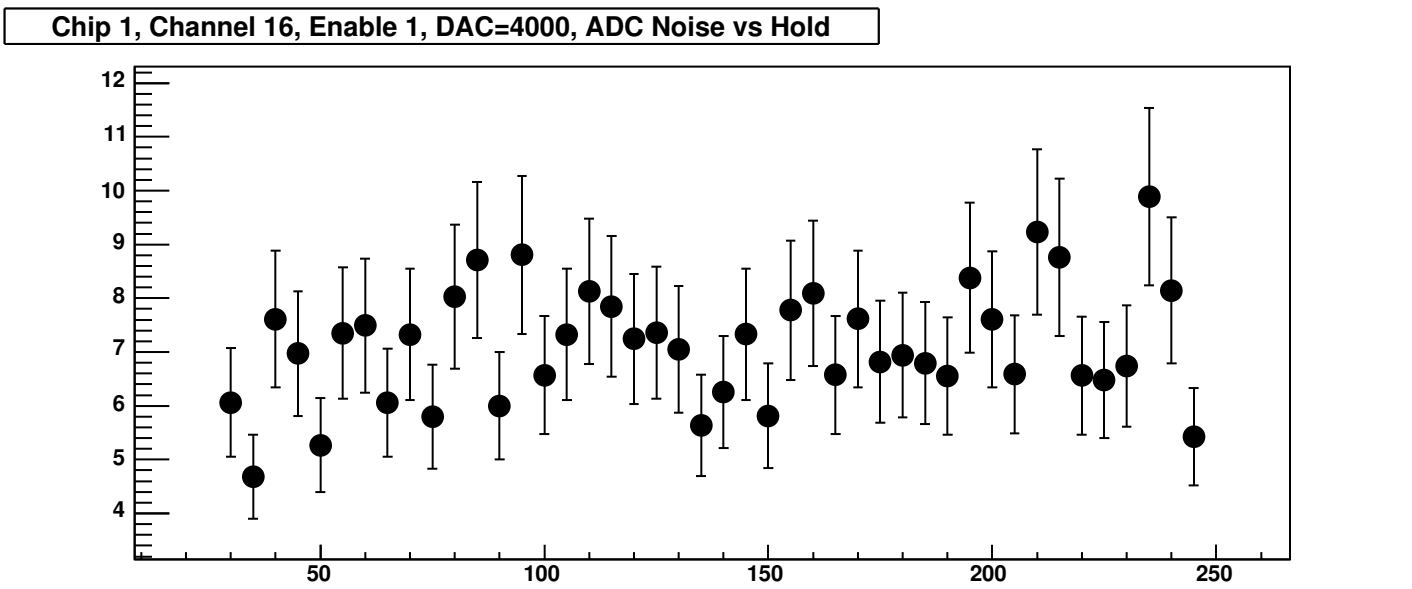
Chip 1, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold

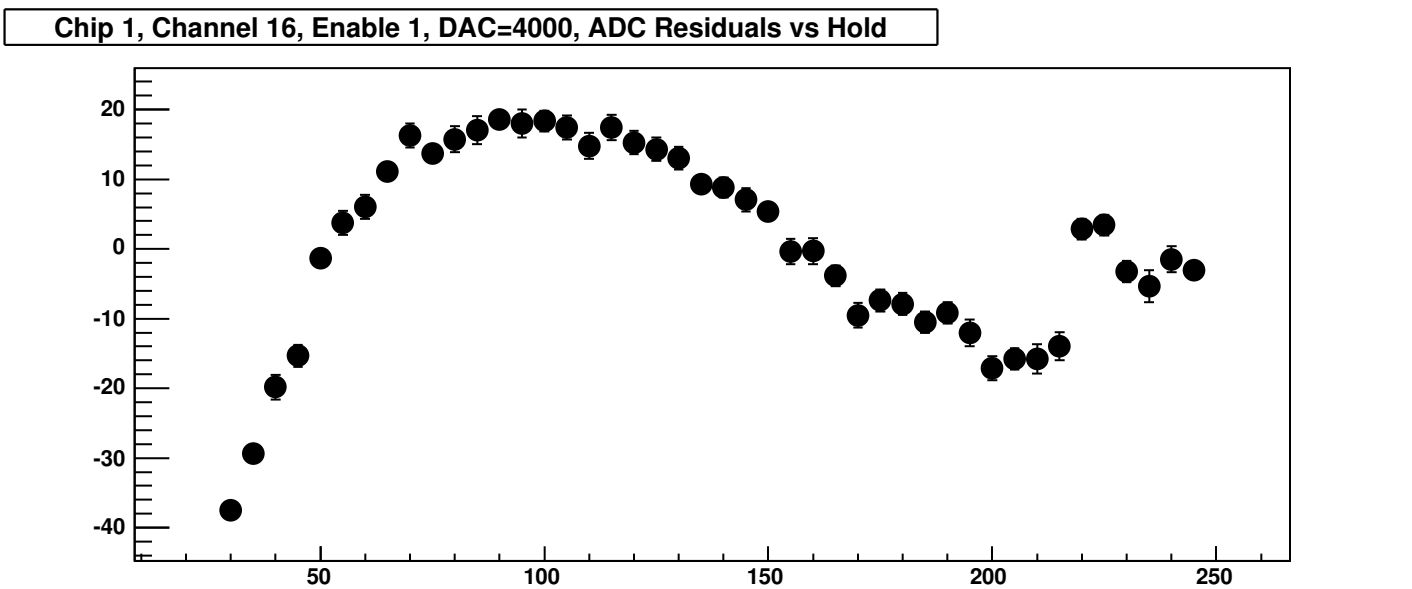


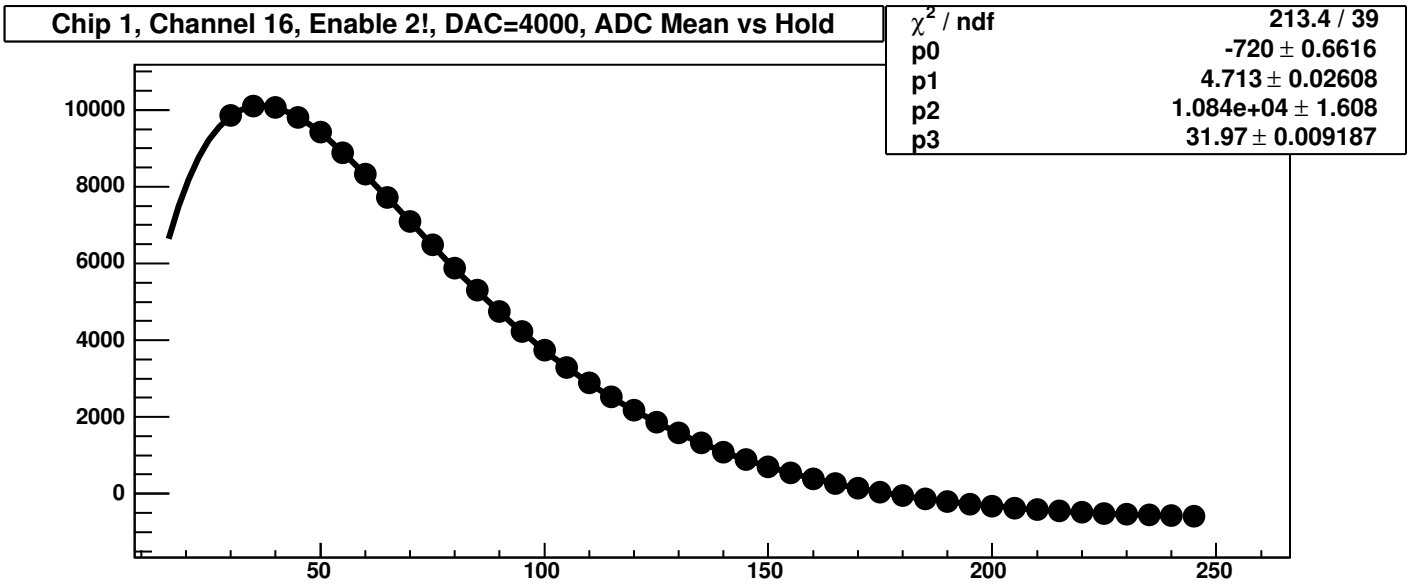
Chip 1, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

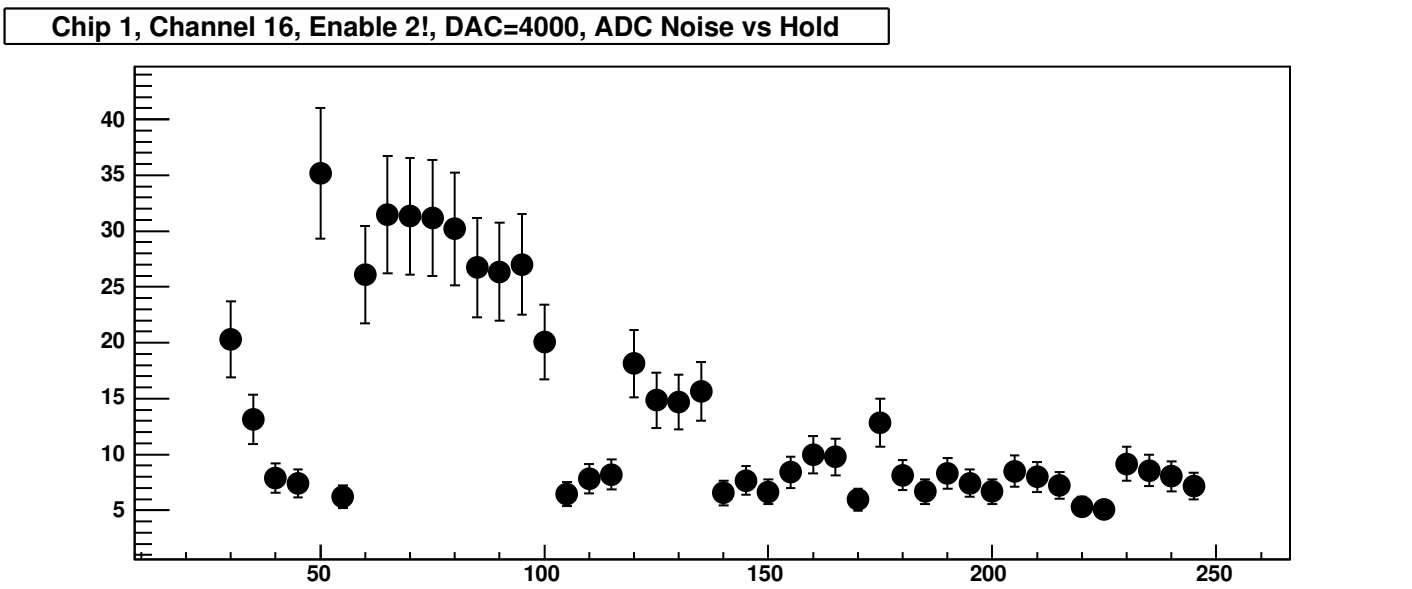


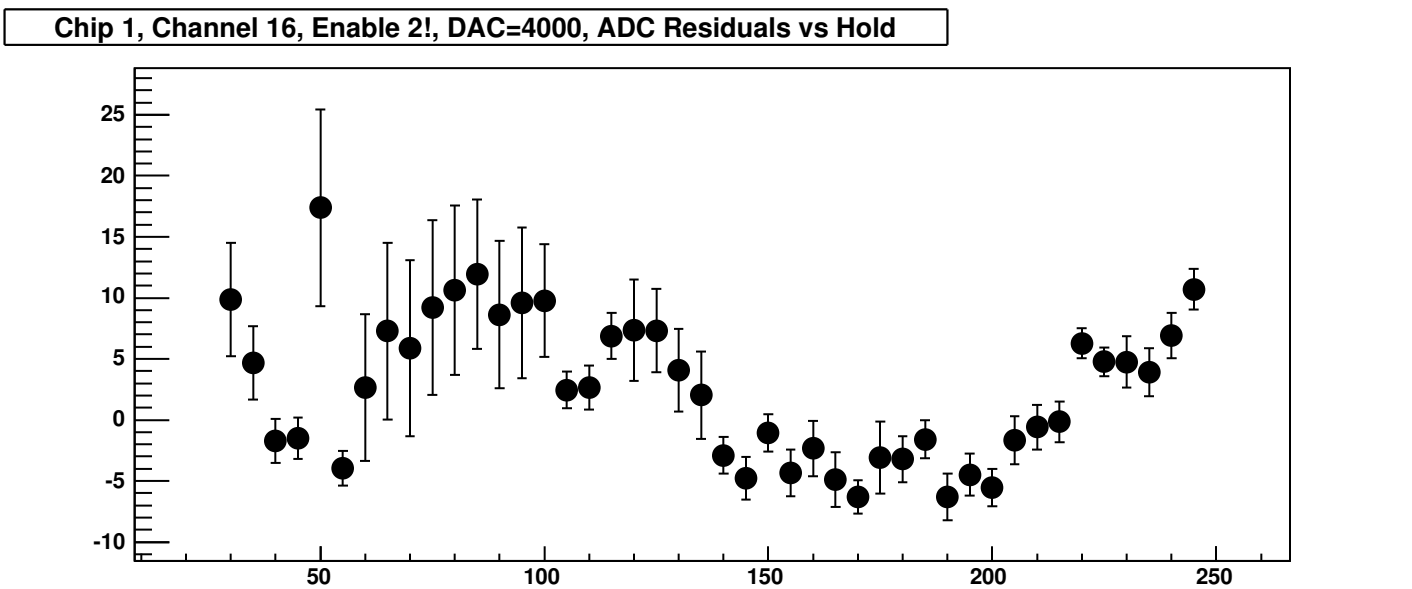

Chip 1, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 3676 / 38
p0 146.9 ± 2.202
p1 219.8 ± 13.27
p2 -25.05 ± 2.053
p3 -3.812 ± 18.02
p4 0.2275 ± 0.003981
150
140
130
120
110
100
90
80
70
60
50
100
150
200
250


Chip 1, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

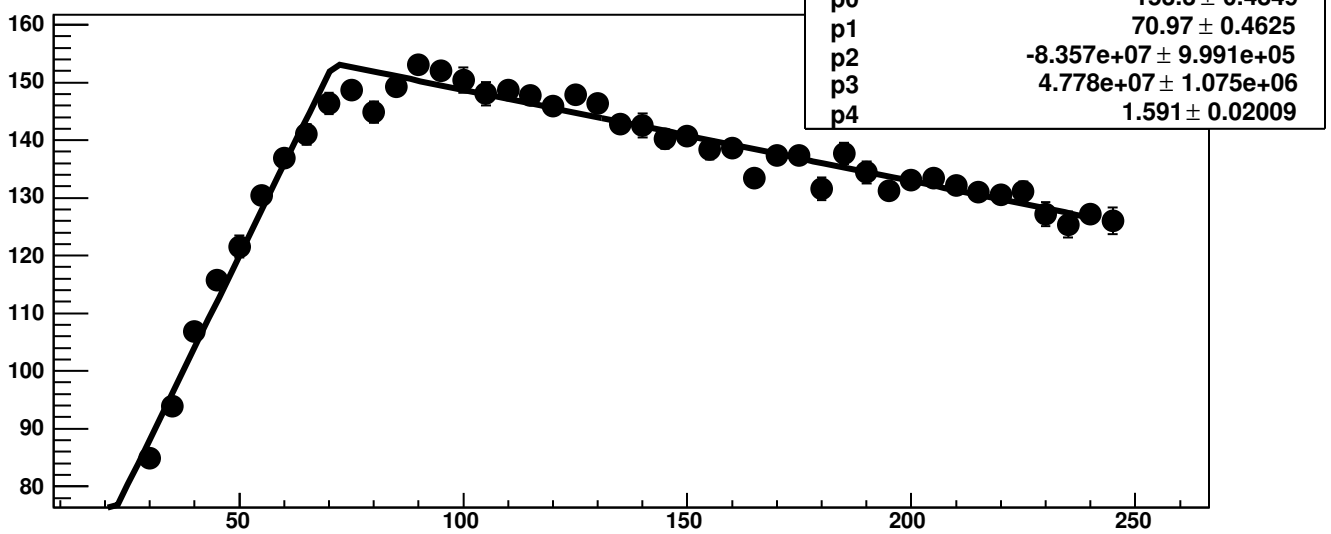

Chip 1, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
50
100
150
200
250


Chip 1, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 213.4 / 39
p0 -720 ± 0.6616
p1 4.713 ± 0.02608
p2 1.084e+04 ± 1.608
p3 31.97 ± 0.009187
10000
8000
6000
4000
2000
0
50
100
150
200
250

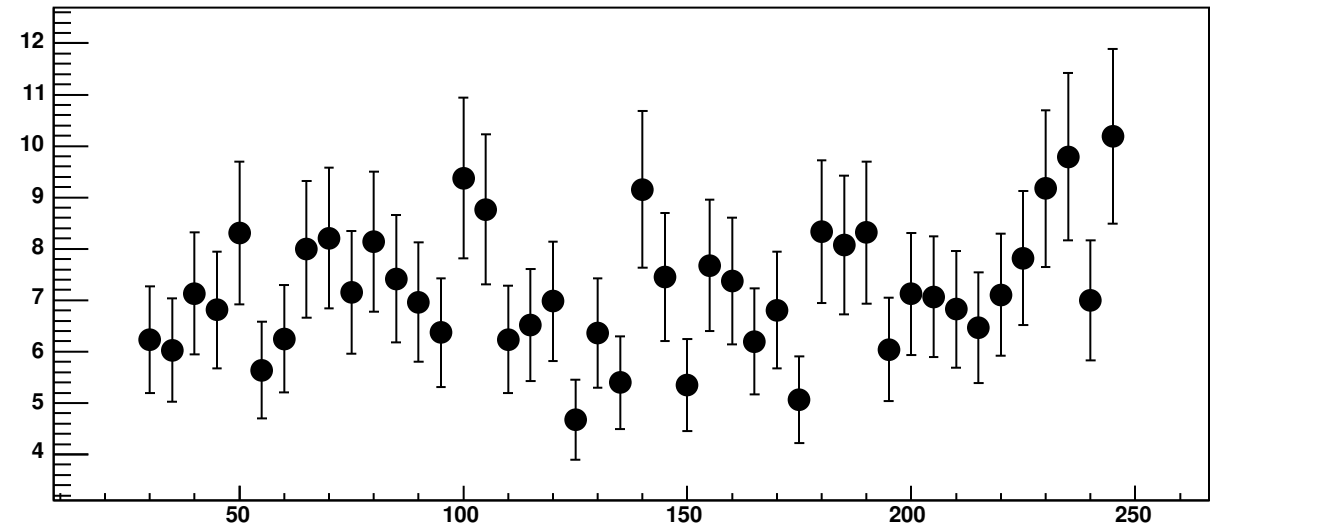

Chip 1, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold
40
35
30
25
20
15
10
5
50
100
150
200
250


Chip 1, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
50
100
150
200
250

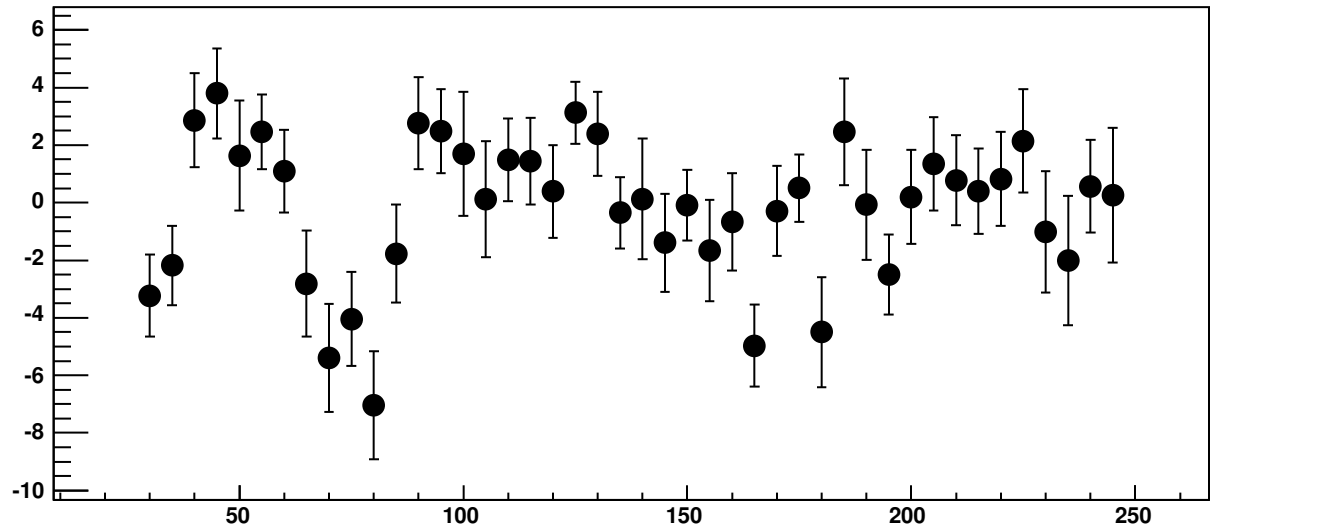
Chip 1, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold



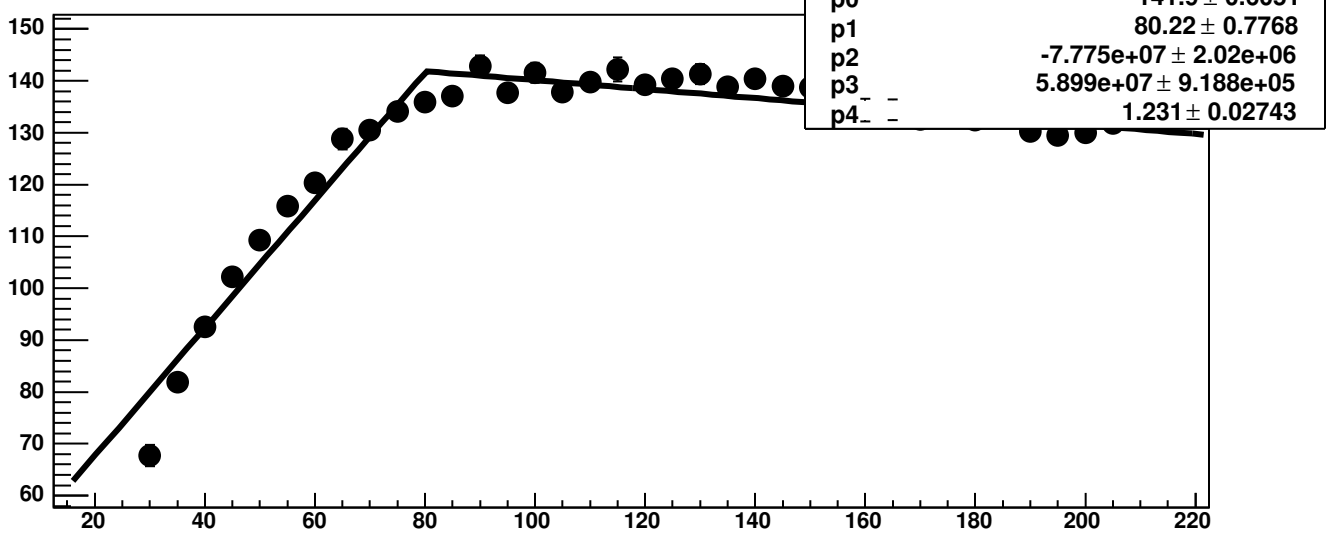
Chip 1, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



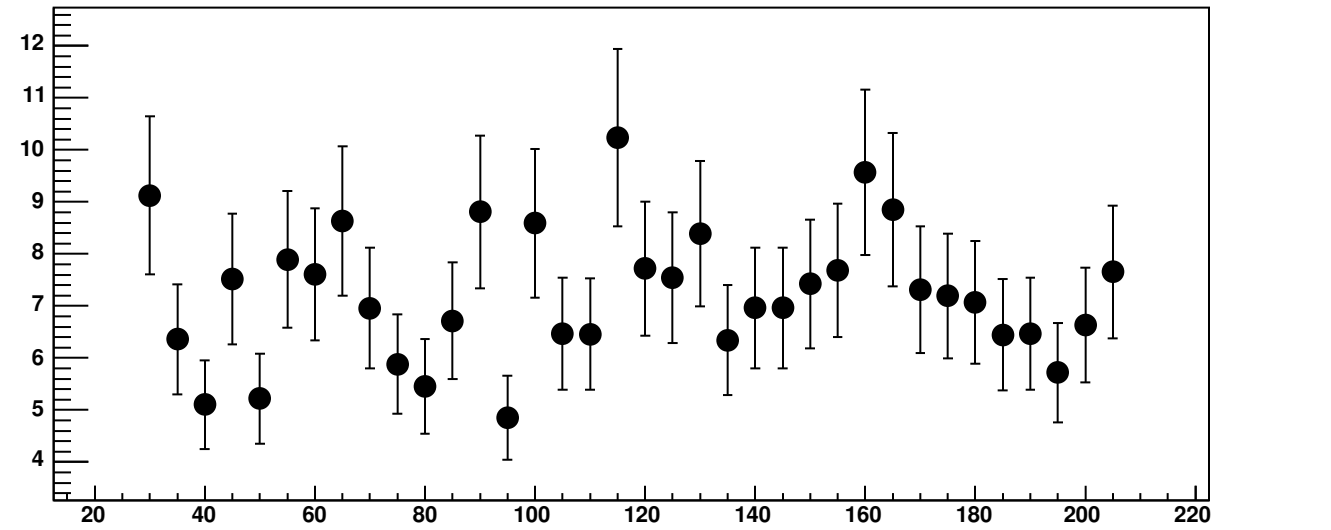
Chip 1, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold



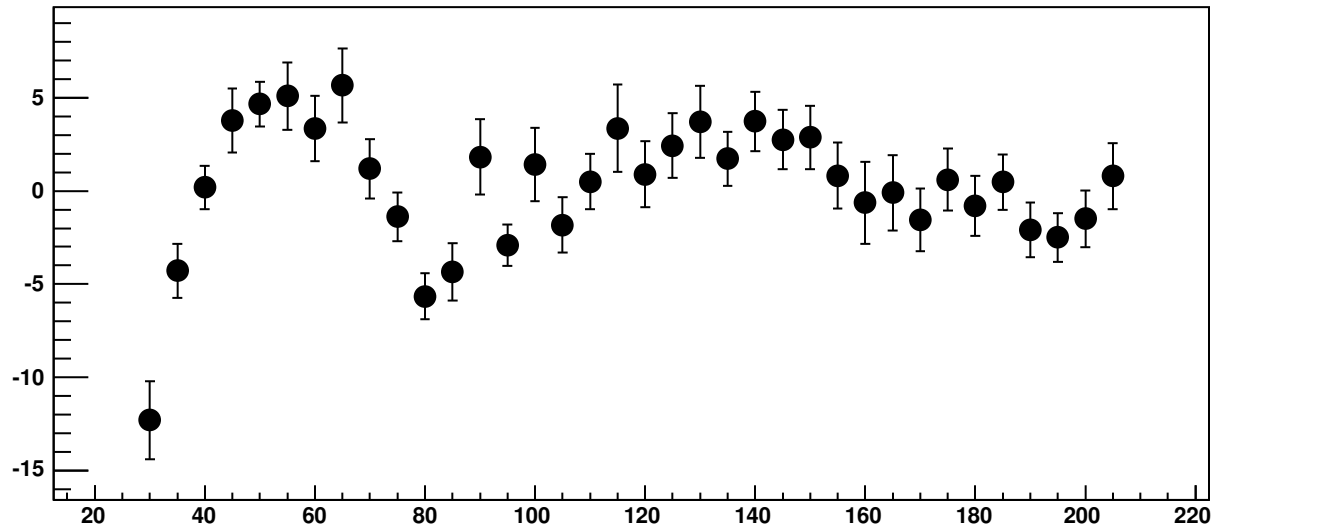
Chip 1, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold



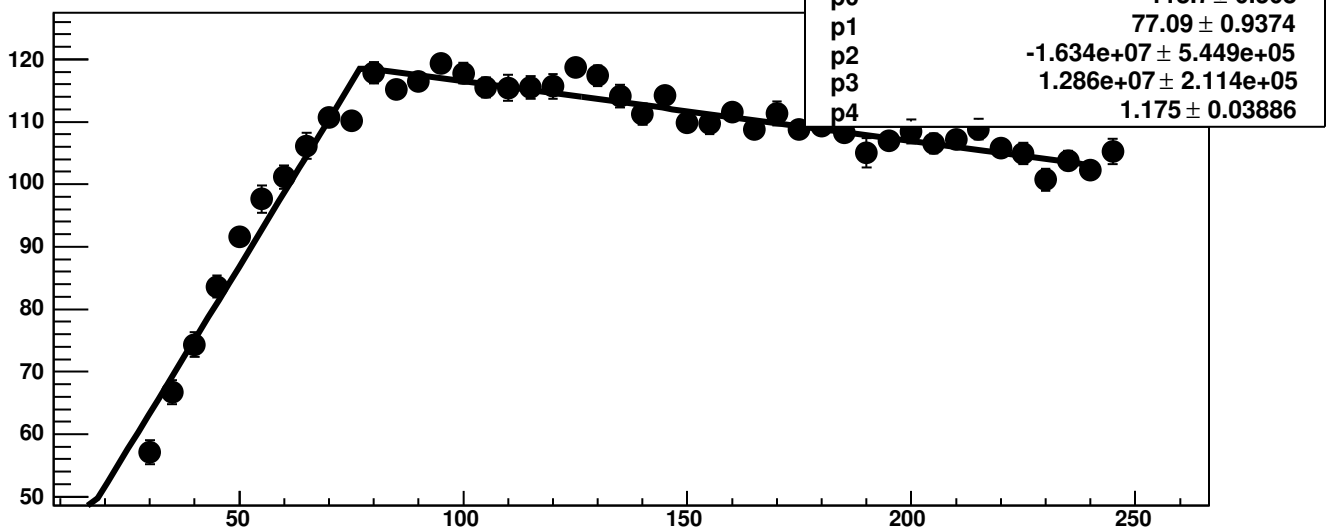
Chip 1, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



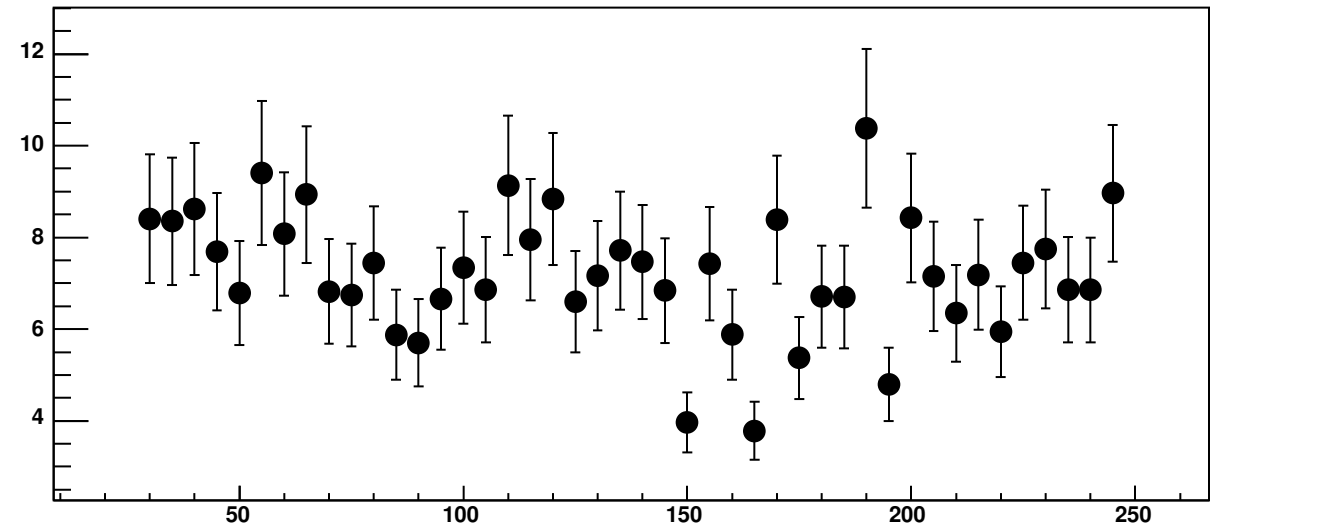
Chip 1, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold



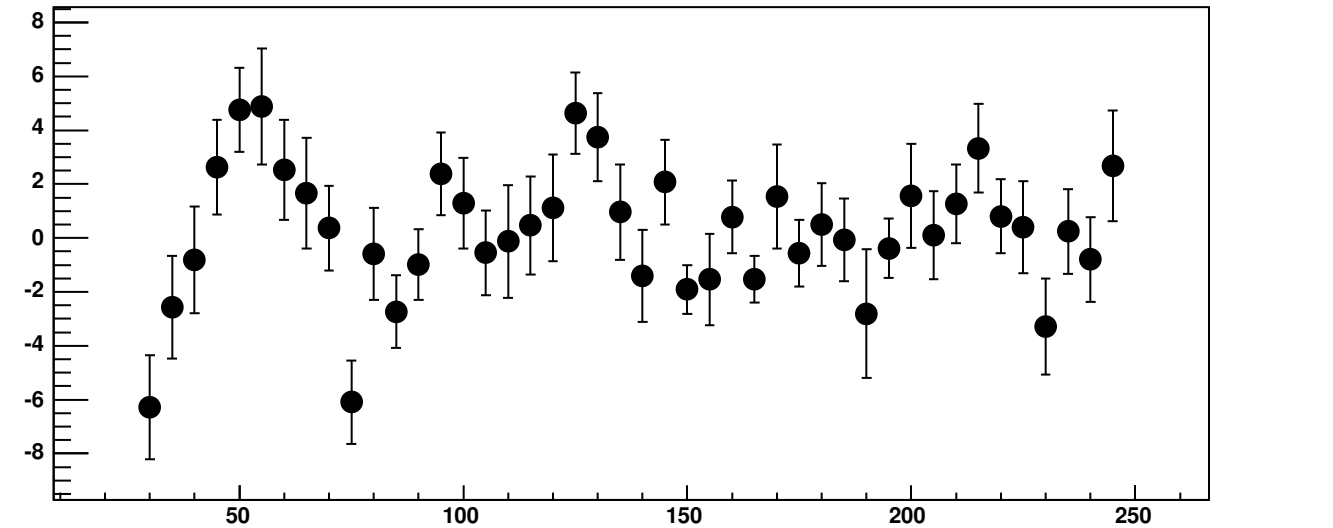
Chip 1, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

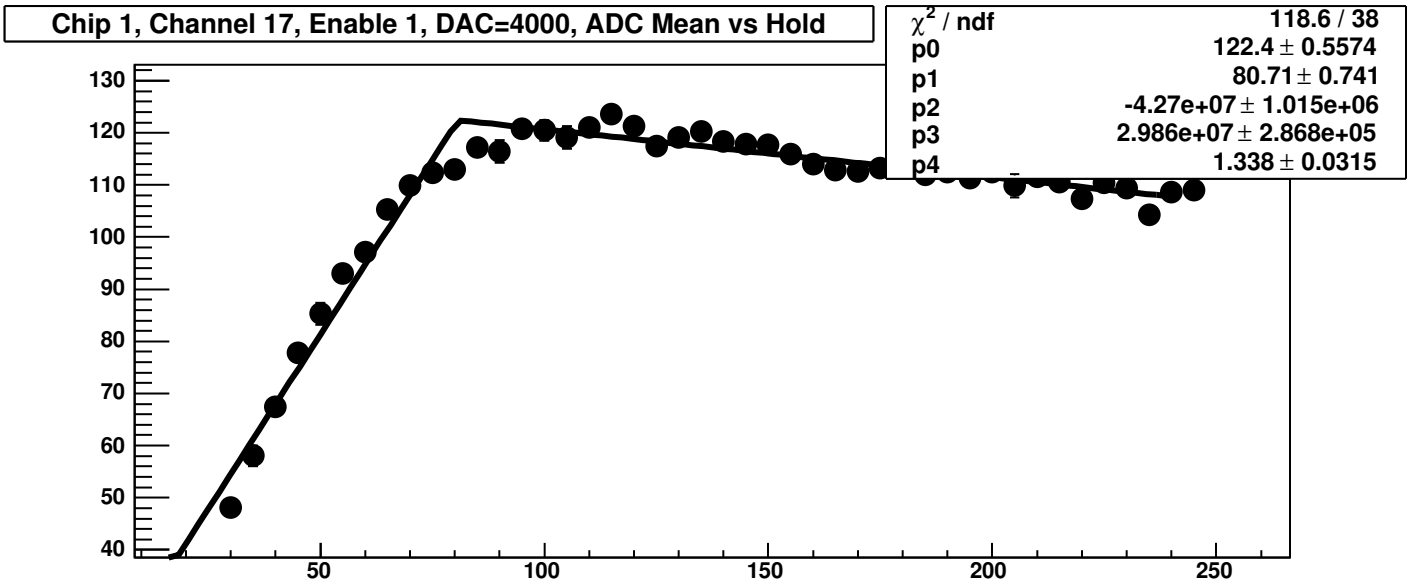


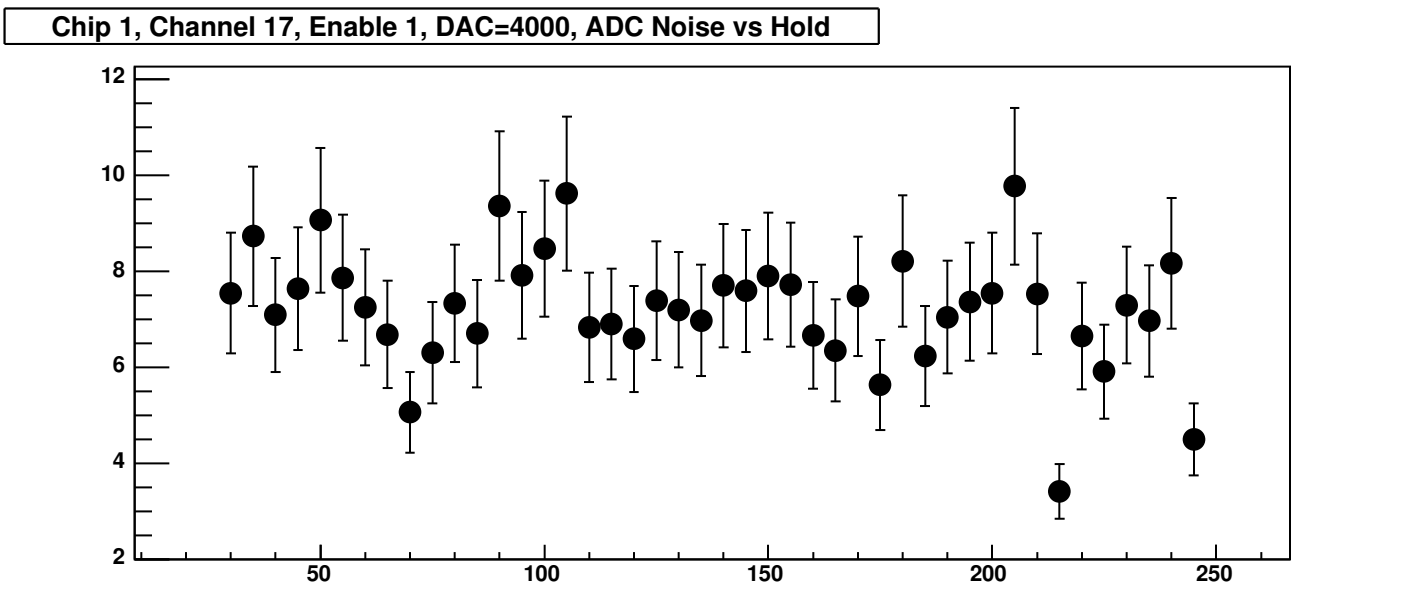
Chip 1, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold

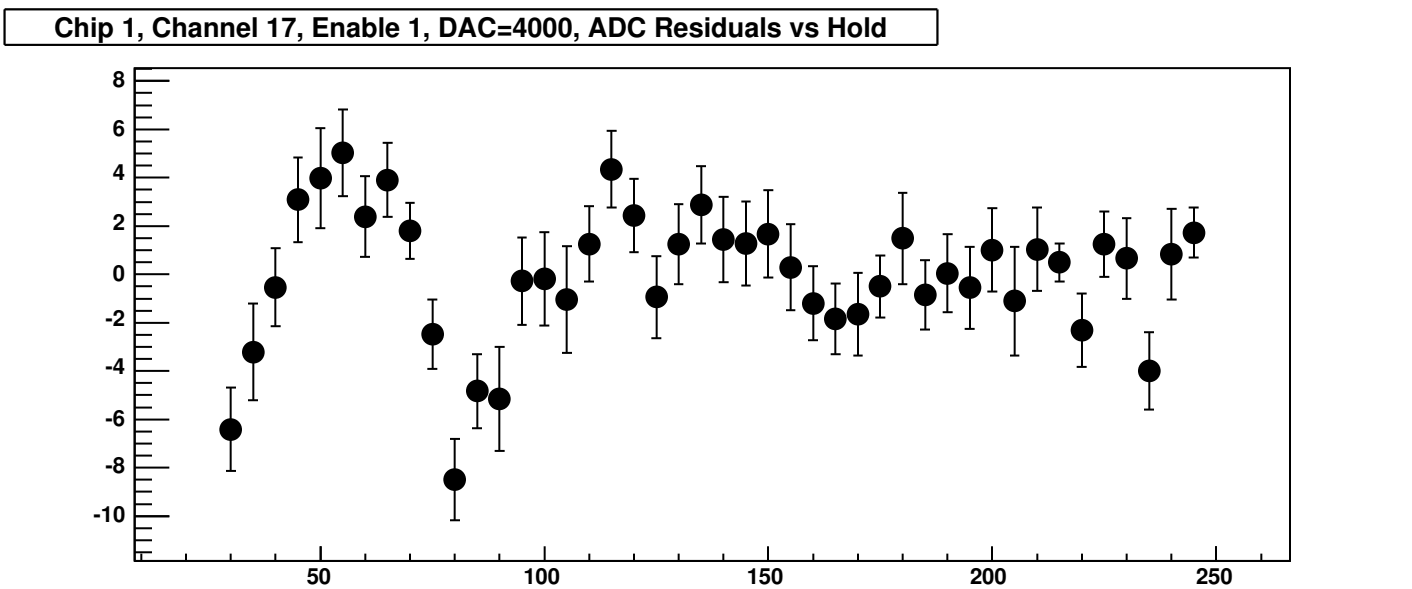


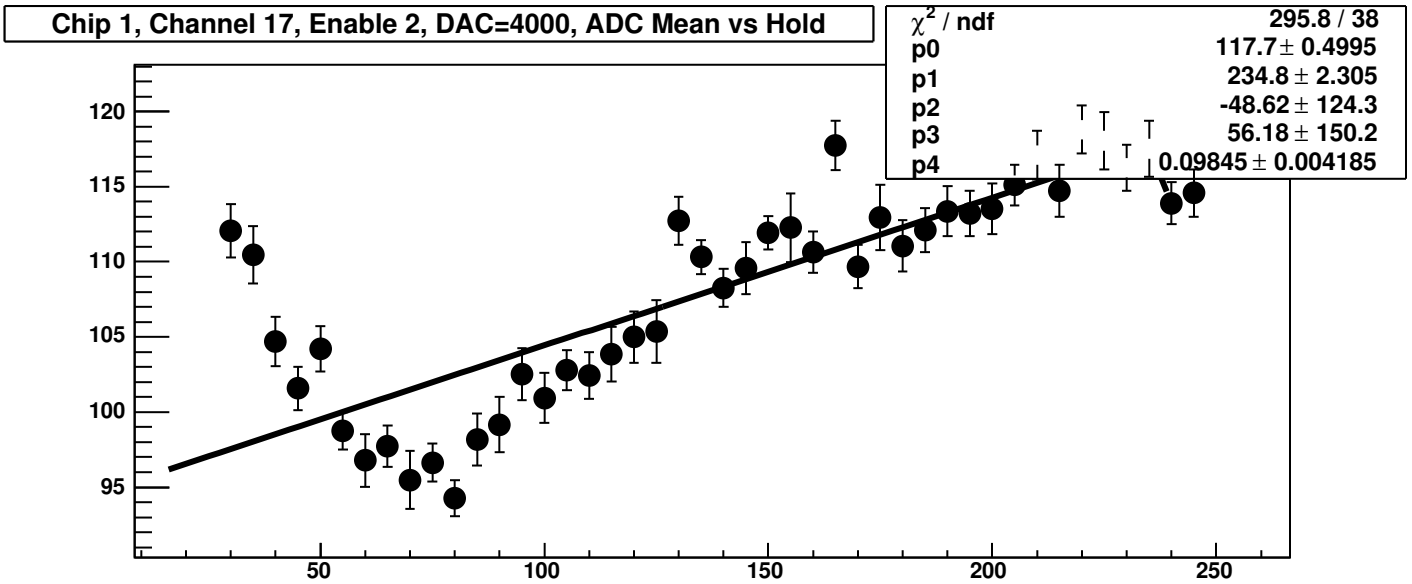
Chip 1, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

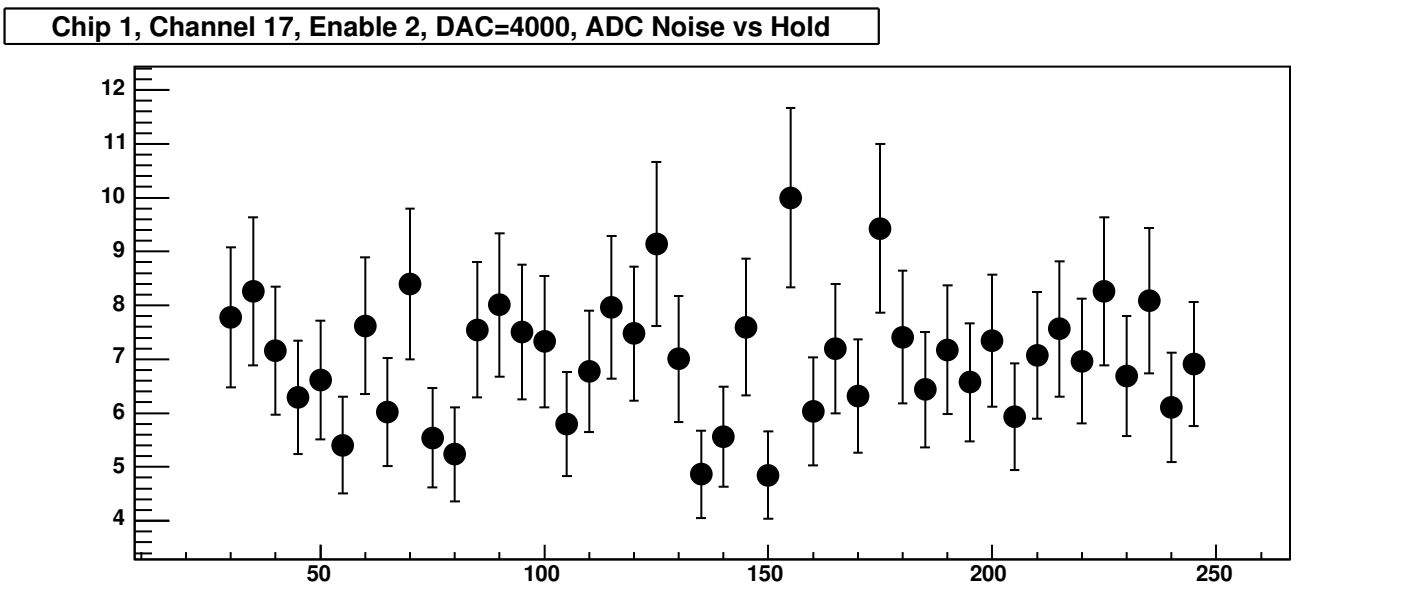


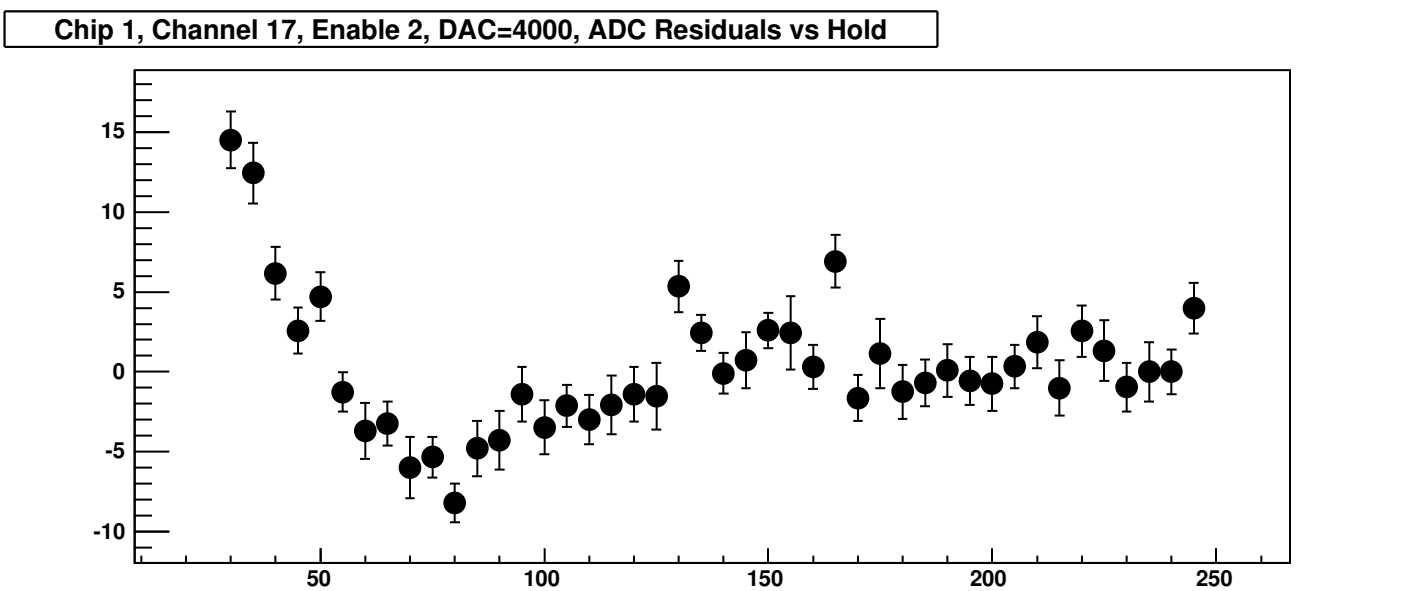

Chip 1, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 118.6 / 38
p0 122.4 ± 0.5574
p1 80.71 ± 0.741
p2 -4.27e+07 ± 1.015e+06
p3 2.986e+07 ± 2.868e+05
p4 1.338 ± 0.0315
130
120
110
100
90
80
70
60
50
40
50
100
150
200
250


Chip 1, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
2
50
100
150
200
250

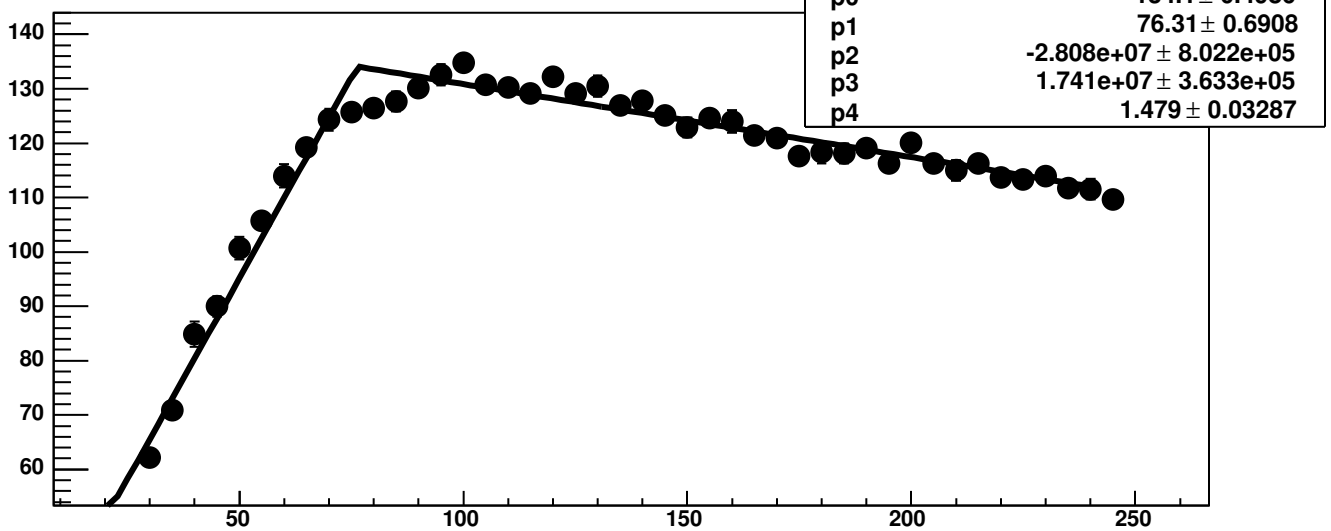

Chip 1, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
50
100
150
200
250


Chip 1, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 295.8 / 38
p0 117.7 ± 0.4995
p1 234.8 ± 2.305
p2 -48.62 ± 124.3
p3 56.18 ± 150.2
p4 0.09845 ± 0.004185
120
115
110
105
100
95
50
100
150
200
250

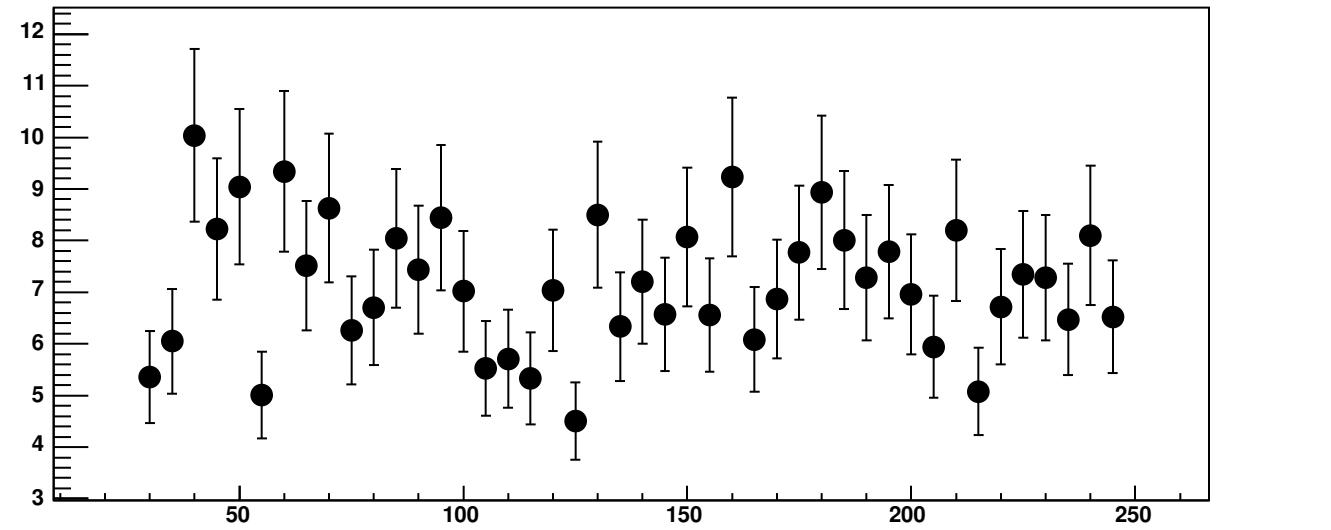

Chip 1, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 1, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
50
100
150
200
250

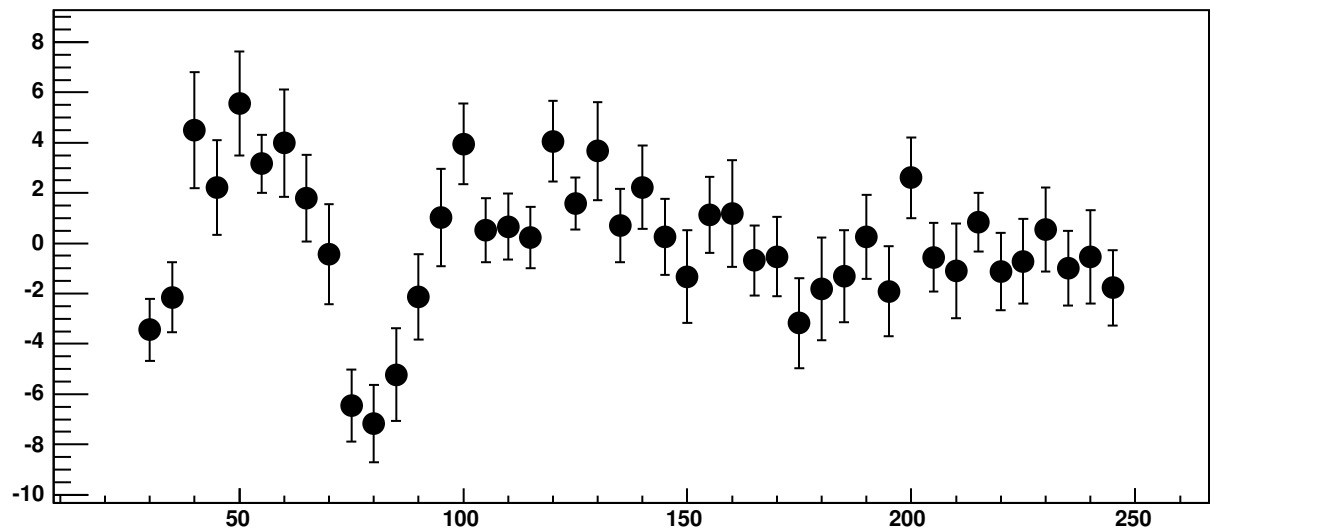
Chip 1, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



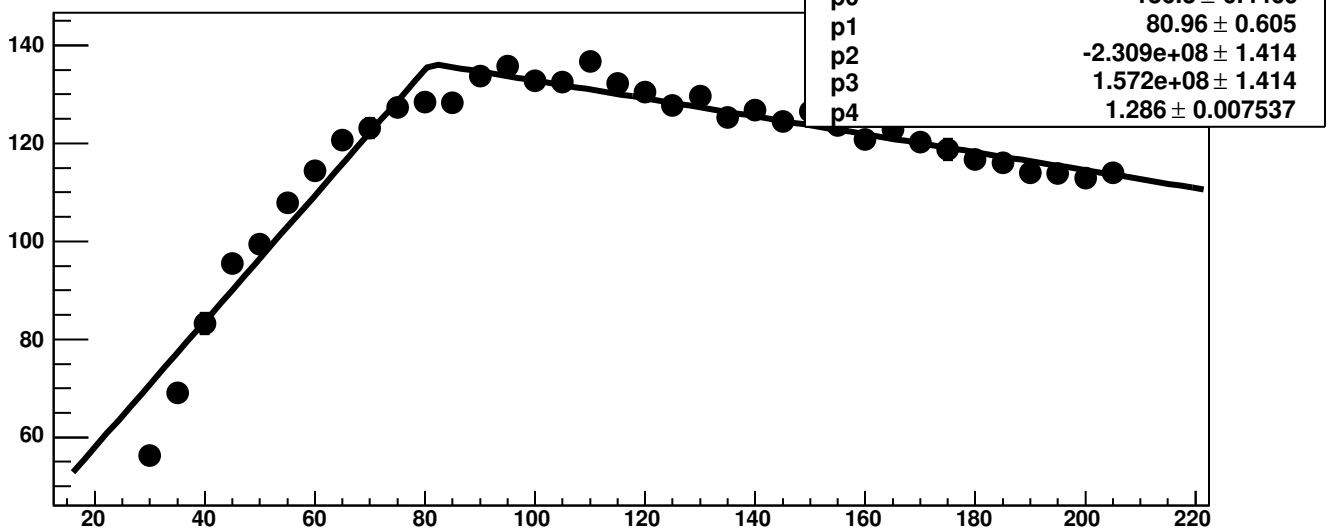
Chip 1, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



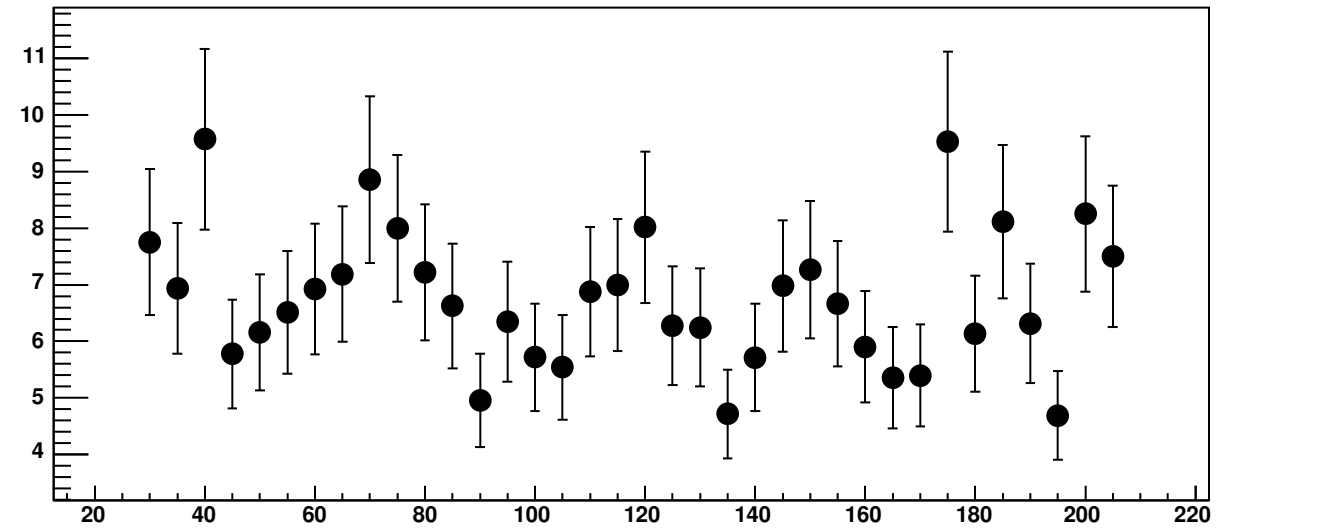
Chip 1, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold



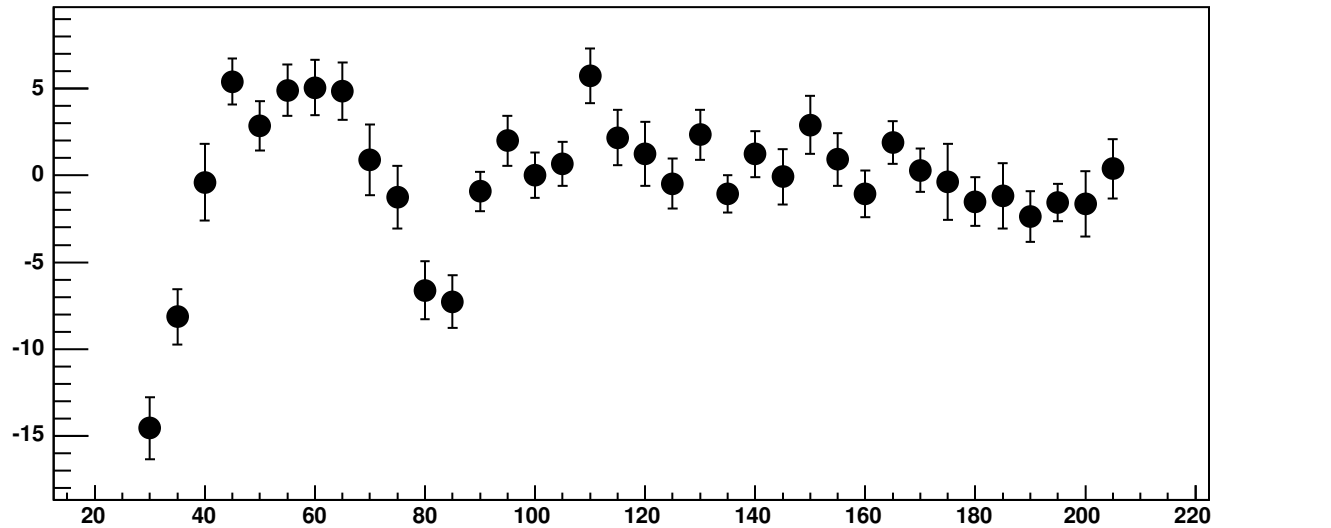
Chip 1, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



Chip 1, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold

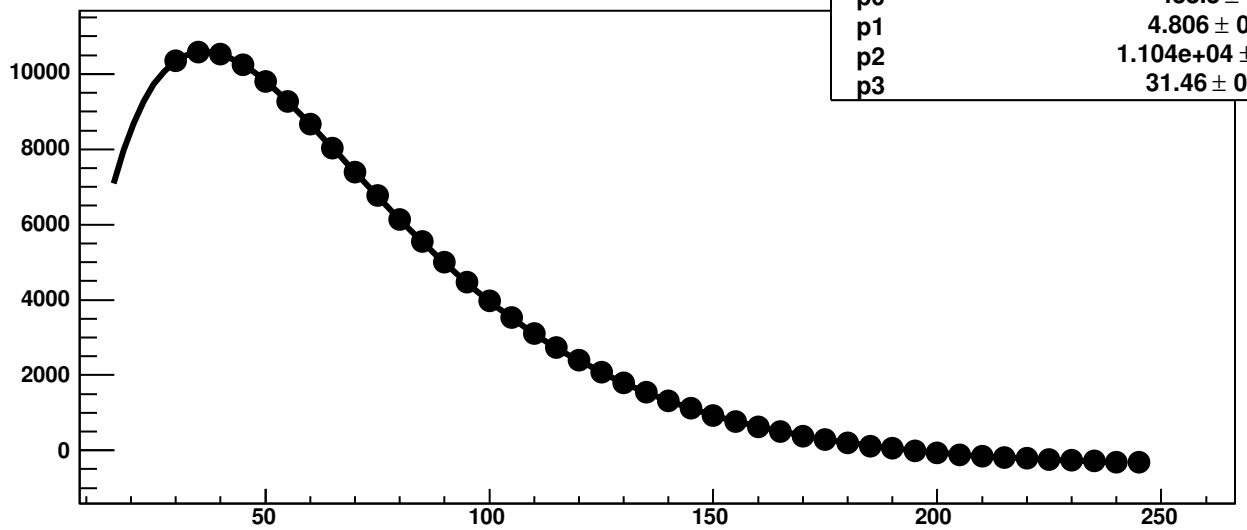


Chip 1, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

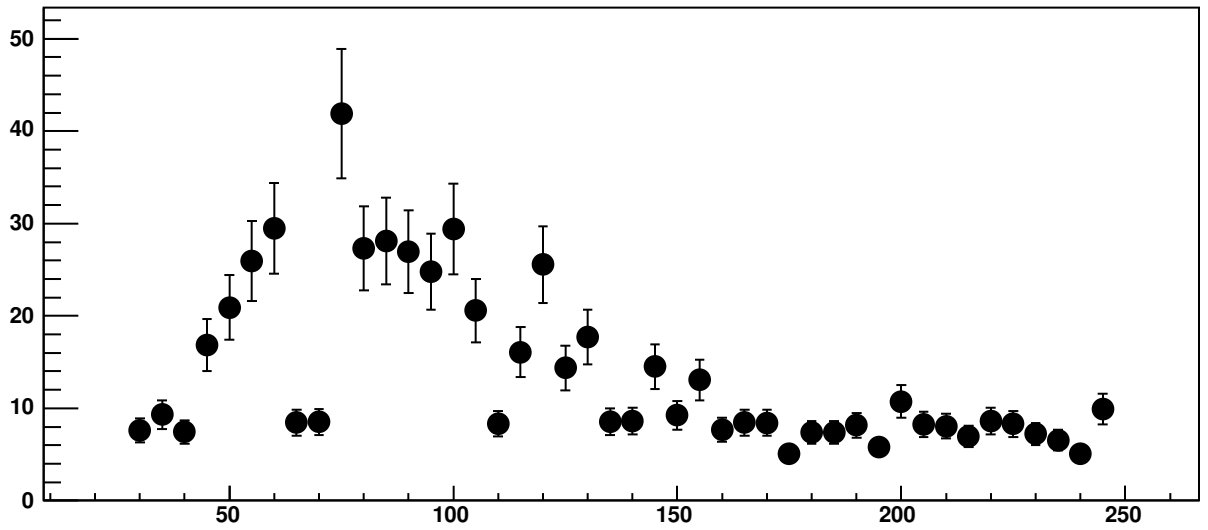


Chip 2, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold

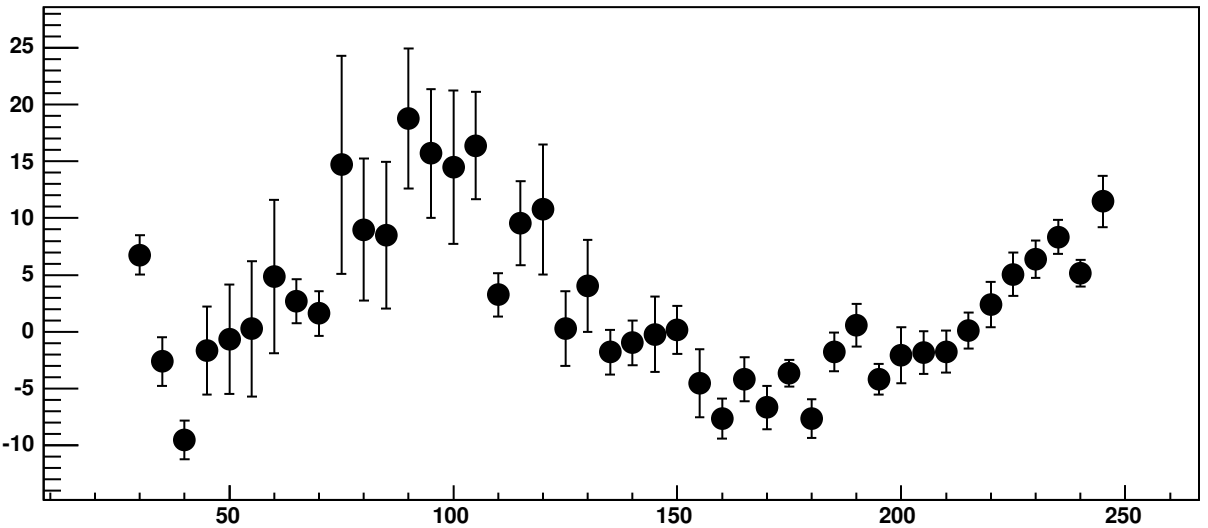
χ^2 / ndf	261.3 / 39
p0	-438.3 ± 0.6595
p1	4.806 ± 0.01941
p2	1.104e+04 ± 1.069
p3	31.46 ± 0.00913

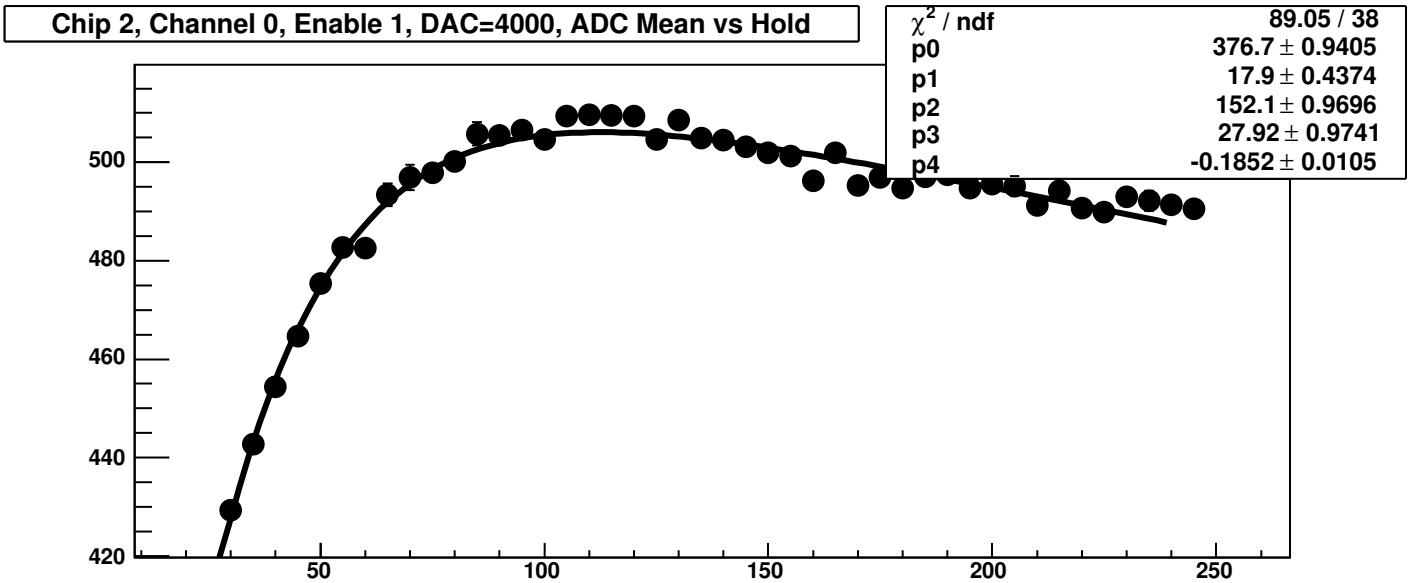


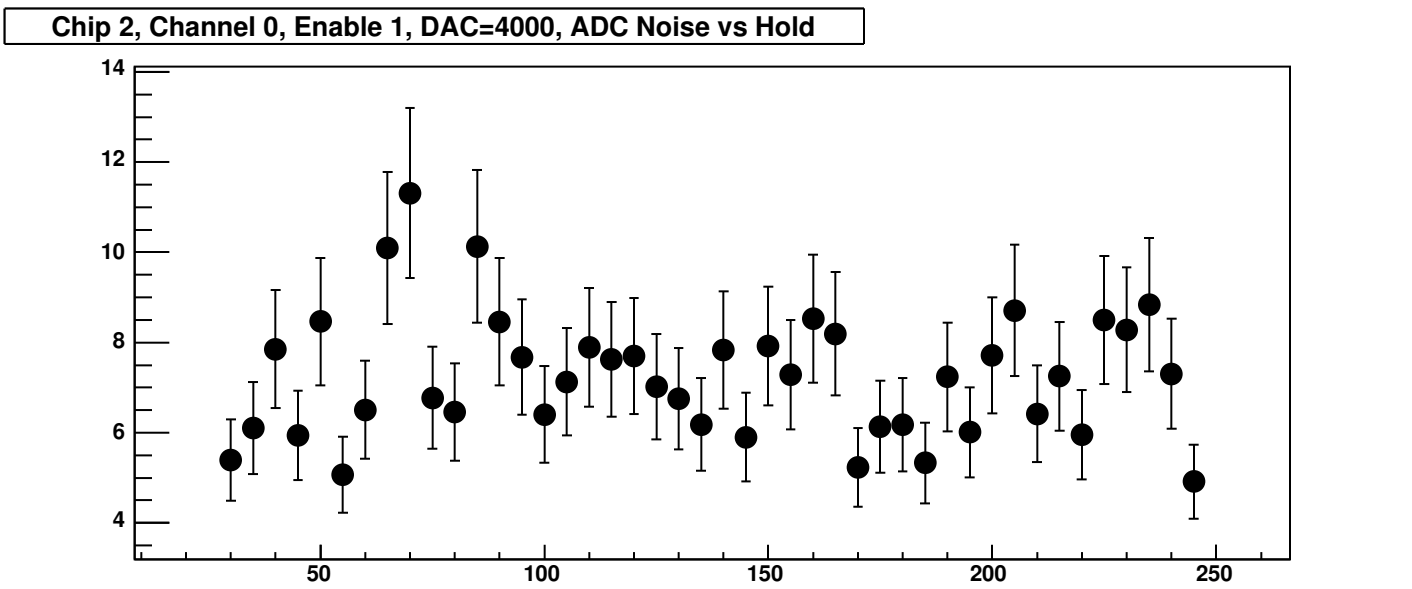
Chip 2, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold

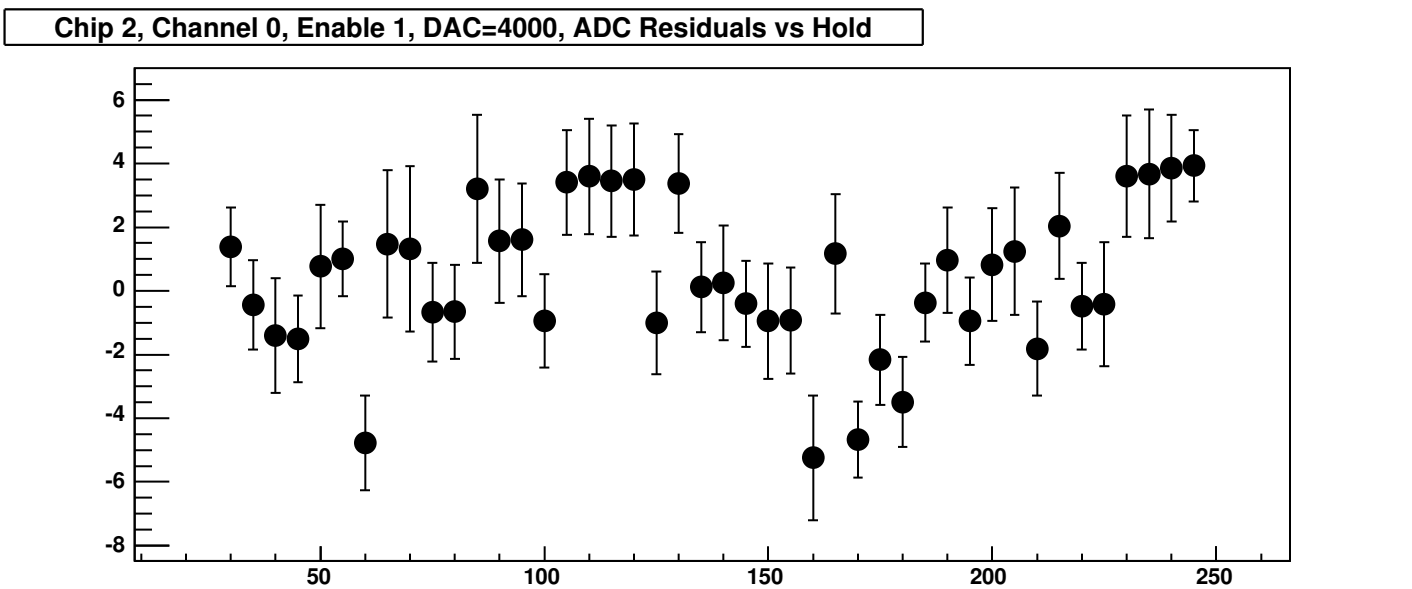


Chip 2, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold

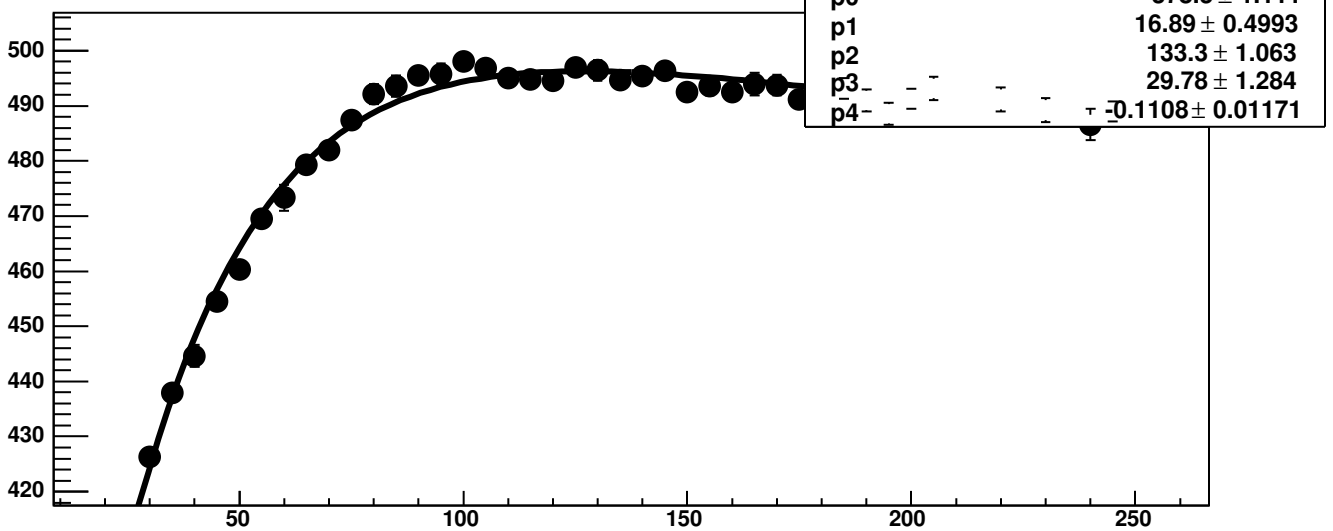



Chip 2, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 89.05 / 38
p0 376.7 ± 0.9405
p1 17.9 ± 0.4374
p2 152.1 ± 0.9696
p3 27.92 ± 0.9741
p4 -0.1852 ± 0.0105
500
480
460
440
420
50
100
150
200
250

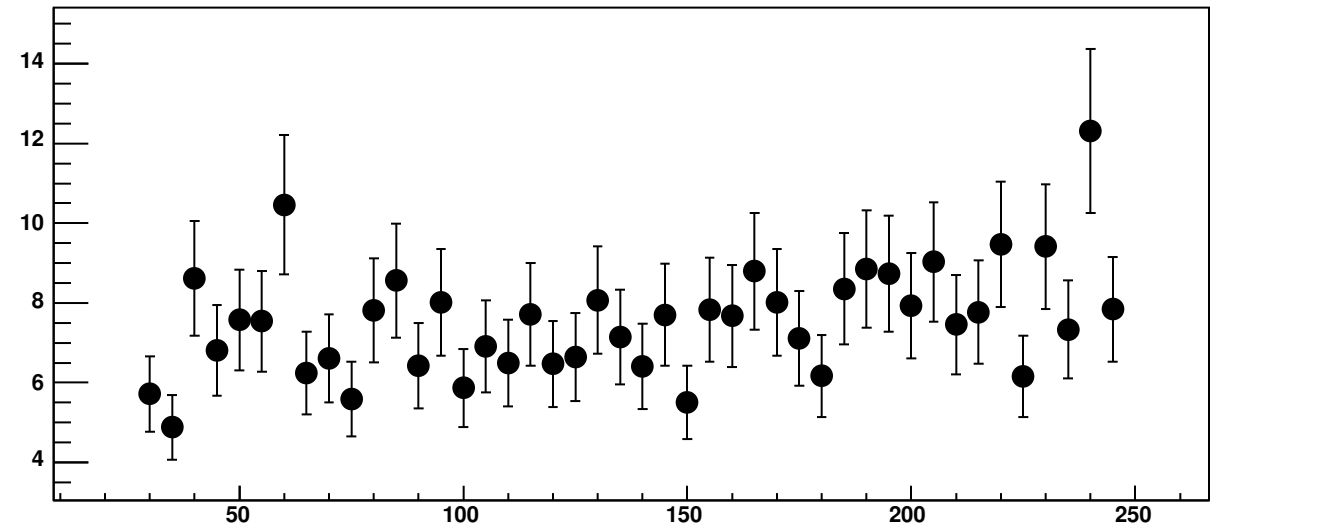

Chip 2, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250


Chip 2, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

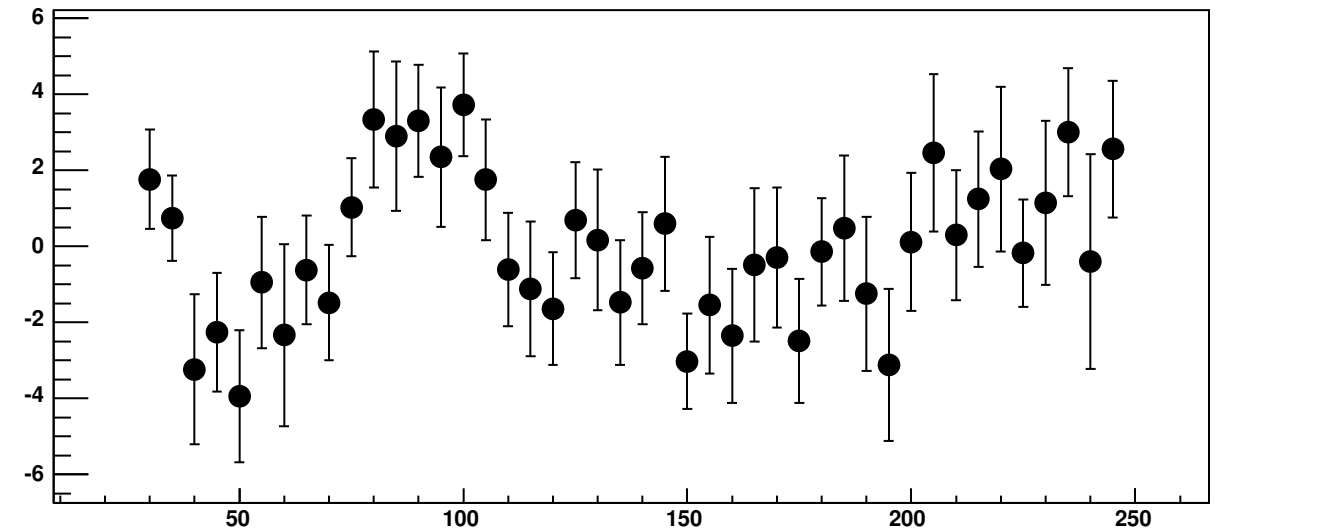
Chip 2, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold



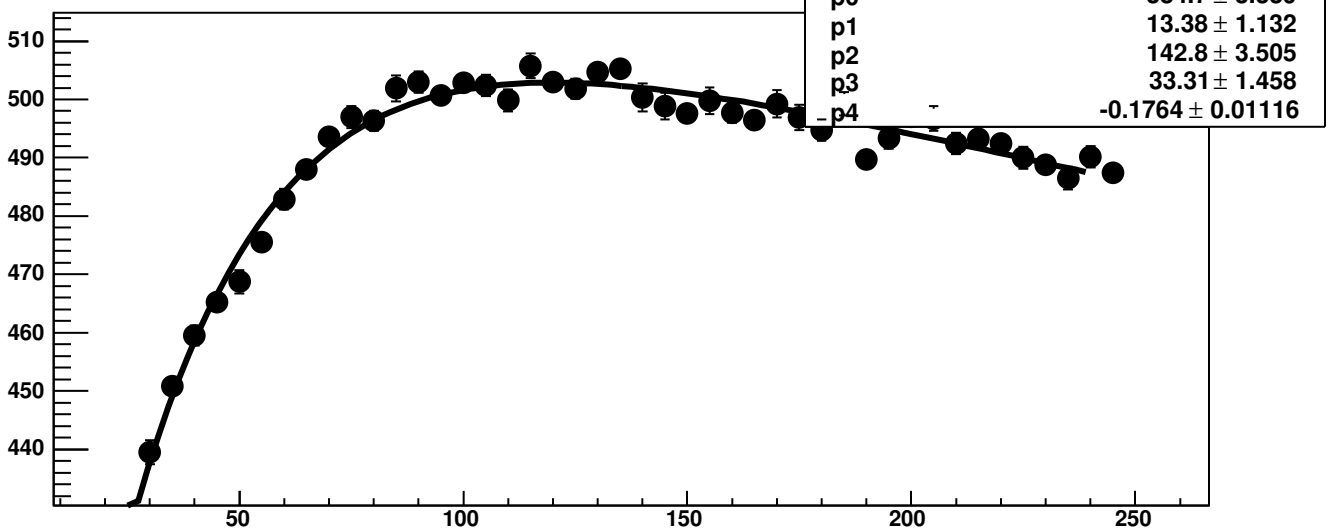
Chip 2, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



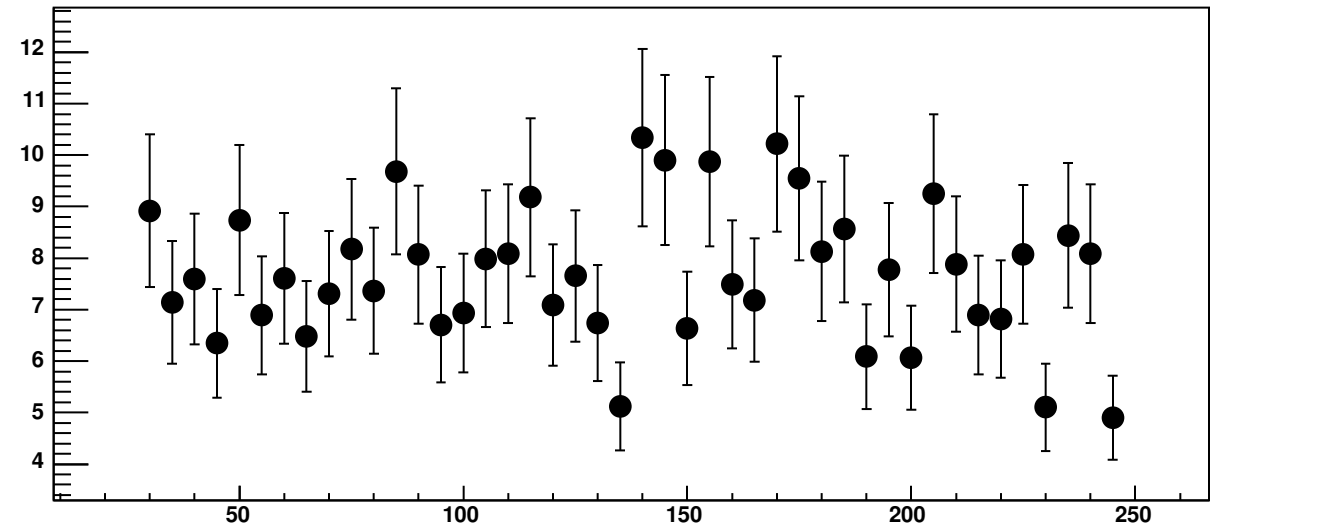
Chip 2, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold



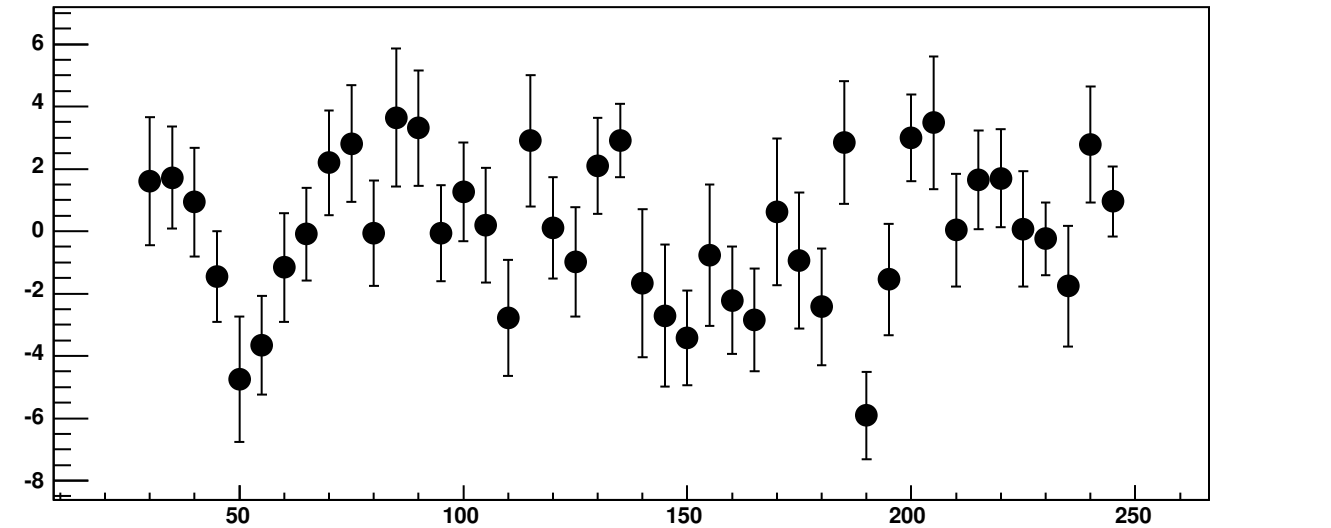
Chip 2, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold



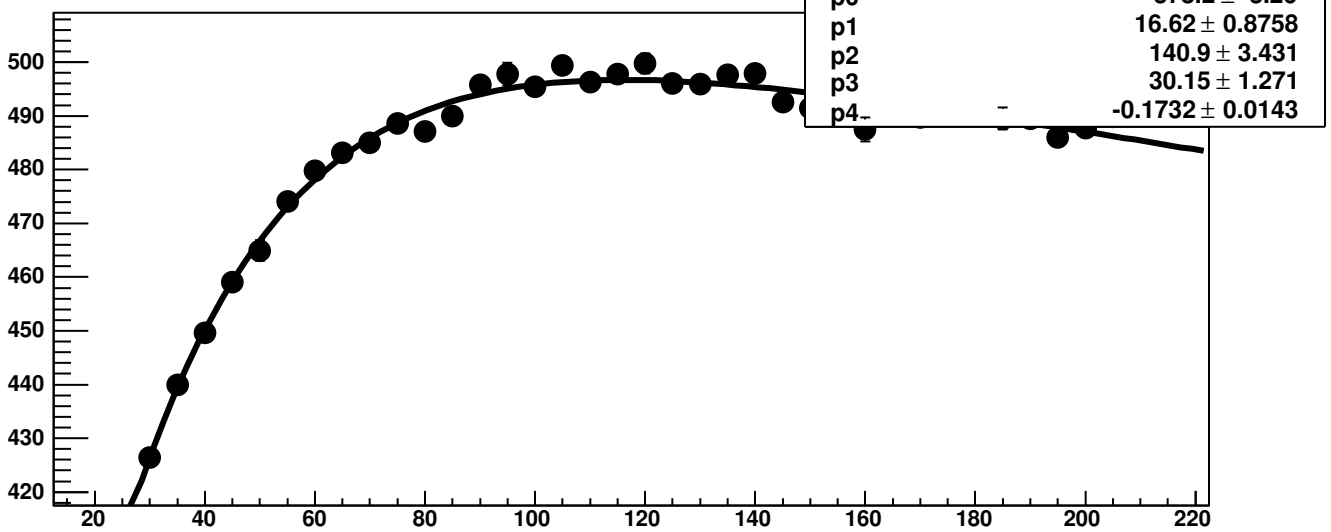
Chip 2, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold



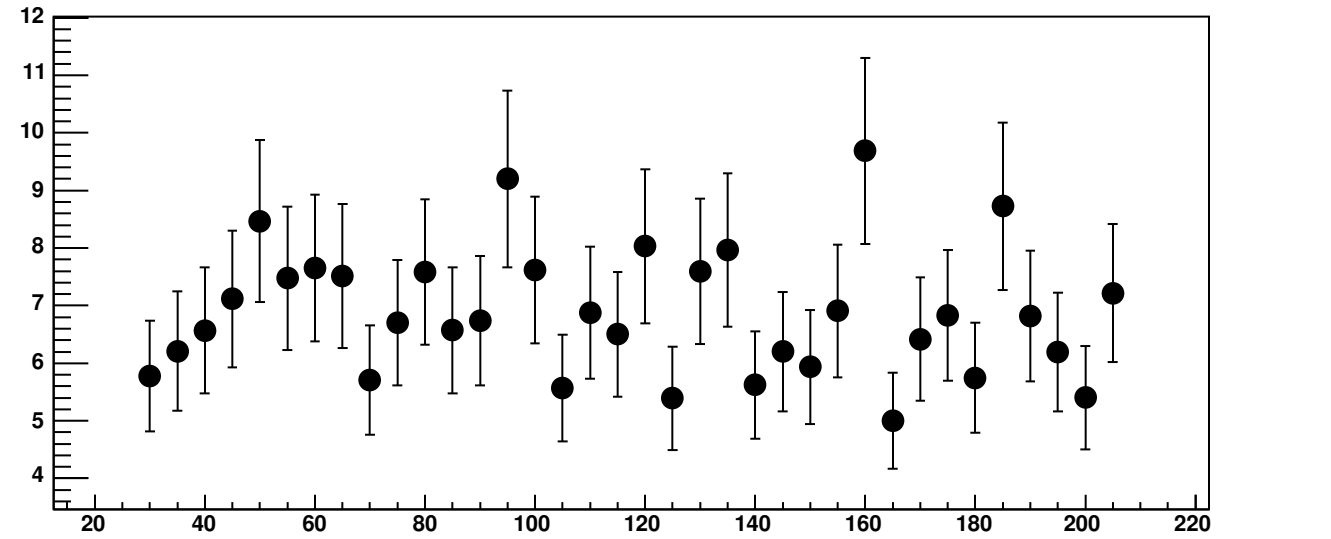
Chip 2, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold



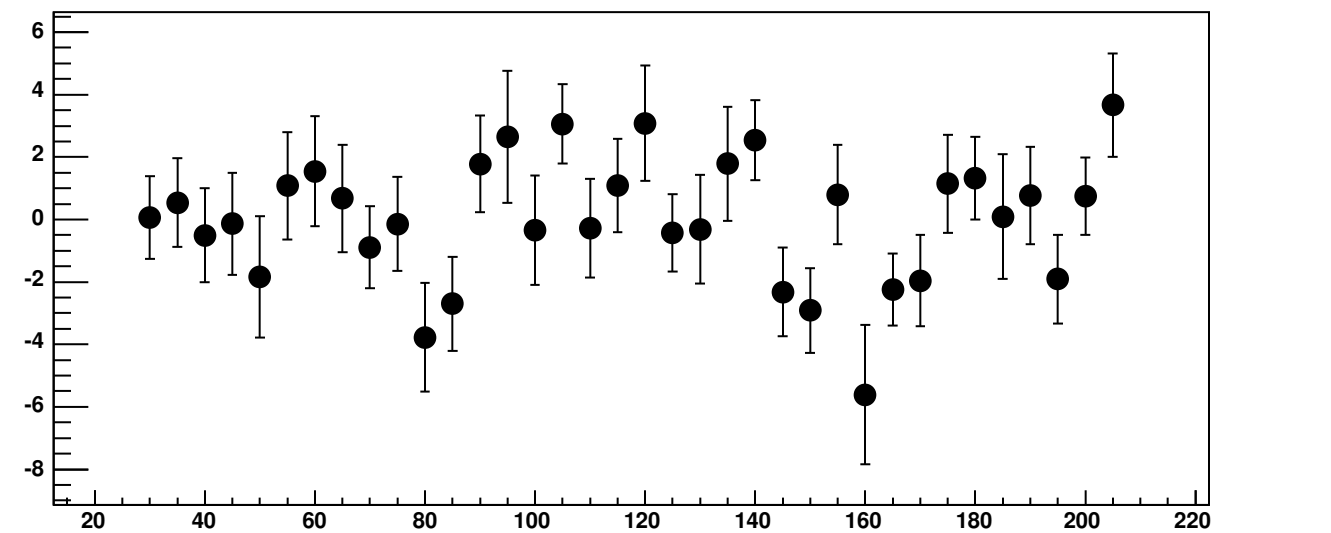
Chip 2, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold



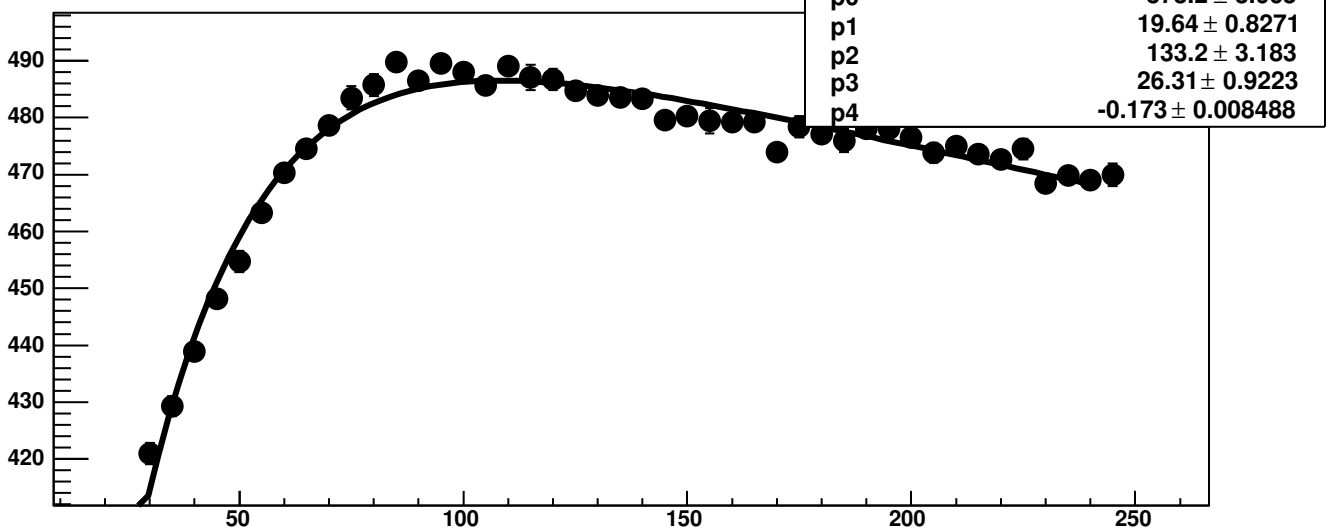
Chip 2, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



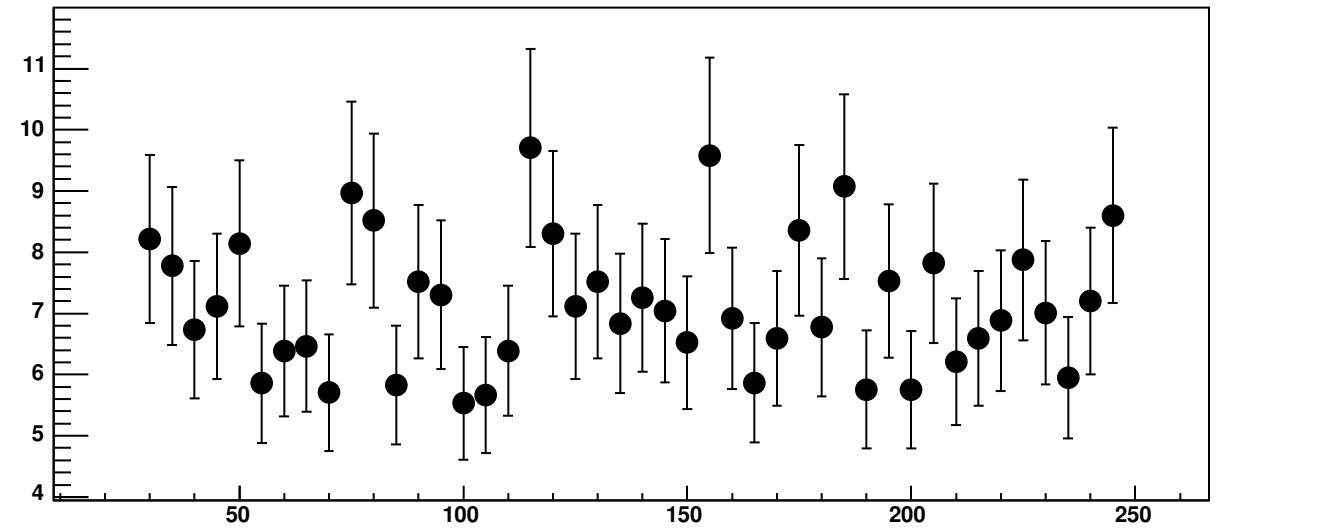
Chip 2, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold



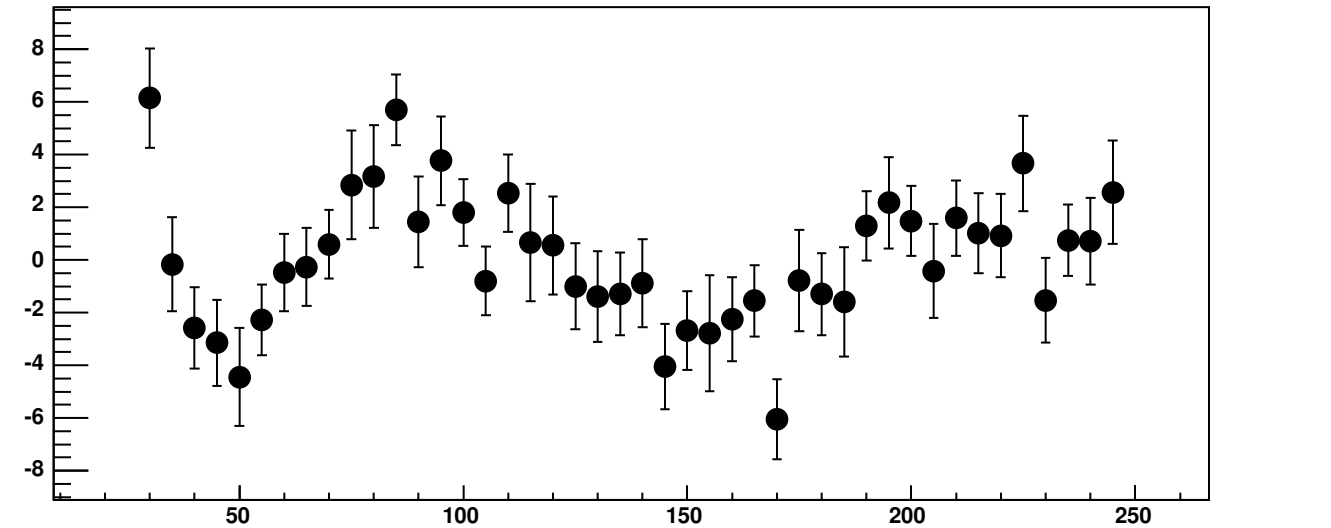
Chip 2, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold



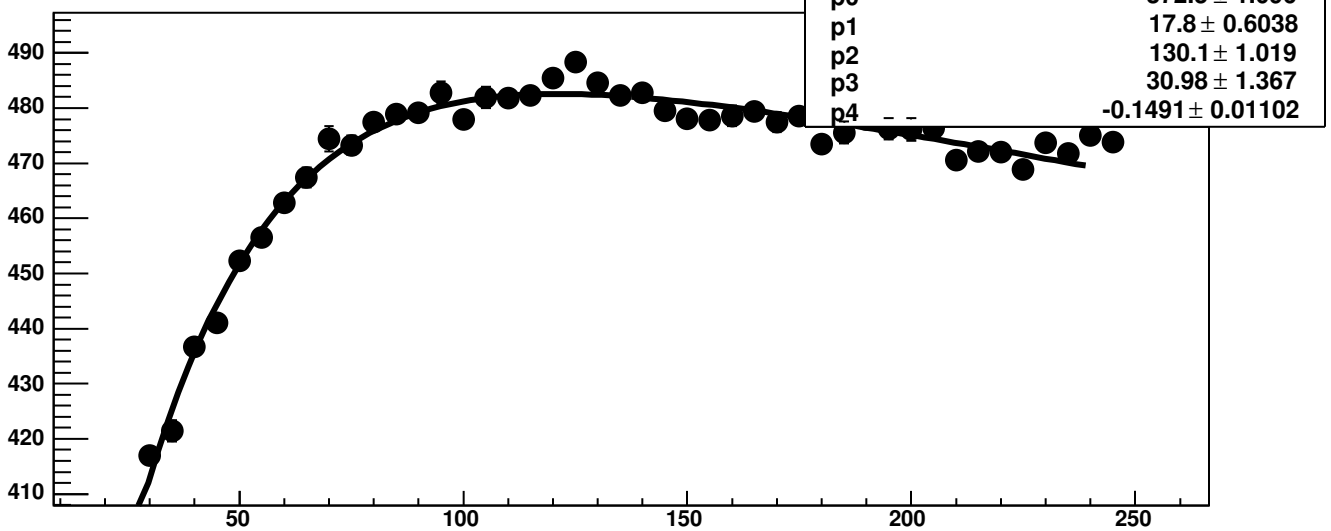
Chip 2, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold



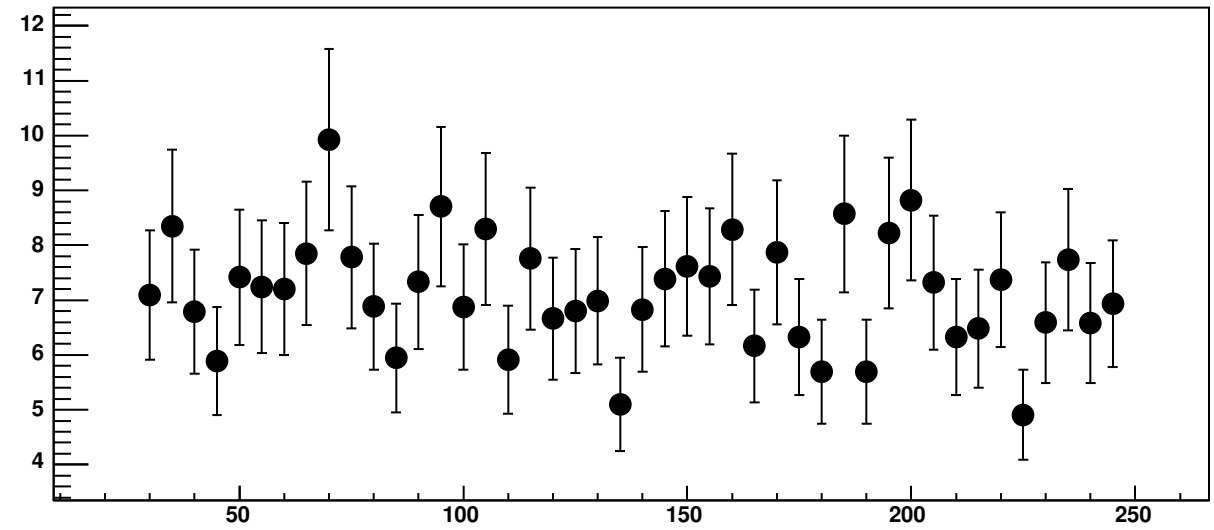
Chip 2, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold



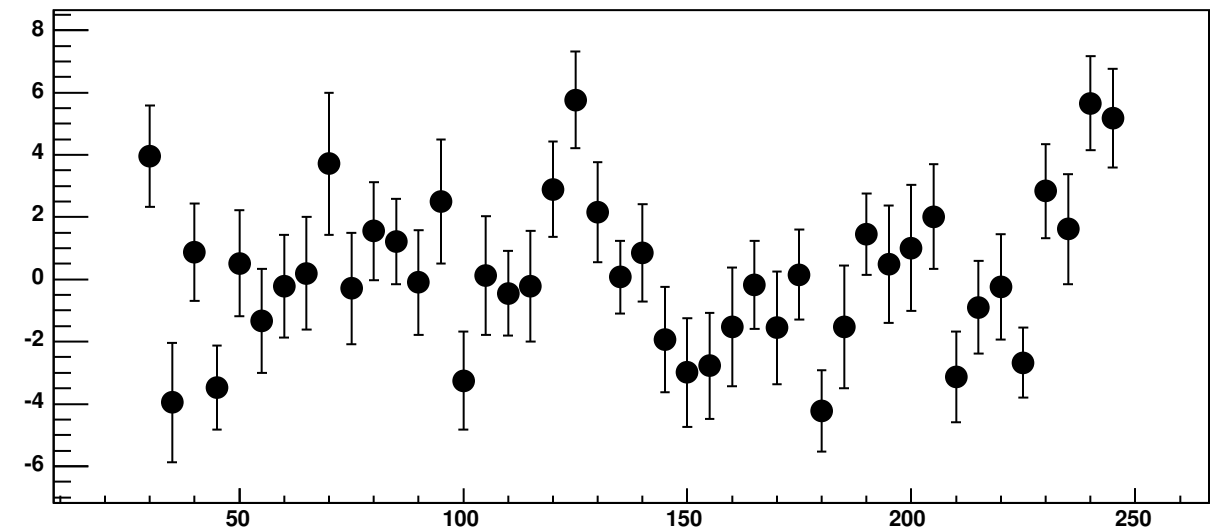
Chip 2, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold



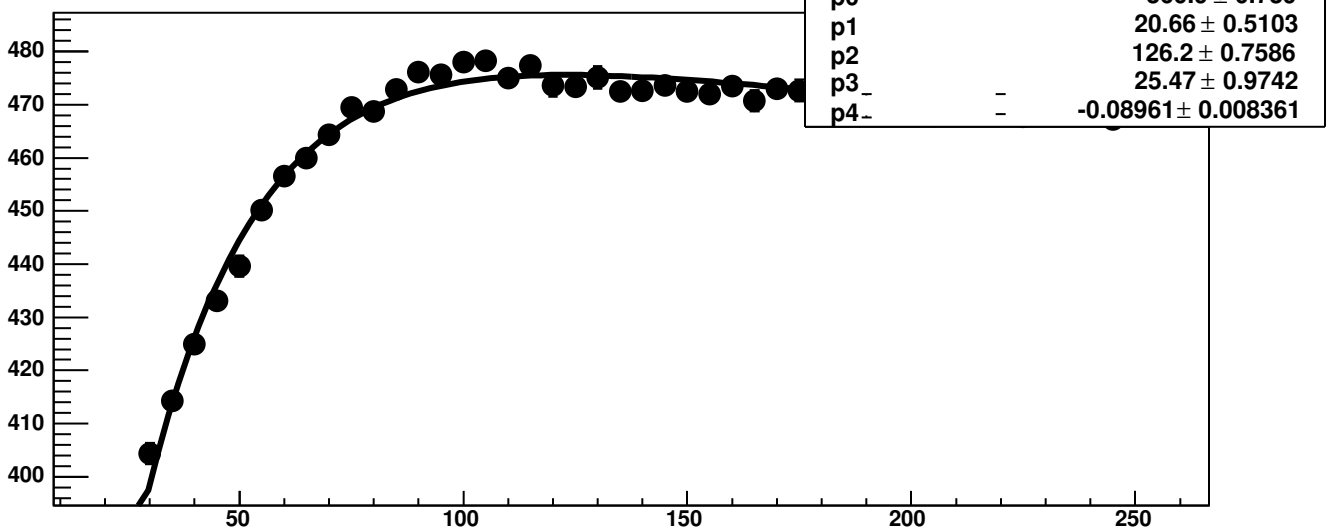
Chip 2, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



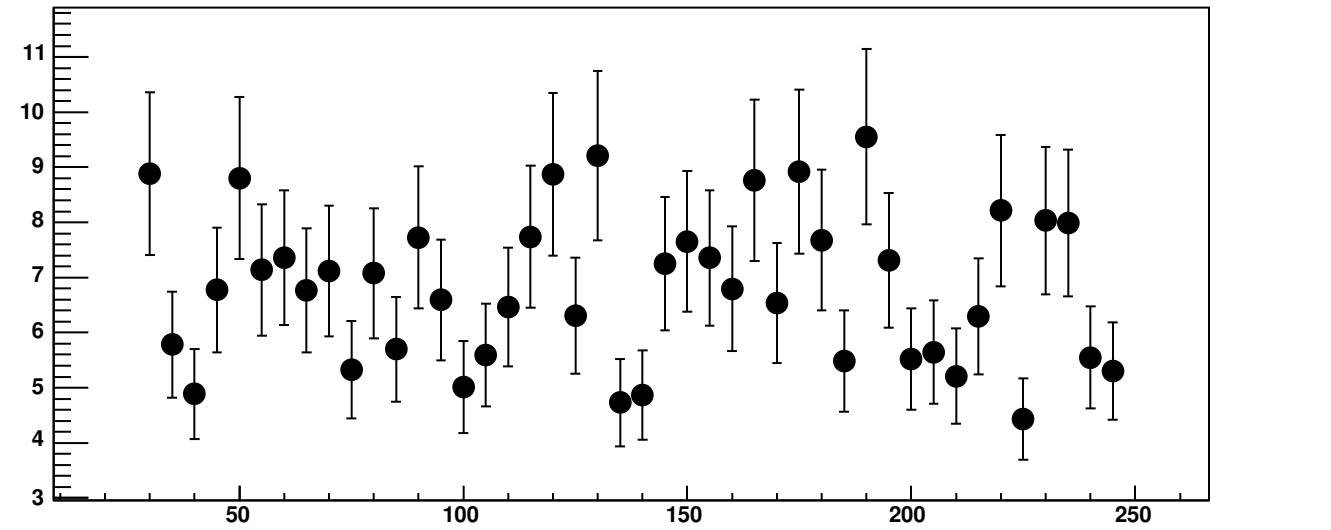
Chip 2, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold



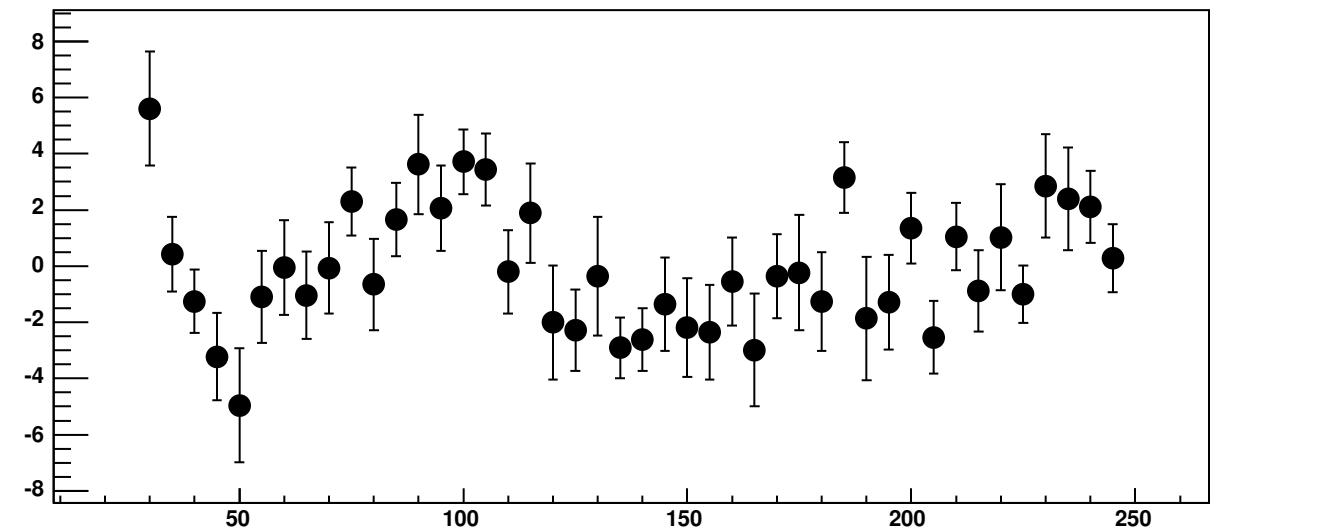
Chip 2, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold



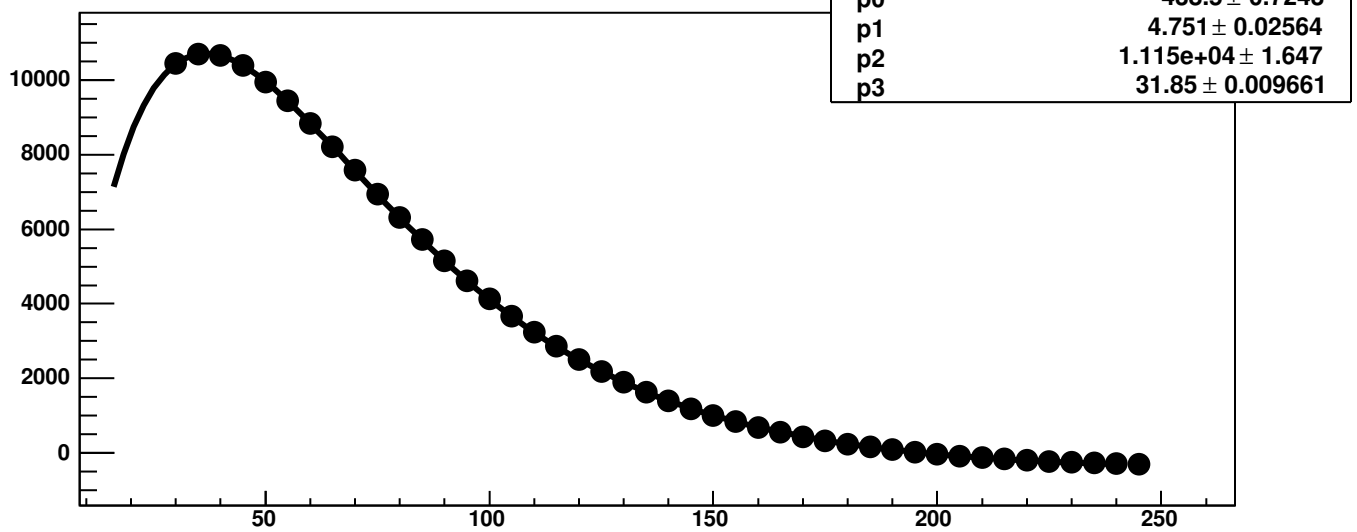
Chip 2, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



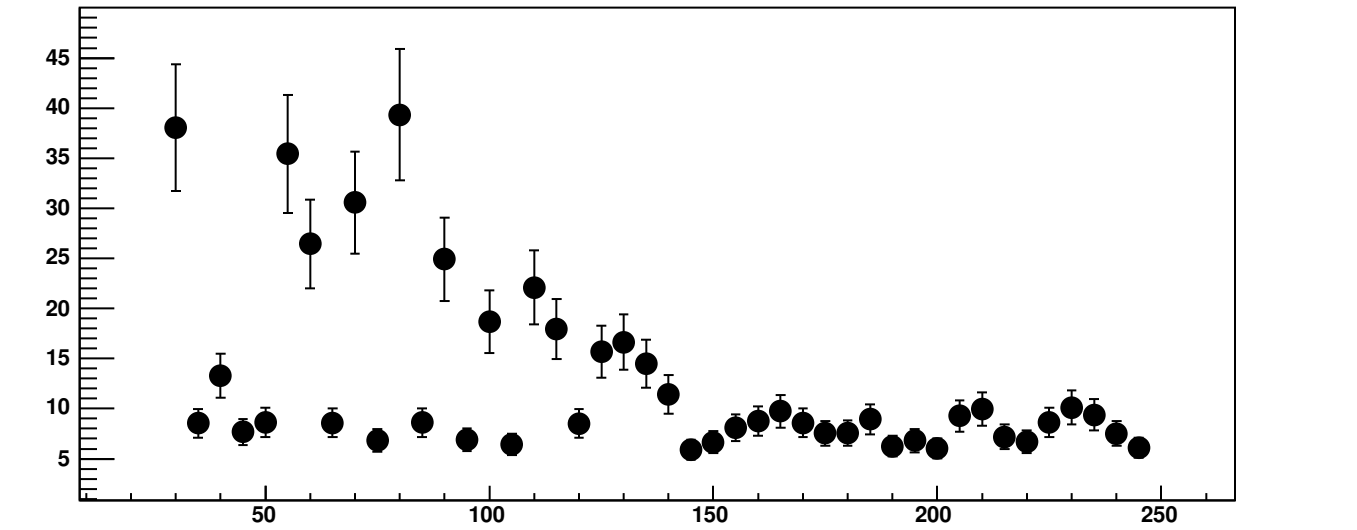
Chip 2, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold



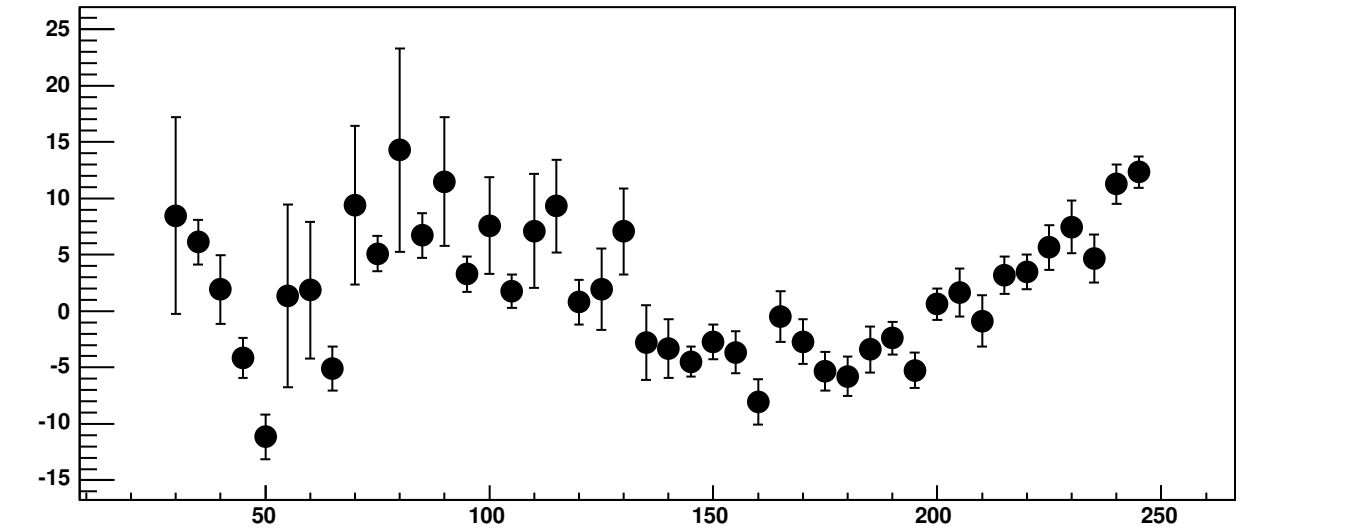
Chip 2, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold



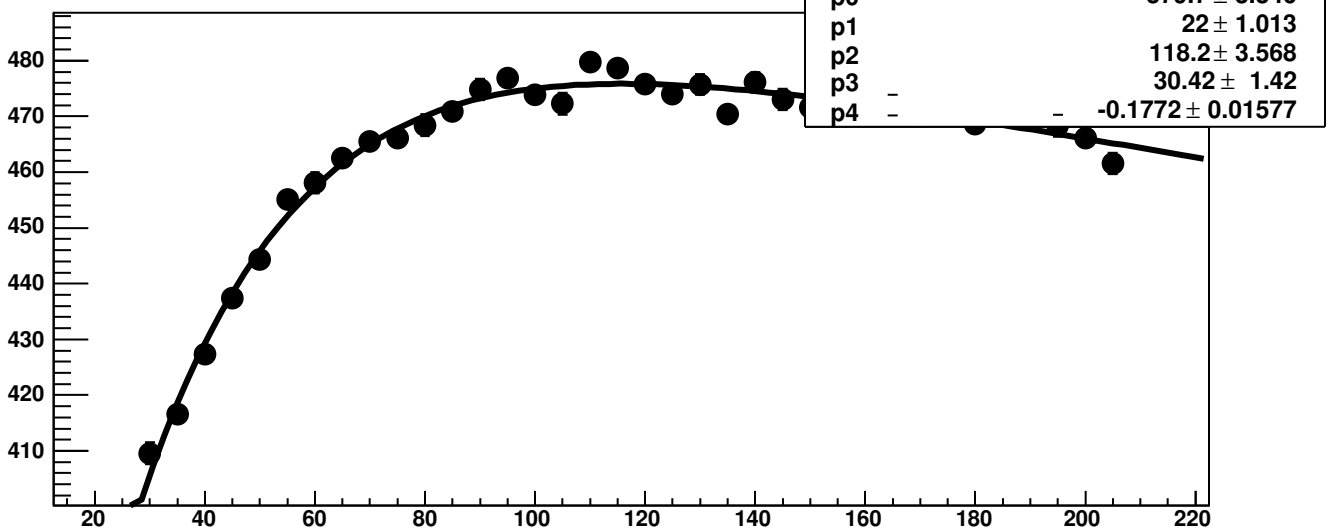
Chip 2, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold



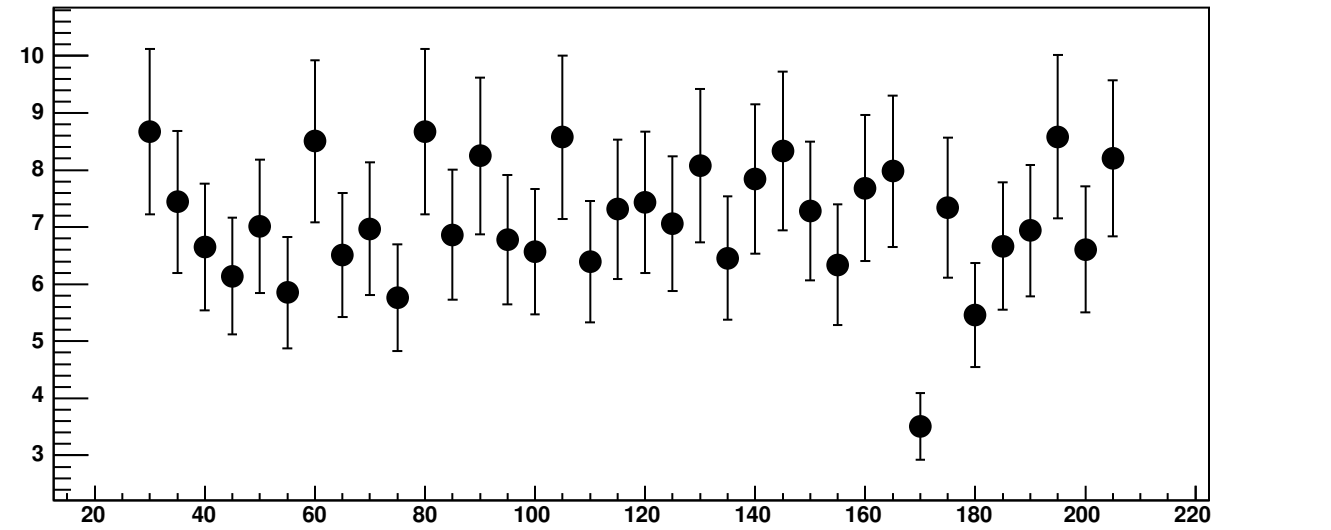
Chip 2, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold



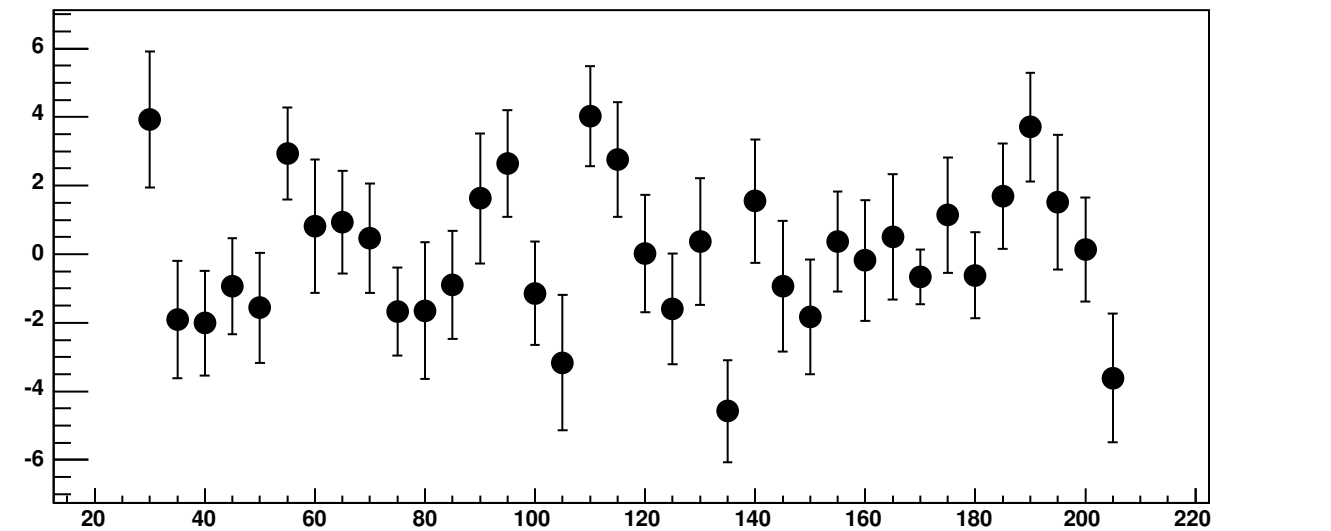
Chip 2, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold



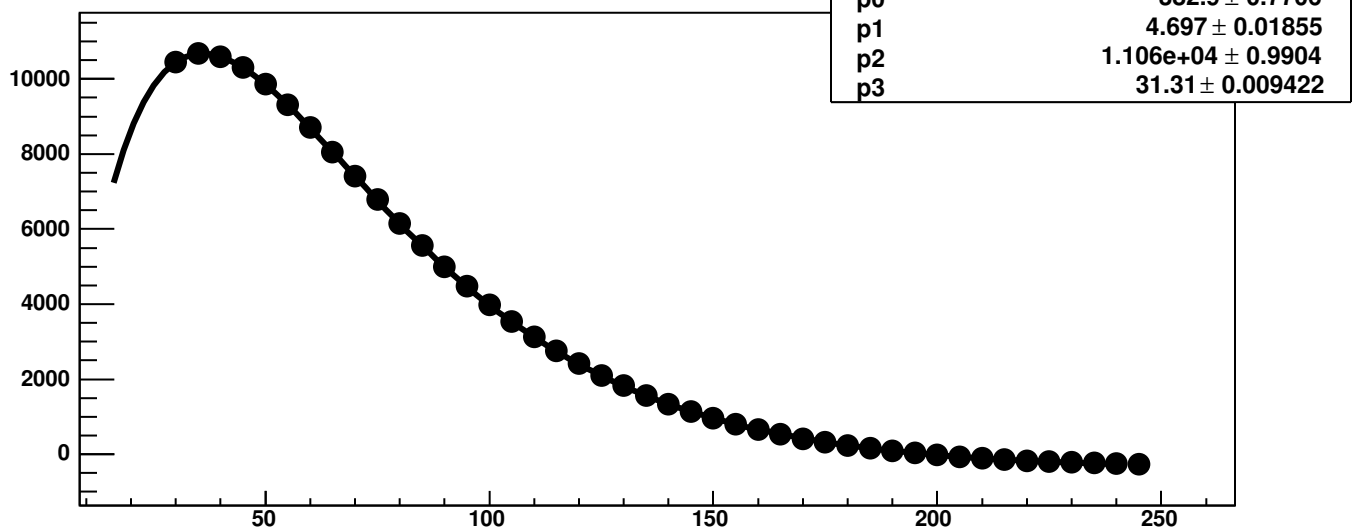
Chip 2, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



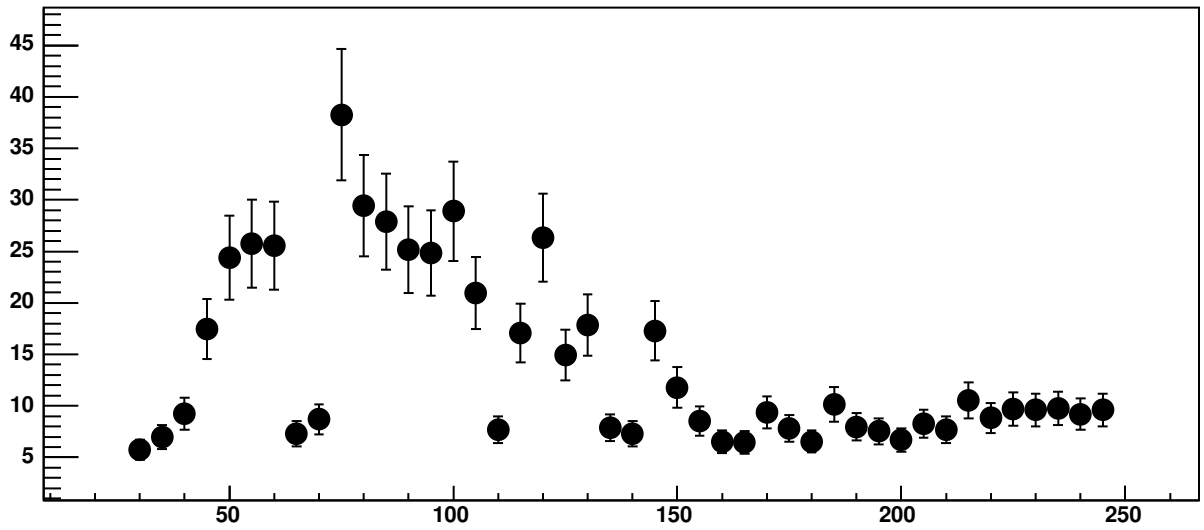
Chip 2, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold



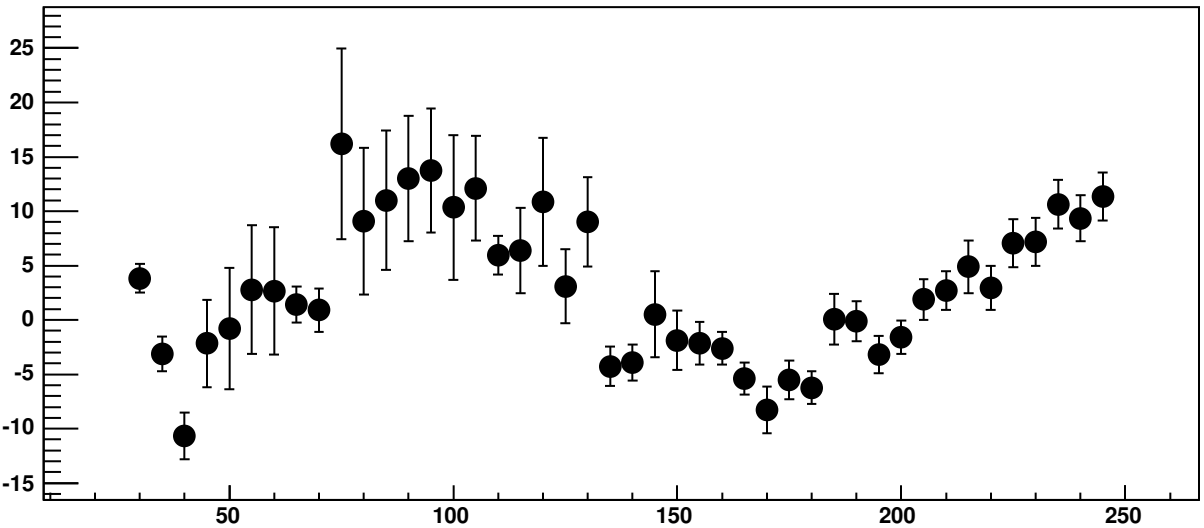
Chip 2, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold

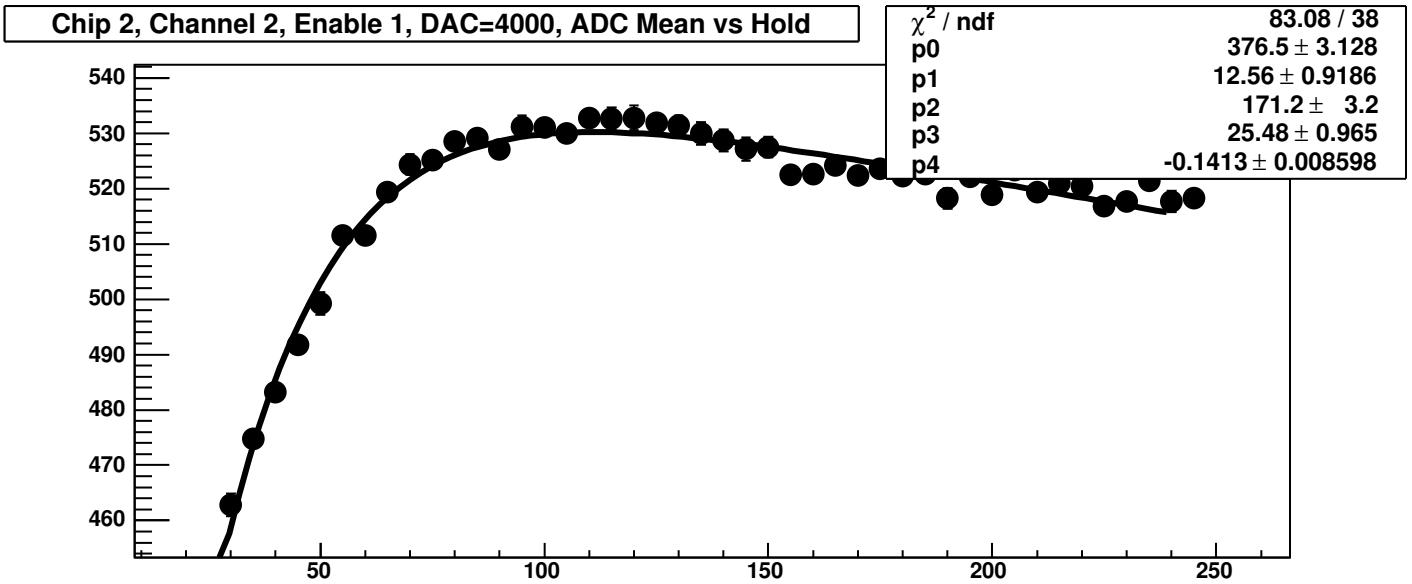


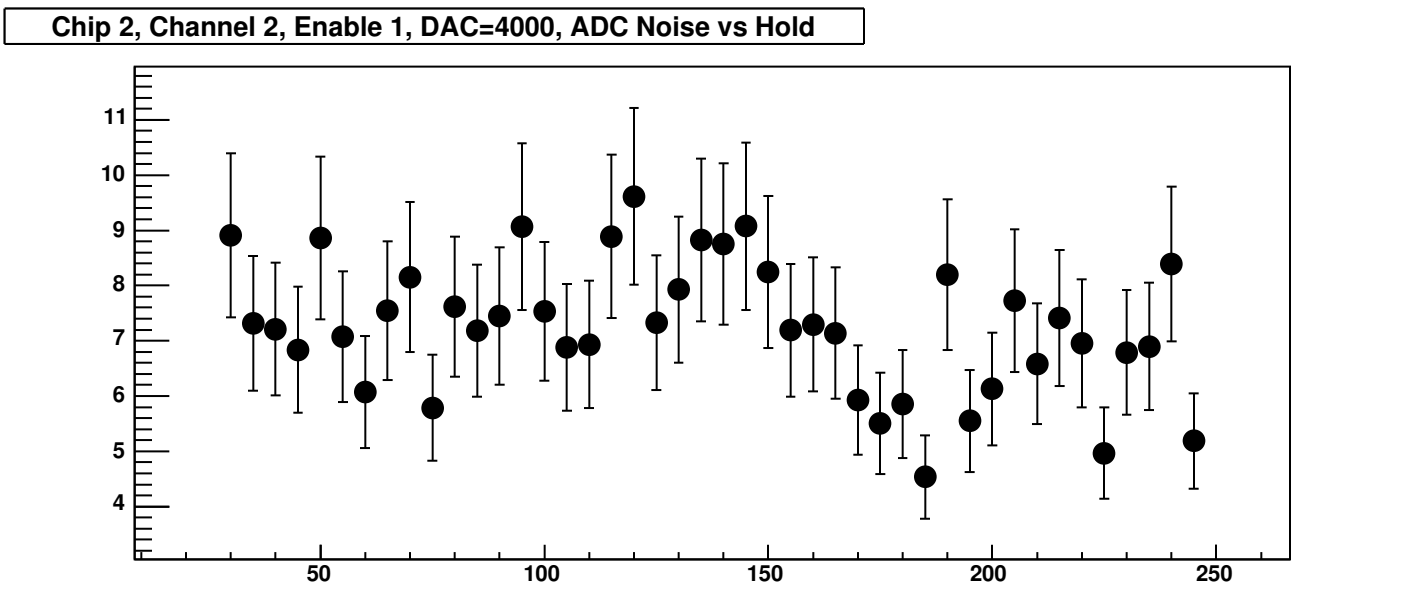
Chip 2, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold

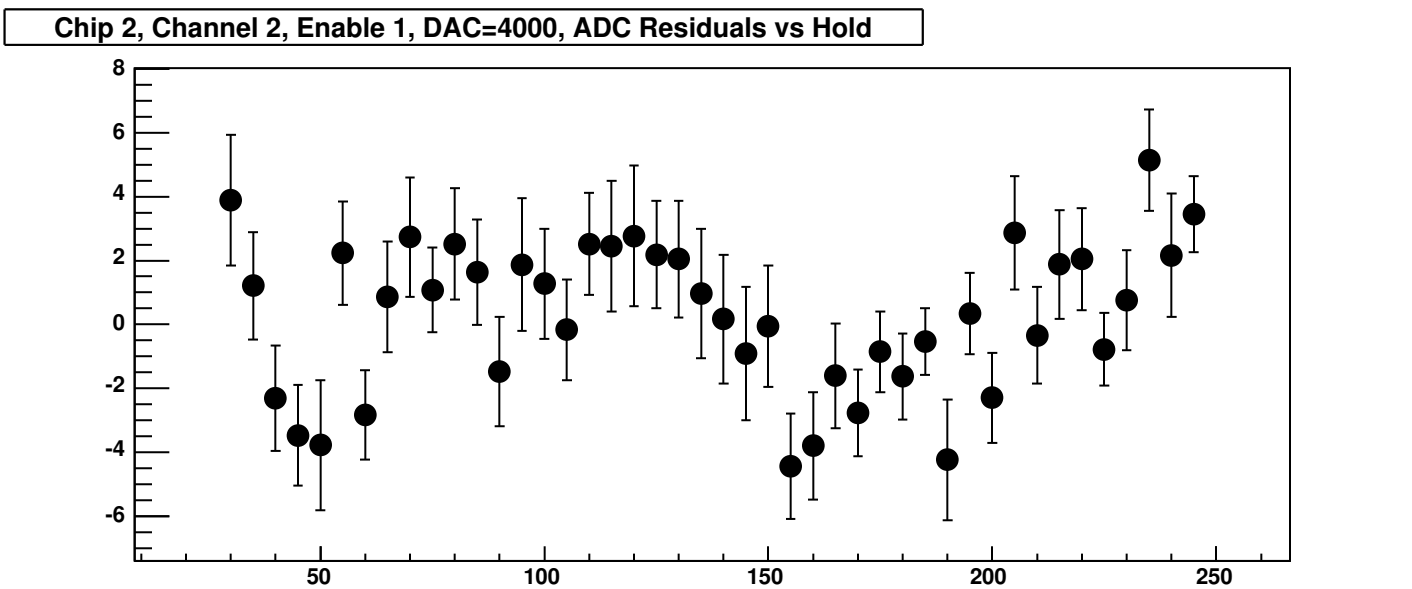


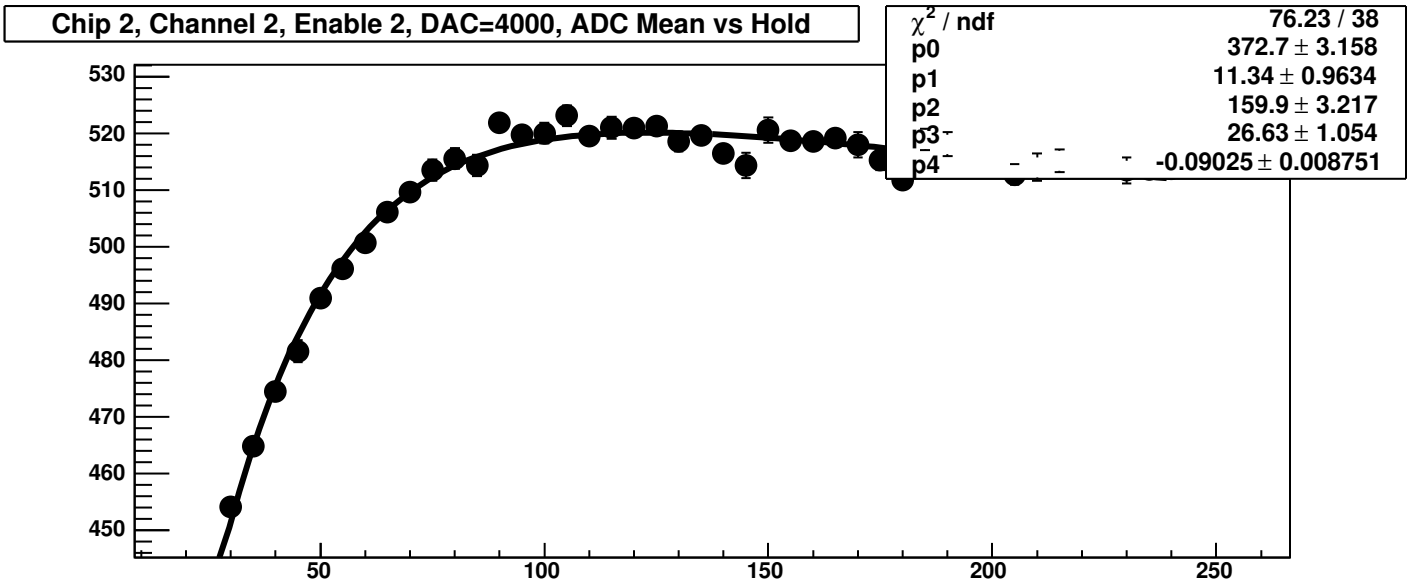
Chip 2, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold

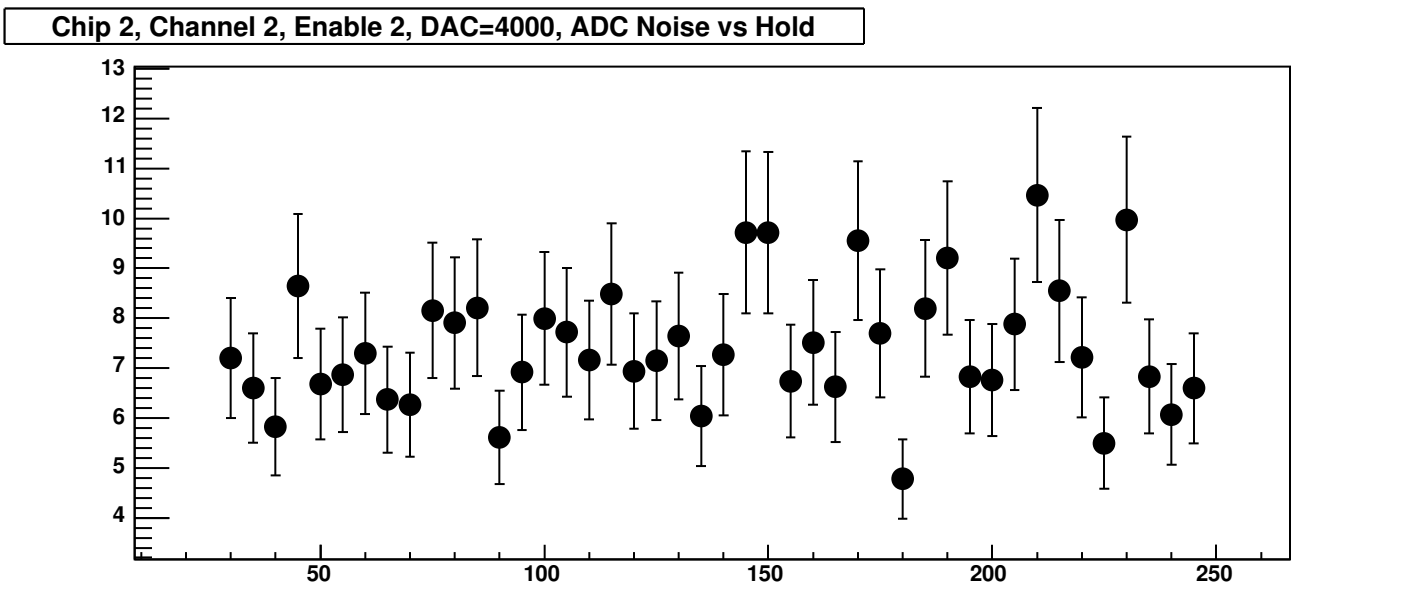


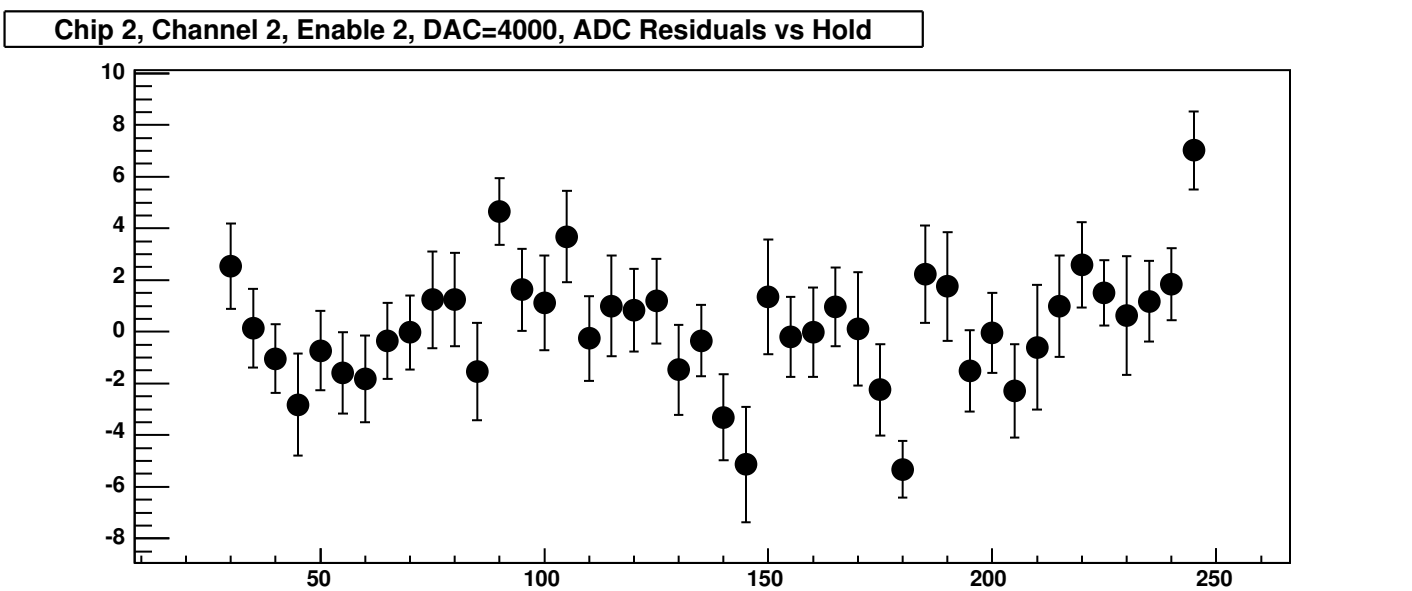

Chip 2, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 83.08 / 38
p0 376.5 ± 3.128
p1 12.56 ± 0.9186
p2 171.2 ± 3.2
p3 25.48 ± 0.965
p4 -0.1413 ± 0.008598
540
530
520
510
500
490
480
470
460
50
100
150
200
250


Chip 2, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250

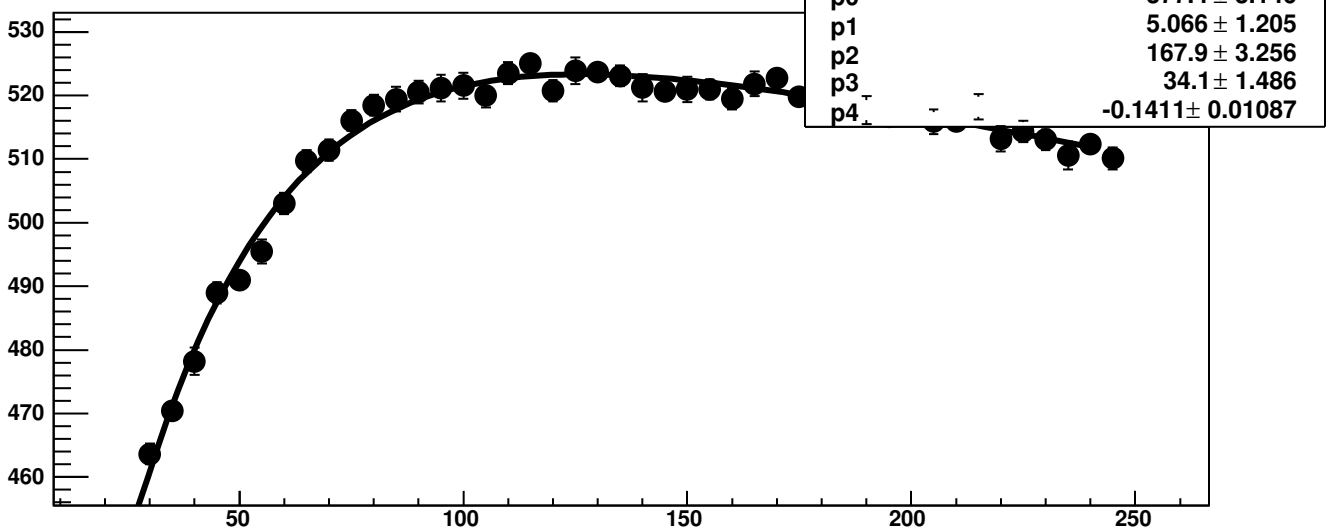

Chip 2, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
50
100
150
200
250


Chip 2, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 76.23 / 38
p0 372.7 ± 3.158
p1 11.34 ± 0.9634
p2 159.9 ± 3.217
p3 26.63 ± 1.054
p4 -0.09025 ± 0.008751
530
520
510
500
490
480
470
460
450
50
100
150
200
250

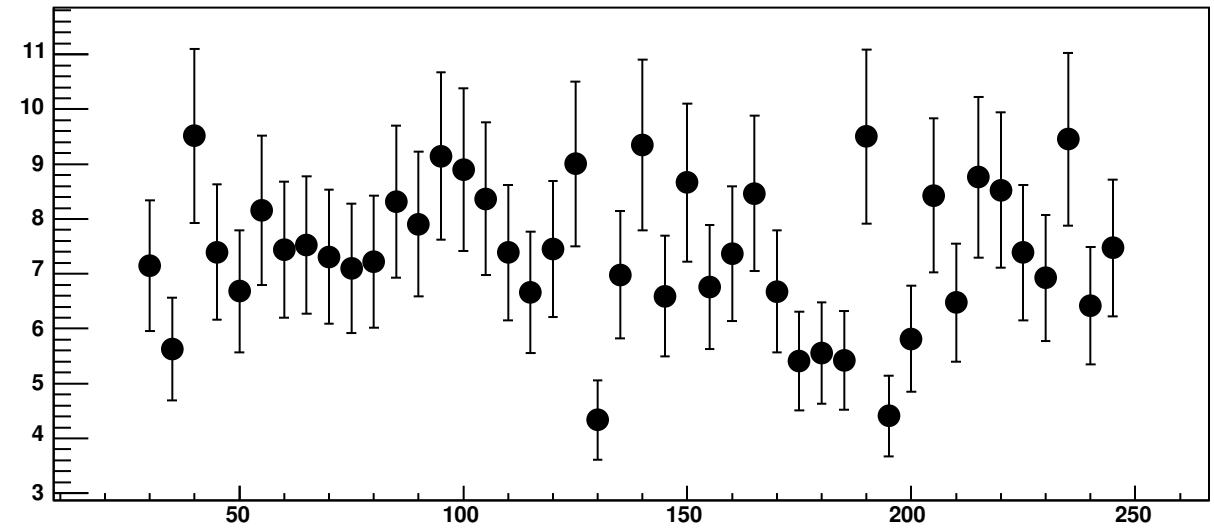

Chip 2, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 2, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

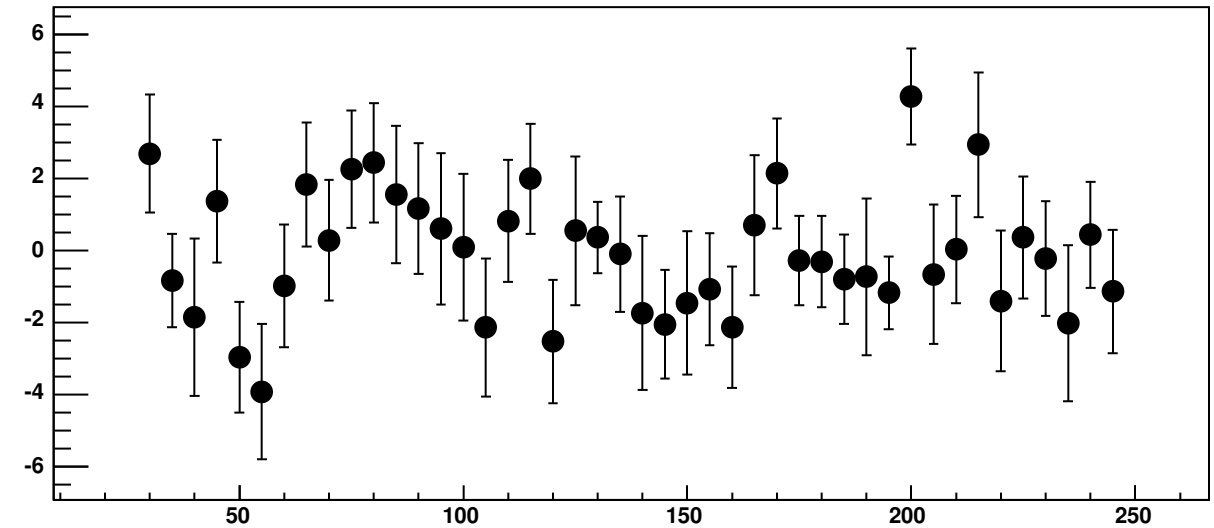
Chip 2, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold

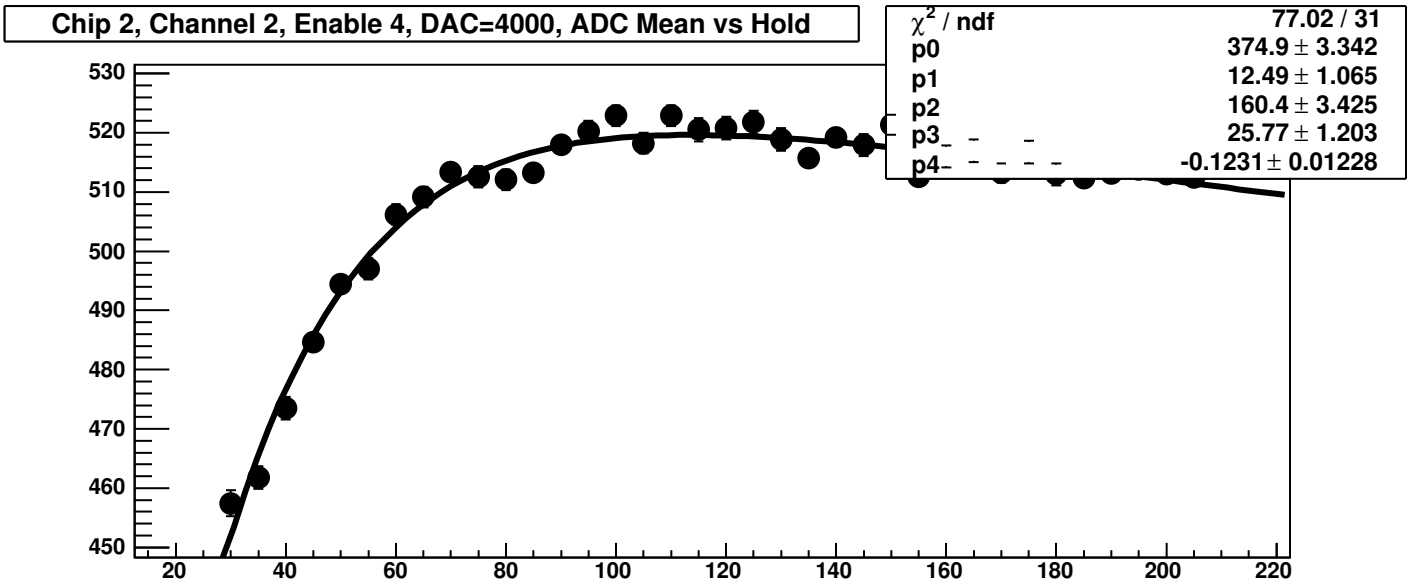


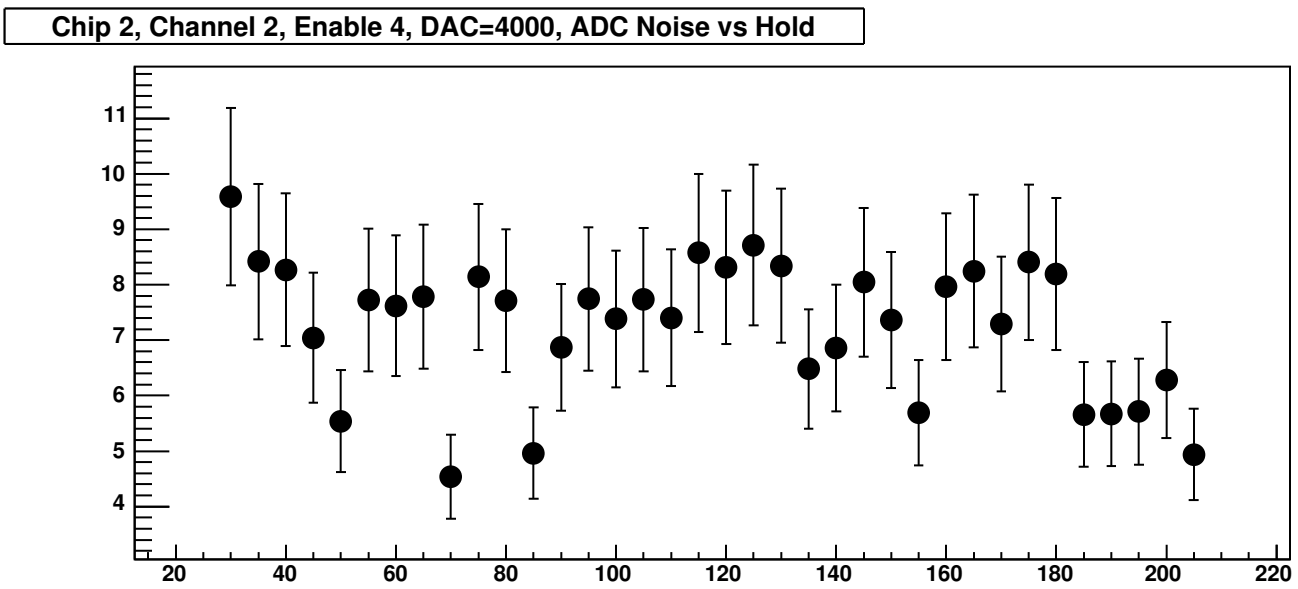
Chip 2, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold

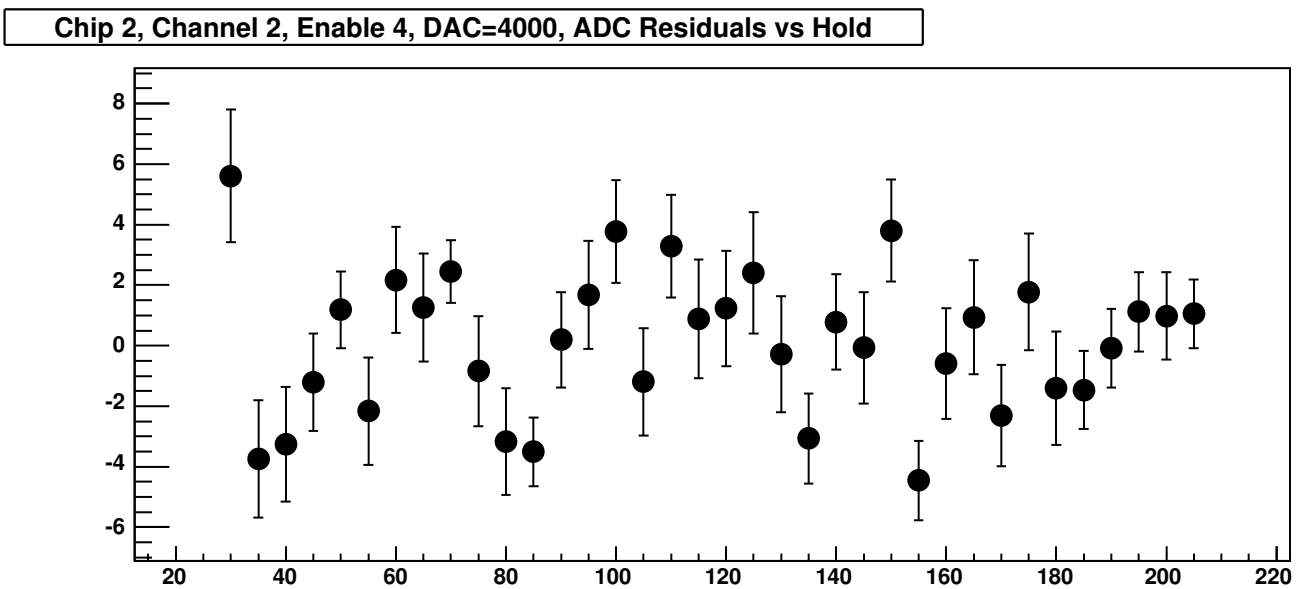


Chip 2, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold

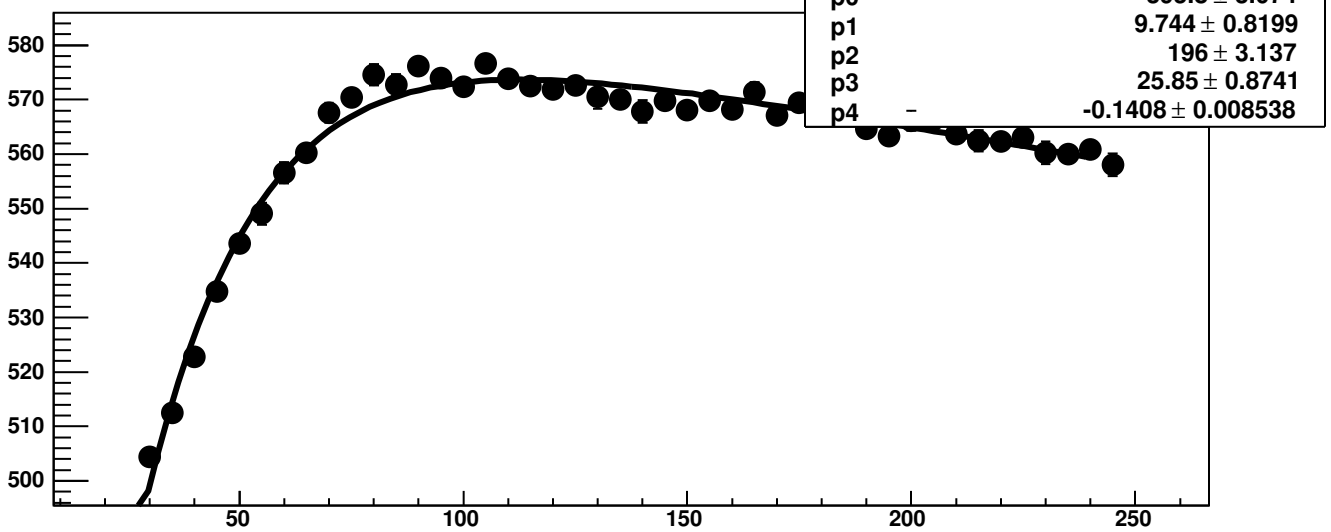



Chip 2, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 77.02 / 31
p0 374.9 ± 3.342
p1 12.49 ± 1.065
p2 160.4 ± 3.425
p3 25.77 ± 1.203
p4 -0.1231 ± 0.01228

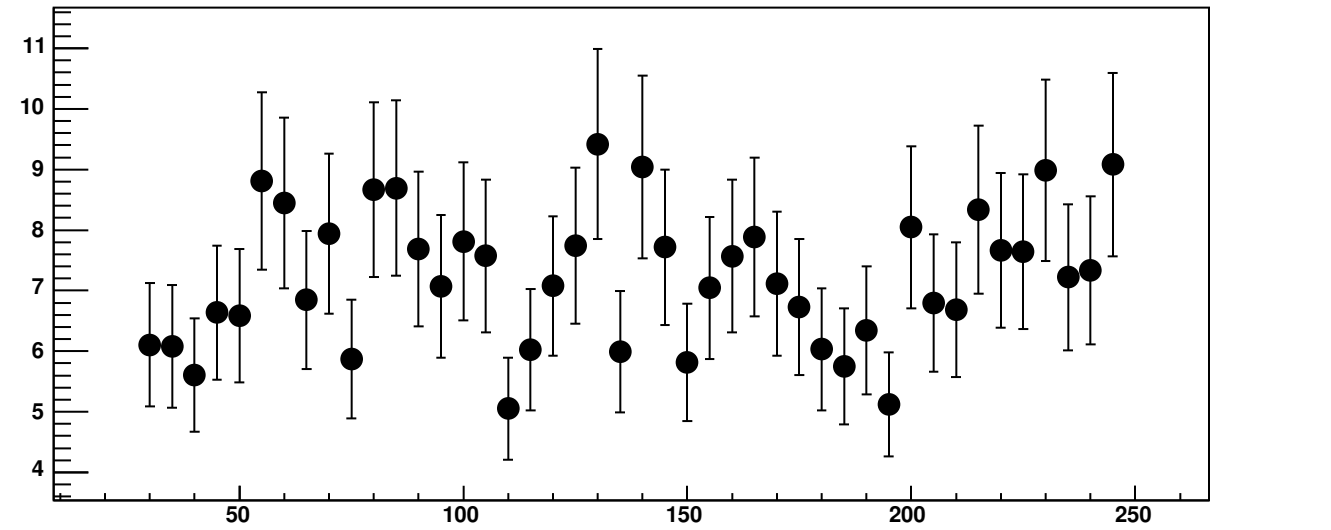

Chip 2, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold


Chip 2, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold

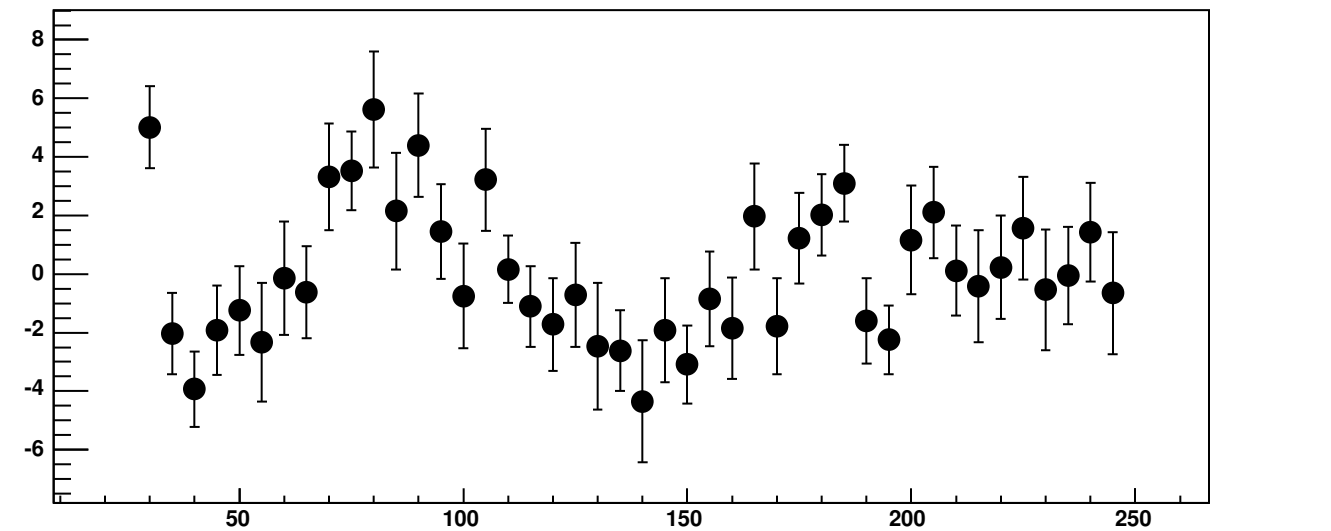
Chip 2, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold



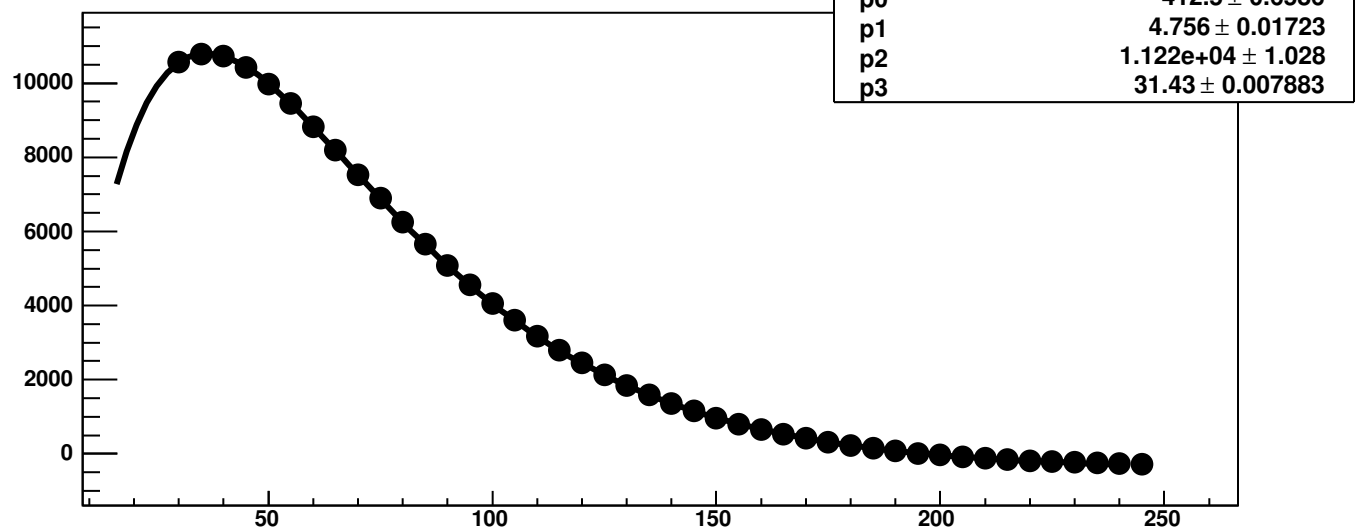
Chip 2, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



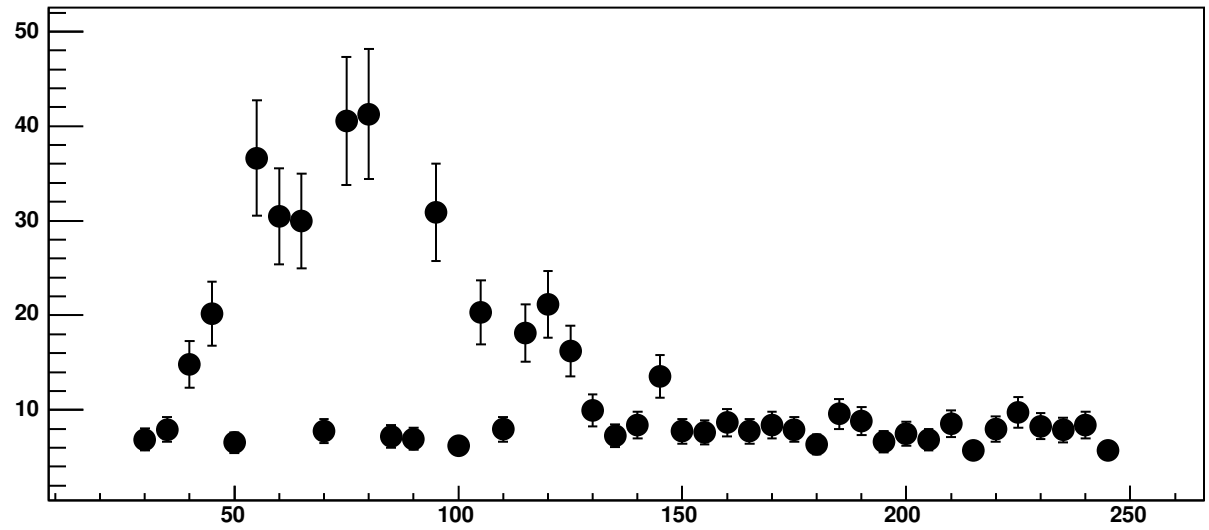
Chip 2, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold



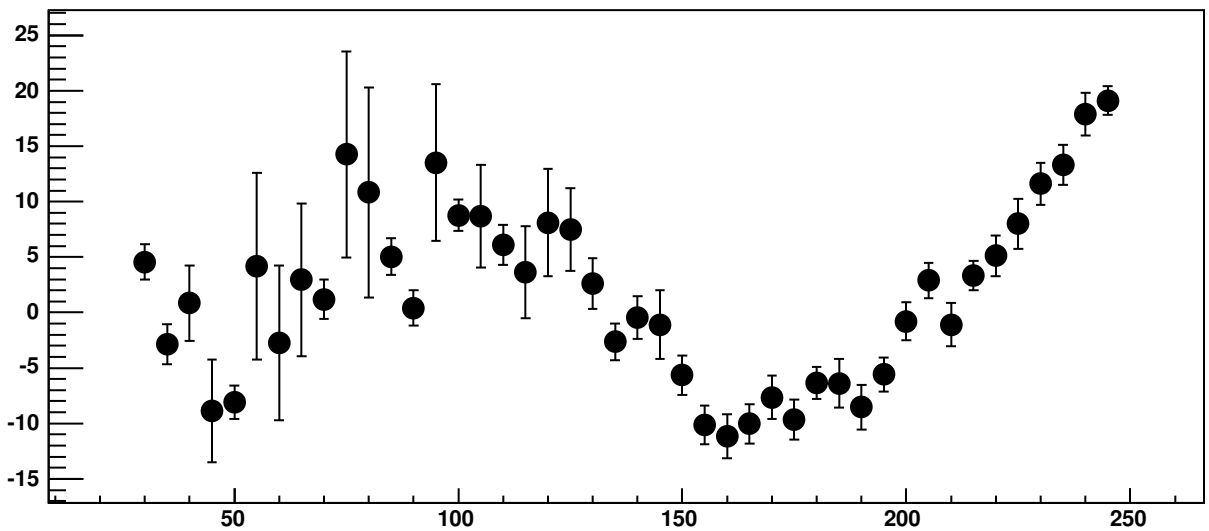
Chip 2, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold

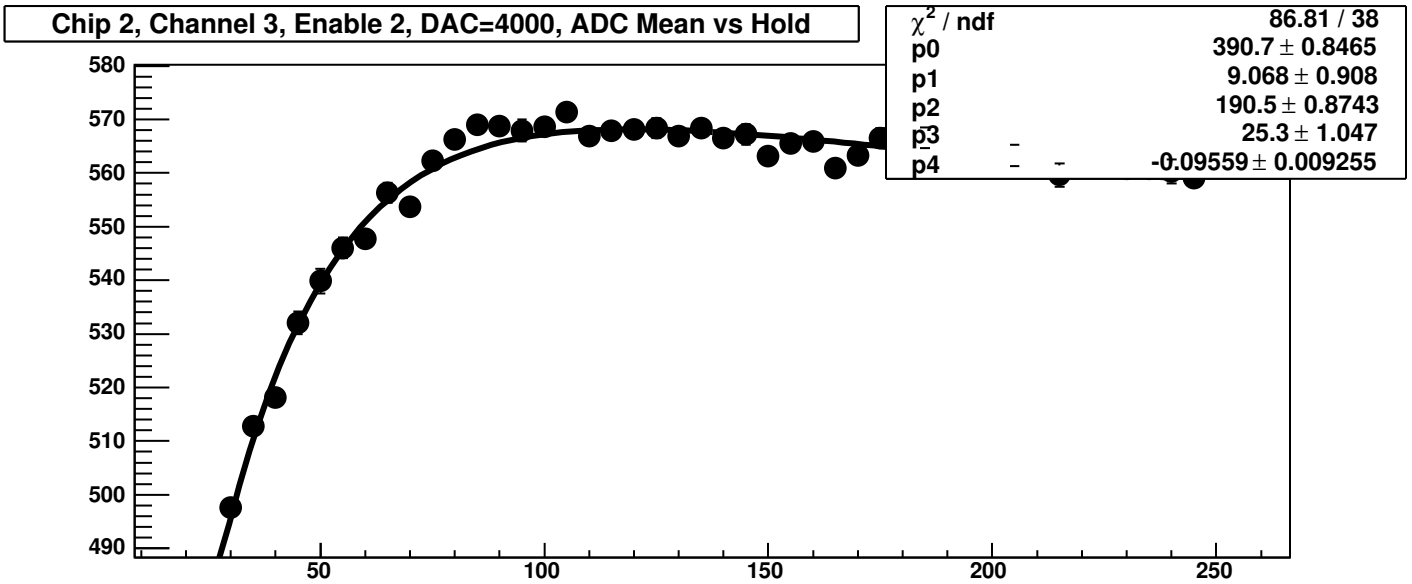


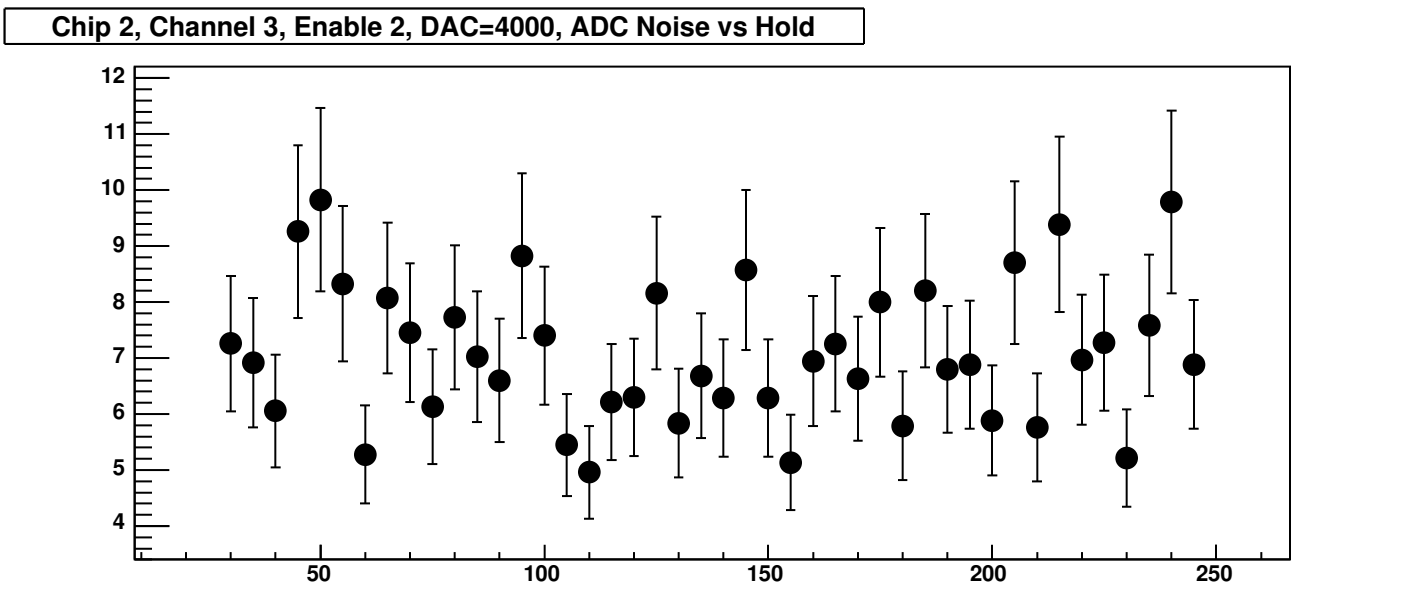
Chip 2, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold

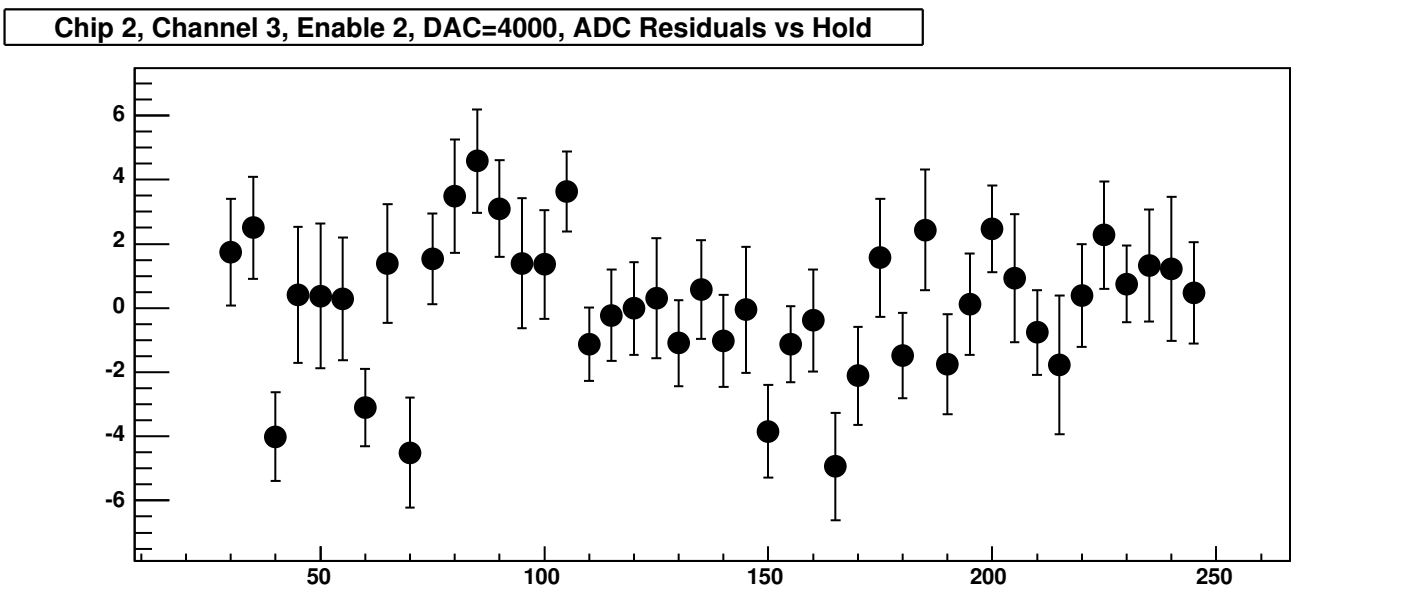


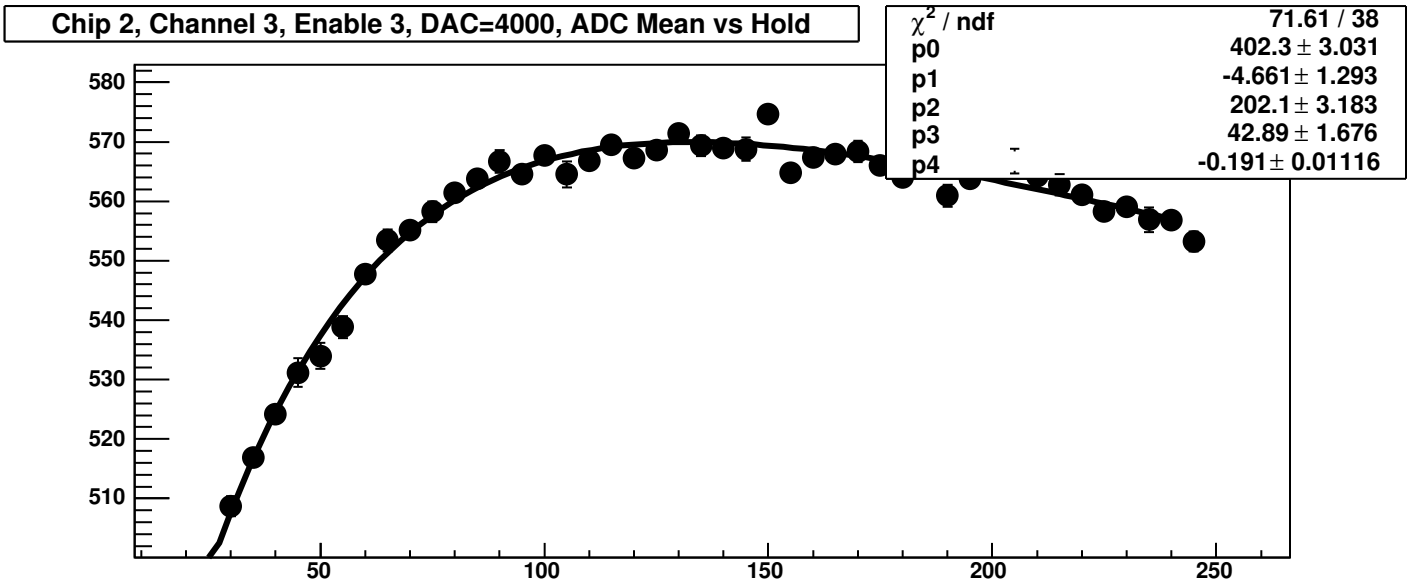
Chip 2, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold

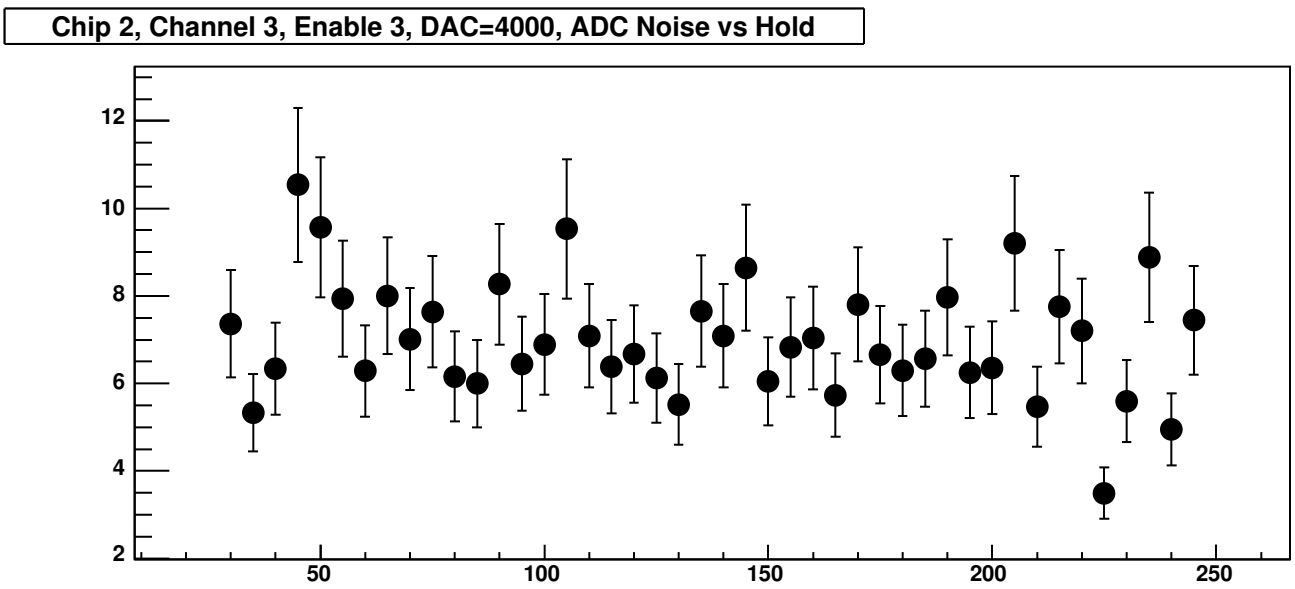


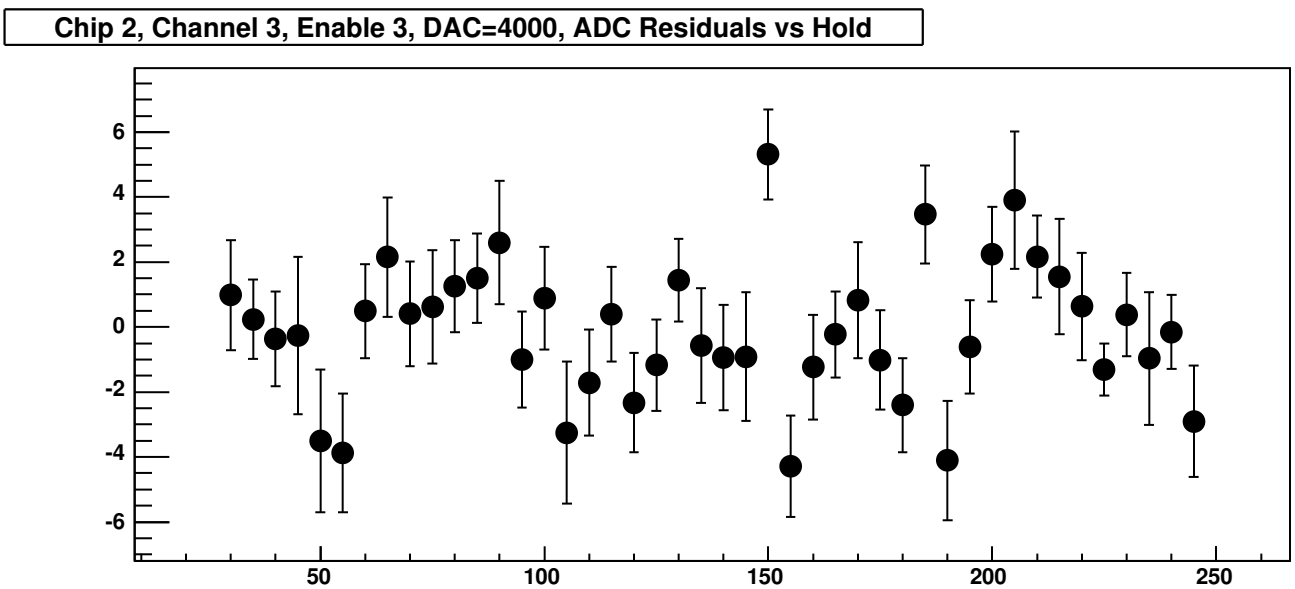

Chip 2, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 86.81 / 38
p0 390.7 ± 0.8465
p1 9.068 ± 0.908
p2 190.5 ± 0.8743
p3 25.3 ± 1.047
p4 -0.09559 ± 0.009255
580
570
560
550
540
530
520
510
500
490
50
100
150
200
250

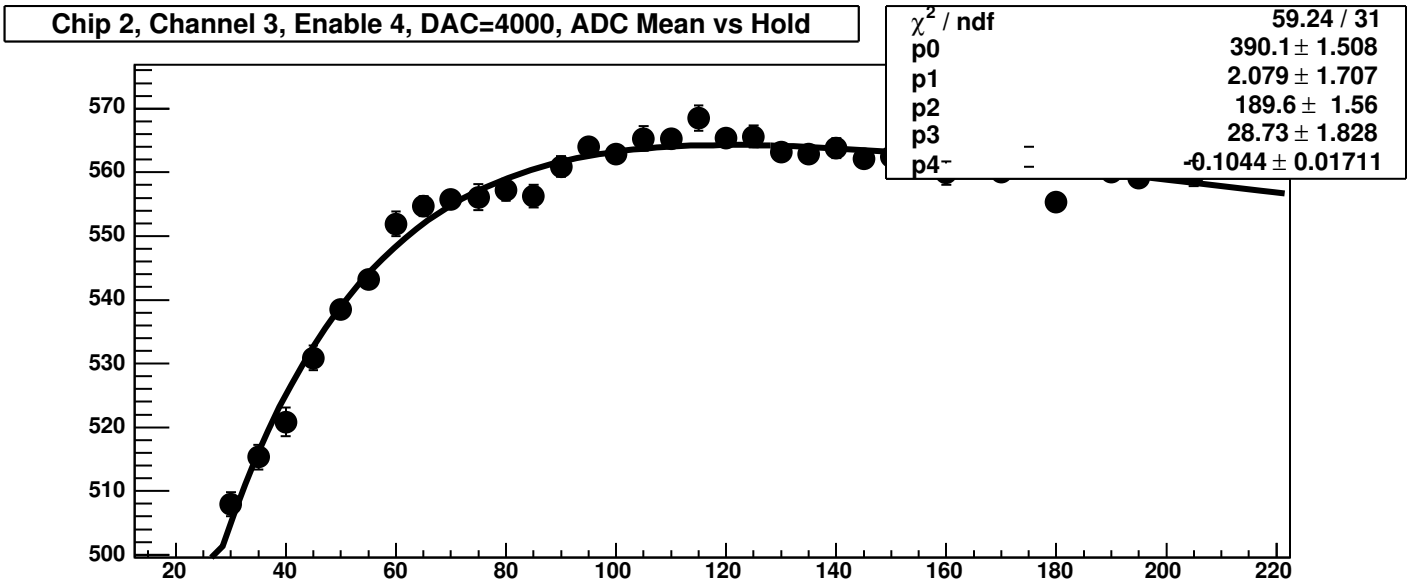

Chip 2, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

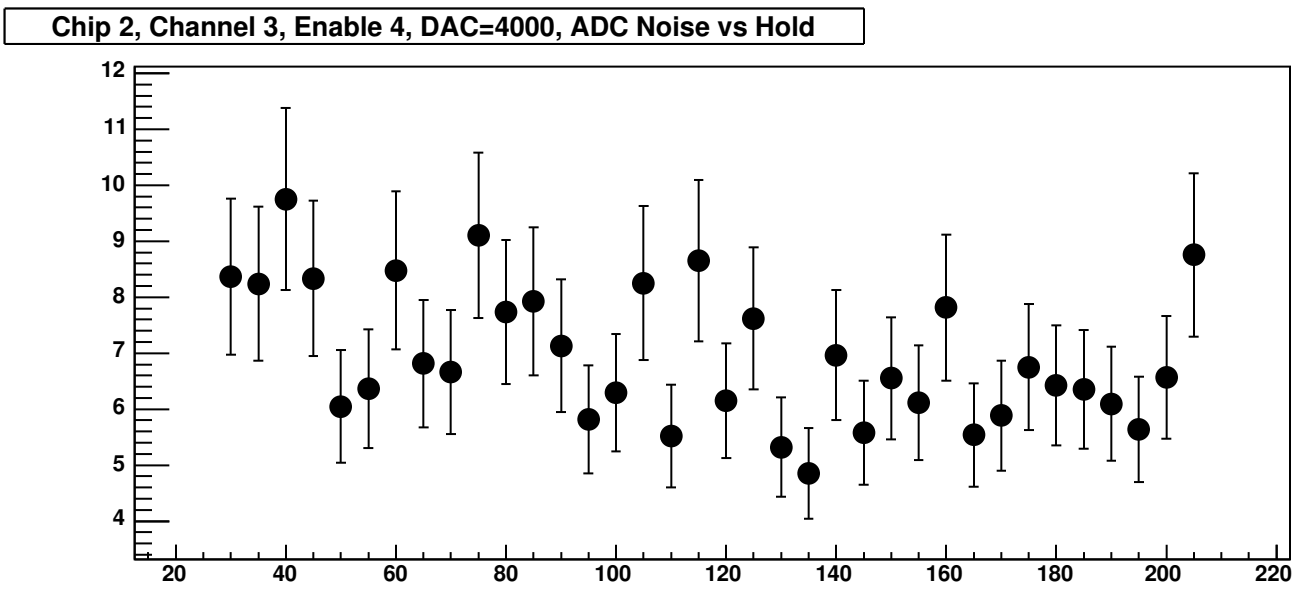

Chip 2, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
50
100
150
200
250

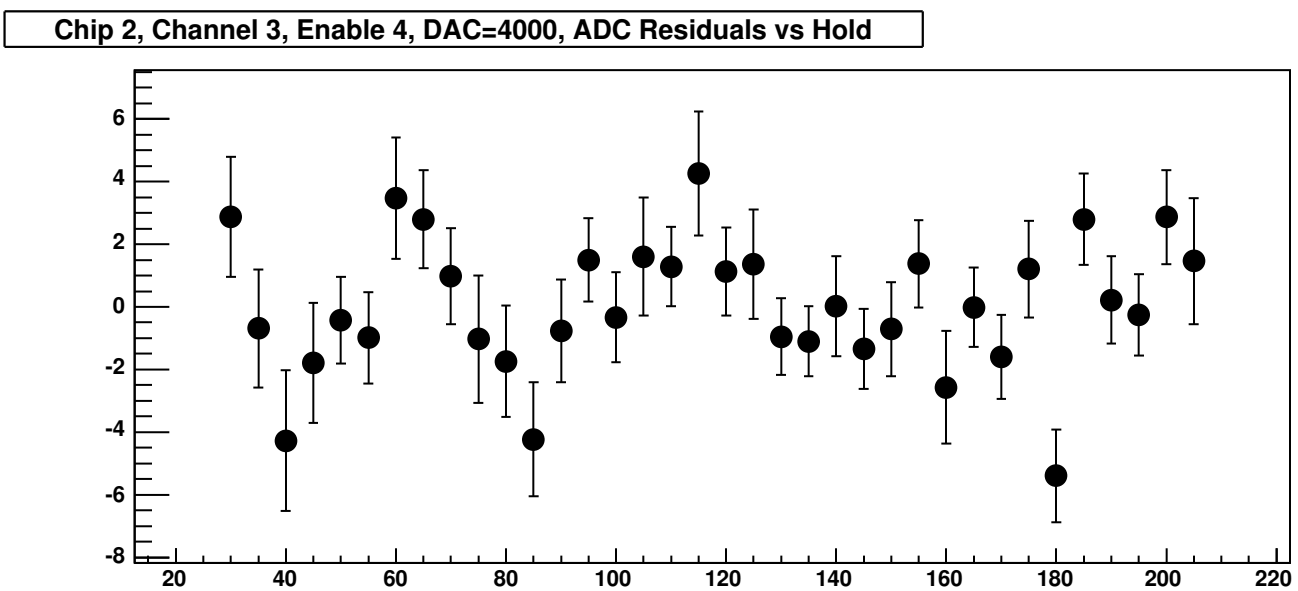

Chip 2, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 71.61 / 38
p0 402.3 ± 3.031
p1 -4.661 ± 1.293
p2 202.1 ± 3.183
p3 42.89 ± 1.676
p4 -0.191 ± 0.01116
580
570
560
550
540
530
520
510
50
100
150
200
250


Chip 2, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
2
50
100
150
200
250

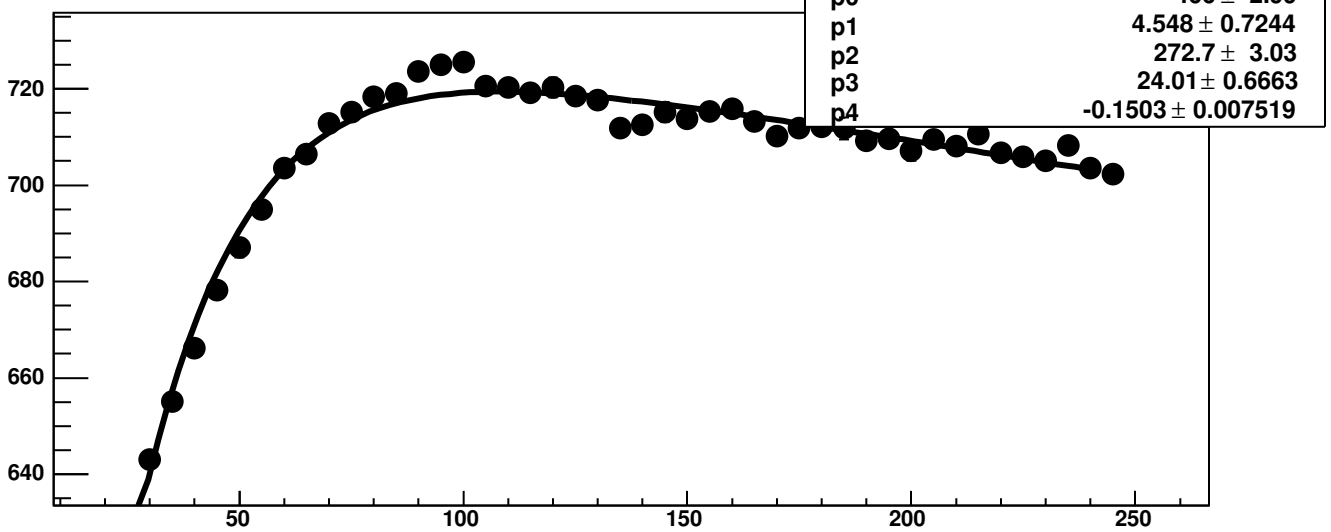

Chip 2, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
50
100
150
200
250


Chip 2, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 59.24 / 31
p0 390.1 ± 1.508
p1 2.079 ± 1.707
p2 189.6 ± 1.56
p3 28.73 ± 1.828
p4 -0.1044 ± 0.01711
570
560
550
540
530
520
510
500
20
40
60
80
100
120
140
160
180
200
220

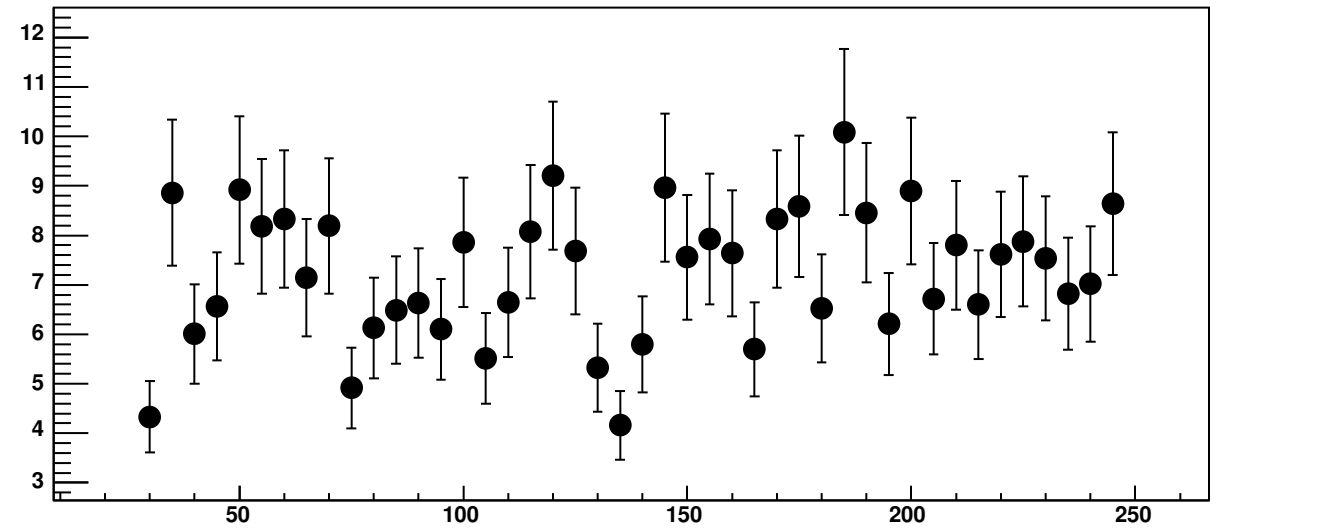

Chip 2, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
20
40
60
80
100
120
140
160
180
200
220


Chip 2, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
-8
20
40
60
80
100
120
140
160
180
200
220

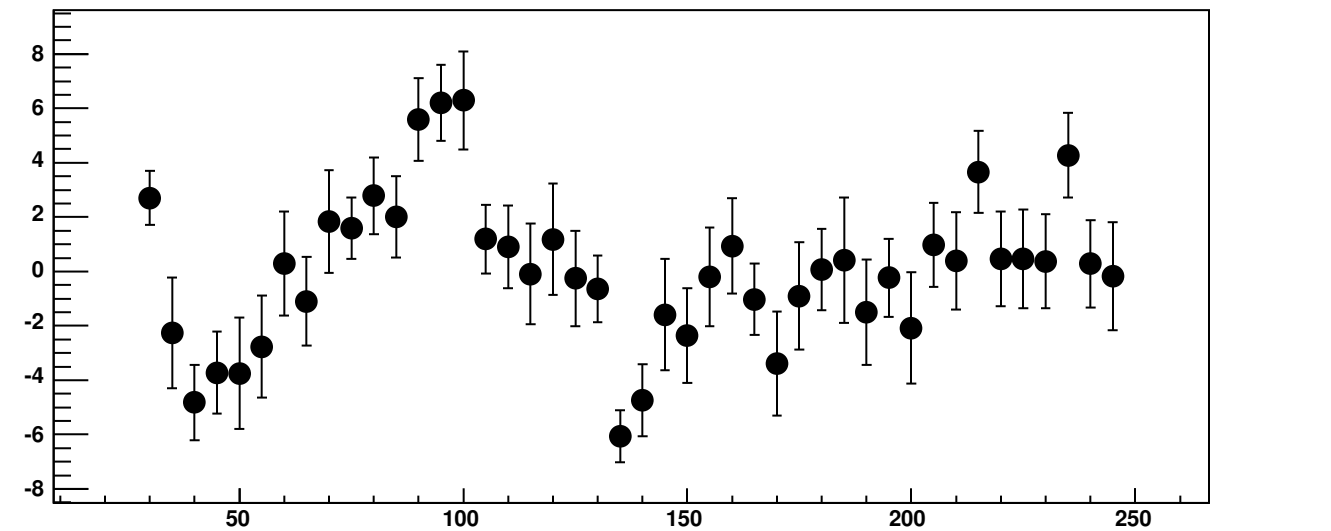
Chip 2, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold



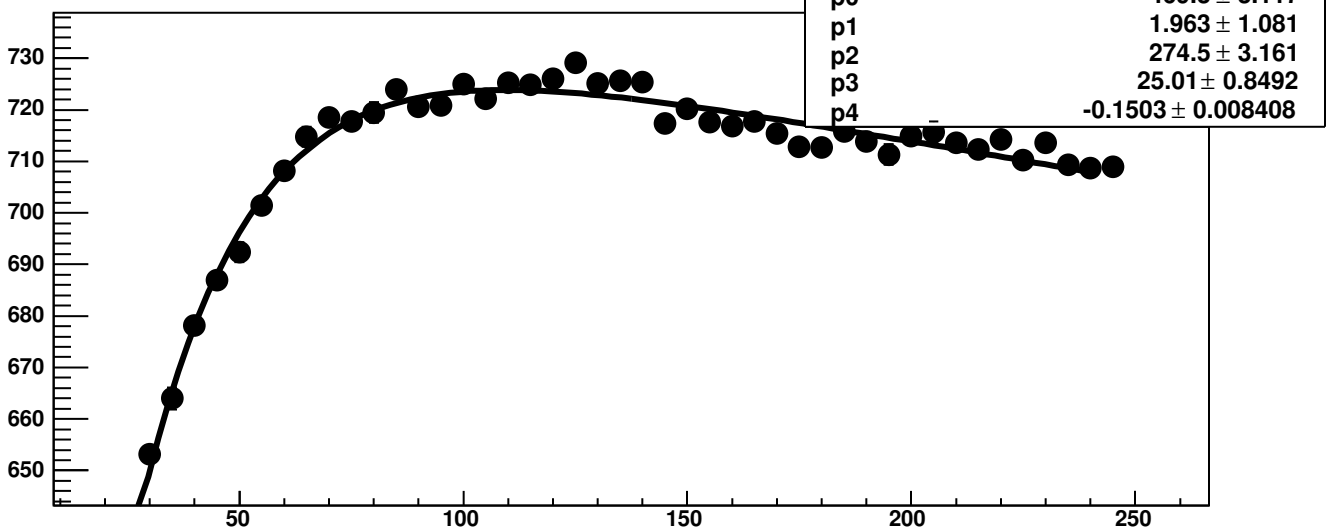
Chip 2, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



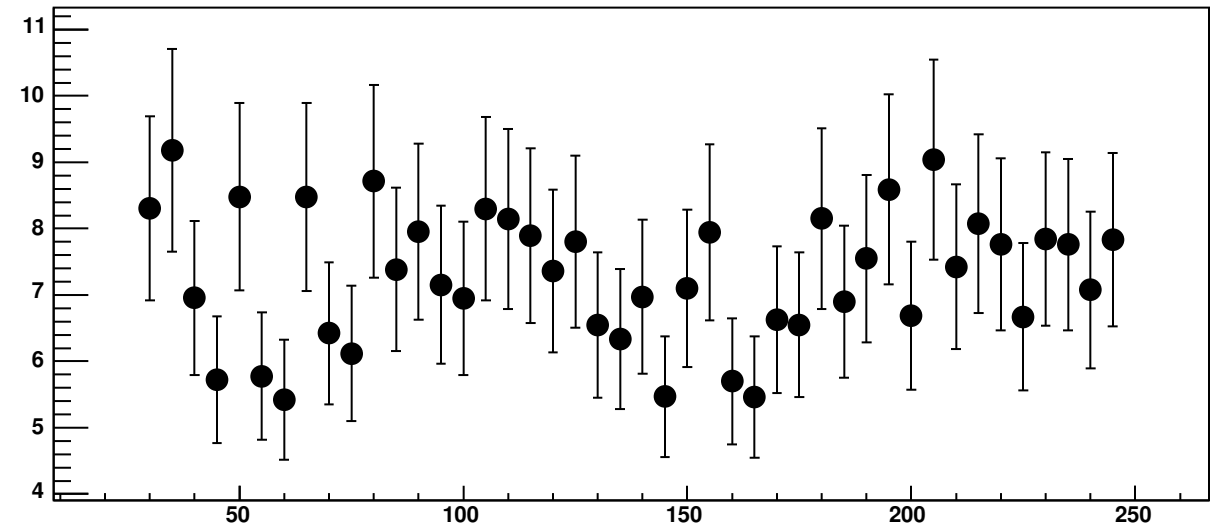
Chip 2, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold



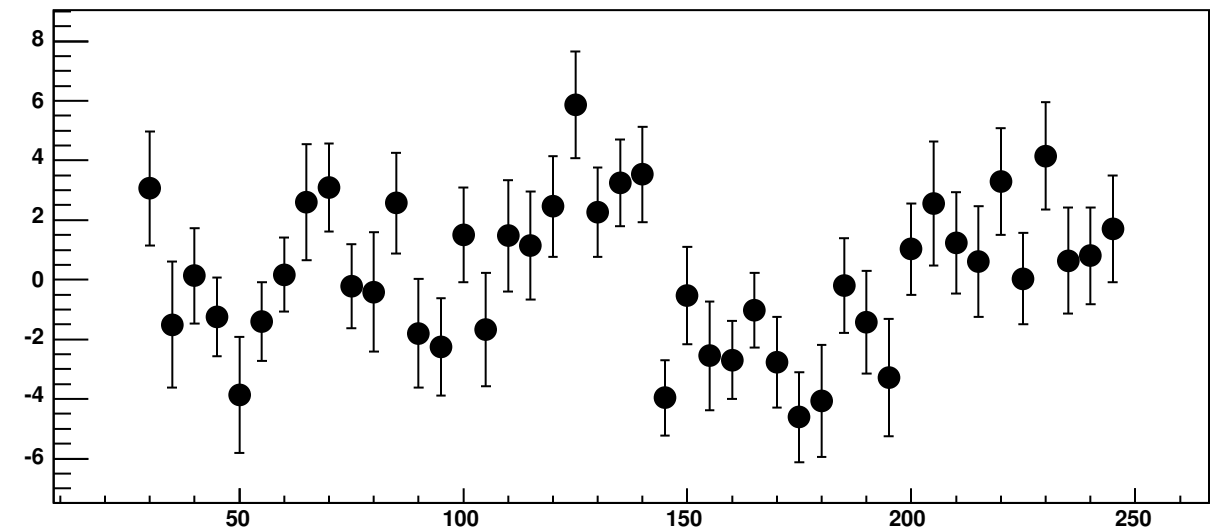
Chip 2, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold

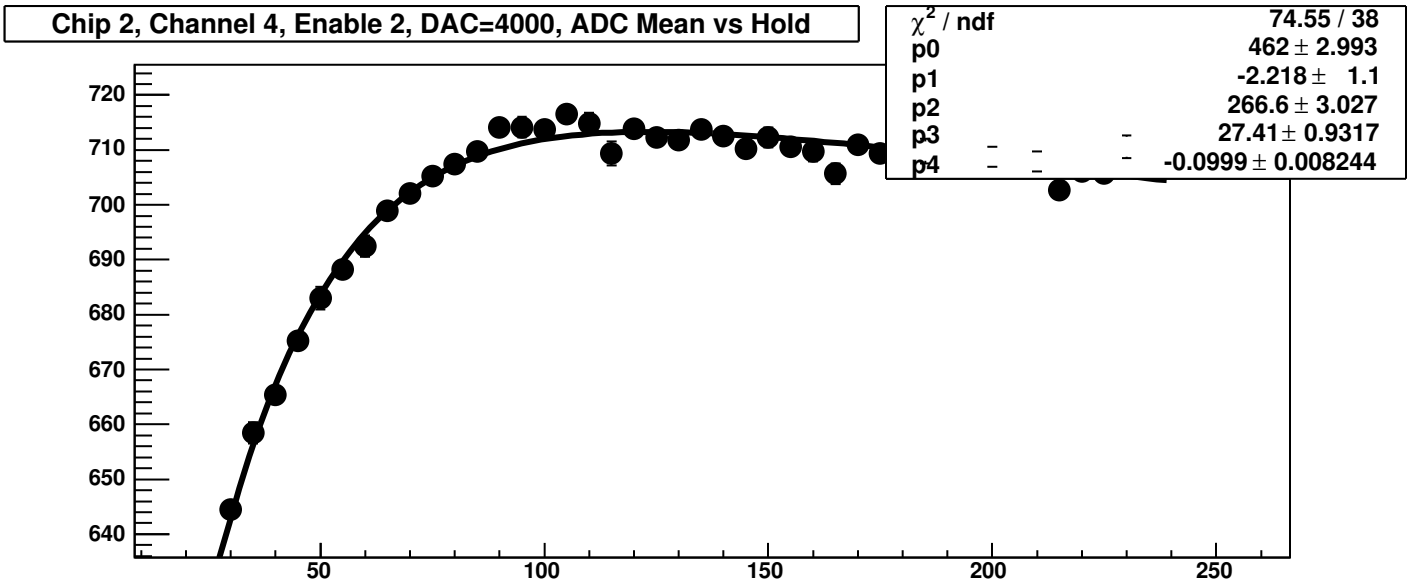


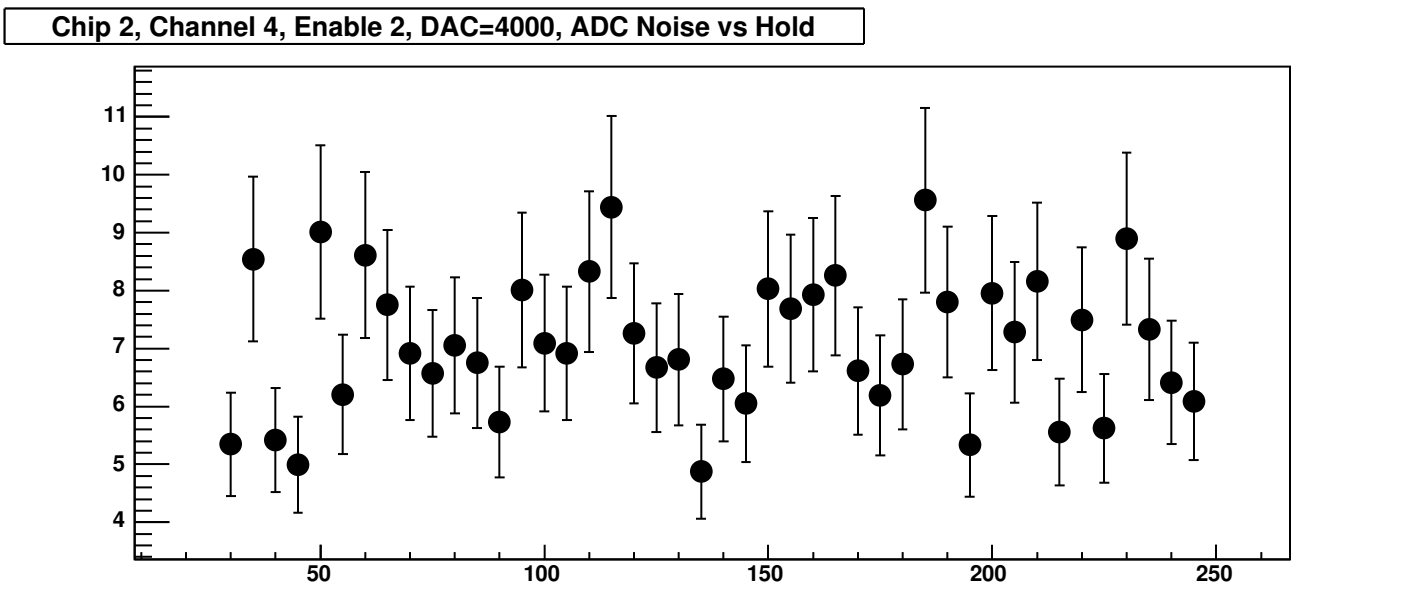
Chip 2, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold

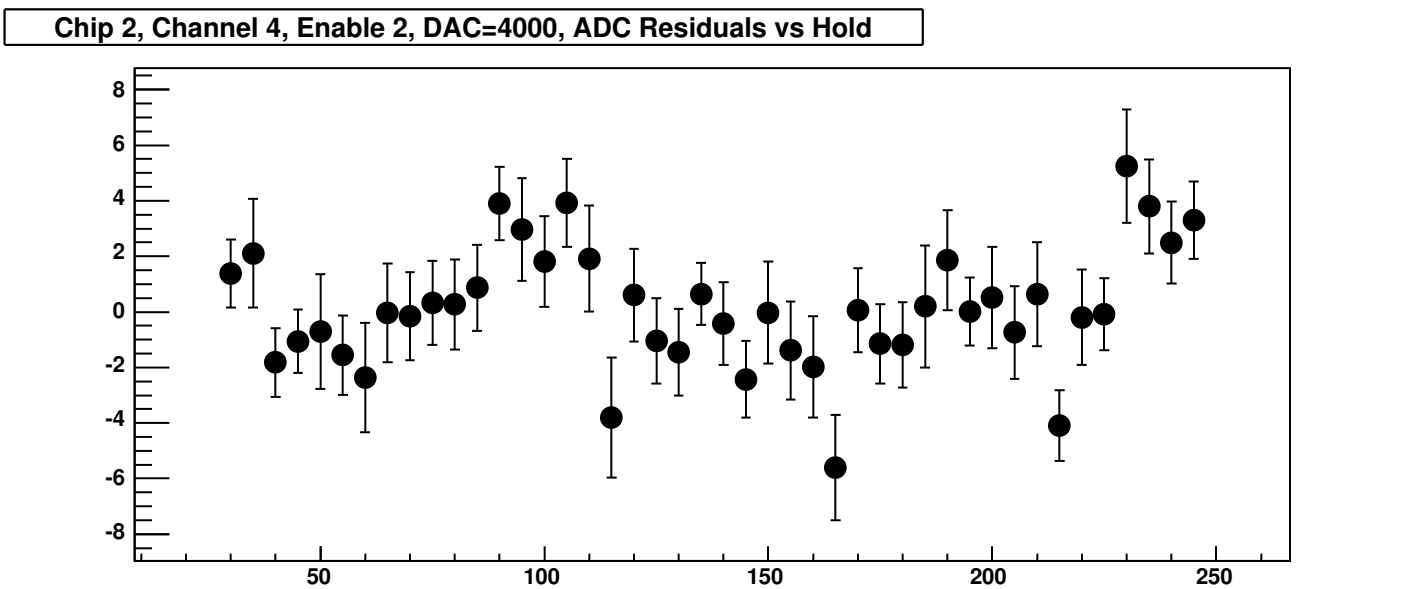


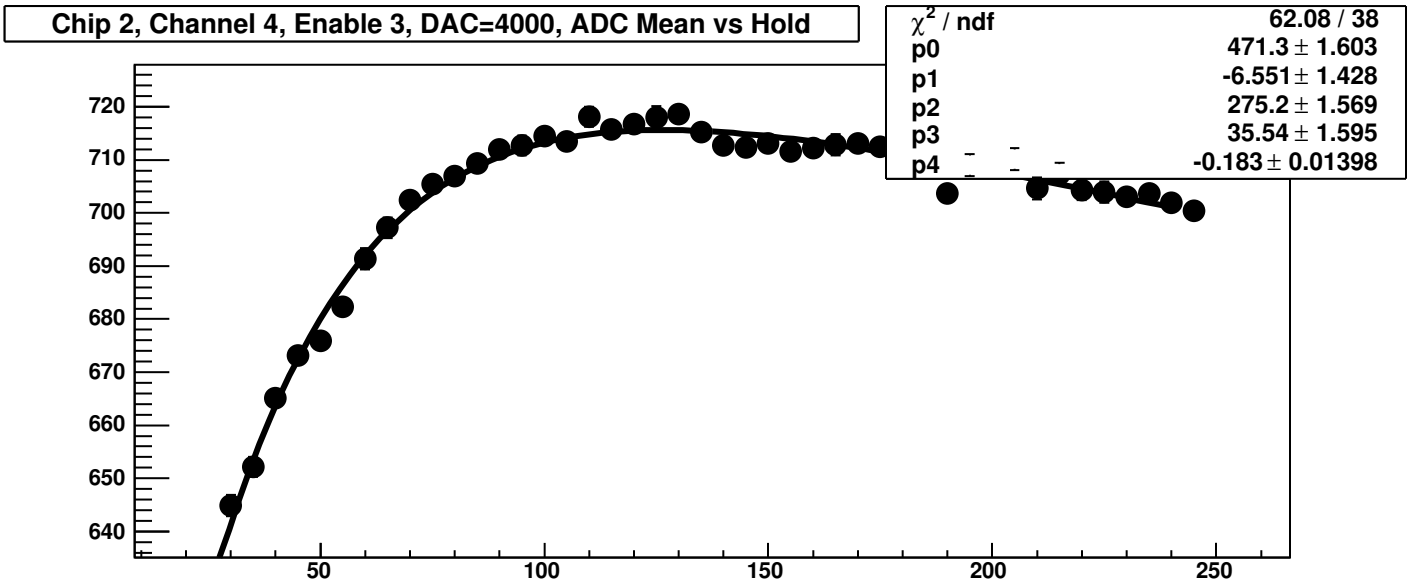
Chip 2, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold

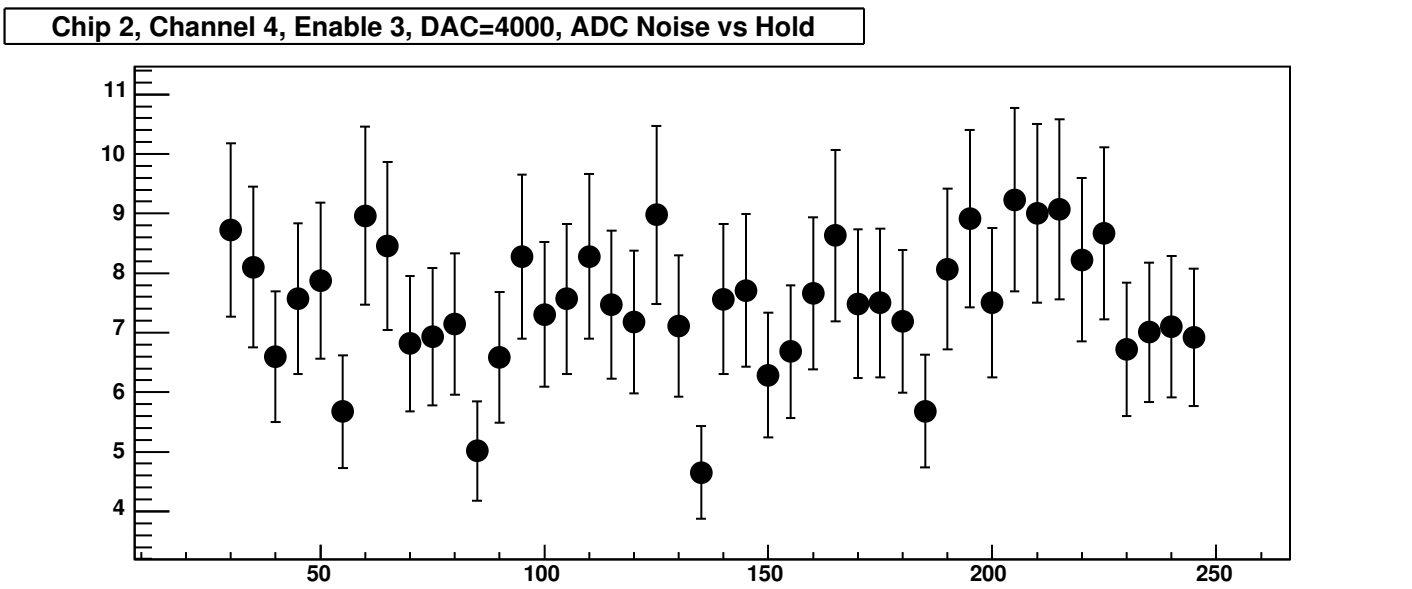


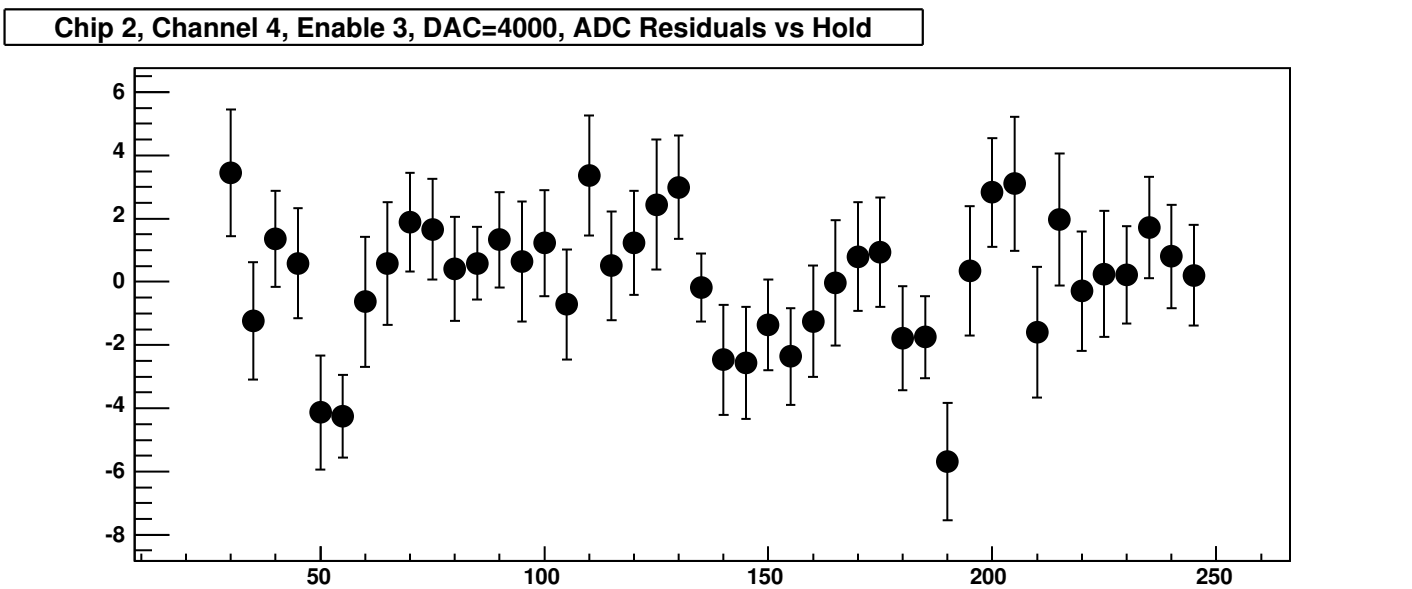

Chip 2, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 74.55 / 38
p0 462 ± 2.993
p1 -2.218 ± 1.1
p2 266.6 ± 3.027
p3 27.41 ± 0.9317
p4 -0.0999 ± 0.008244
720
710
700
690
680
670
660
650
640
50
100
150
200
250

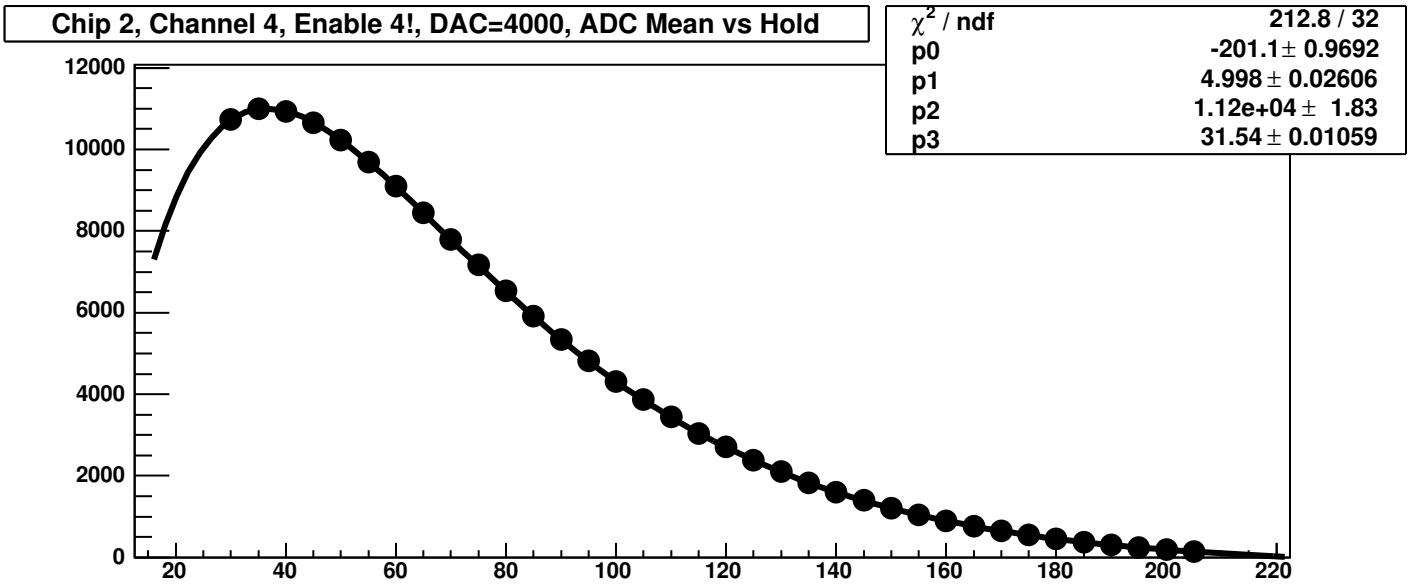

Chip 2, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250

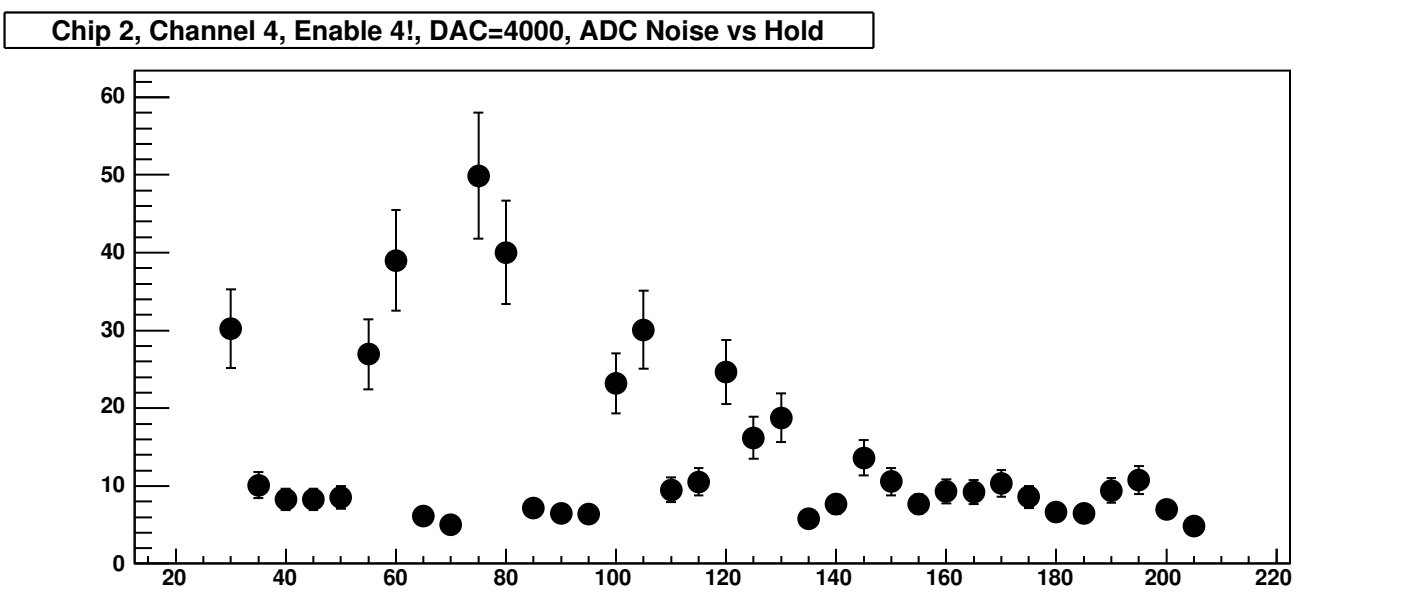

Chip 2, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

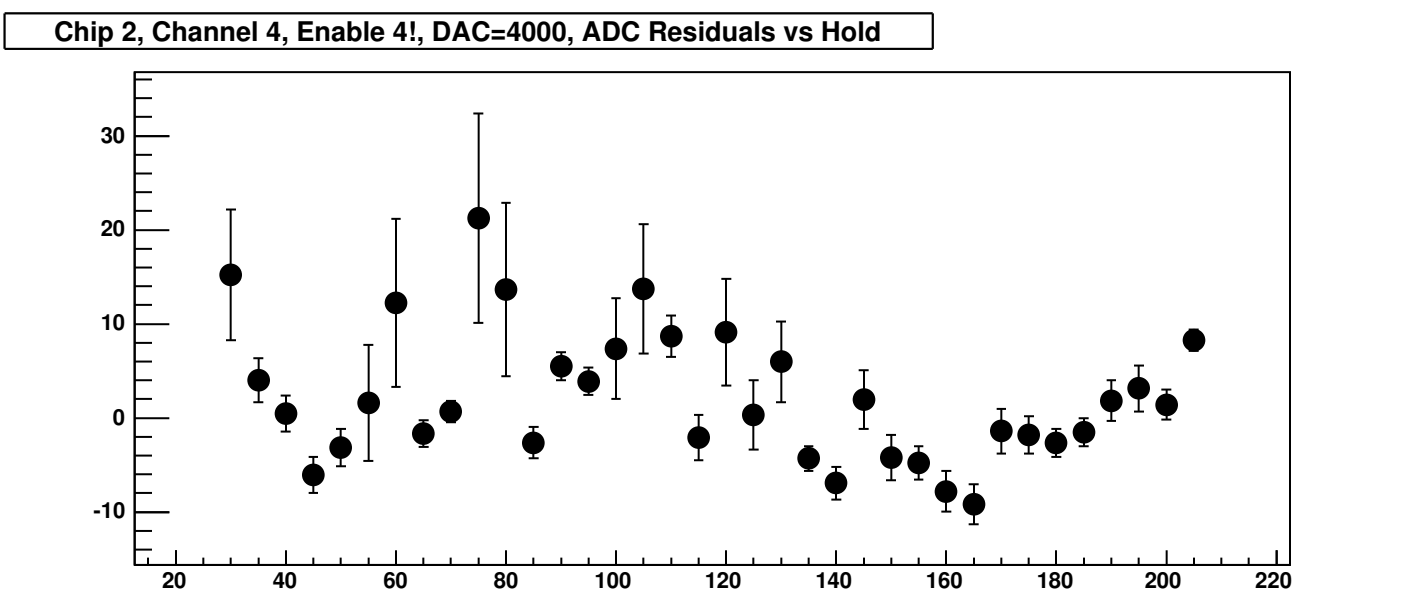

Chip 2, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 62.08 / 38
p0 471.3 ± 1.603
p1 -6.551 ± 1.428
p2 275.2 ± 1.569
p3 35.54 ± 1.595
p4 -0.183 ± 0.01398
720
710
700
690
680
670
660
650
640
50
100
150
200
250


Chip 2, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250

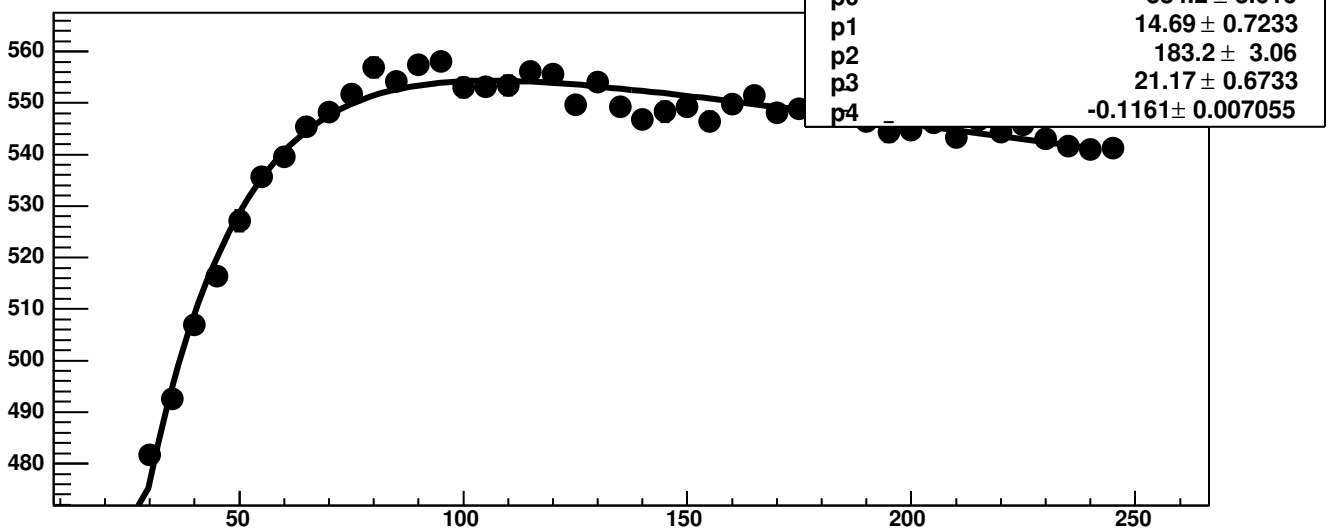

Chip 2, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250


Chip 2, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 212.8 / 32
p0 -201.1 ± 0.9692
p1 4.998 ± 0.02606
p2 1.12e+04 ± 1.83
p3 31.54 ± 0.01059
12000
10000
8000
6000
4000
2000
0
20
40
60
80
100
120
140
160
180
200
220

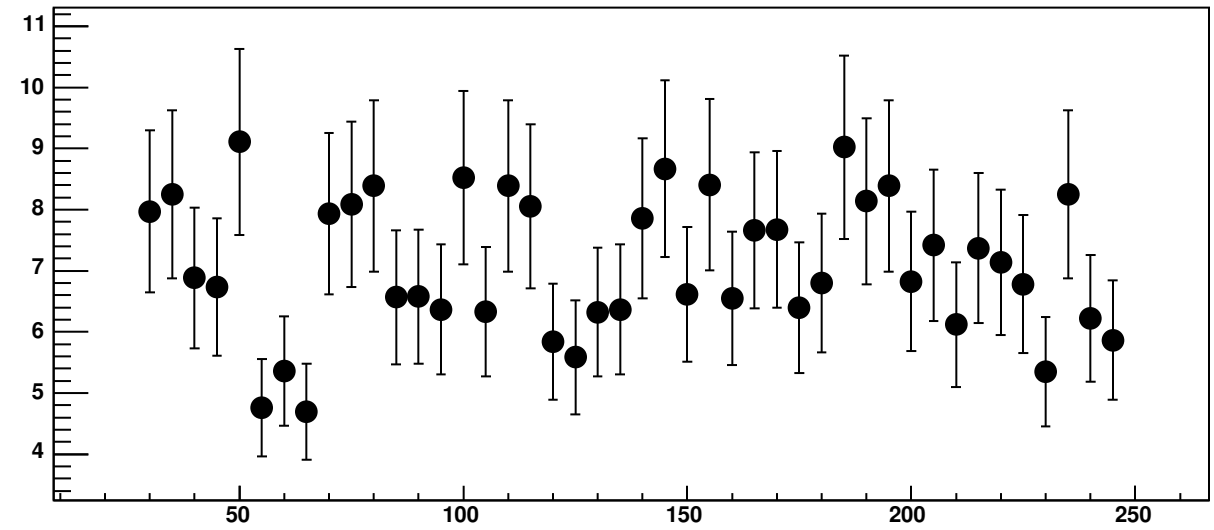

Chip 2, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold
60
50
40
30
20
10
0
20
40
60
80
100
120
140
160
180
200
220


Chip 2, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
20
40
60
80
100
120
140
160
180
200
220

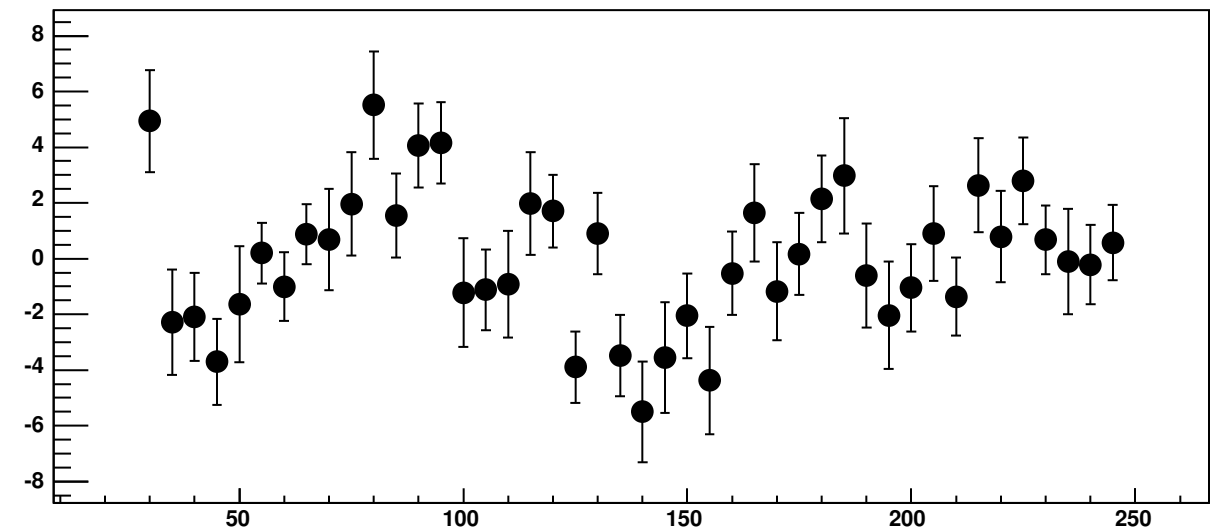
Chip 2, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold



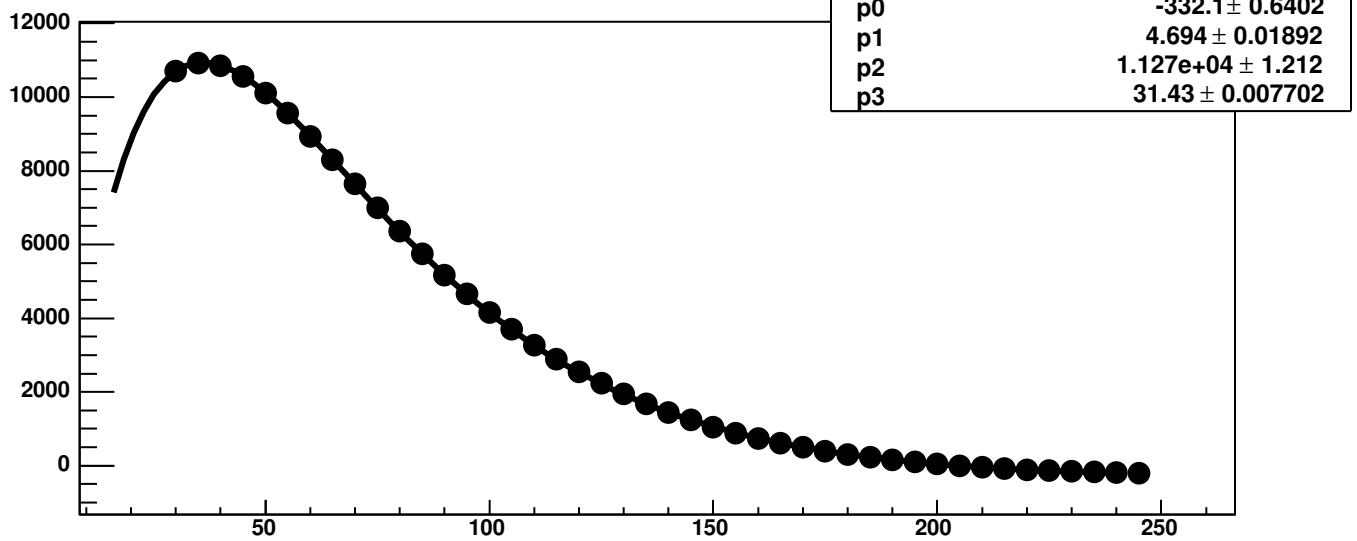
Chip 2, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



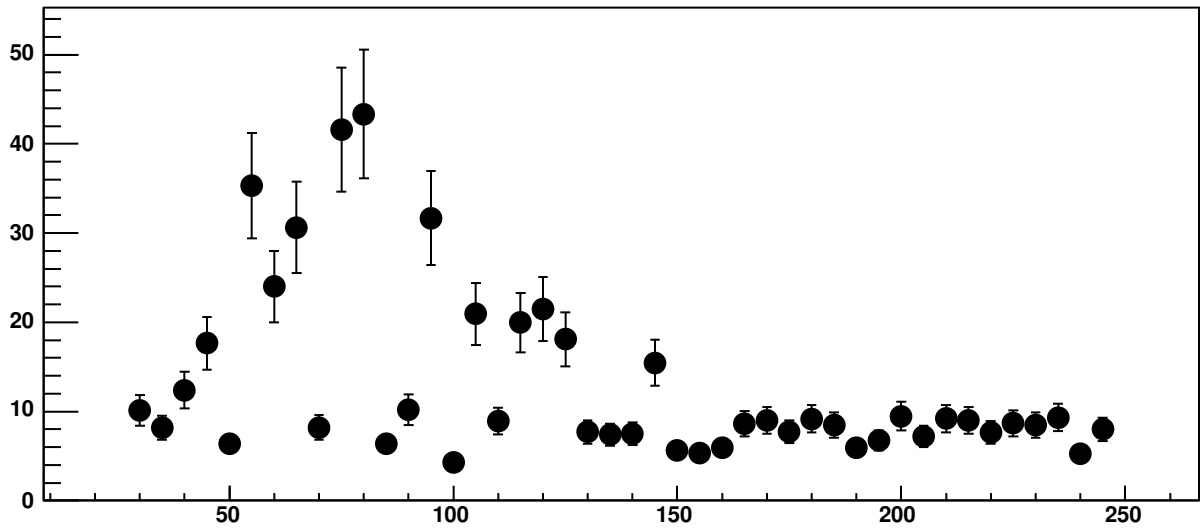
Chip 2, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold



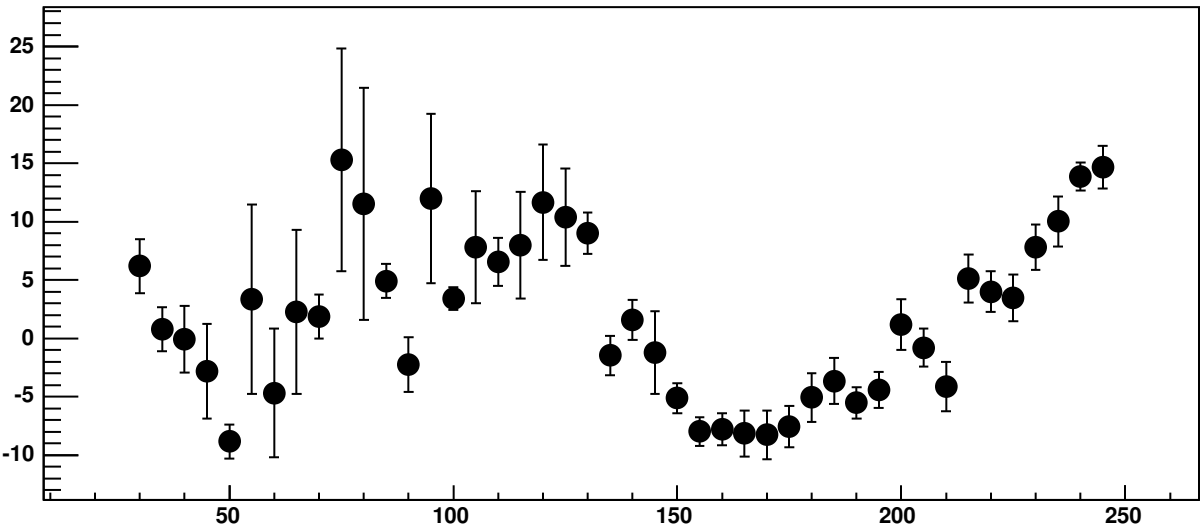
Chip 2, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold



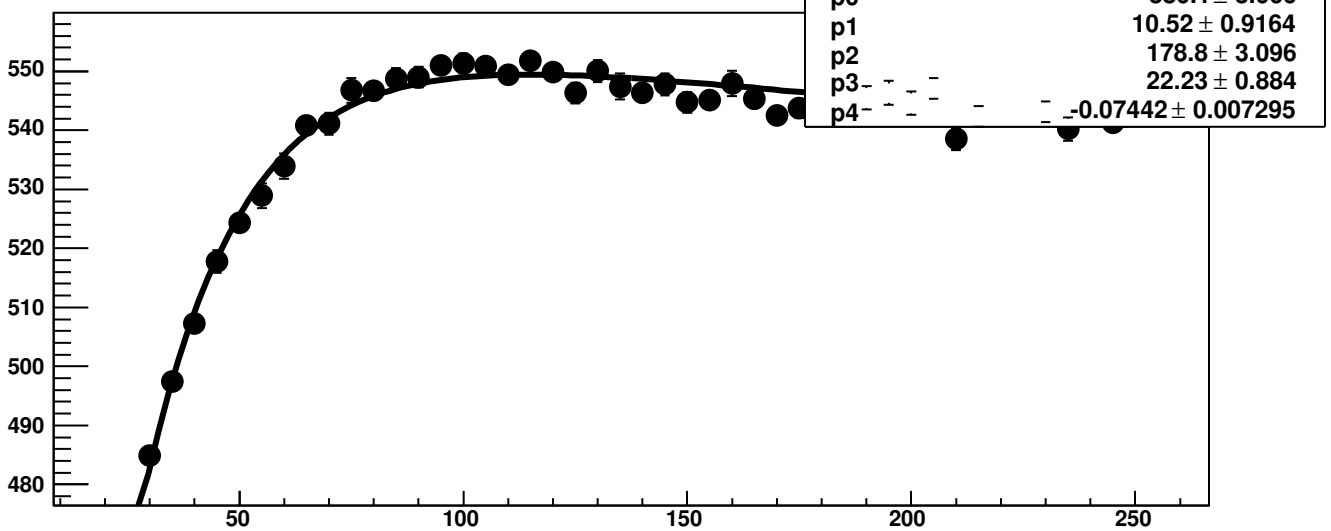
Chip 2, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold



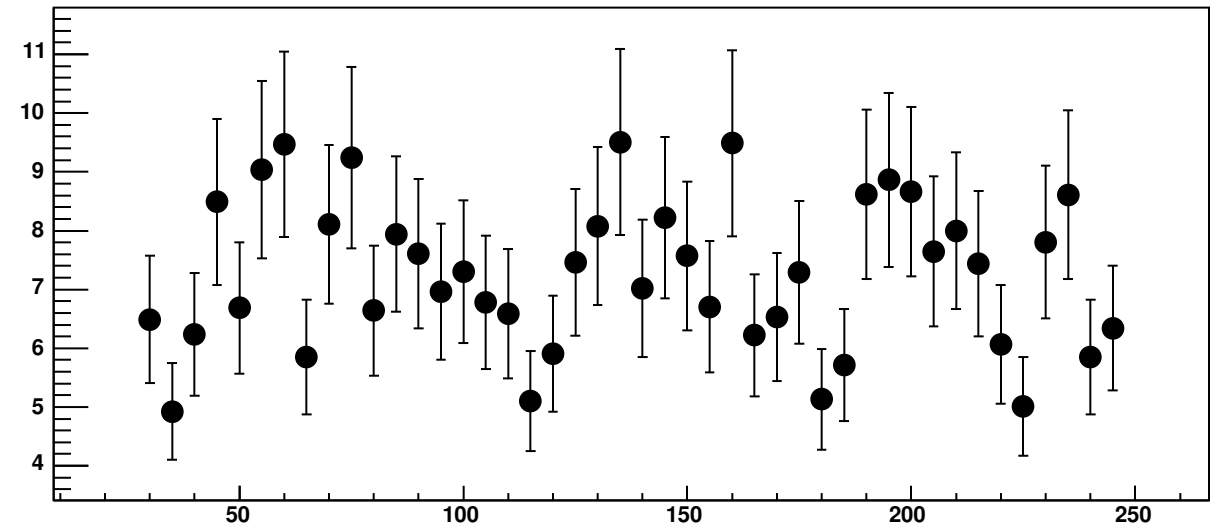
Chip 2, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold



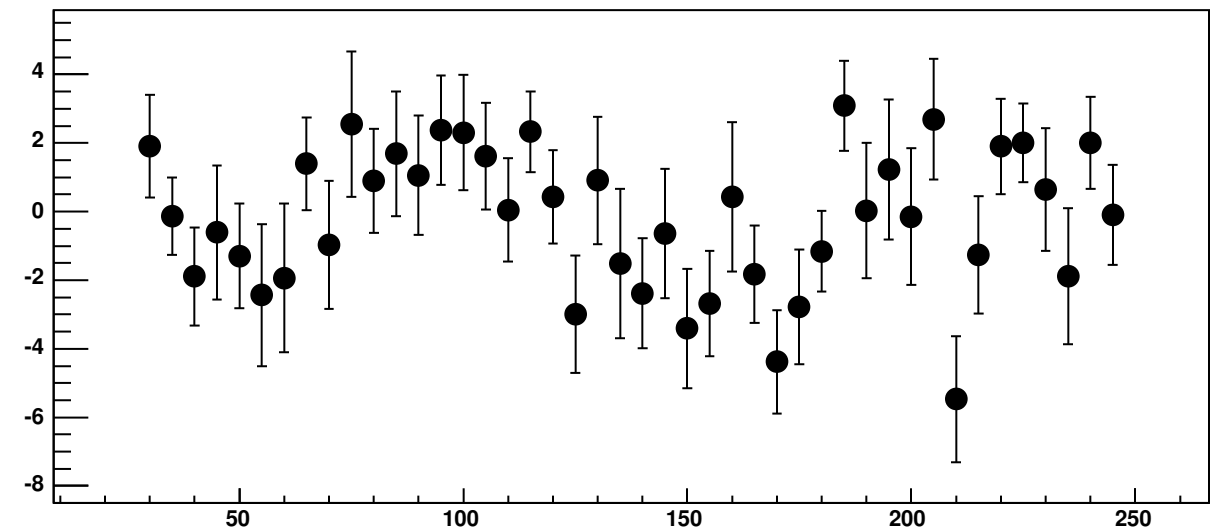
Chip 2, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

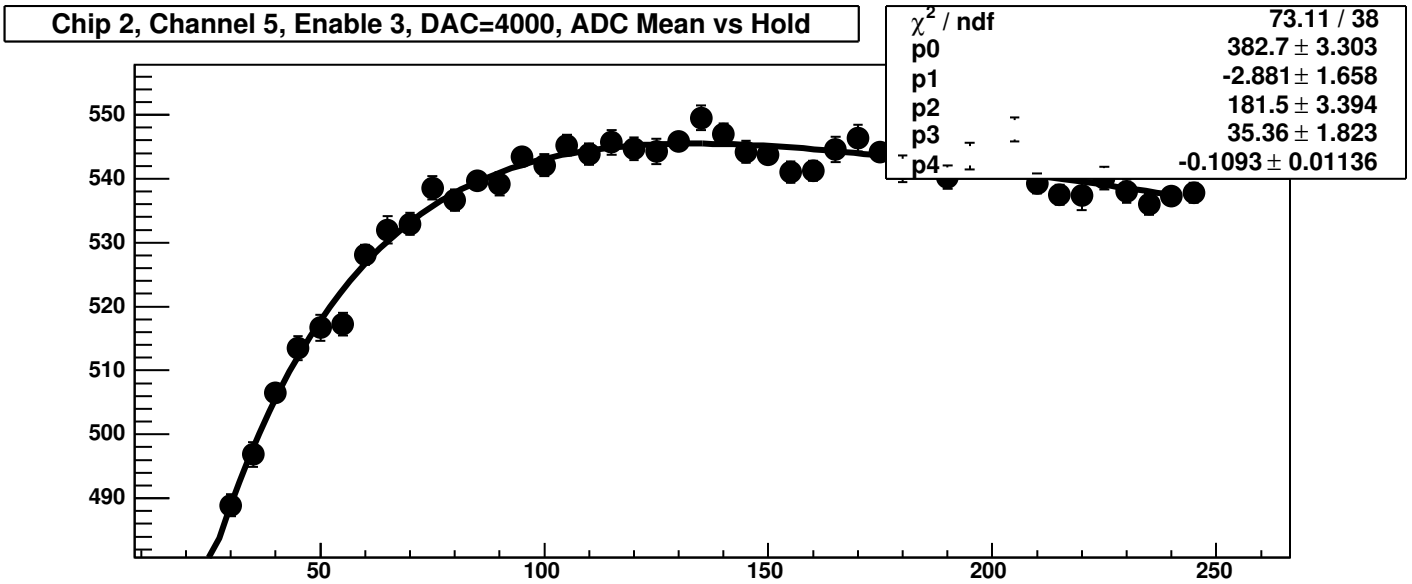


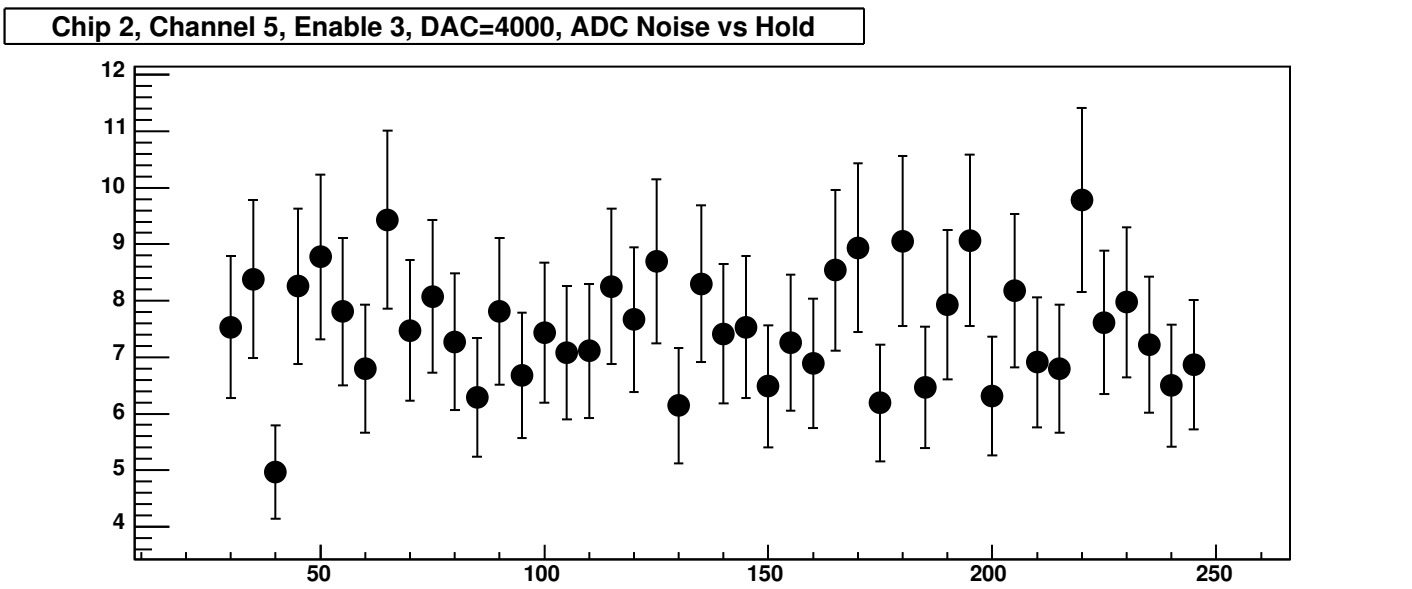
Chip 2, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold

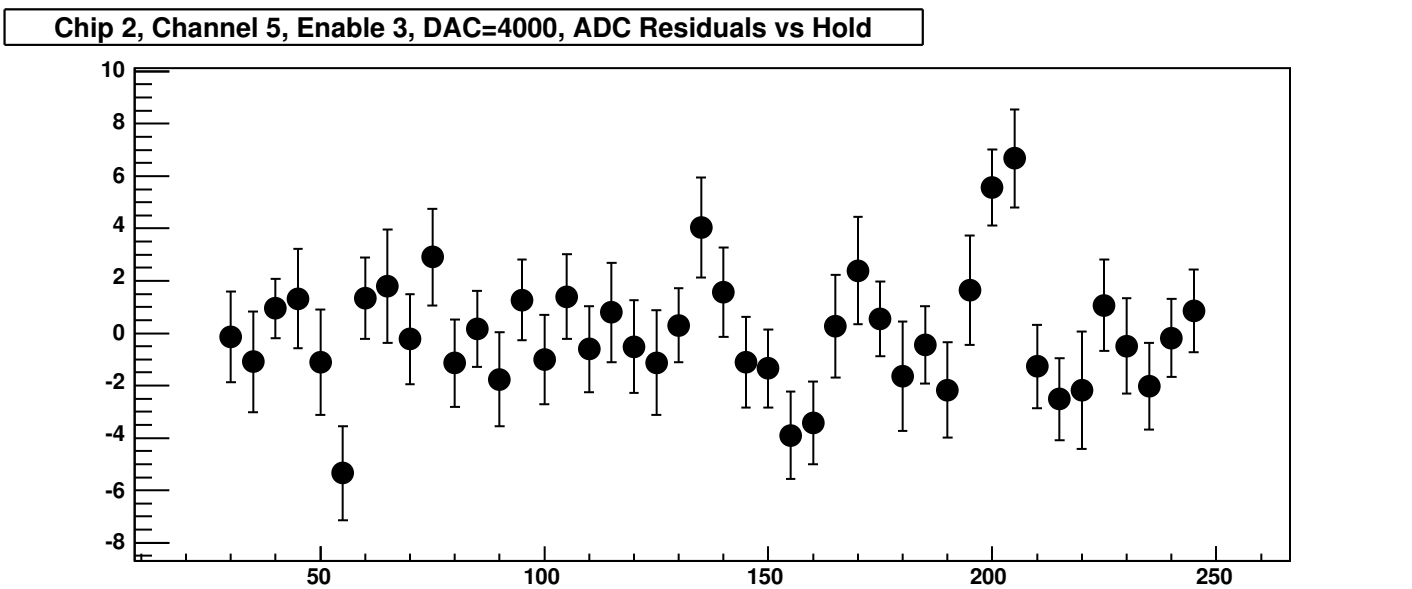


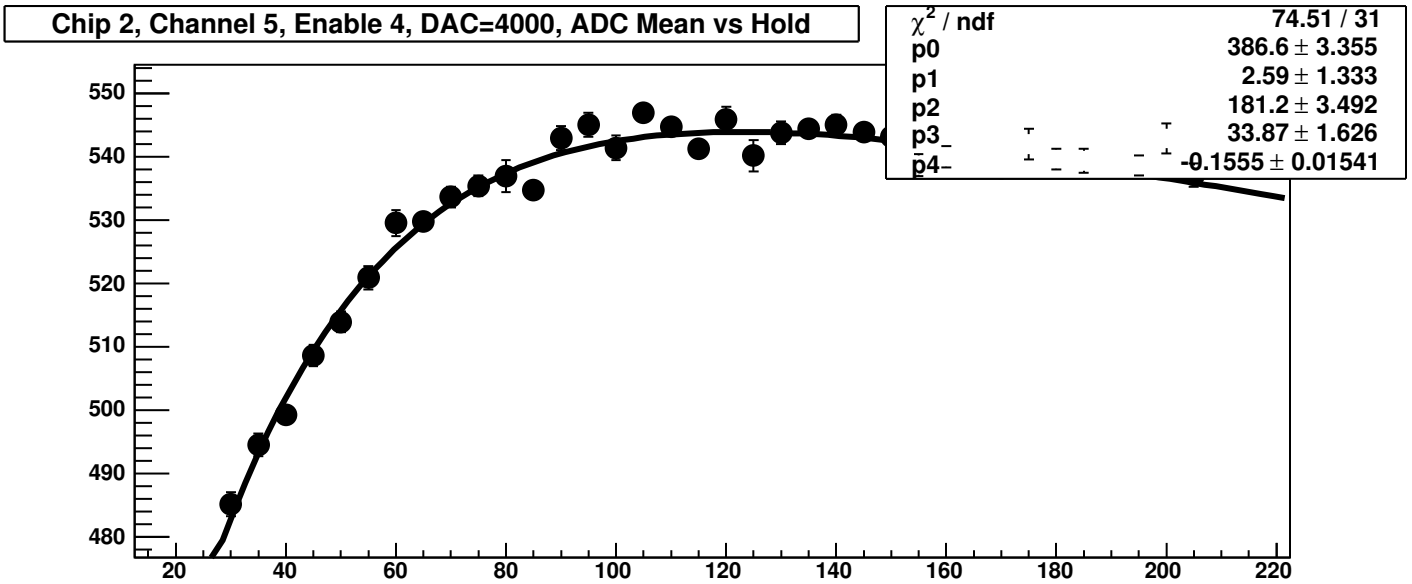
Chip 2, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

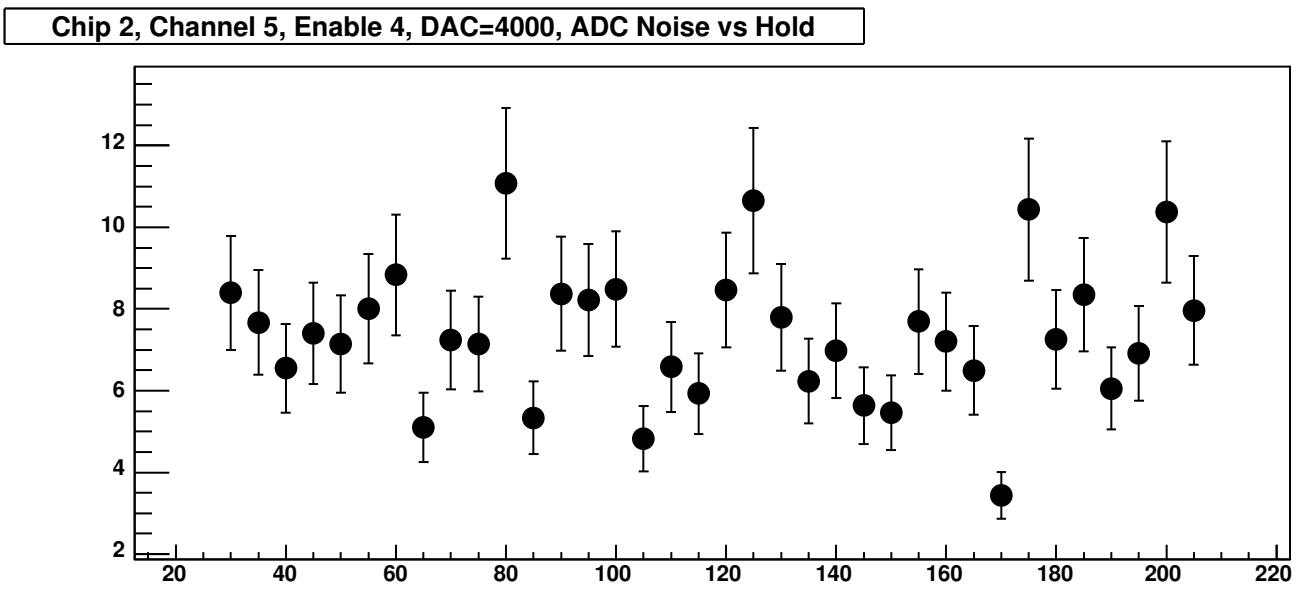


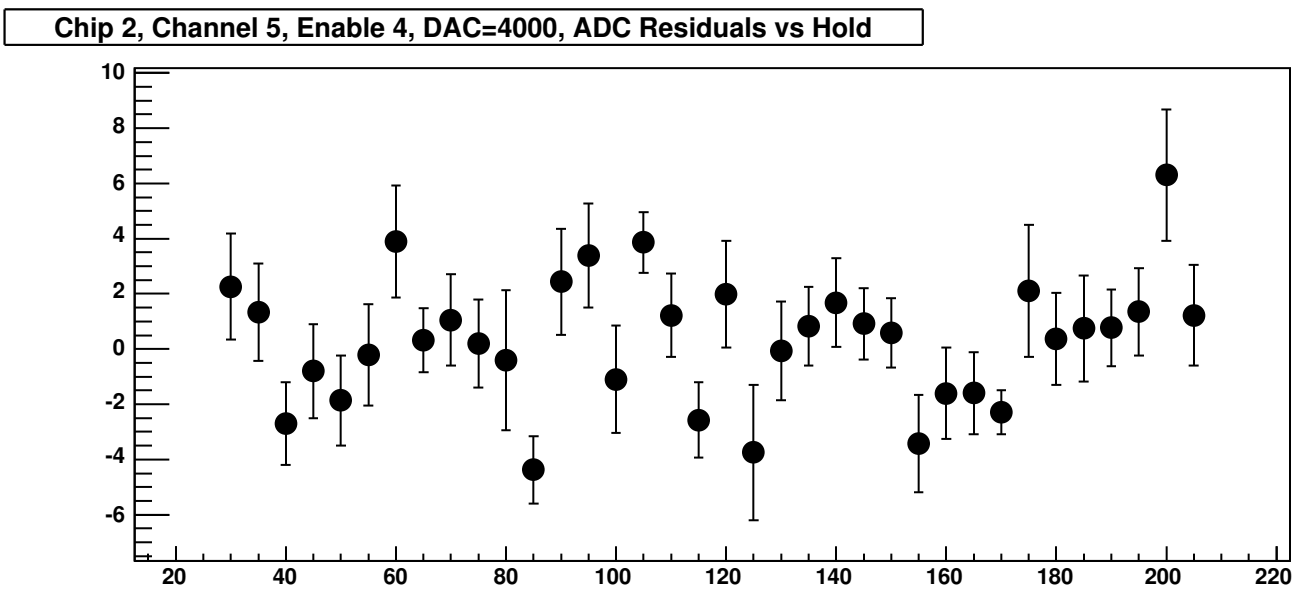

Chip 2, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 73.11 / 38
p0 382.7 ± 3.303
p1 -2.881 ± 1.658
p2 181.5 ± 3.394
p3 35.36 ± 1.823
p4 -0.1093 ± 0.01136
550
540
530
520
510
500
490
50
100
150
200
250

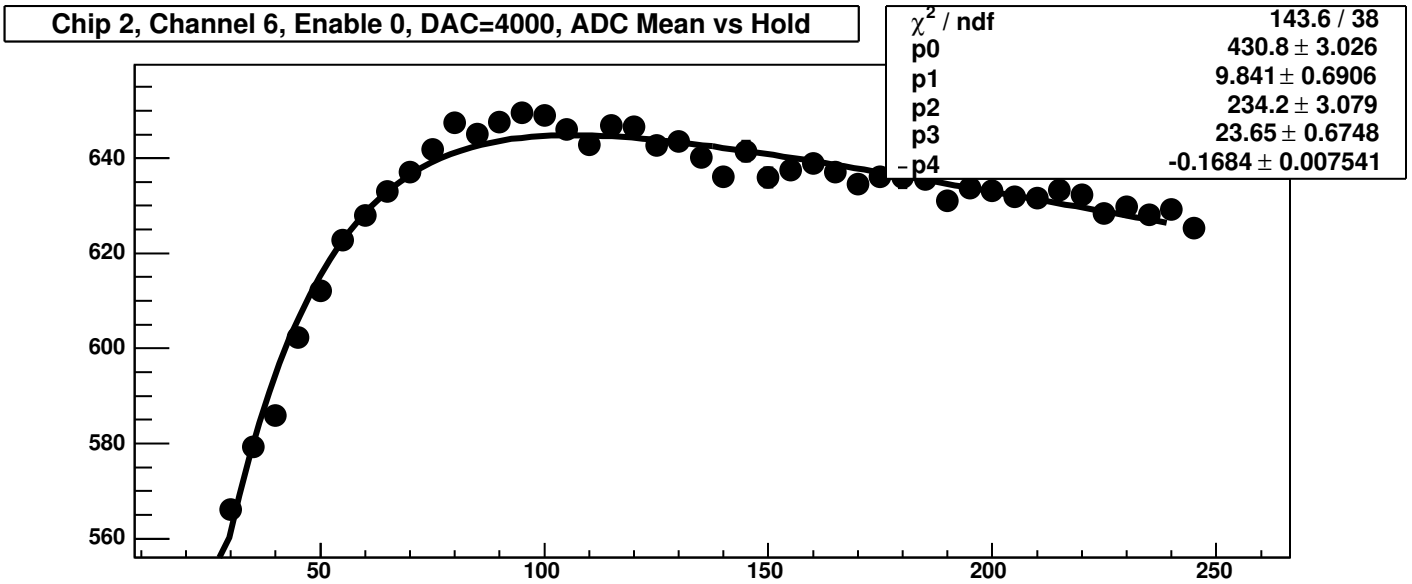

Chip 2, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

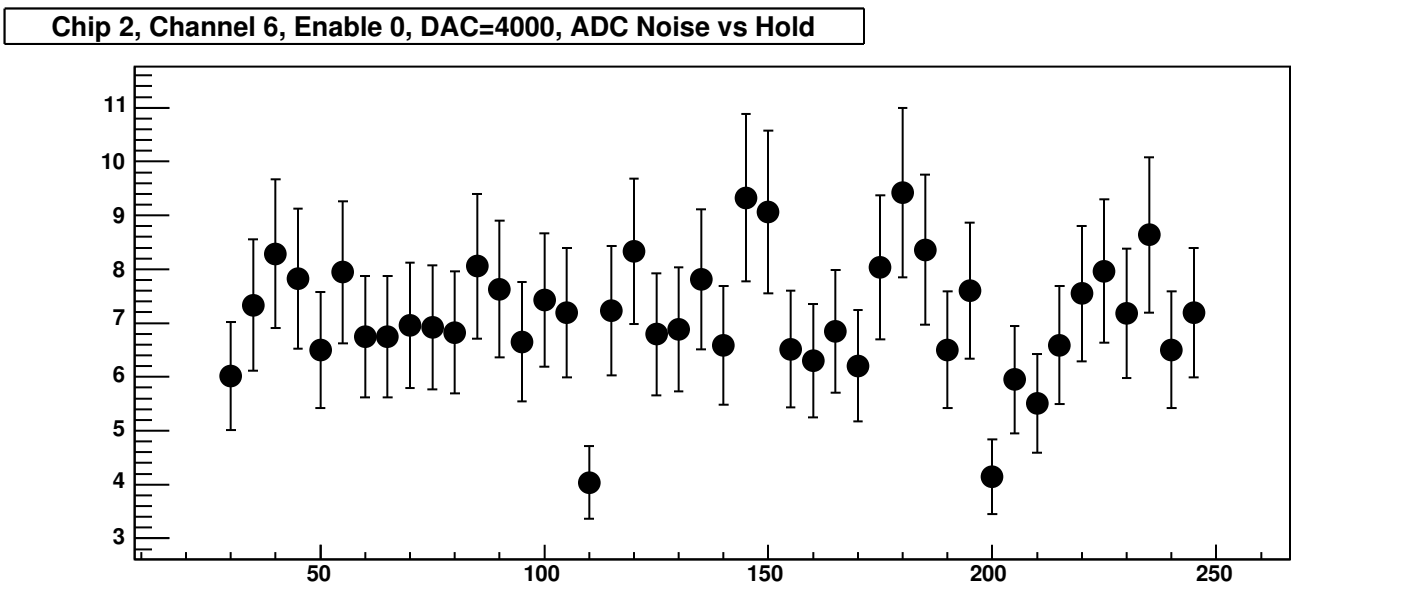

Chip 2, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

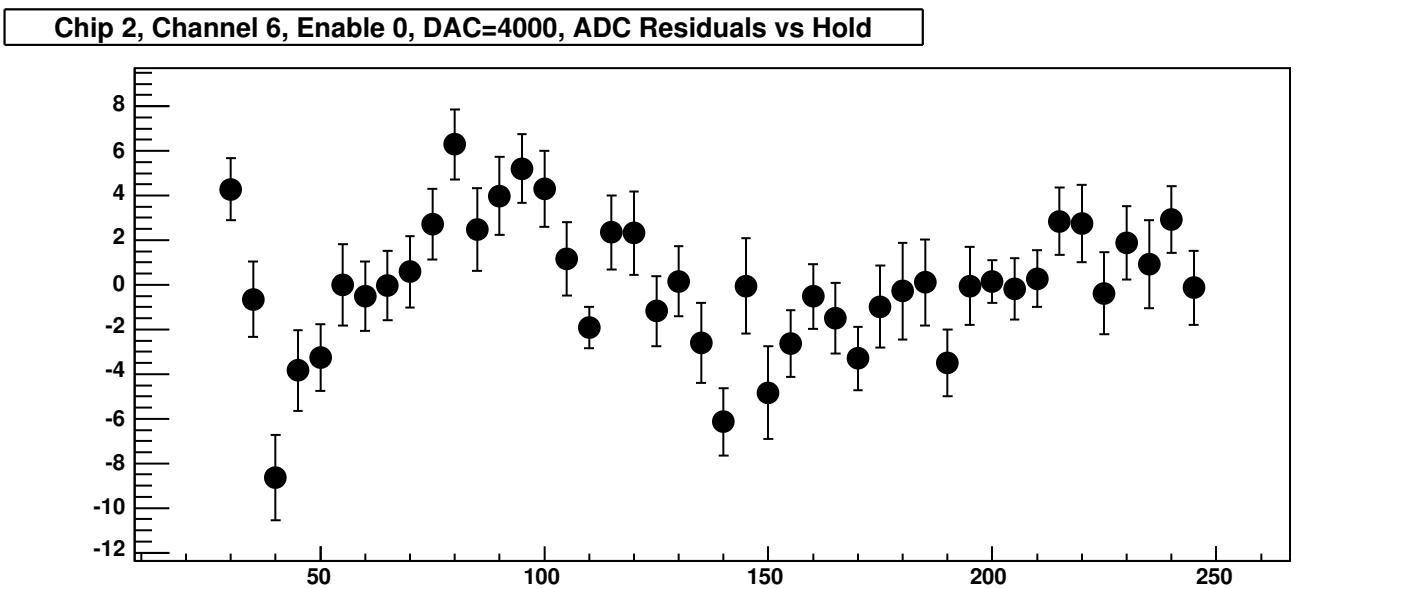

Chip 2, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 74.51 / 31
p0 386.6 ± 3.355
p1 2.59 ± 1.333
p2 181.2 ± 3.492
p3 33.87 ± 1.626
p4 -0.1555 ± 0.01541
550
540
530
520
510
500
490
480
20
40
60
80
100
120
140
160
180
200
220

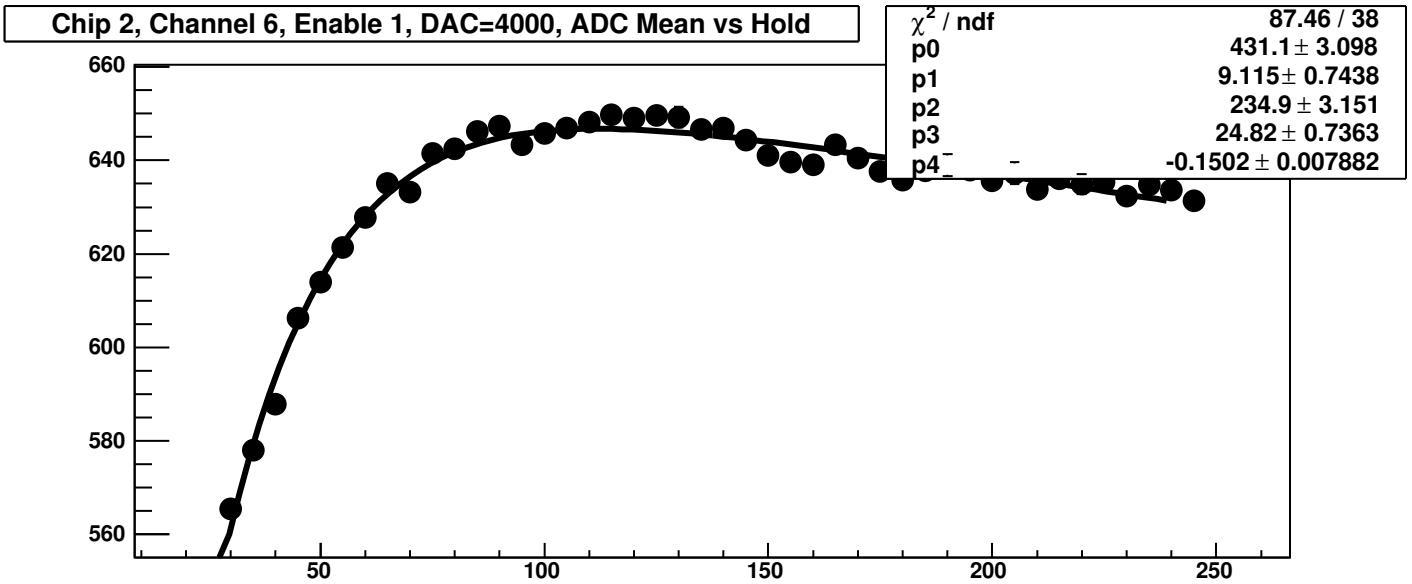

Chip 2, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
2
20
40
60
80
100
120
140
160
180
200
220

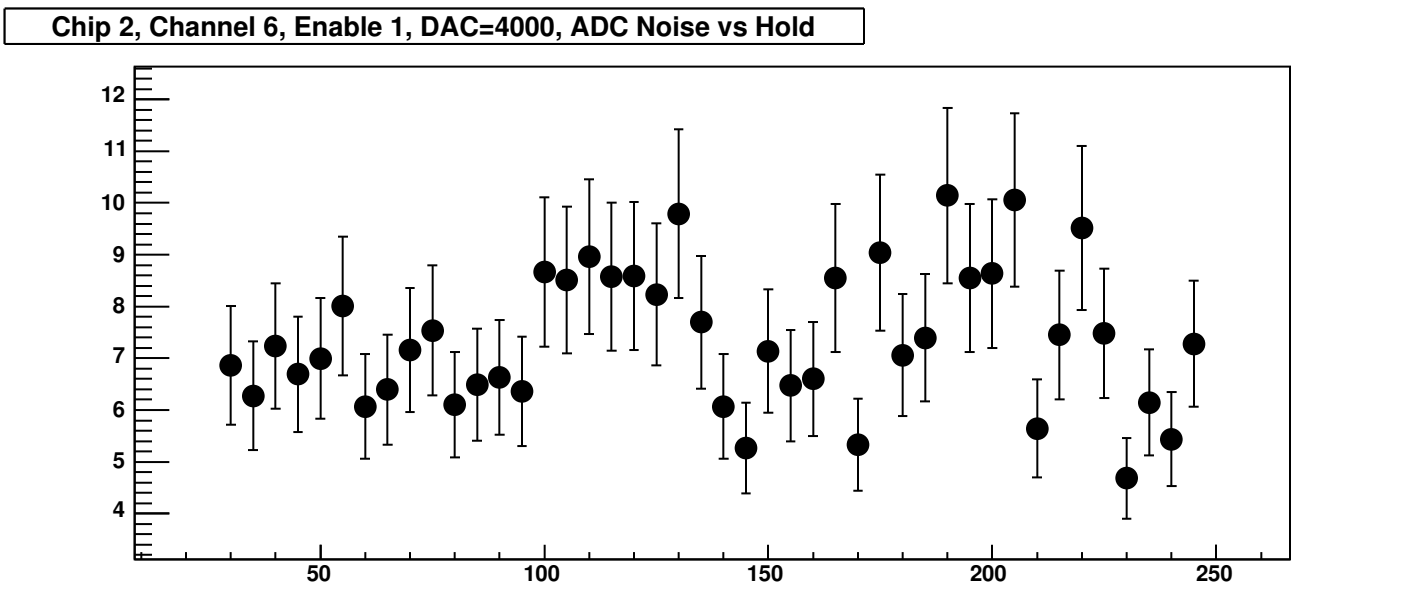

Chip 2, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
20
40
60
80
100
120
140
160
180
200
220

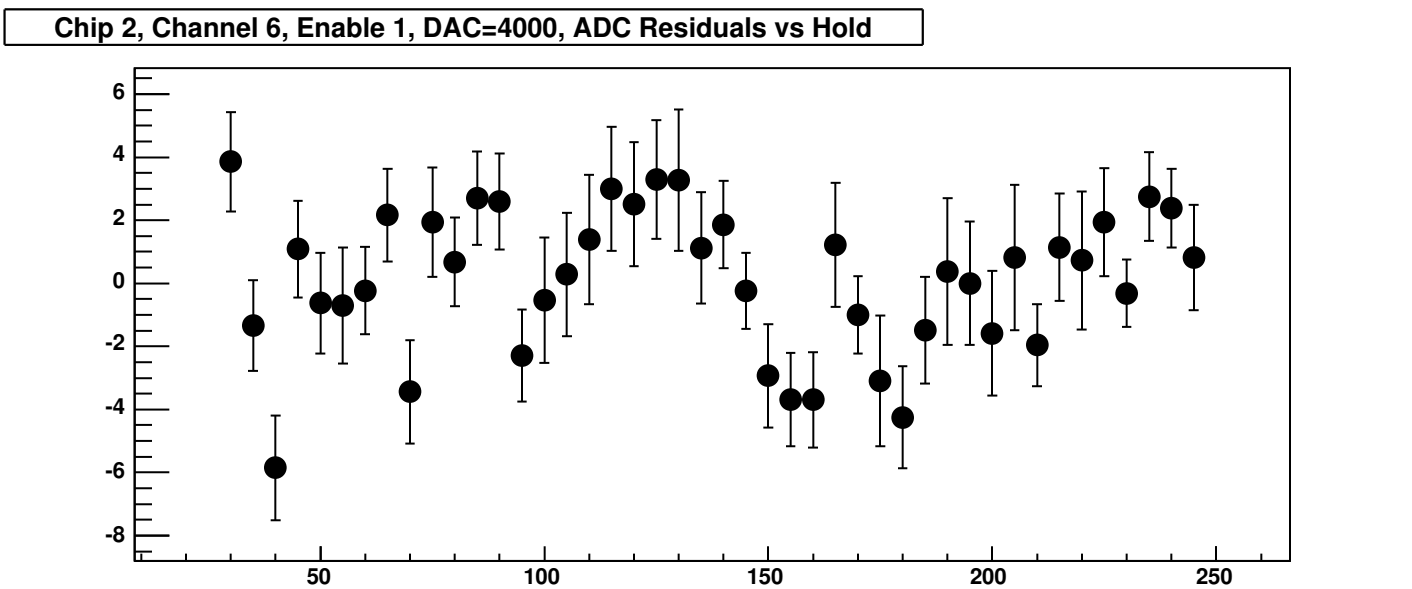

Chip 2, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 143.6 / 38
p0 430.8 ± 3.026
p1 9.841 ± 0.6906
p2 234.2 ± 3.079
p3 23.65 ± 0.6748
p4 -0.1684 ± 0.007541
640
620
600
580
560
50
100
150
200
250


Chip 2, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
50
100
150
200
250

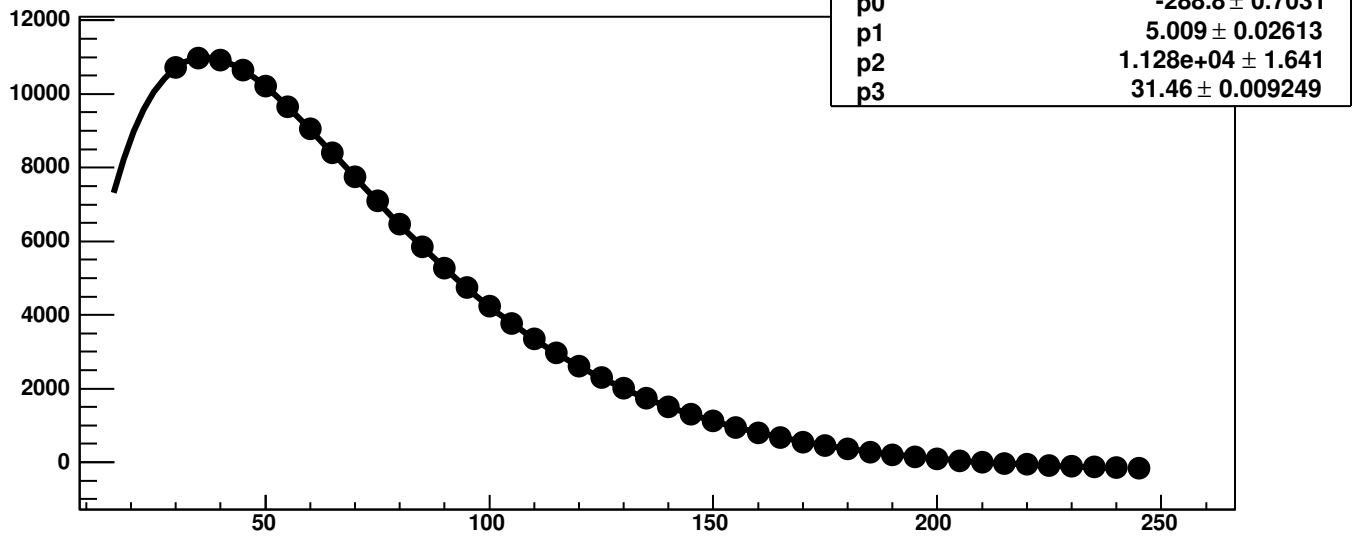

Chip 2, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
-12
50
100
150
200
250


Chip 2, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 87.46 / 38
p0 431.1 ± 3.098
p1 9.115 ± 0.7438
p2 234.9 ± 3.151
p3 24.82 ± 0.7363
p4 -0.1502 ± 0.007882
660
640
620
600
580
560
50
100
150
200
250

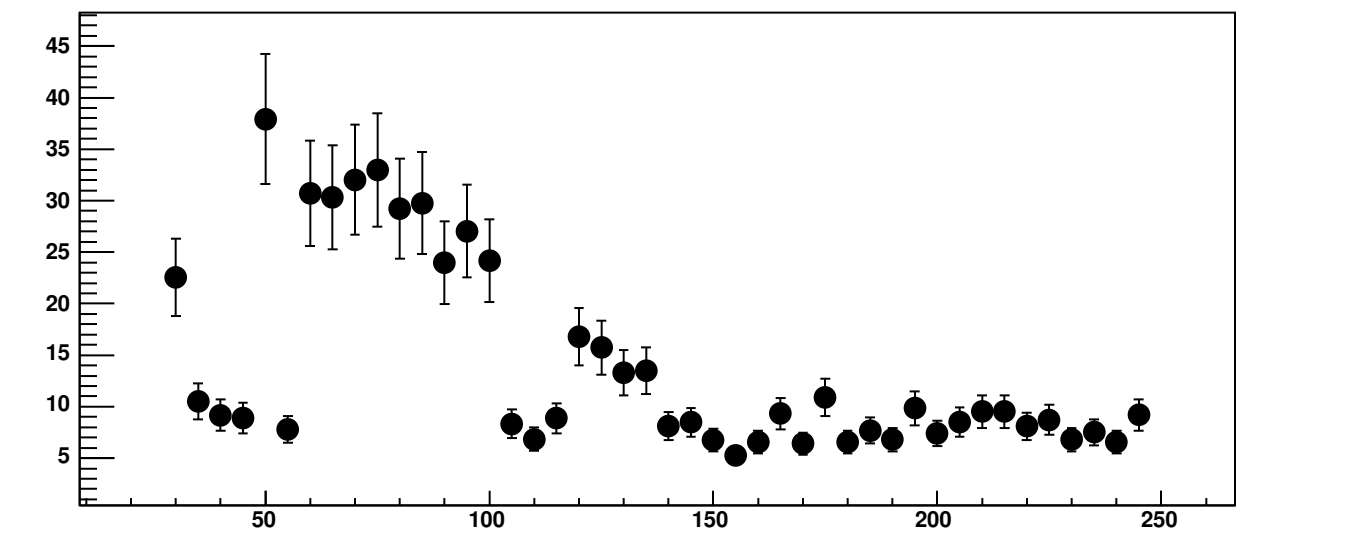

Chip 2, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 2, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

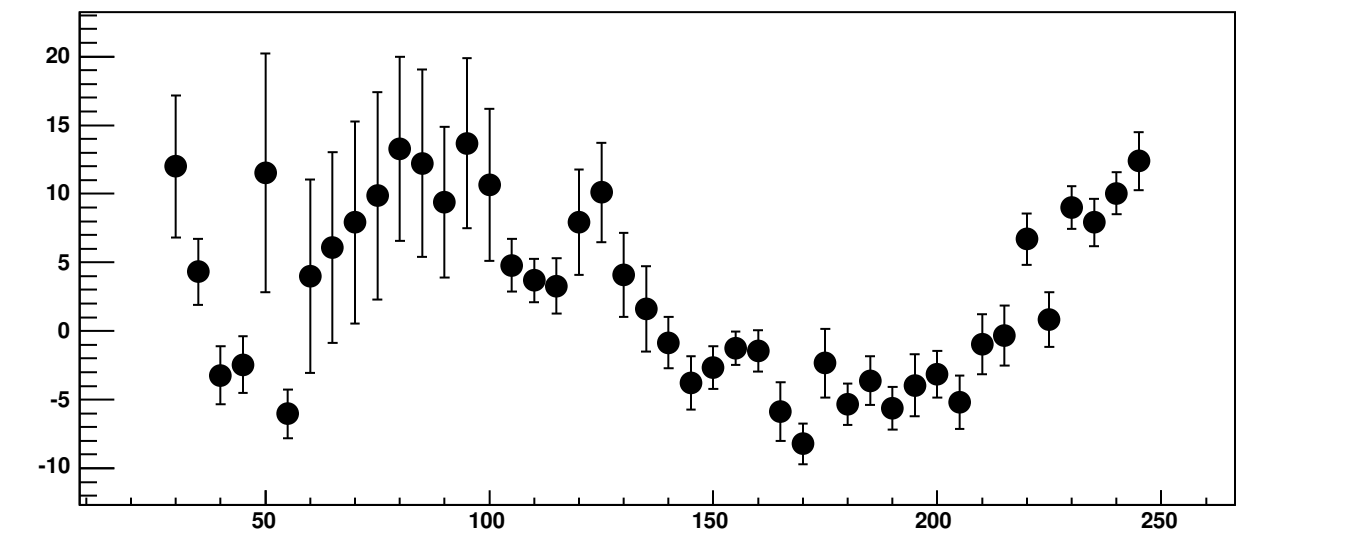
Chip 2, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold

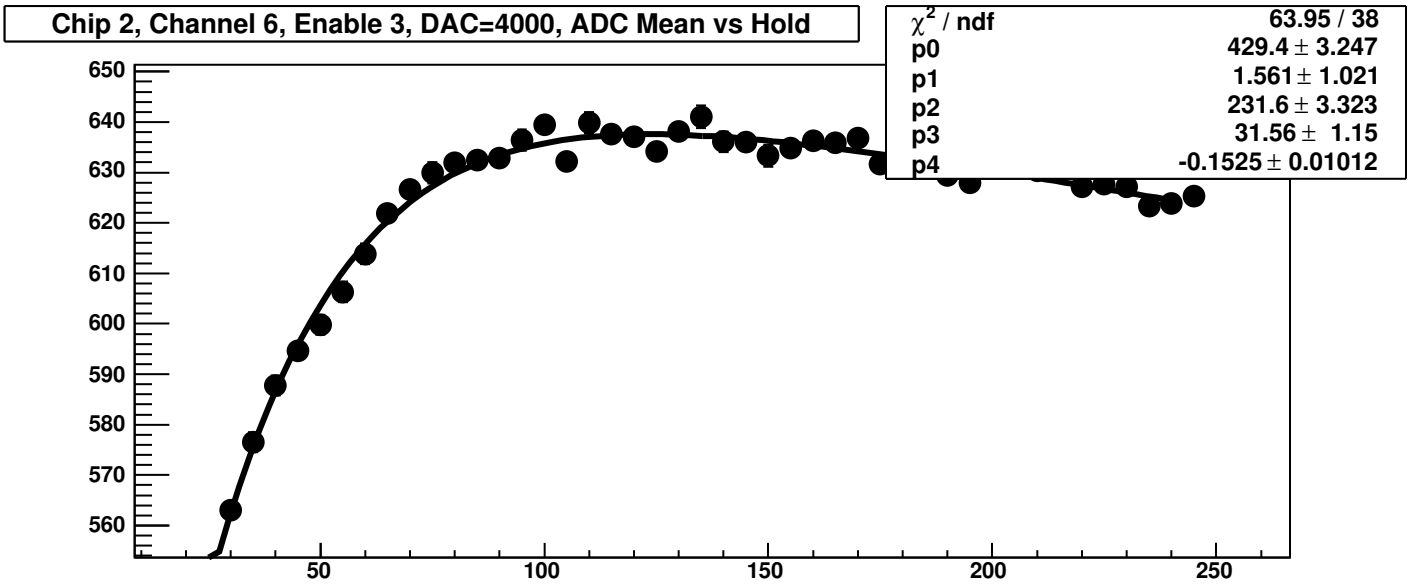


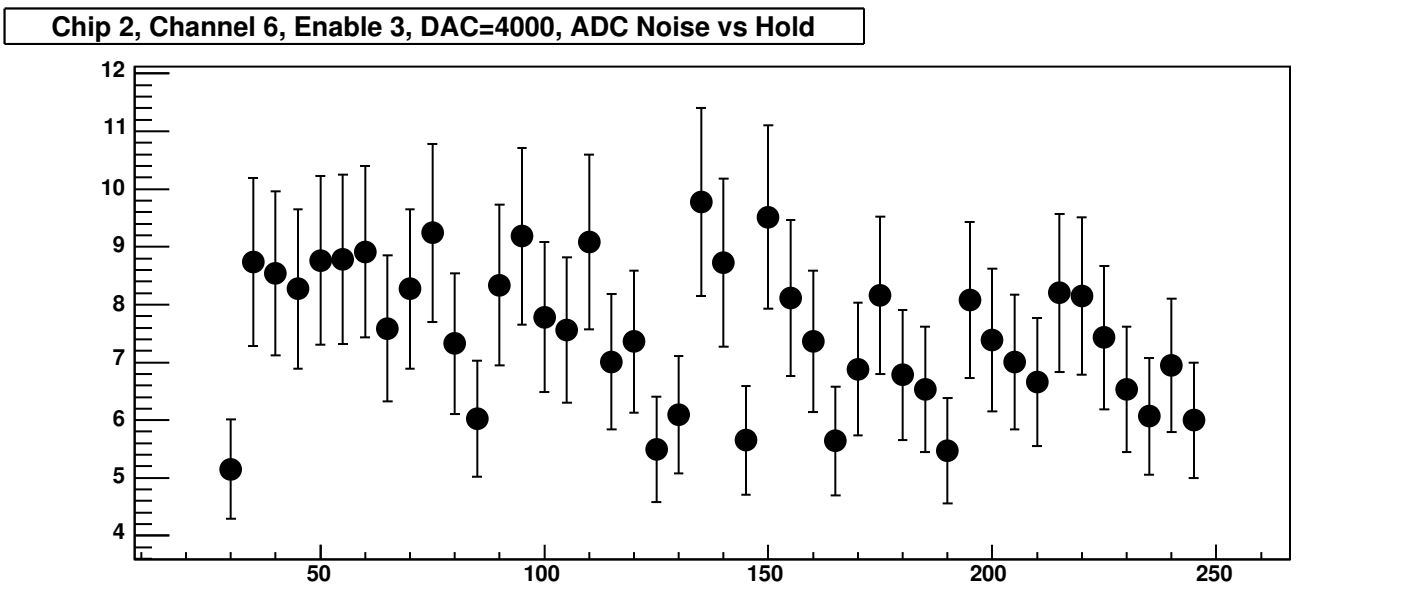
Chip 2, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold

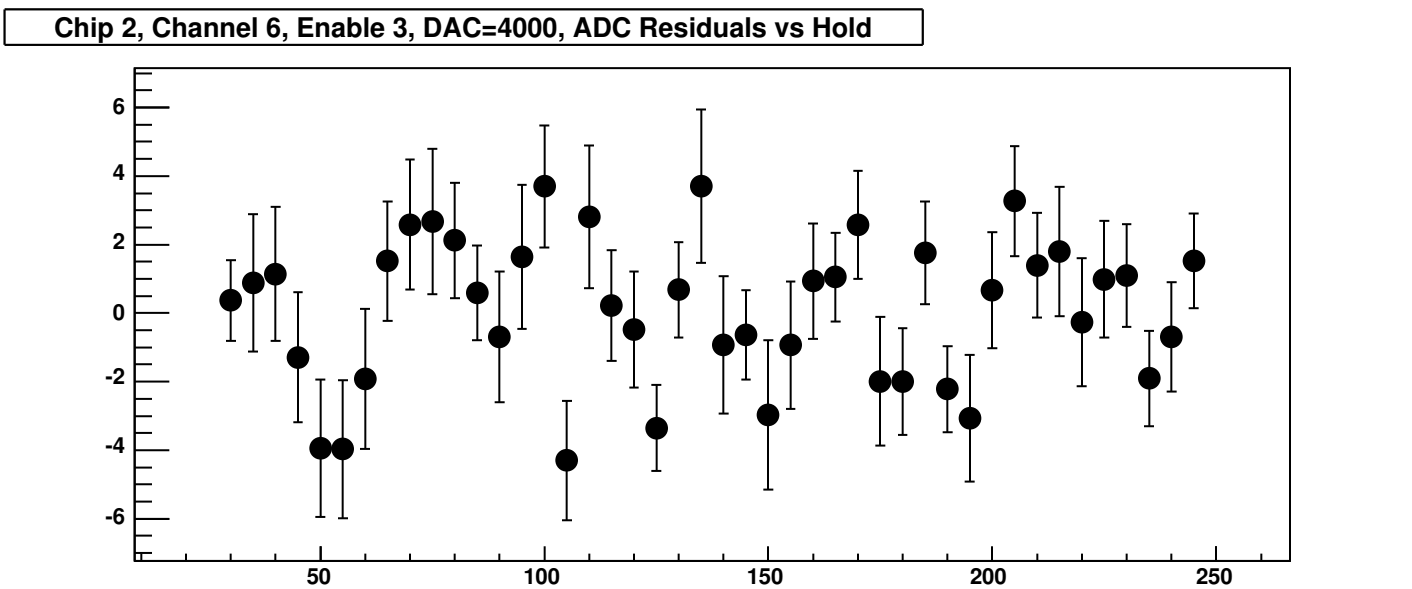


Chip 2, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold

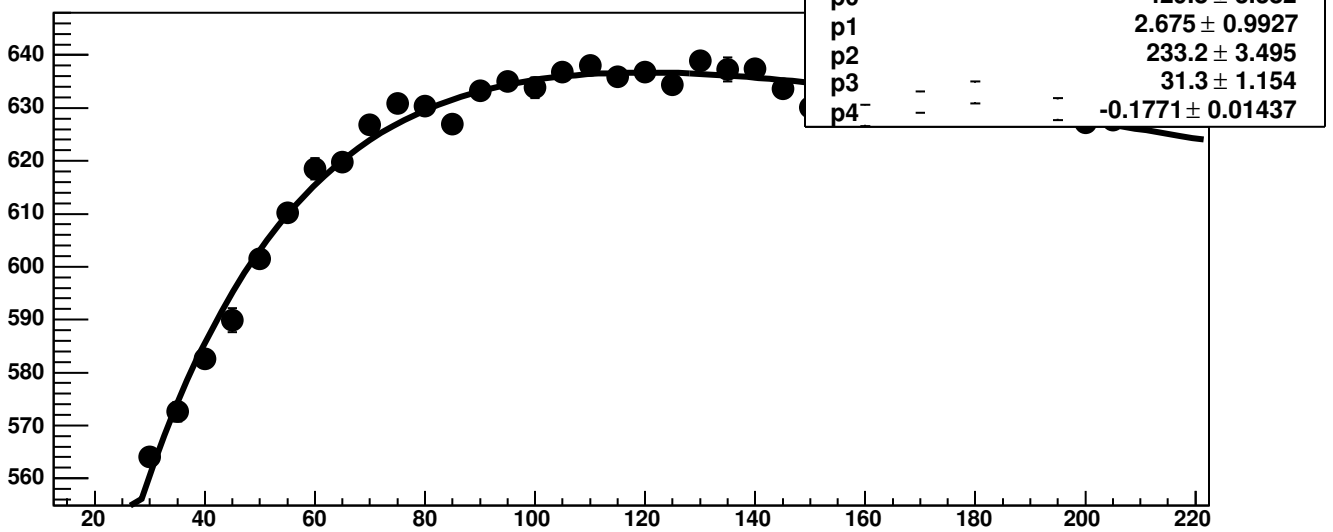



Chip 2, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 63.95 / 38
p0 429.4 ± 3.247
p1 1.561 ± 1.021
p2 231.6 ± 3.323
p3 31.56 ± 1.15
p4 -0.1525 ± 0.01012
650
640
630
620
610
600
590
580
570
560
50
100
150
200
250

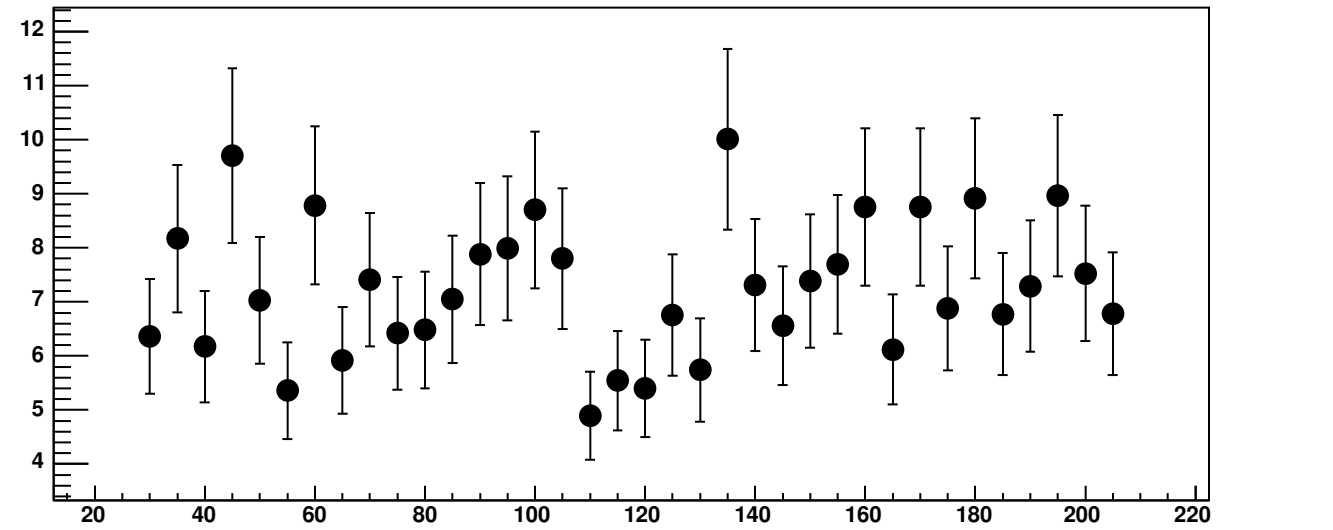

Chip 2, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 2, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
50
100
150
200
250

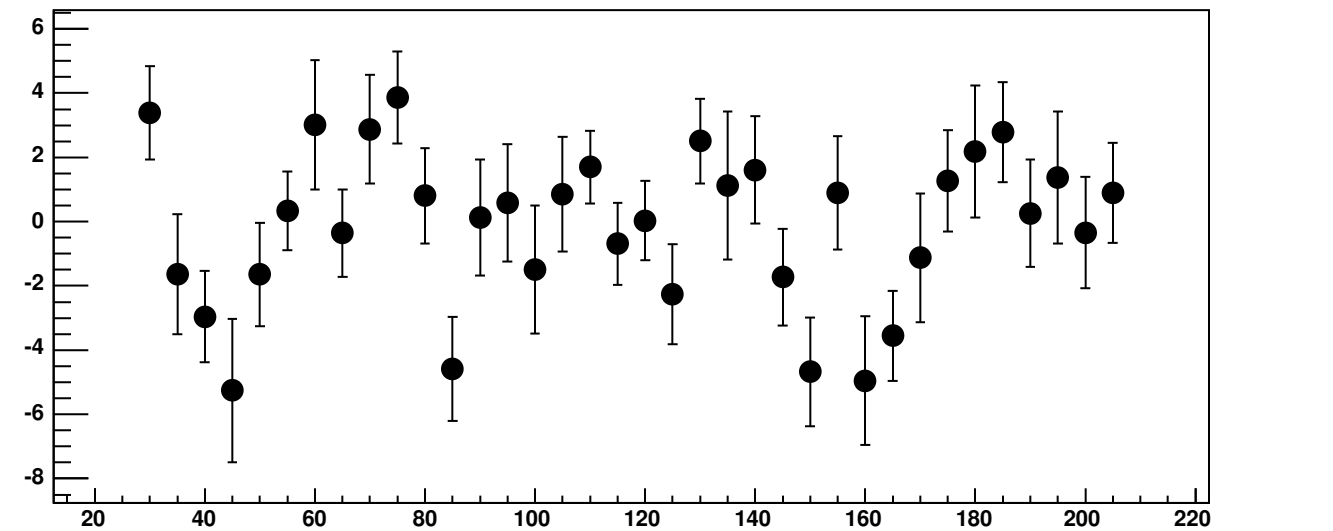
Chip 2, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

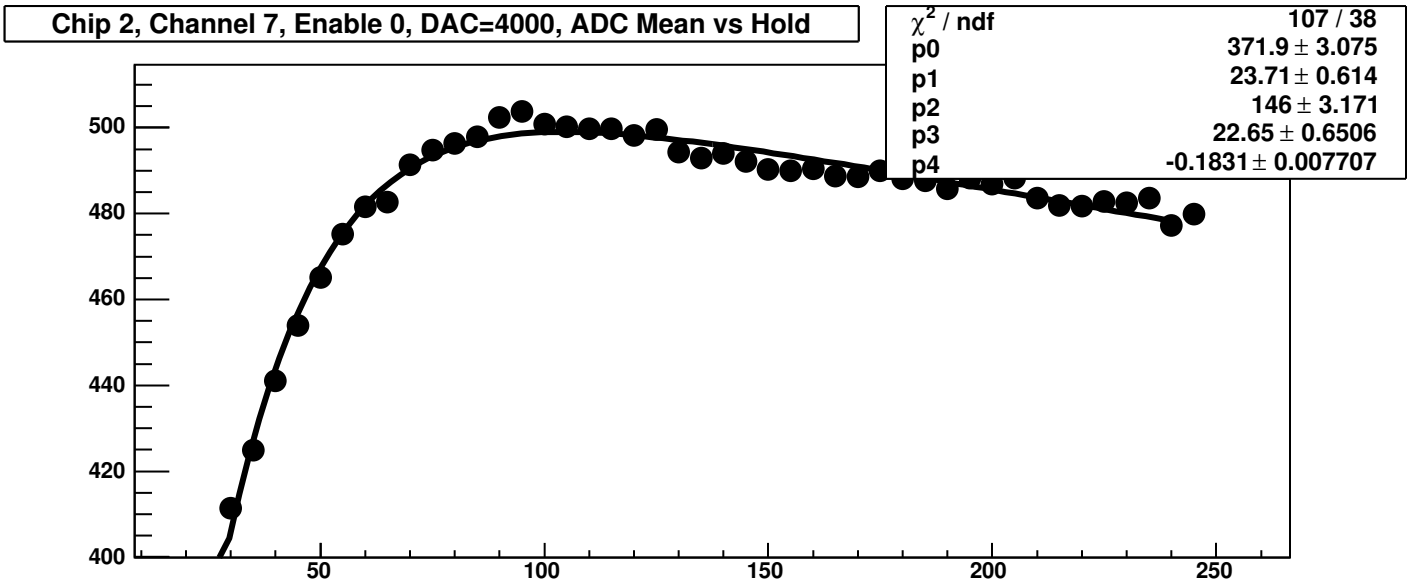


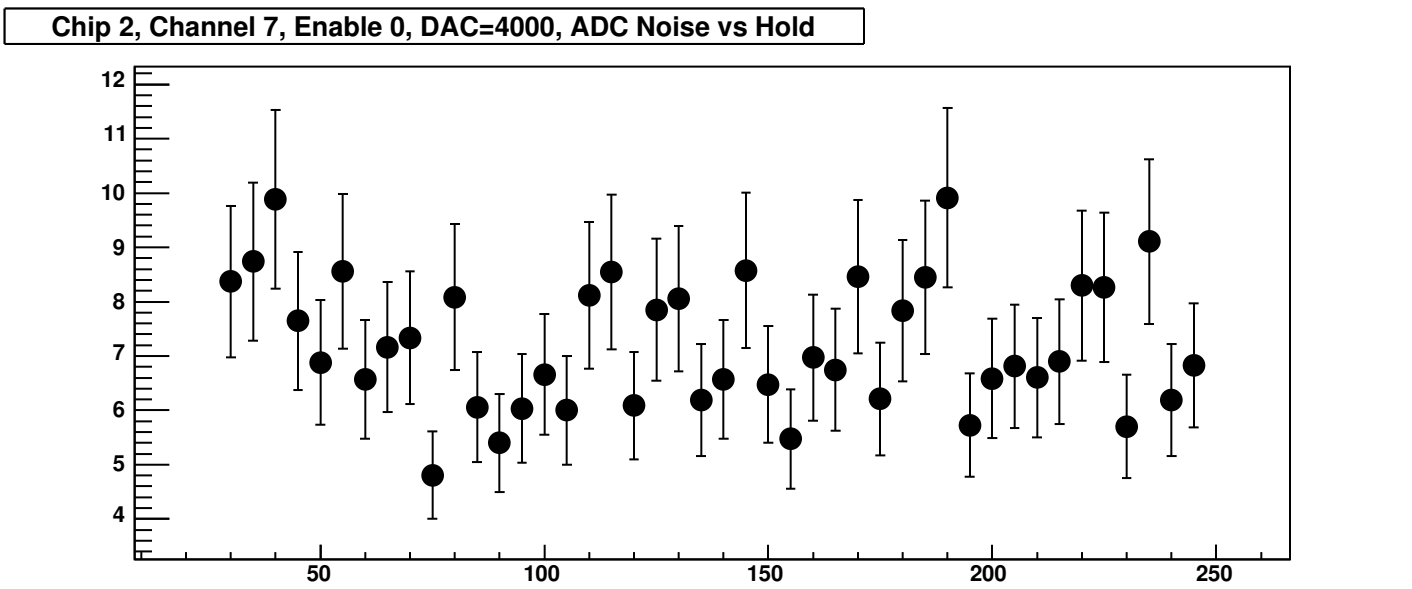
Chip 2, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold

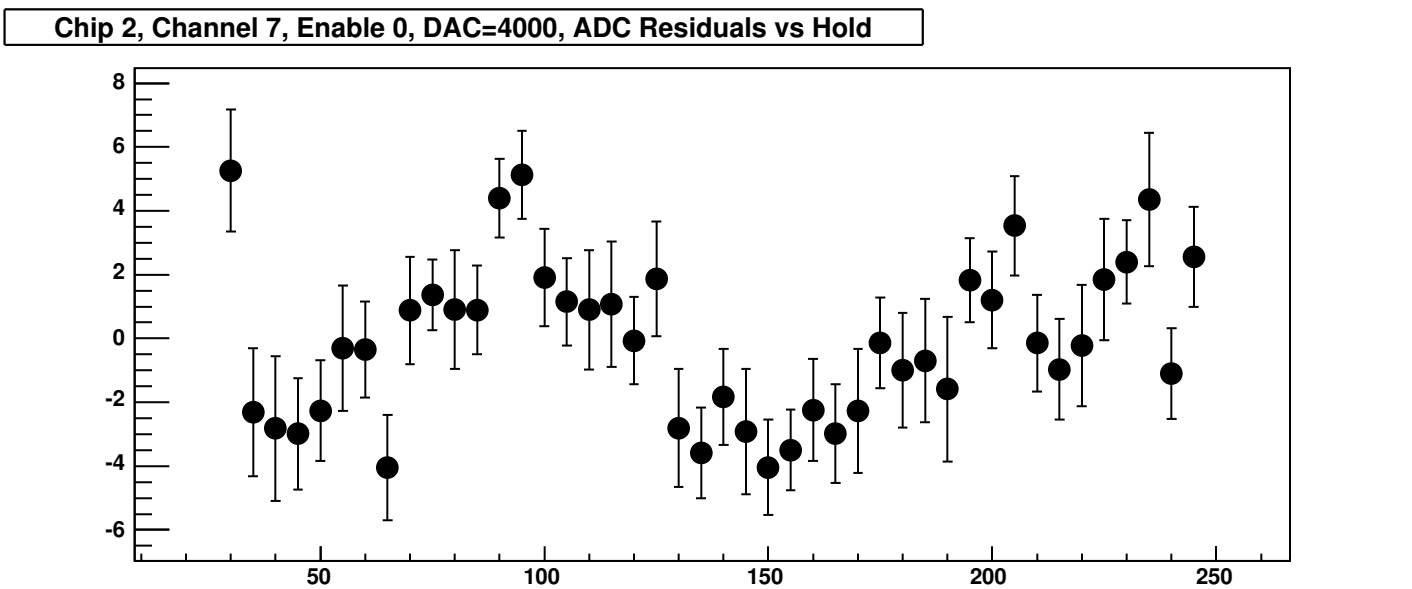


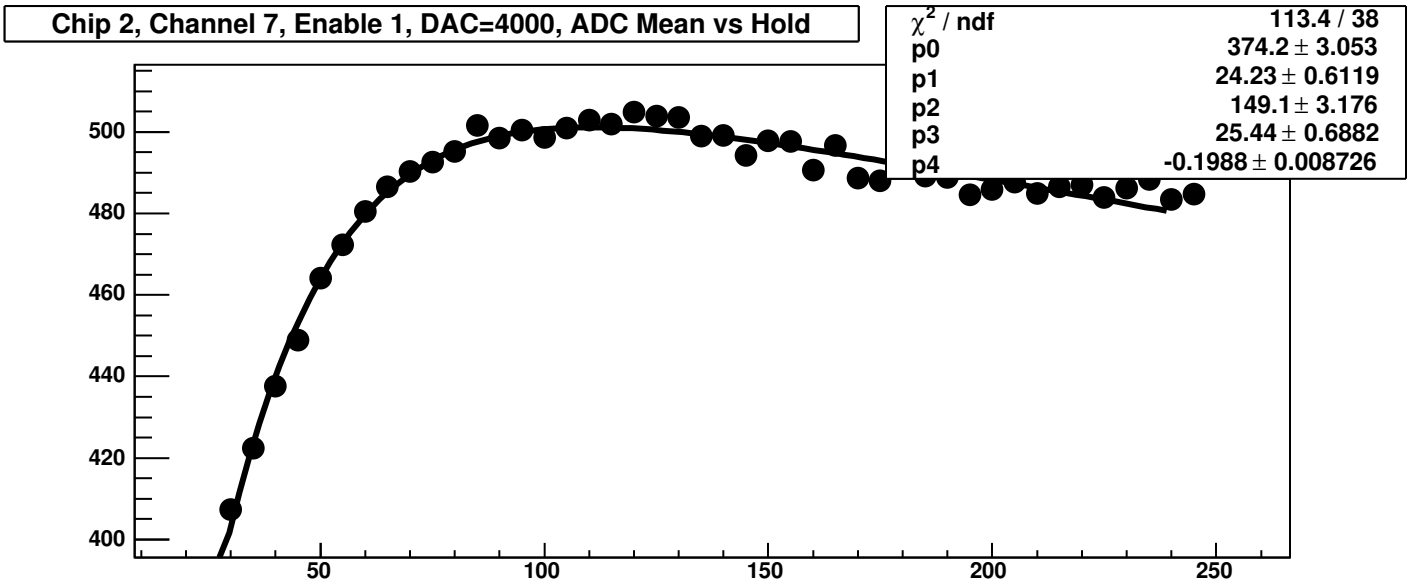
Chip 2, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

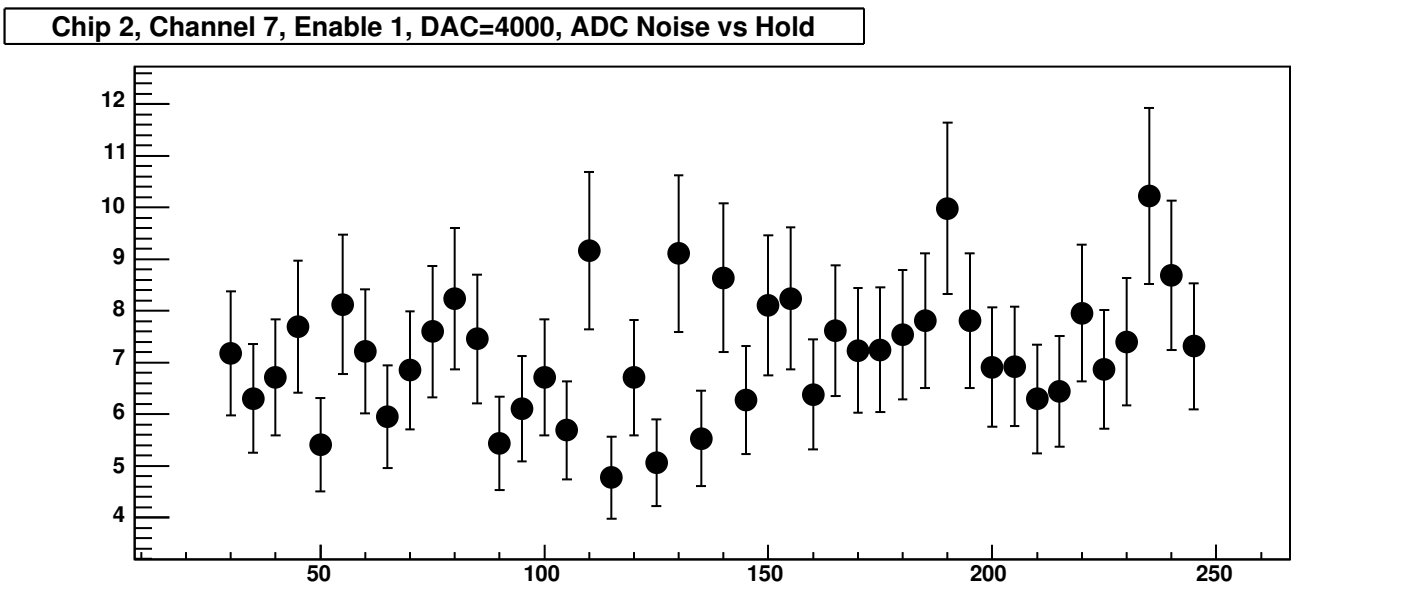


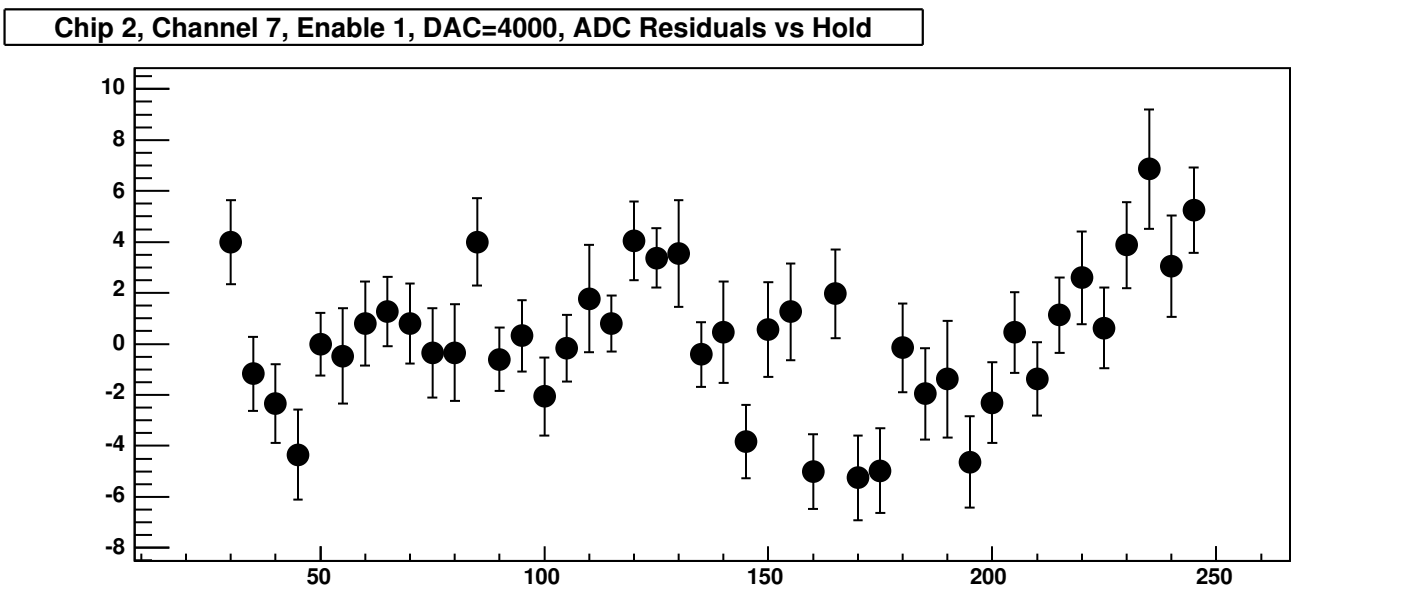

Chip 2, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 107 / 38
p0 371.9 ± 3.075
p1 23.71 ± 0.614
p2 146 ± 3.171
p3 22.65 ± 0.6506
p4 -0.1831 ± 0.007707
500
480
460
440
420
400
50
100
150
200
250

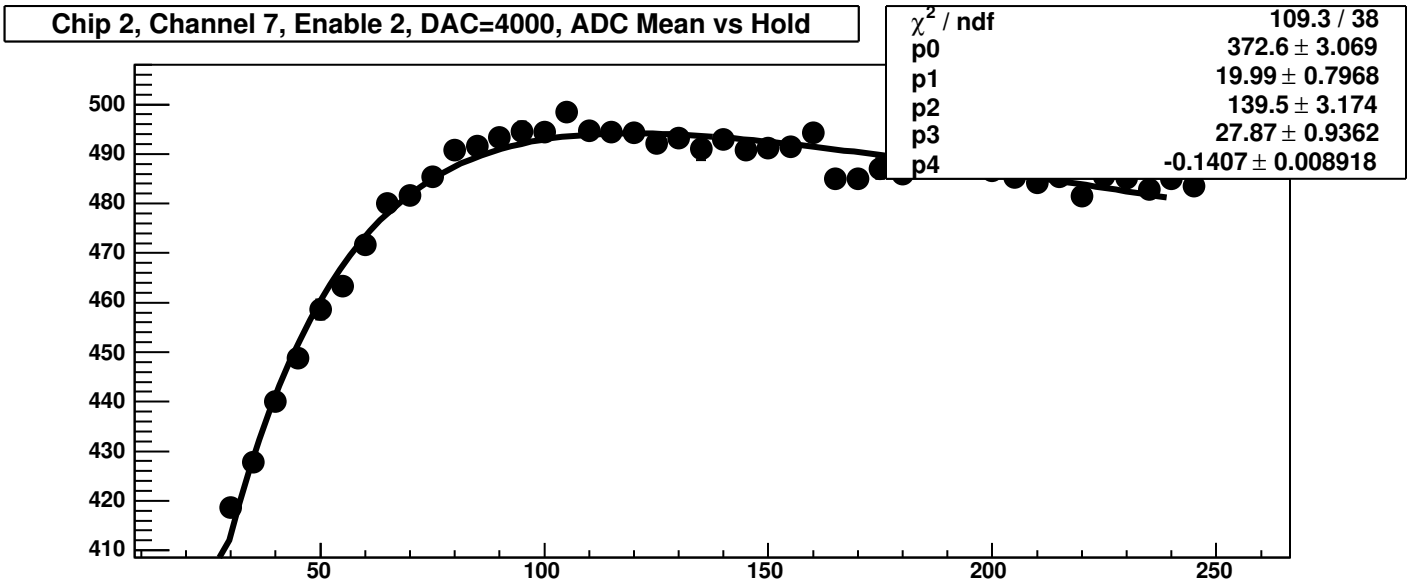

Chip 2, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

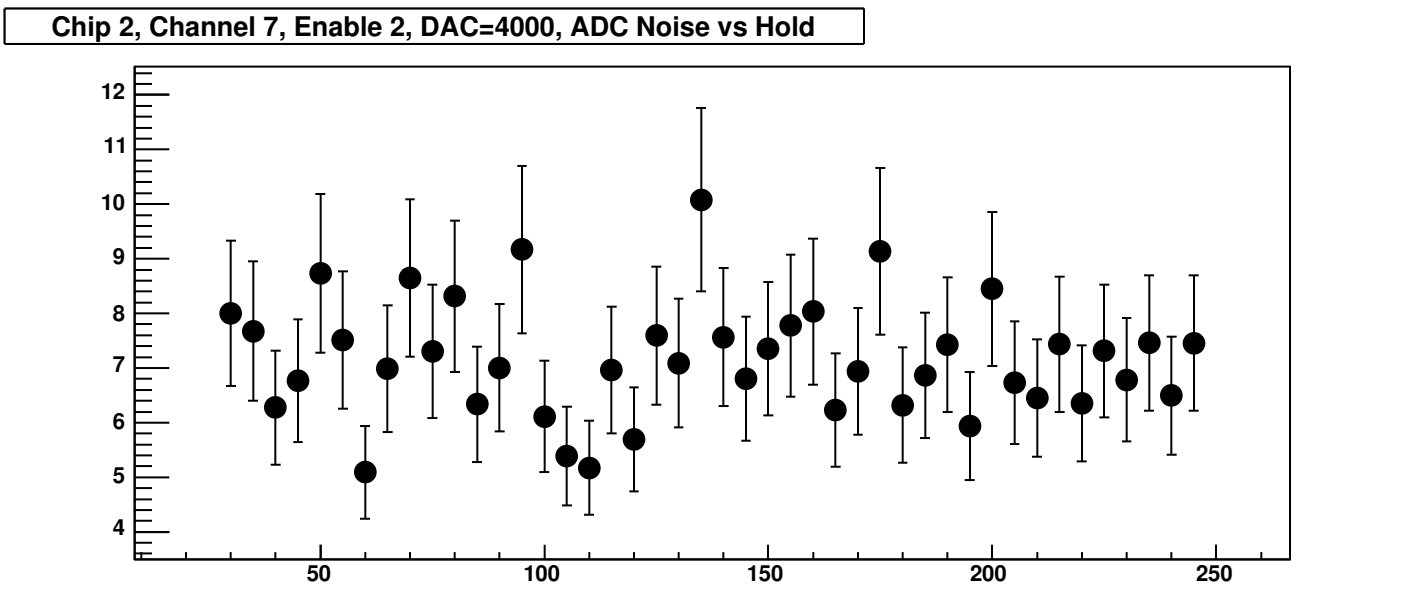

Chip 2, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
50
100
150
200
250

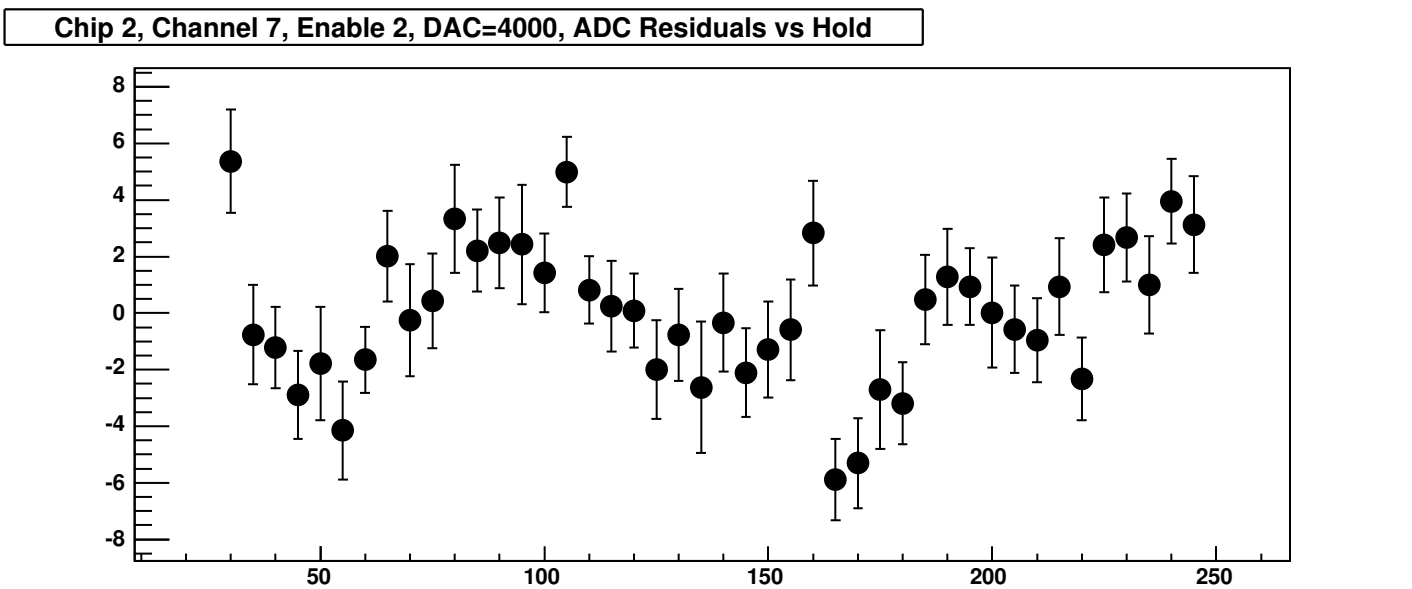

Chip 2, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 113.4 / 38
p0 374.2 ± 3.053
p1 24.23 ± 0.6119
p2 149.1 ± 3.176
p3 25.44 ± 0.6882
p4 -0.1988 ± 0.008726
500
480
460
440
420
400
50
100
150
200
250

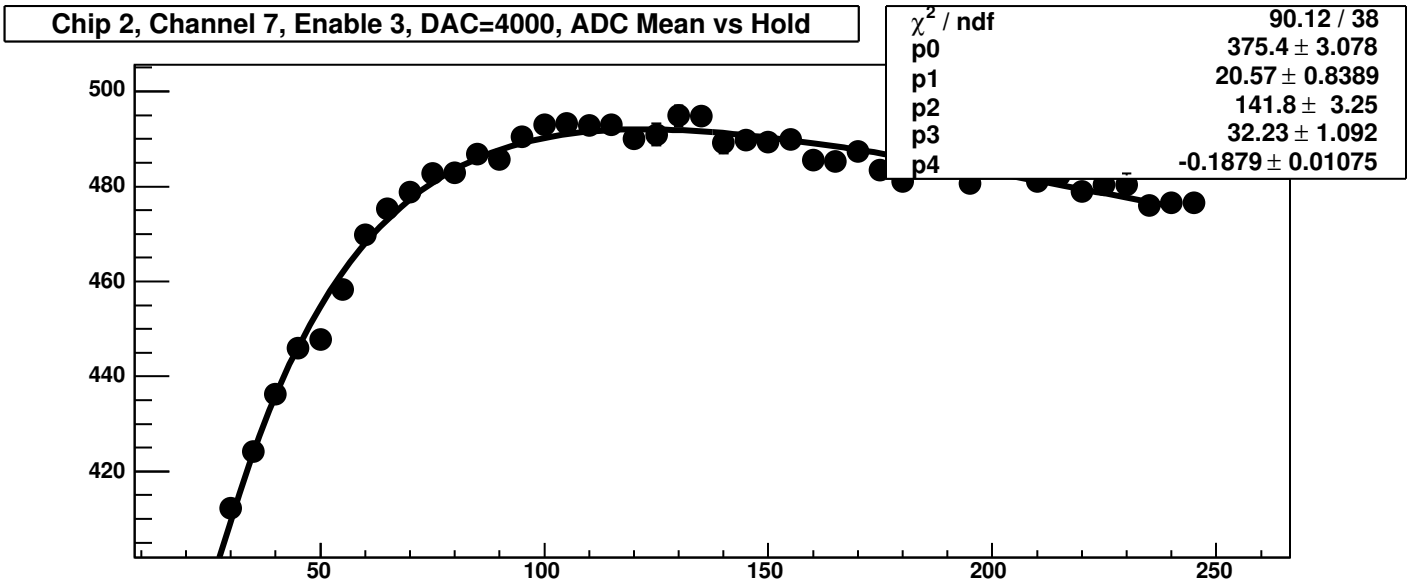

Chip 2, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

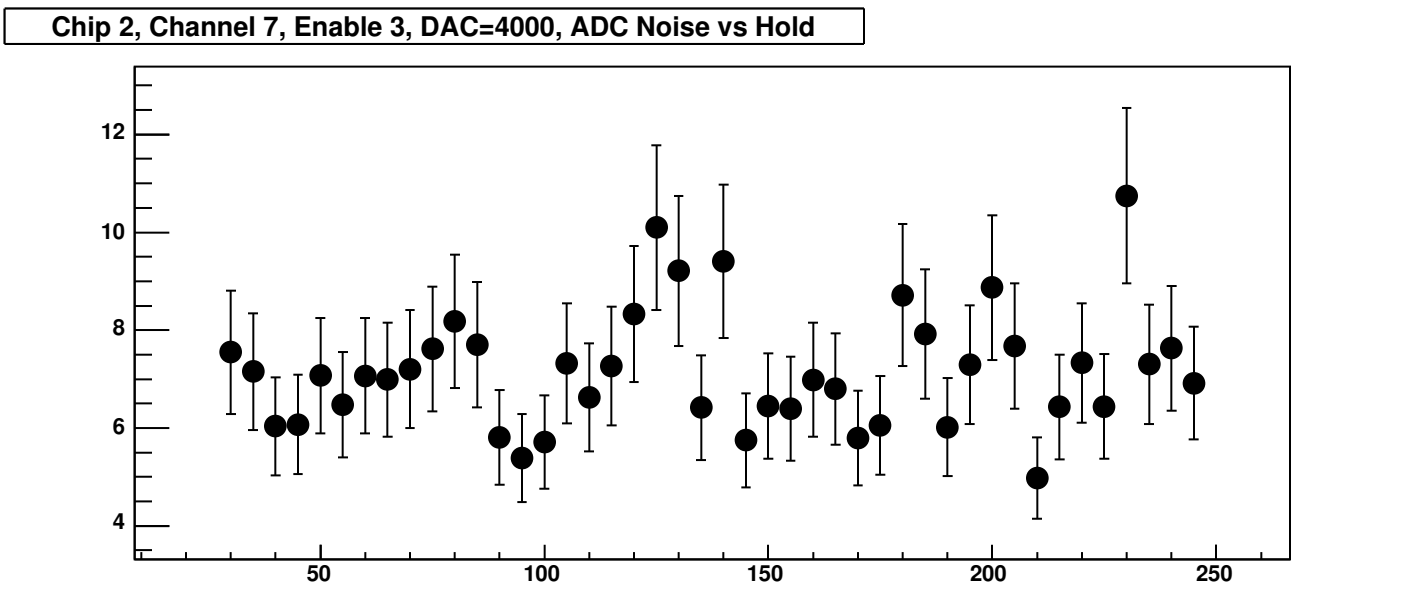

Chip 2, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

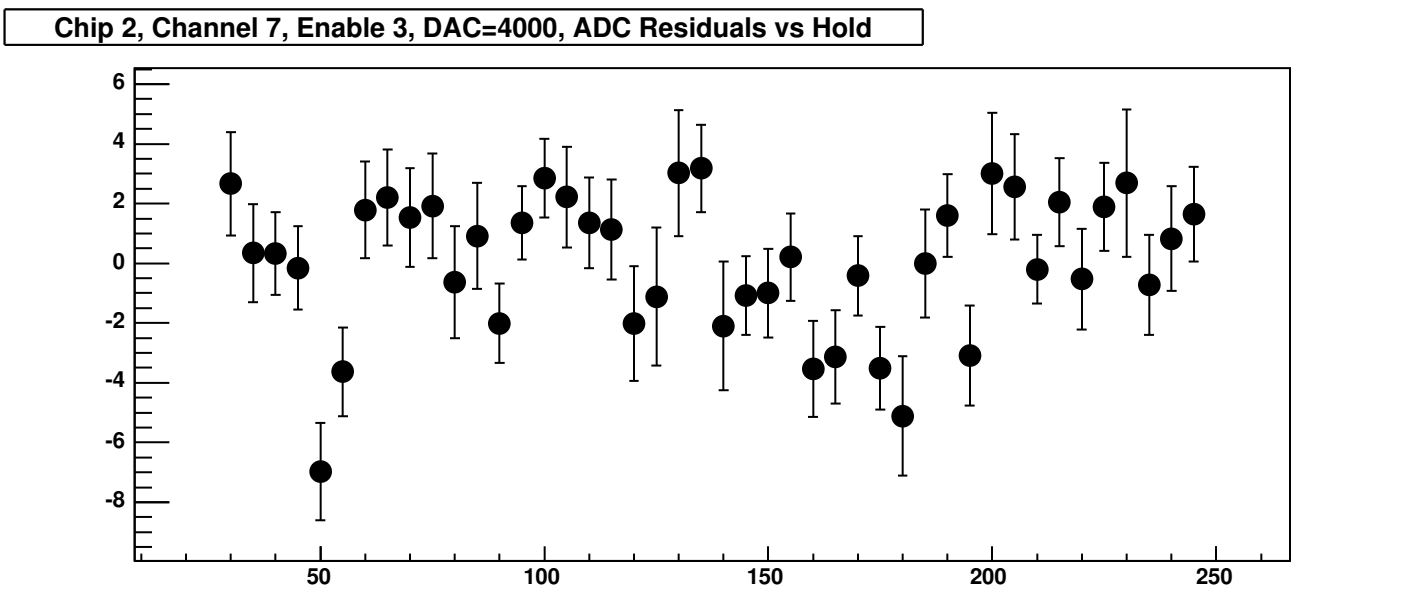

Chip 2, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 109.3 / 38
p0 372.6 ± 3.069
p1 19.99 ± 0.7968
p2 139.5 ± 3.174
p3 27.87 ± 0.9362
p4 -0.1407 ± 0.008918
500
490
480
470
460
450
440
430
420
410
50
100
150
200
250


Chip 2, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

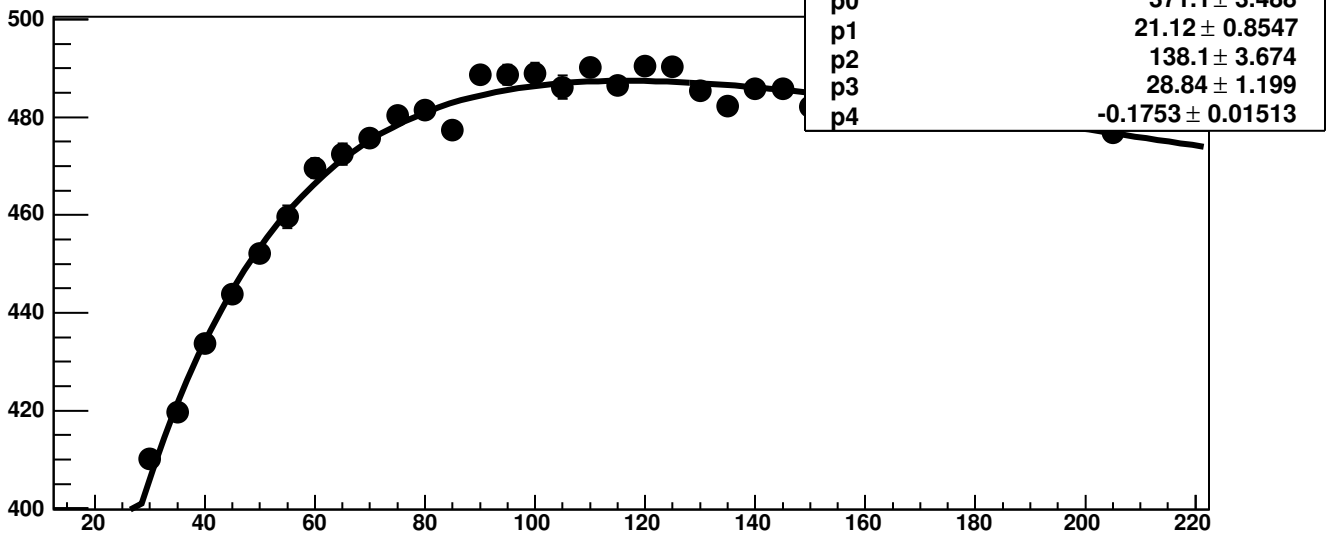

Chip 2, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250


Chip 2, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 90.12 / 38
p0 375.4 ± 3.078
p1 20.57 ± 0.8389
p2 141.8 ± 3.25
p3 32.23 ± 1.092
p4 -0.1879 ± 0.01075
500
480
460
440
420
50
100
150
200
250

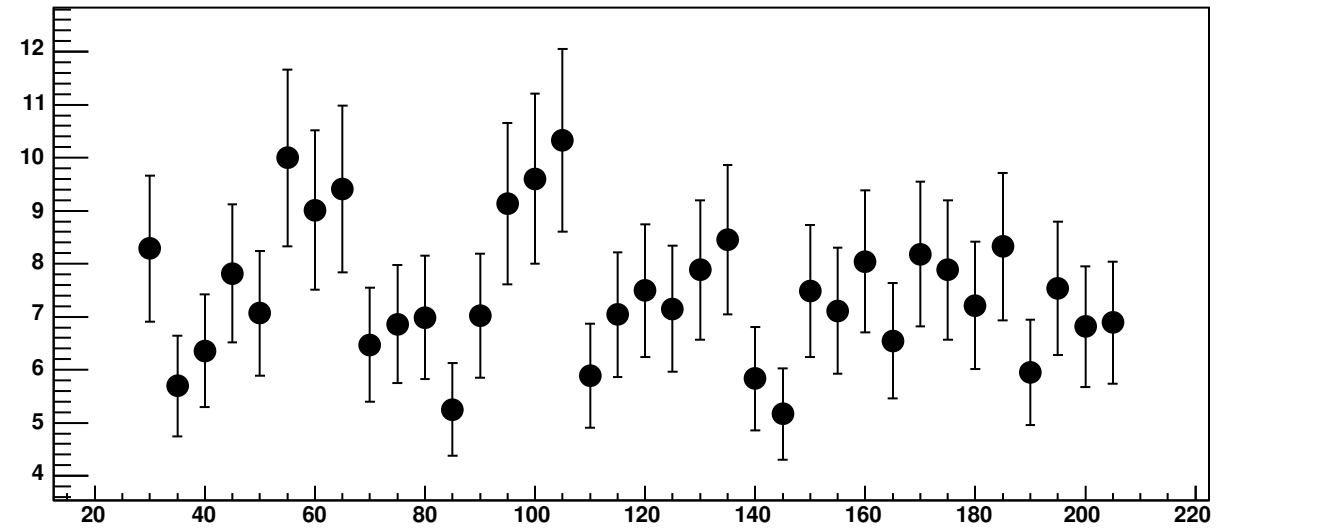

Chip 2, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250


Chip 2, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

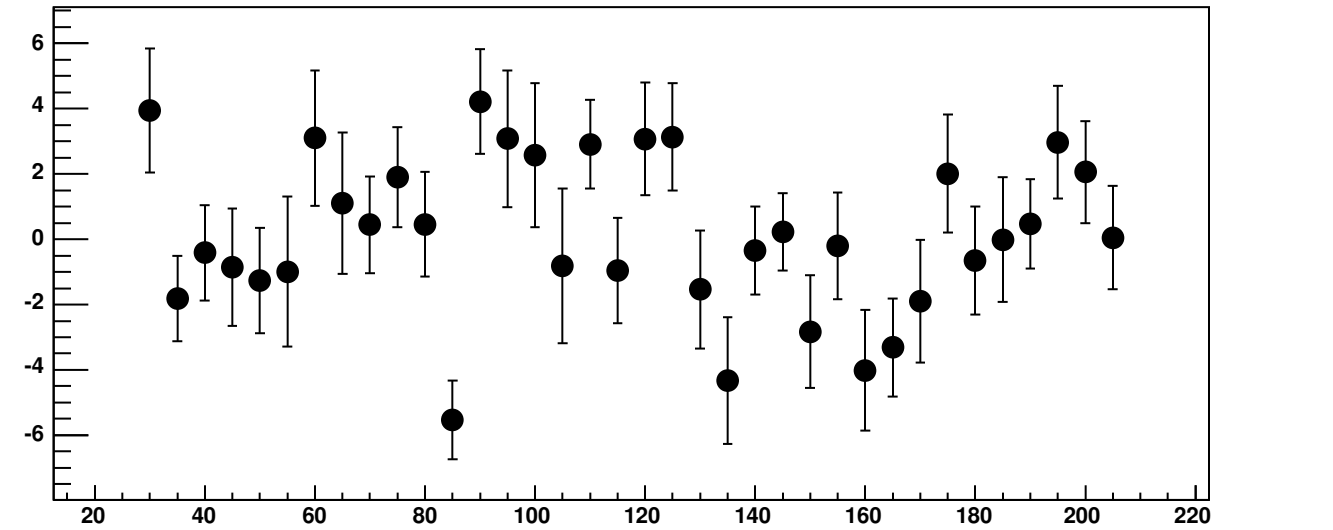
Chip 2, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold



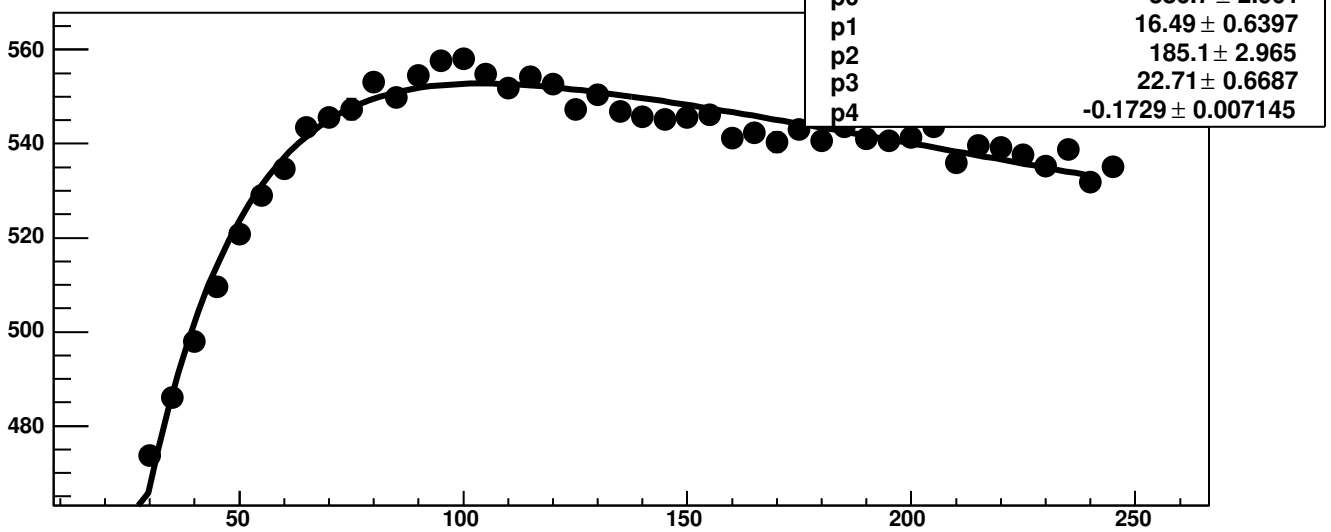
Chip 2, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



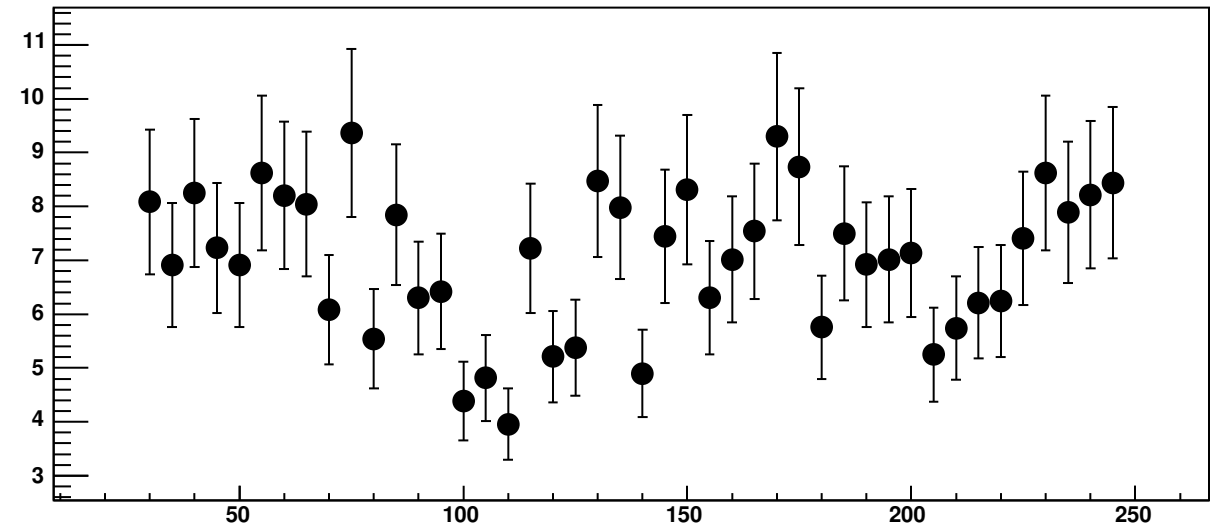
Chip 2, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold



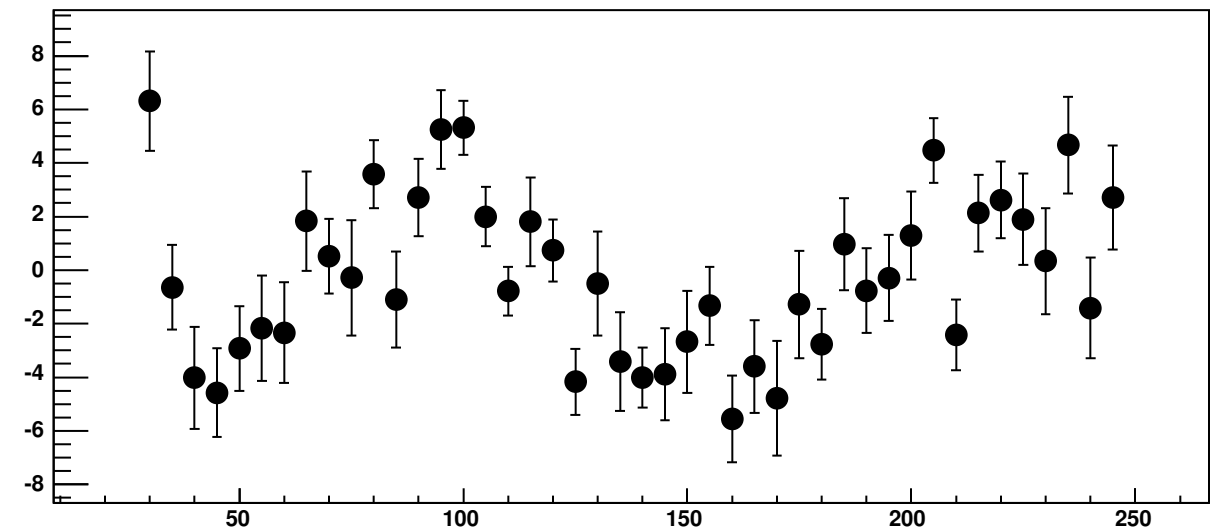
Chip 2, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

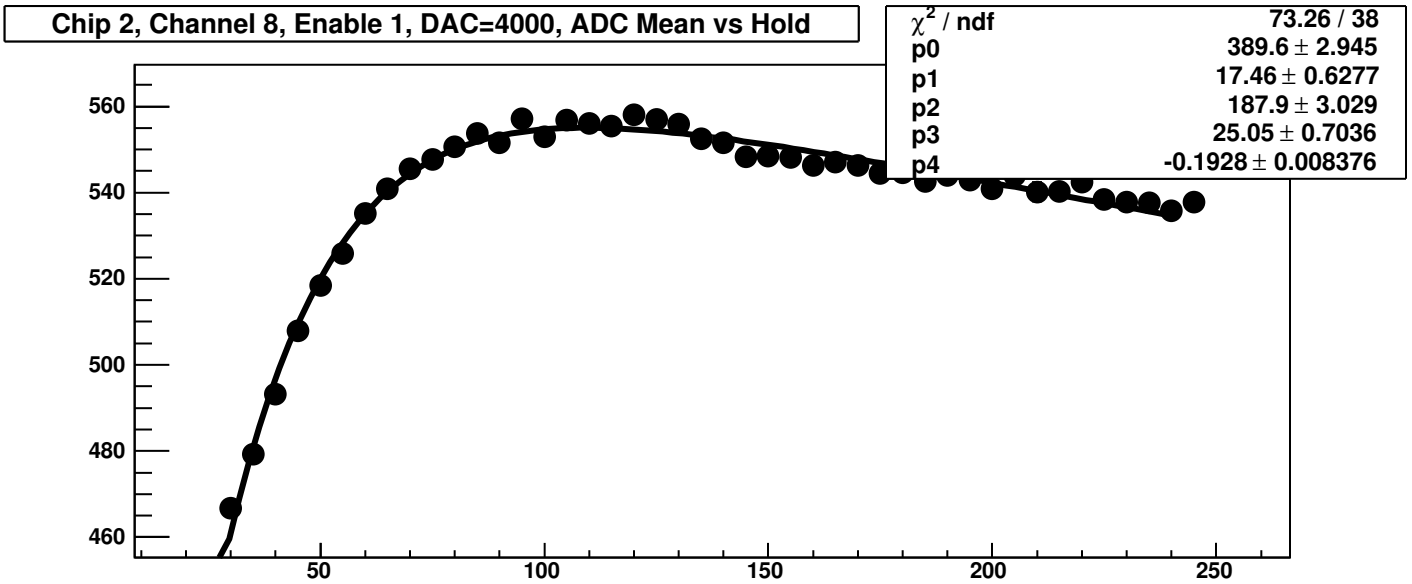


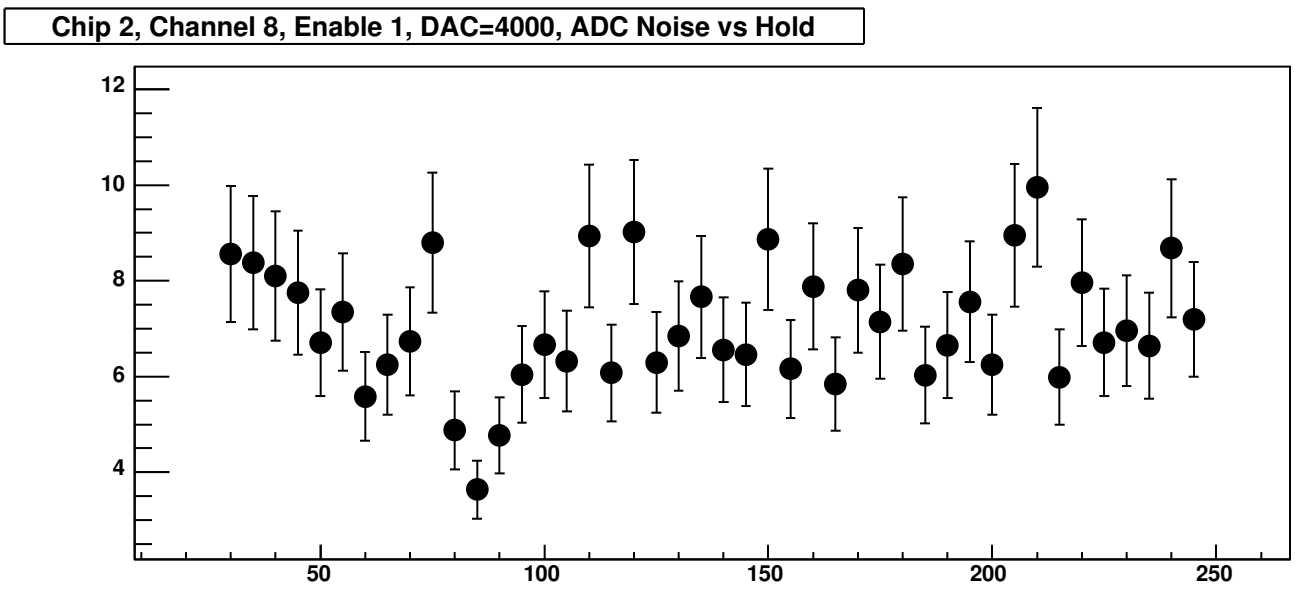
Chip 2, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold

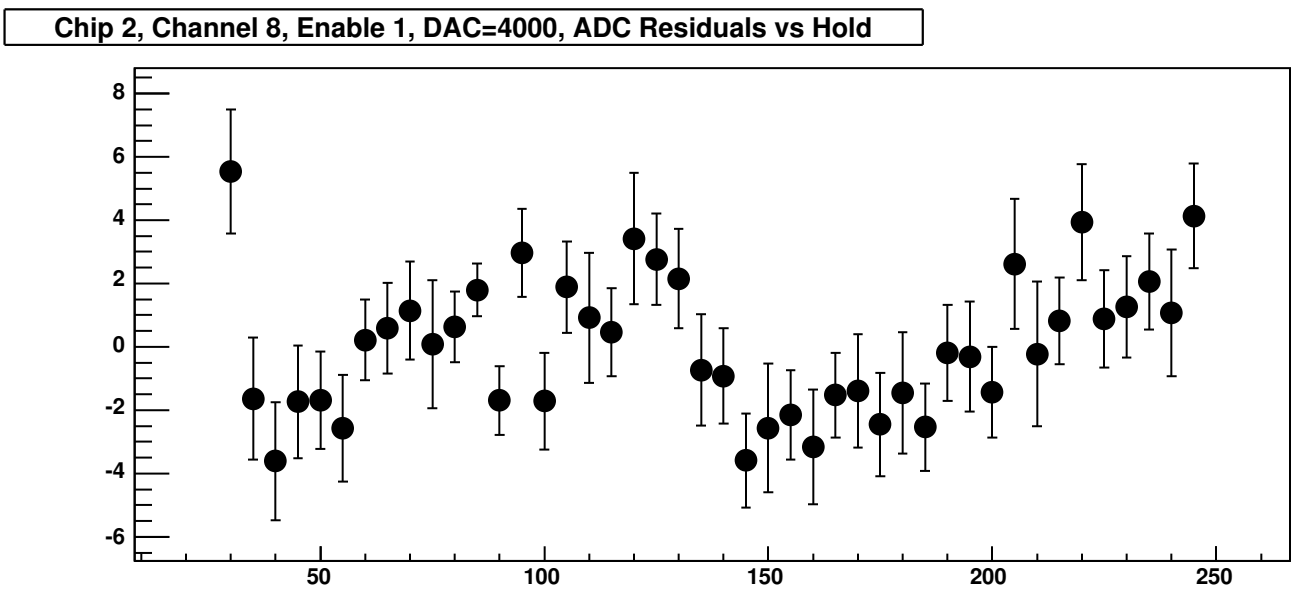


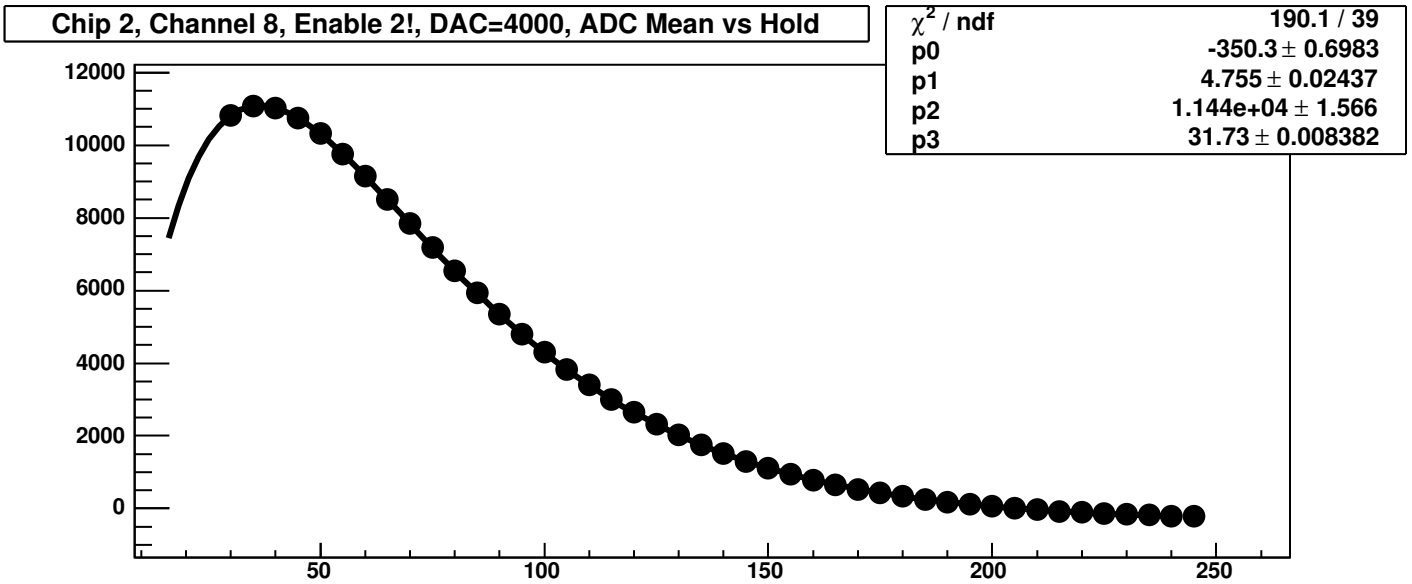
Chip 2, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

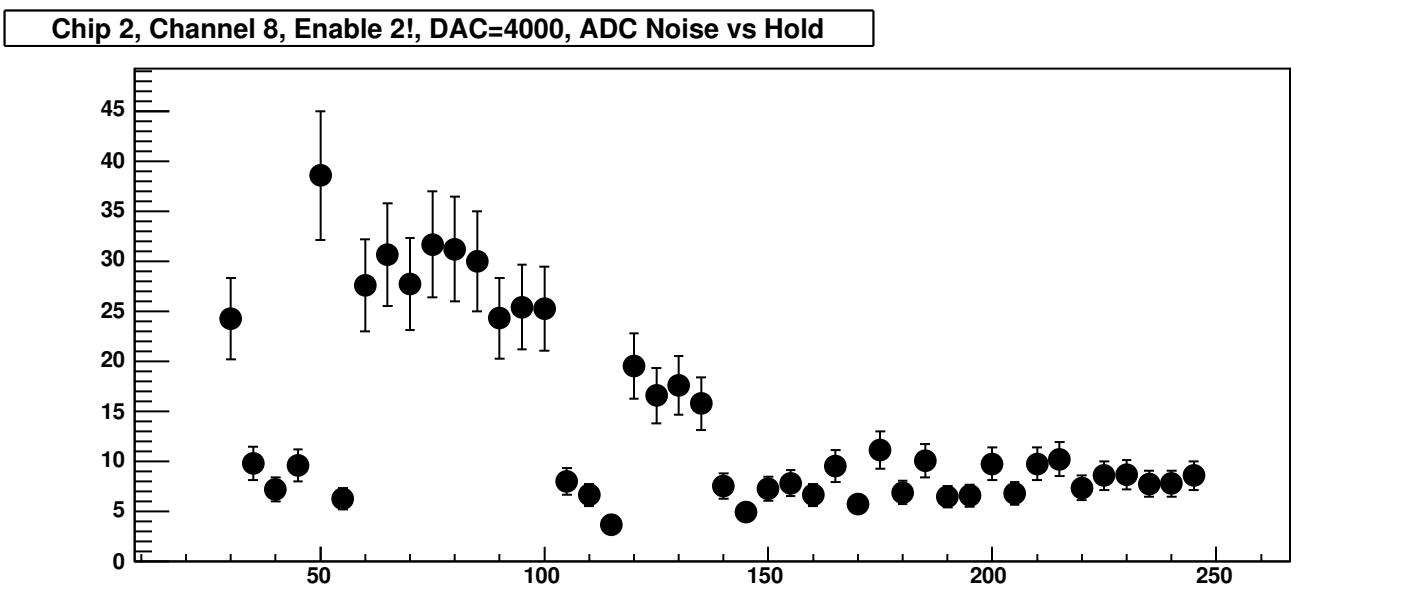


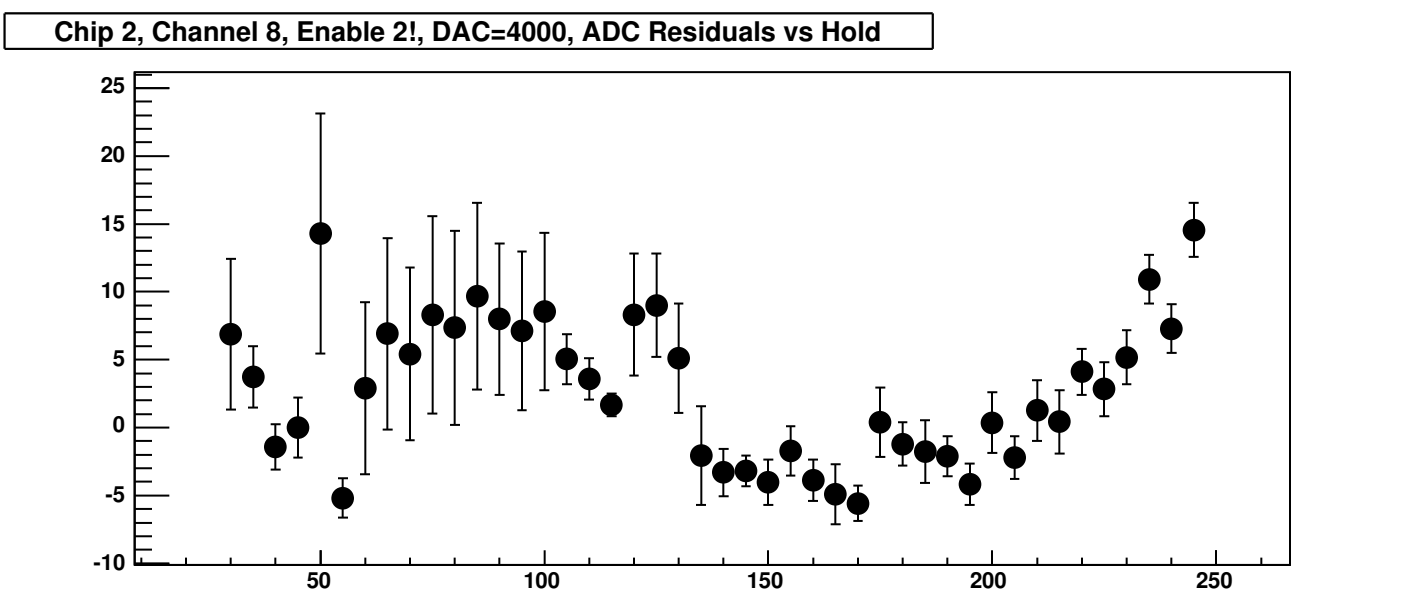

Chip 2, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 73.26 / 38
p0 389.6 ± 2.945
p1 17.46 ± 0.6277
p2 187.9 ± 3.029
p3 25.05 ± 0.7036
p4 -0.1928 ± 0.008376
560
540
520
500
480
460
50
100
150
200
250


Chip 2, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250

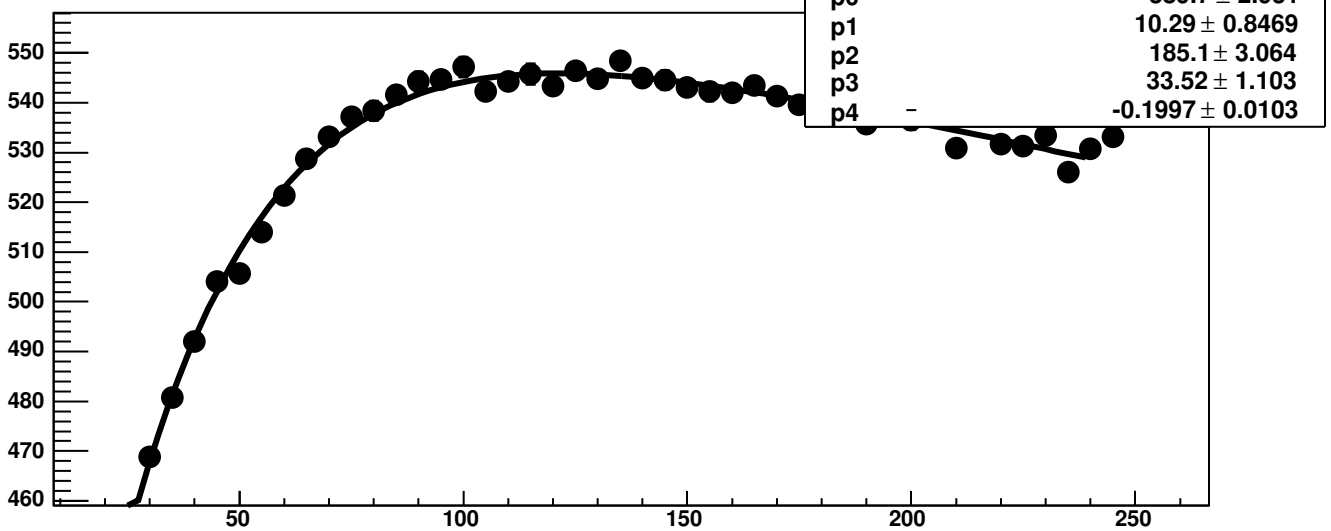

Chip 2, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
50
100
150
200
250


Chip 2, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 190.1 / 39
p0 -350.3 ± 0.6983
p1 4.755 ± 0.02437
p2 1.144e+04 ± 1.566
p3 31.73 ± 0.008382
12000
10000
8000
6000
4000
2000
0
50
100
150
200
250

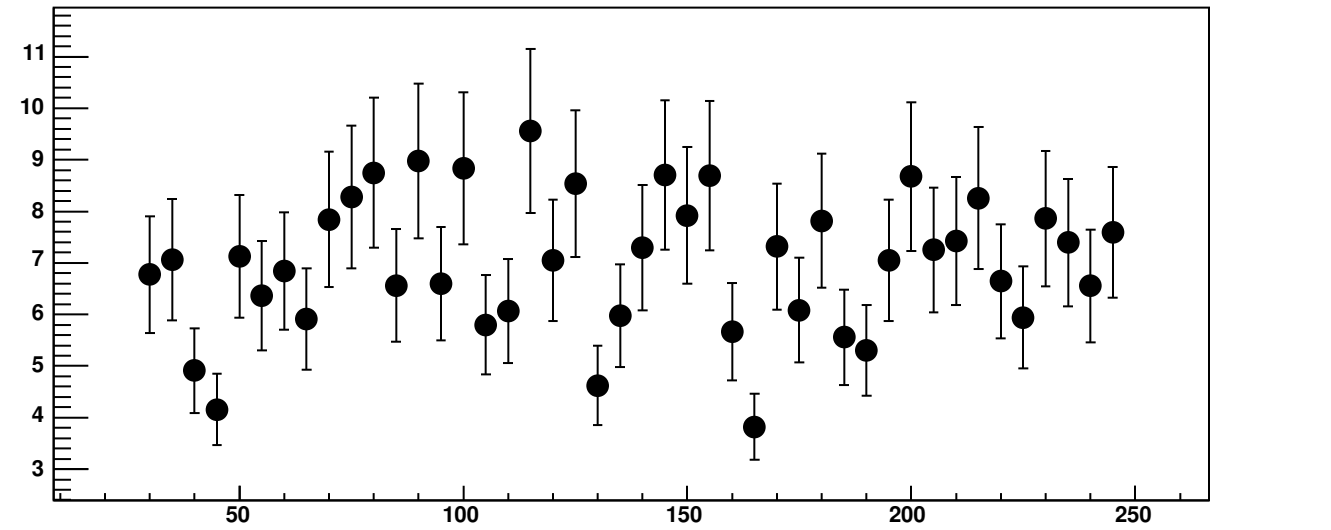

Chip 2, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold
45
40
35
30
25
20
15
10
5
0
50
100
150
200
250


Chip 2, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
50
100
150
200
250

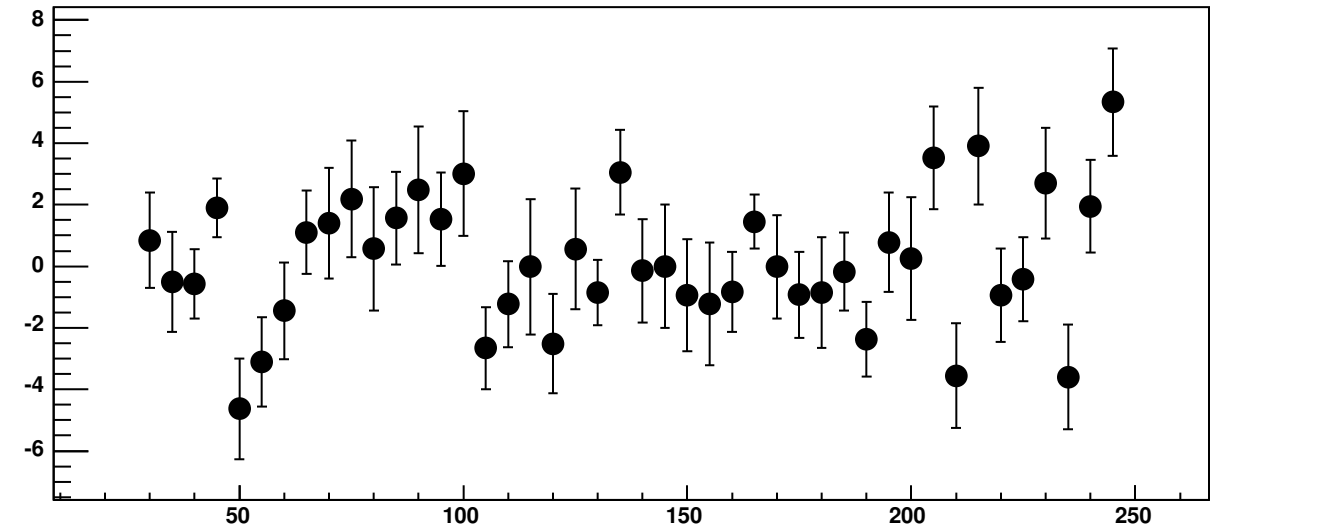
Chip 2, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold



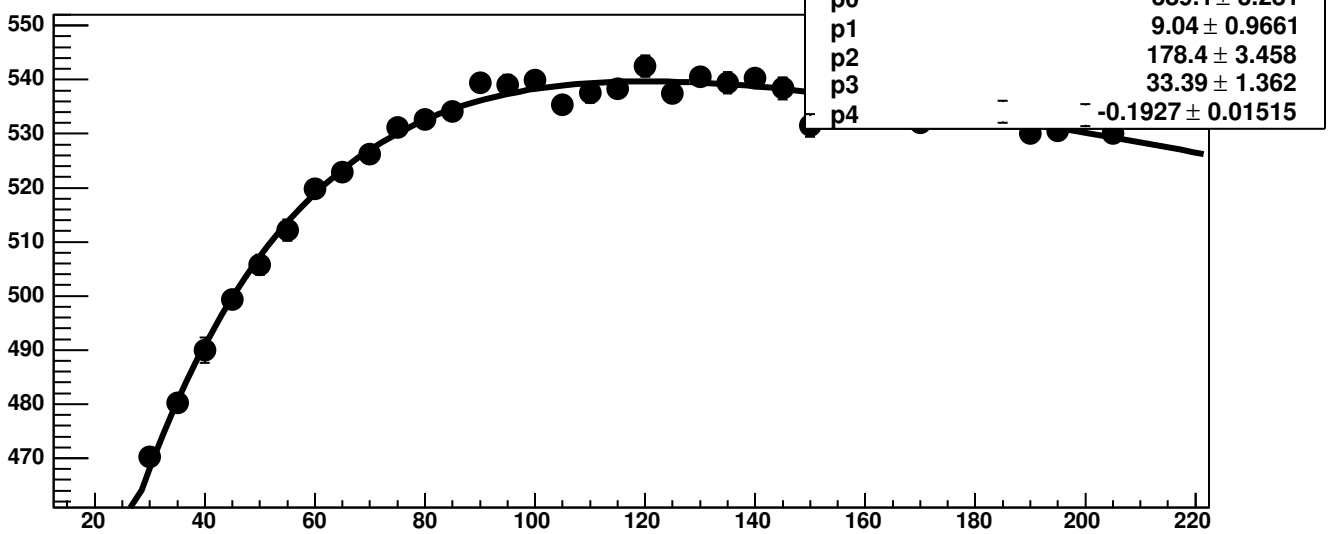
Chip 2, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



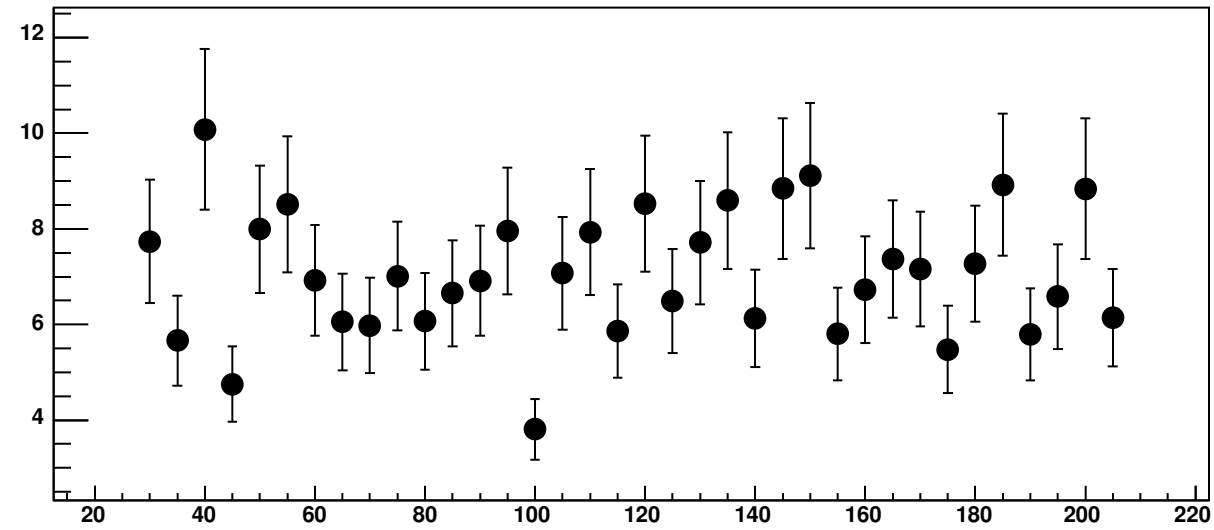
Chip 2, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold



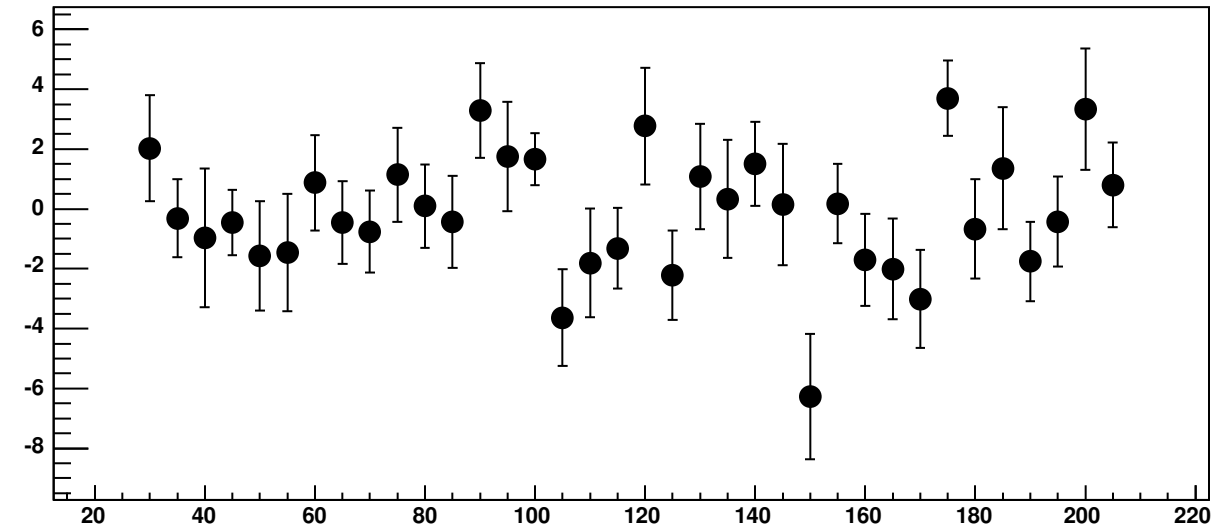
Chip 2, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

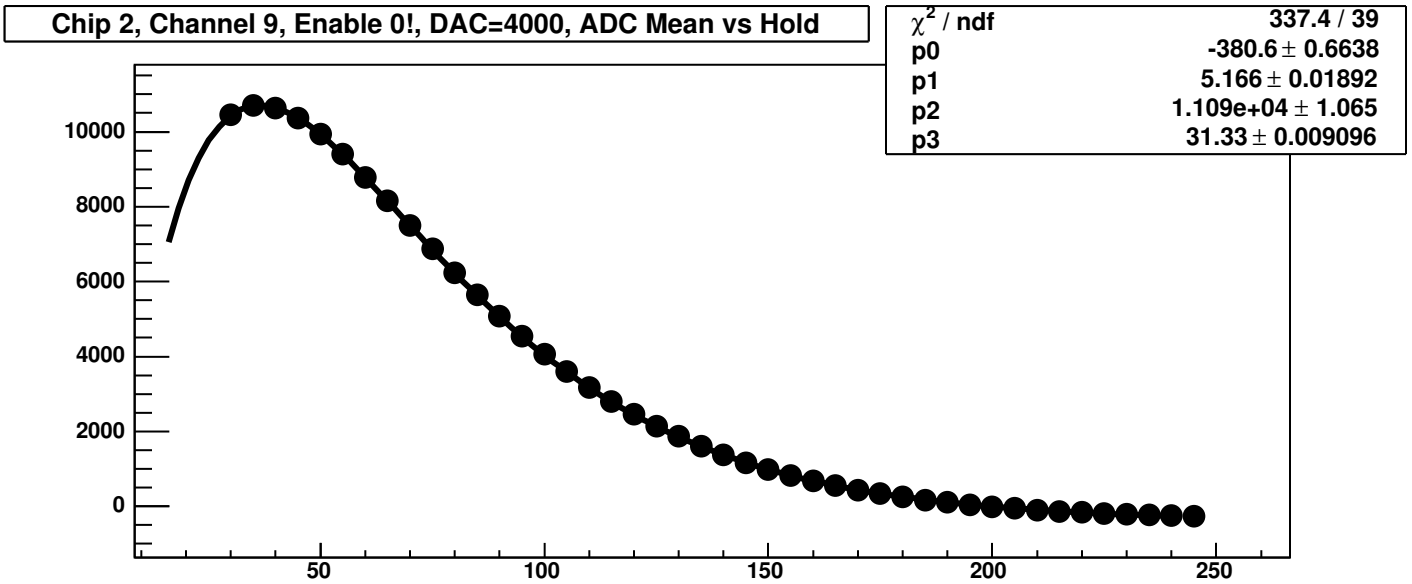


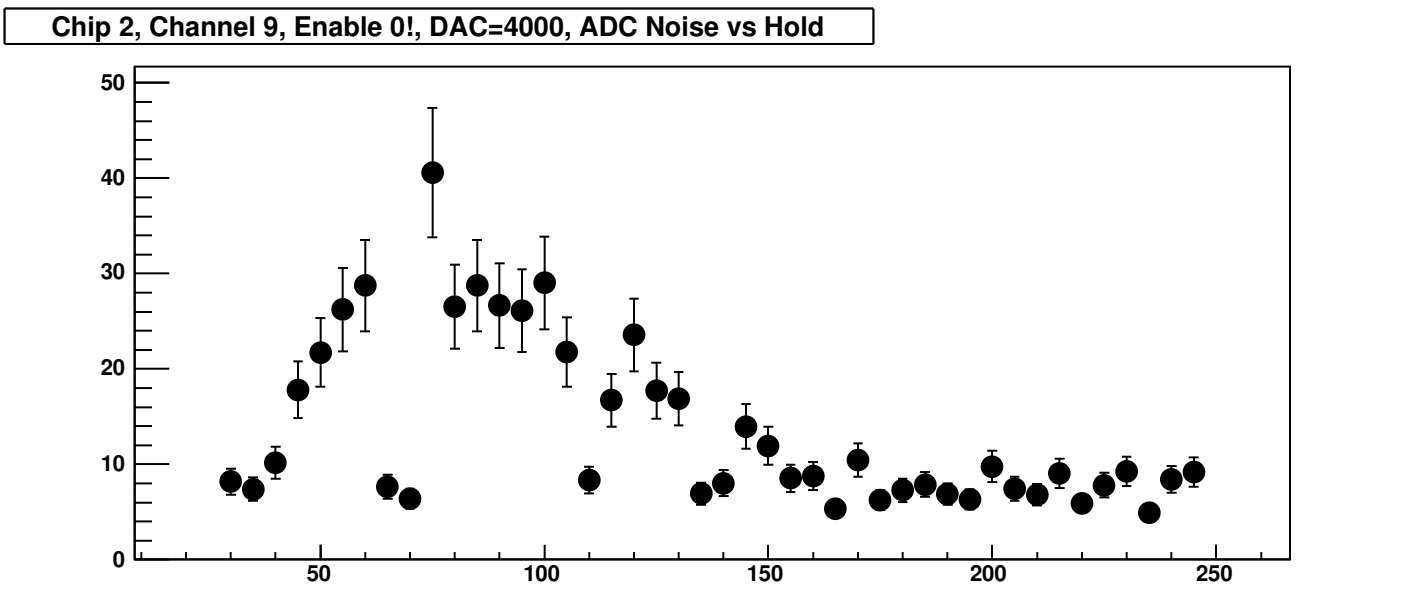
Chip 2, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold

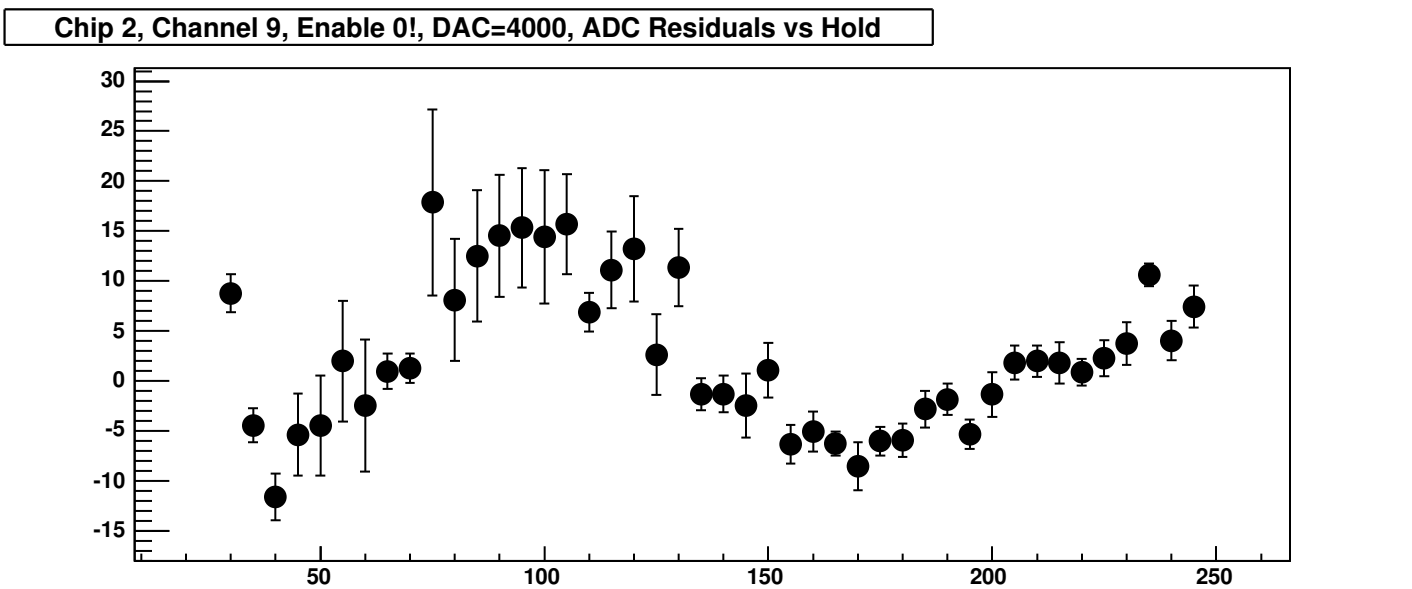


Chip 2, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

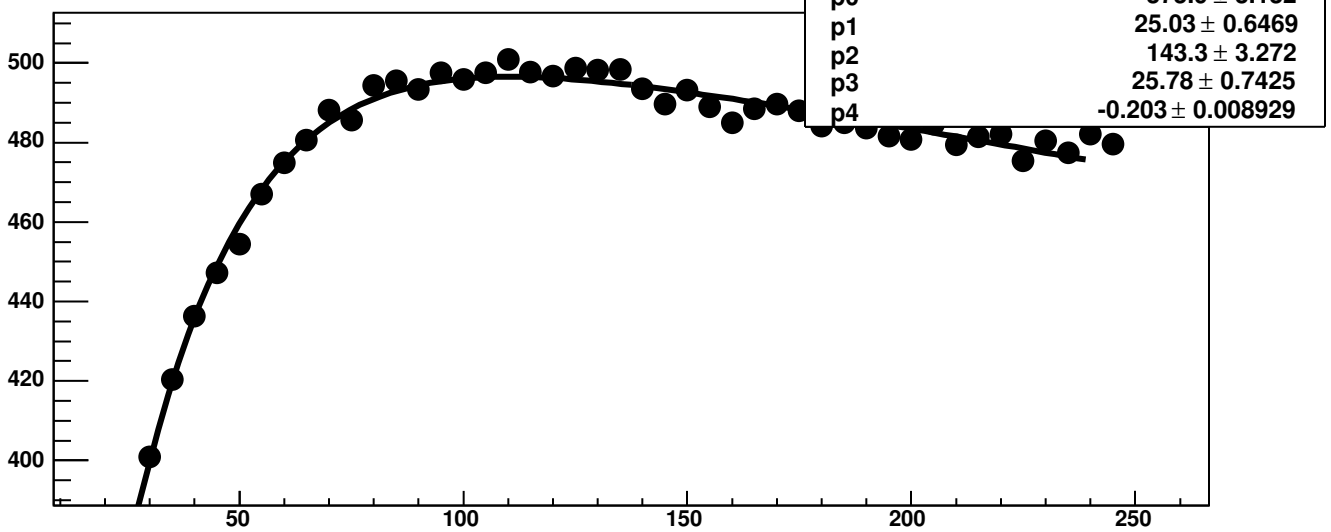



Chip 2, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 337.4 / 39
p0 -380.6 ± 0.6638
p1 5.166 ± 0.01892
p2 1.109e+04 ± 1.065
p3 31.33 ± 0.009096
10000
8000
6000
4000
2000
0
50
100
150
200
250

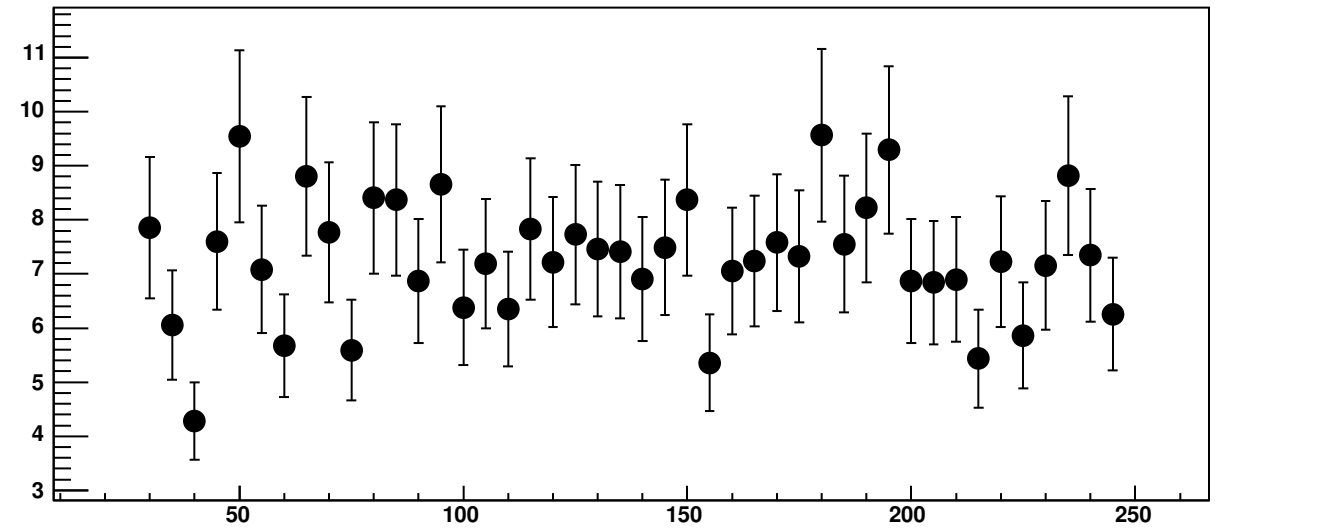

Chip 2, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
0
50
100
150
200
250


Chip 2, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
-15
50
100
150
200
250

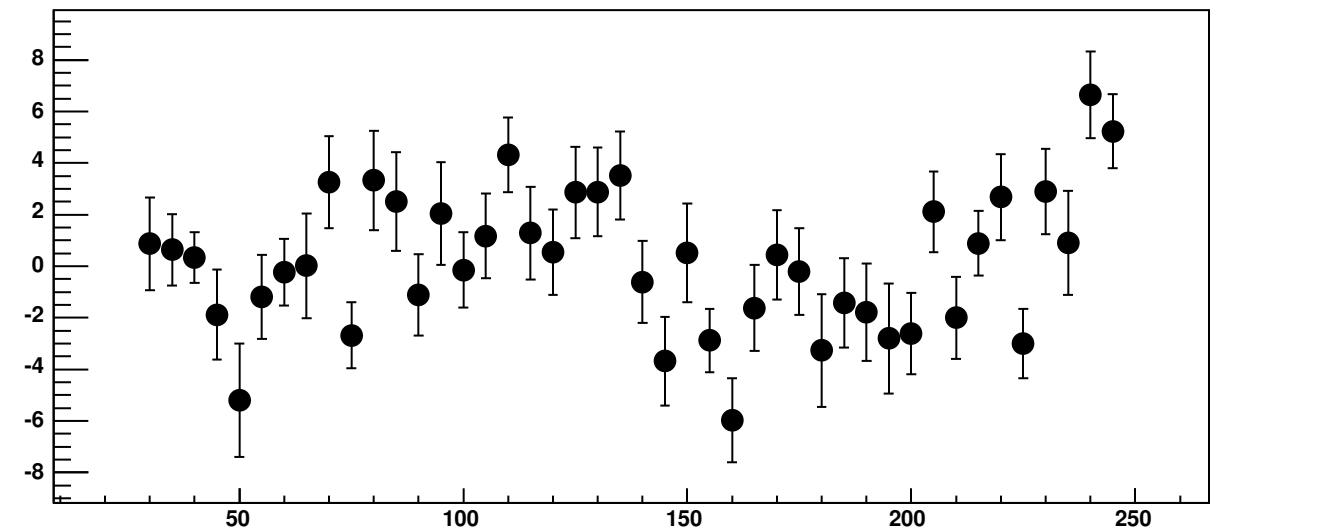
Chip 2, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold

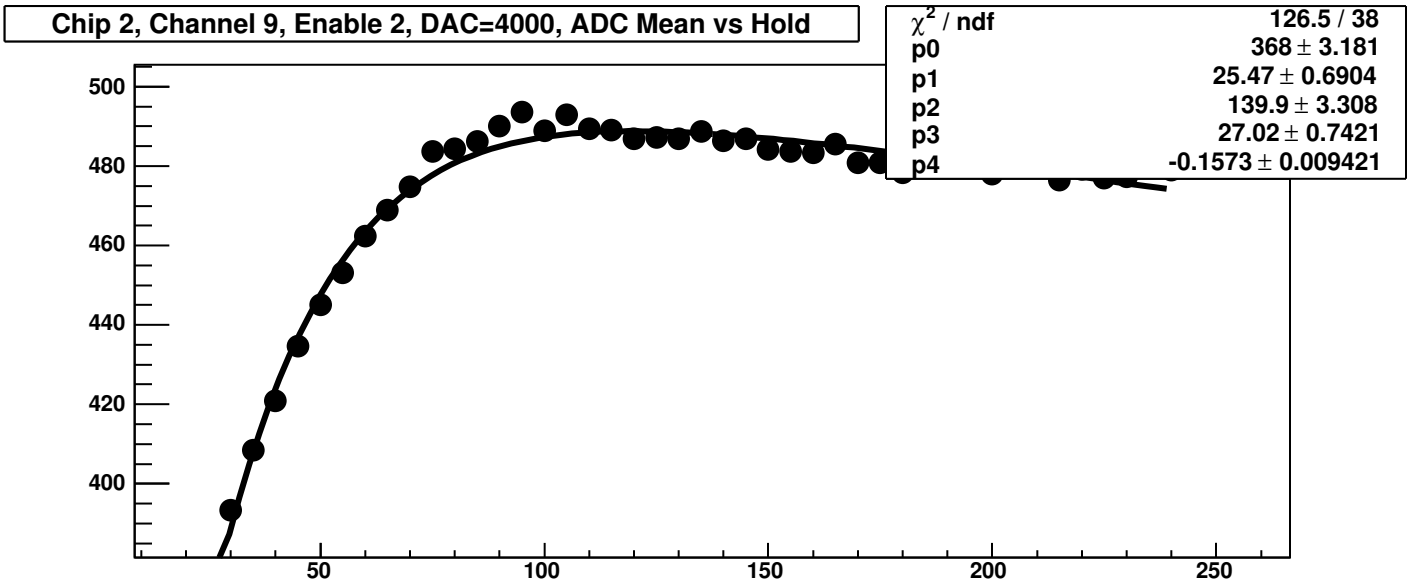


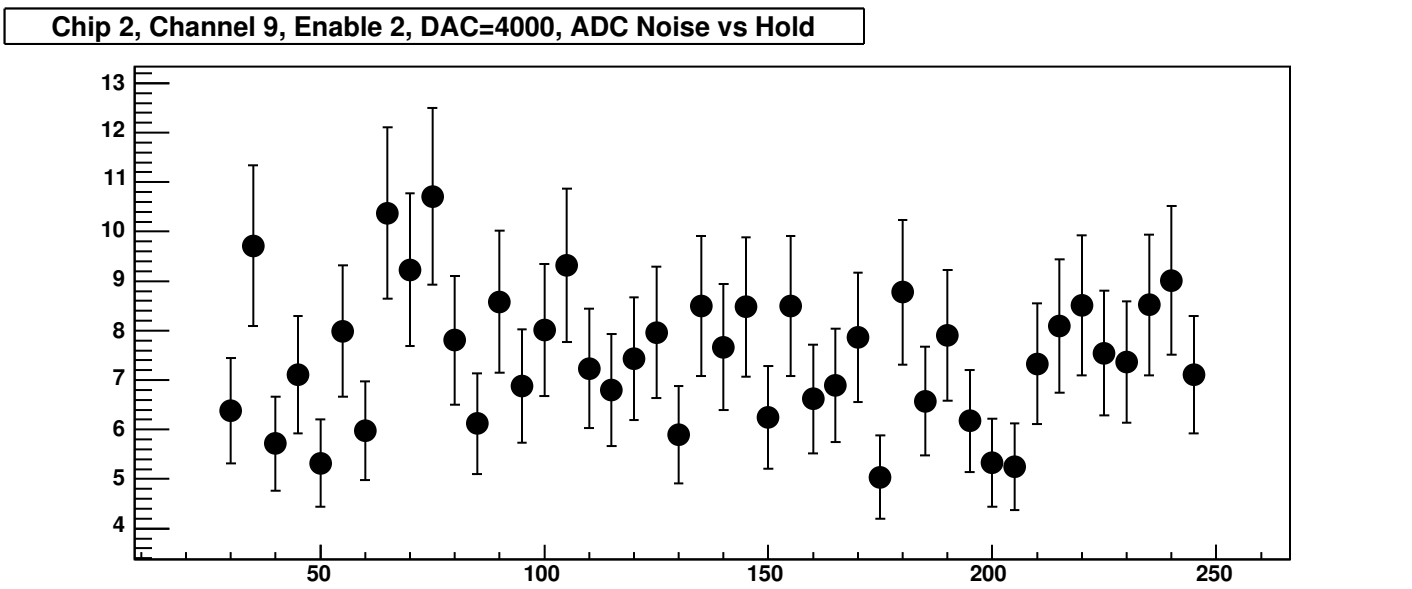
Chip 2, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold

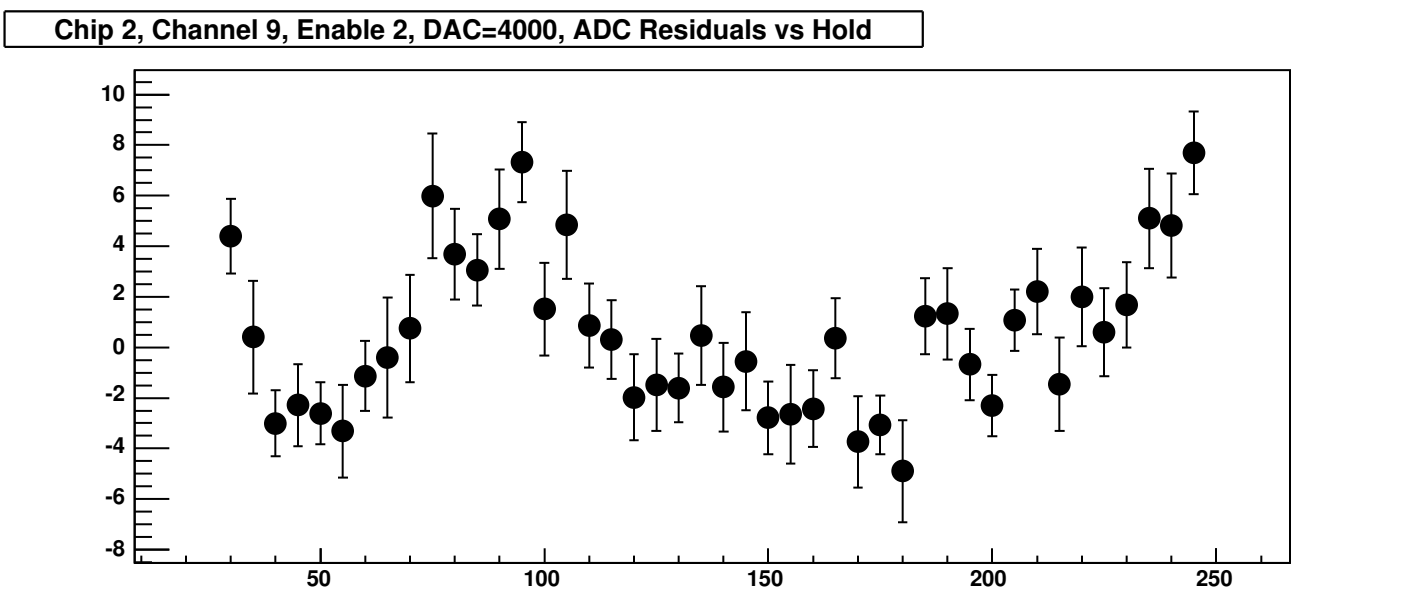


Chip 2, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

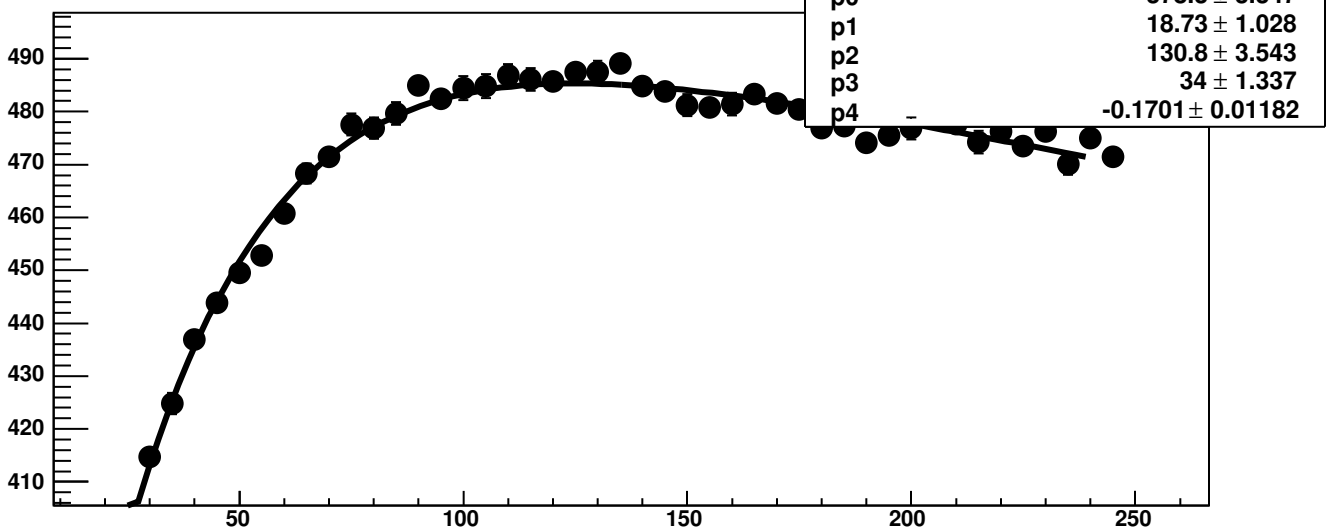



Chip 2, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 126.5 / 38
p0 368 ± 3.181
p1 25.47 ± 0.6904
p2 139.9 ± 3.308
p3 27.02 ± 0.7421
p4 -0.1573 ± 0.009421
500
480
460
440
420
400
50
100
150
200
250

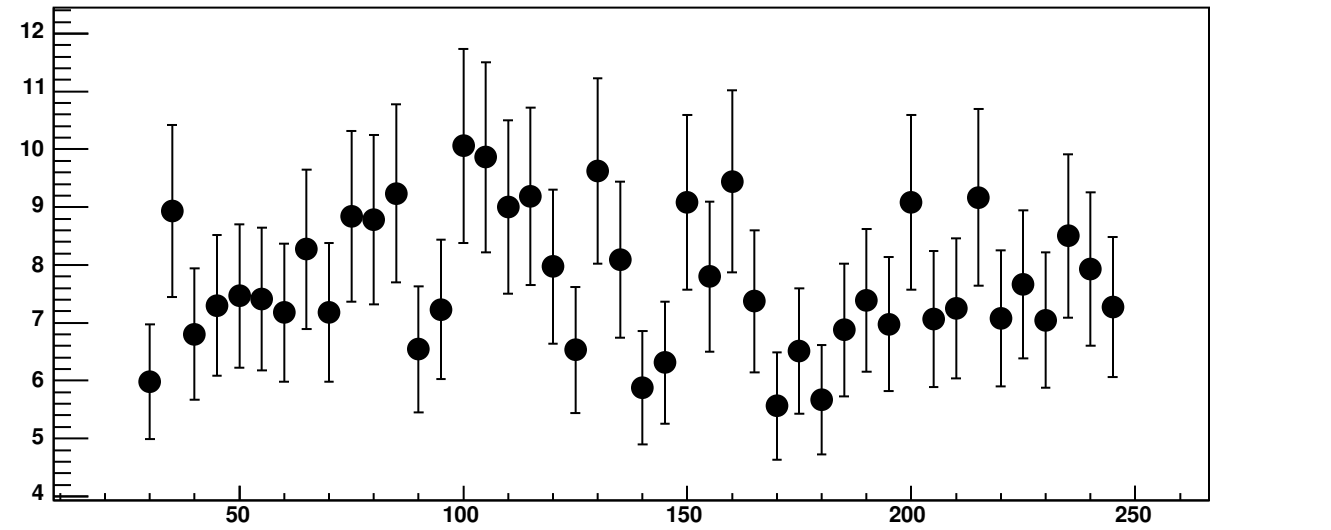

Chip 2, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 2, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

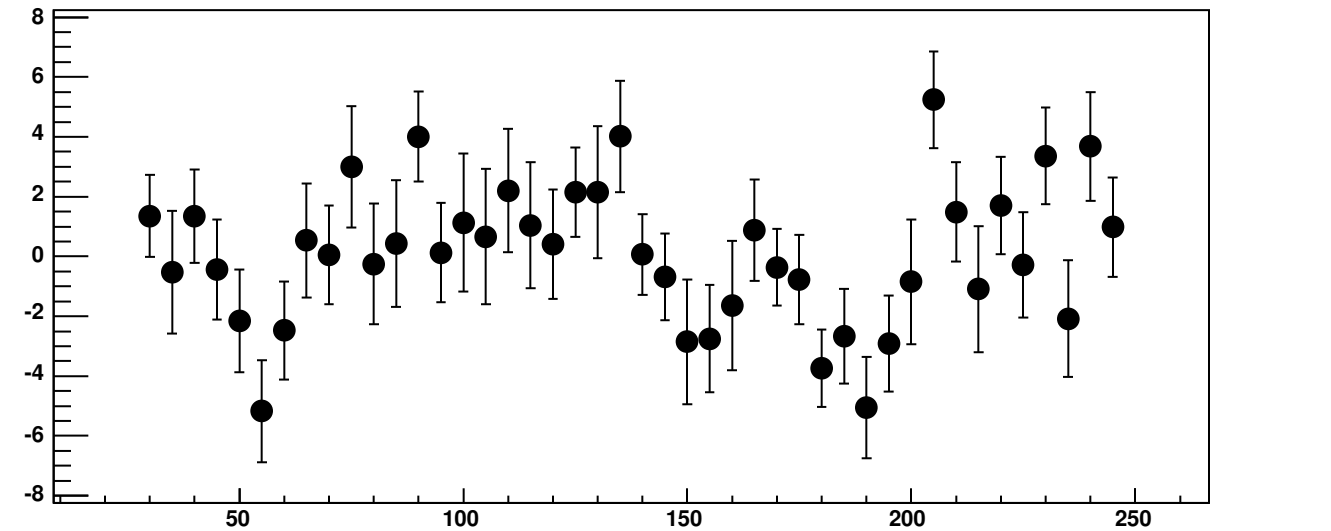
Chip 2, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold



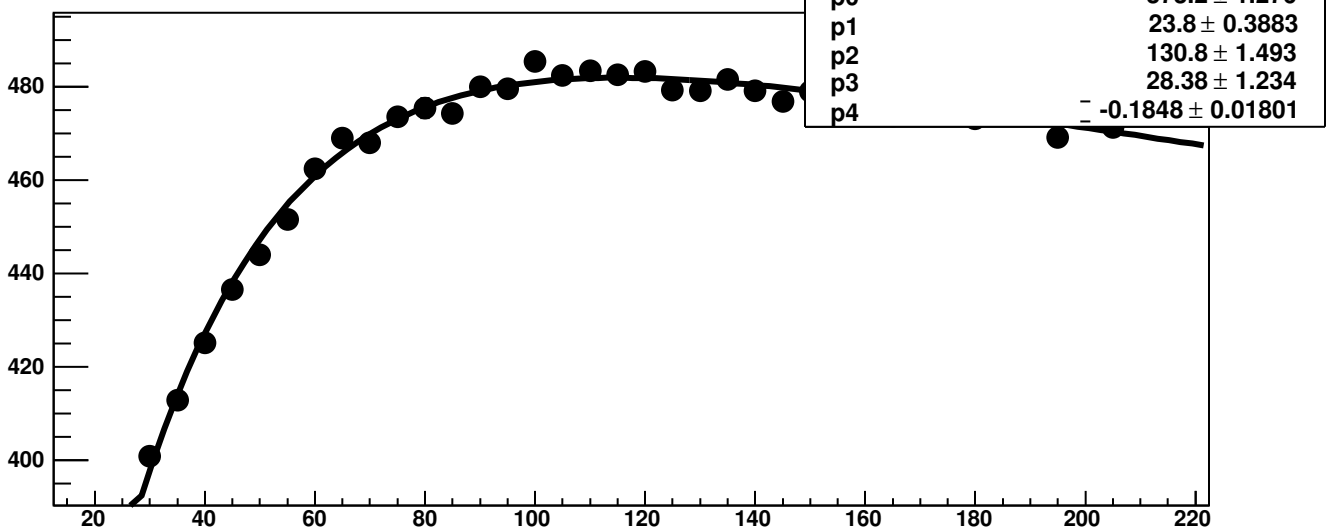
Chip 2, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold



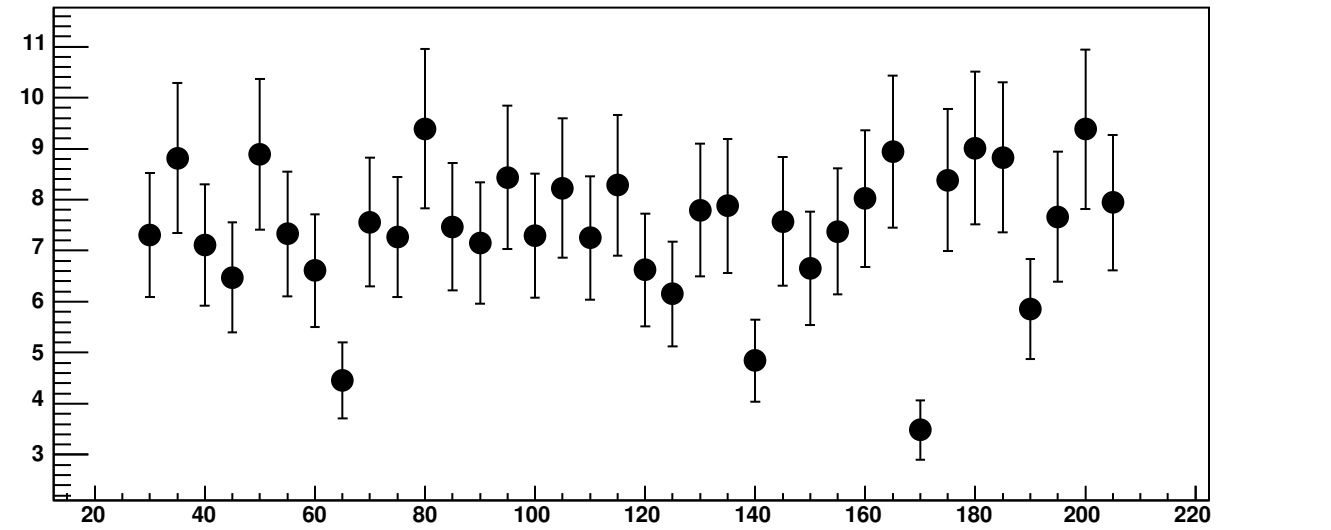
Chip 2, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold



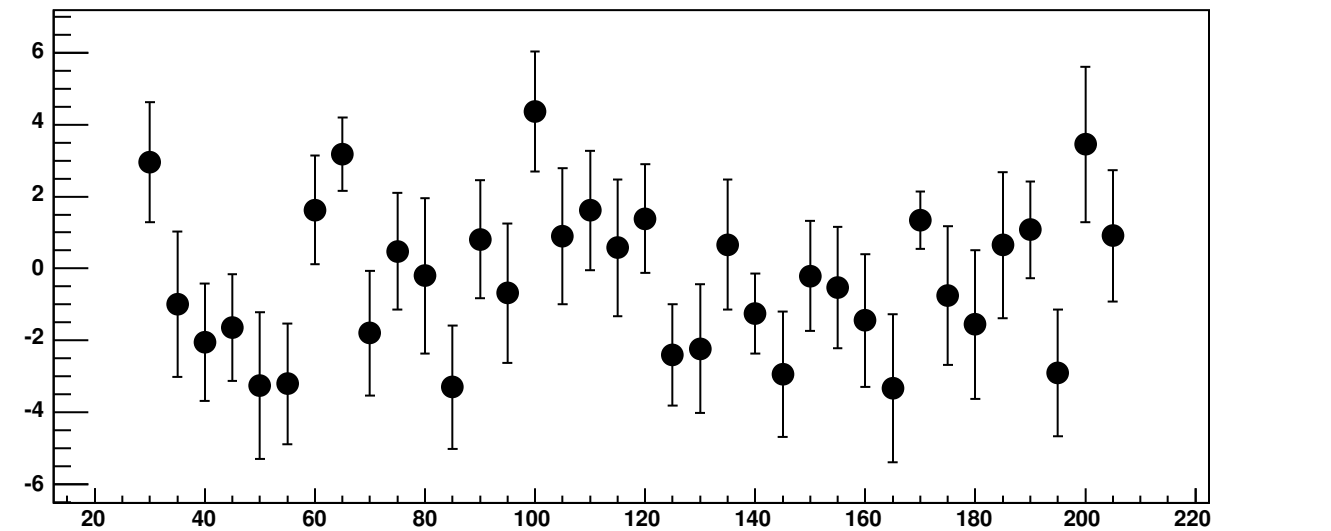
Chip 2, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

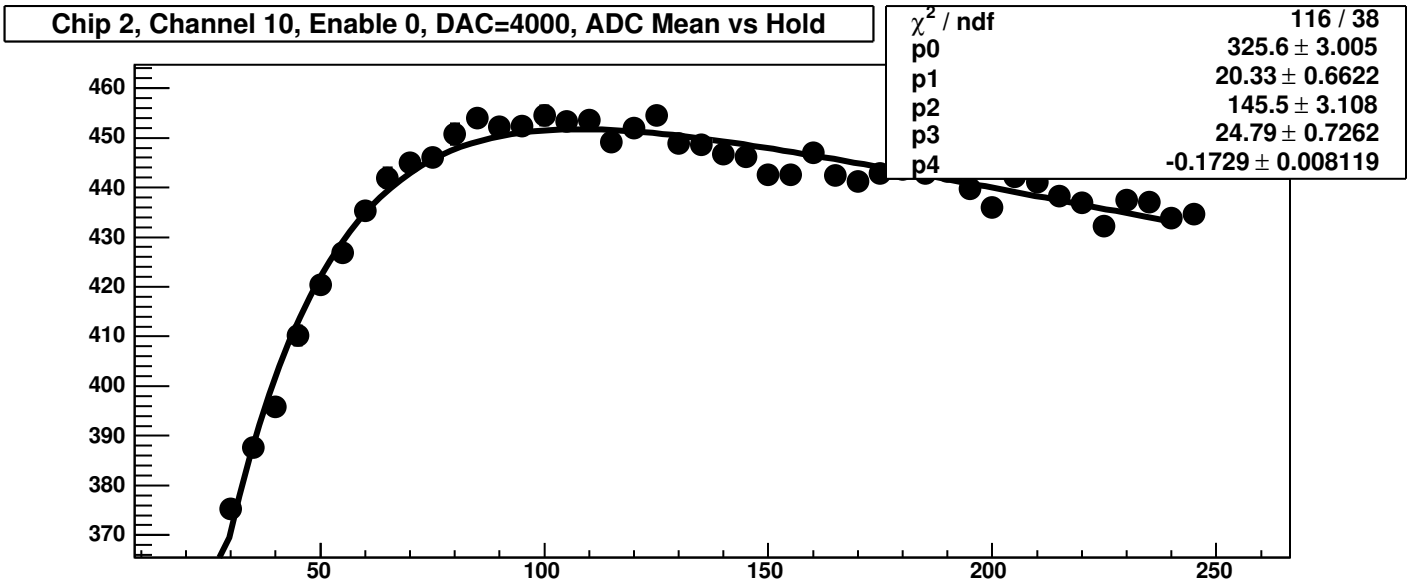


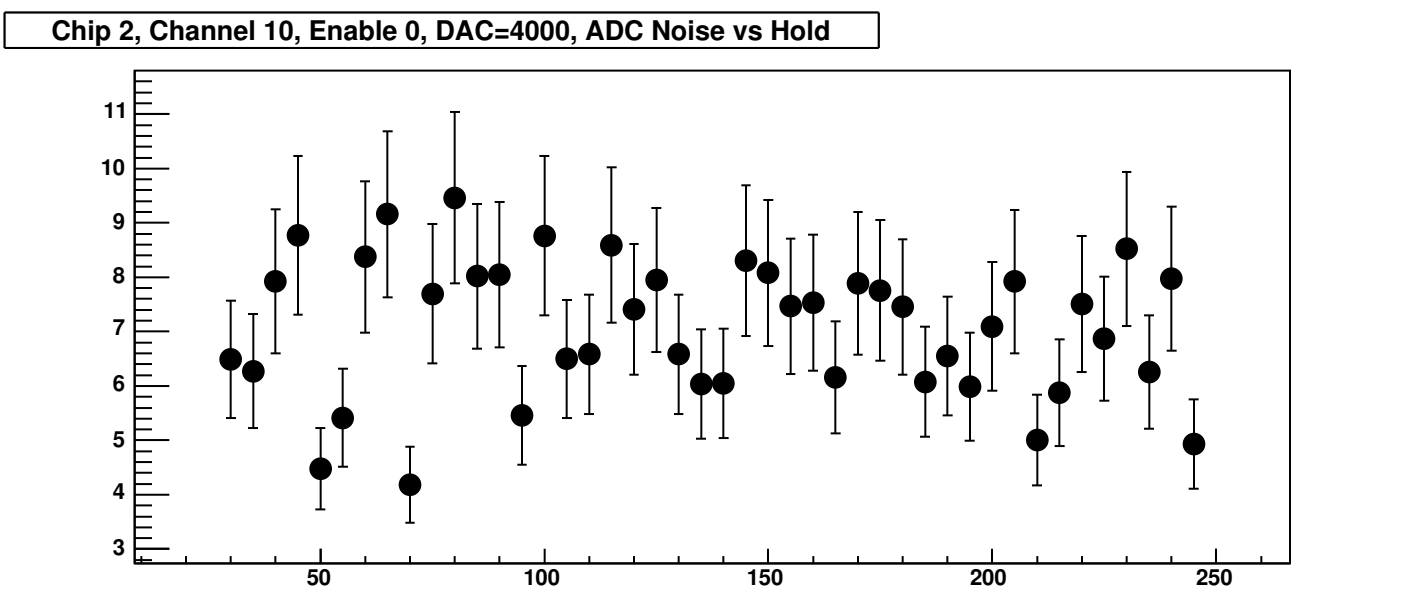
Chip 2, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold

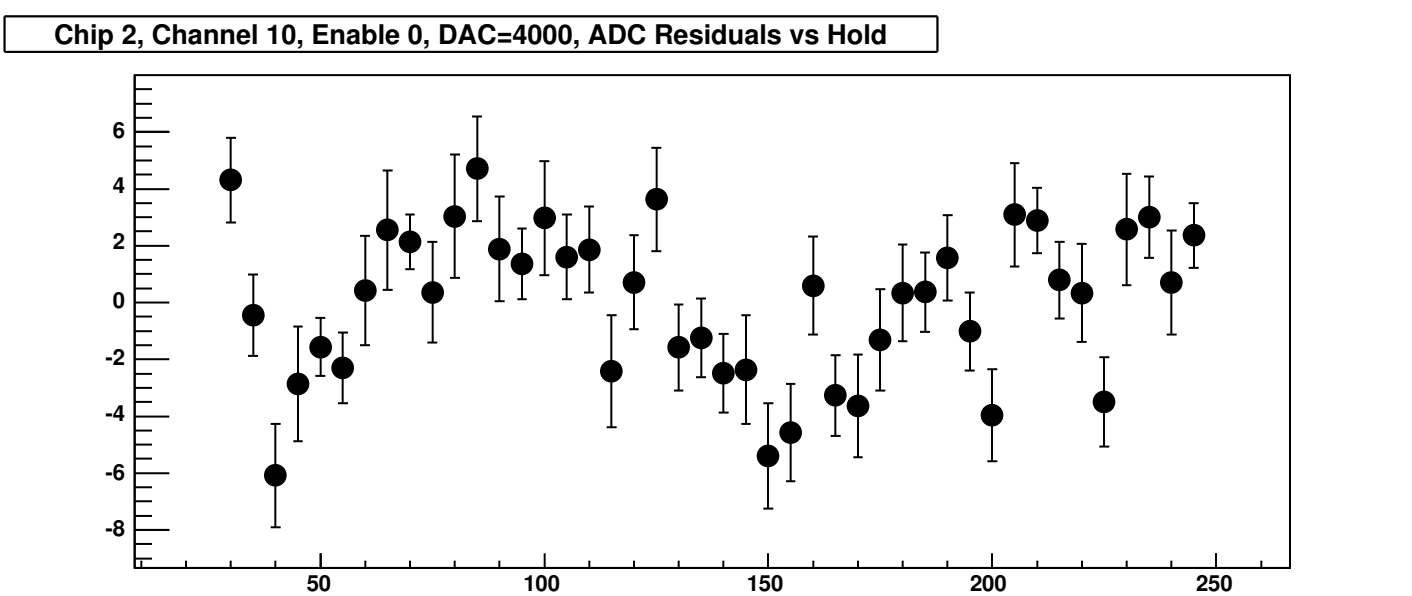


Chip 2, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

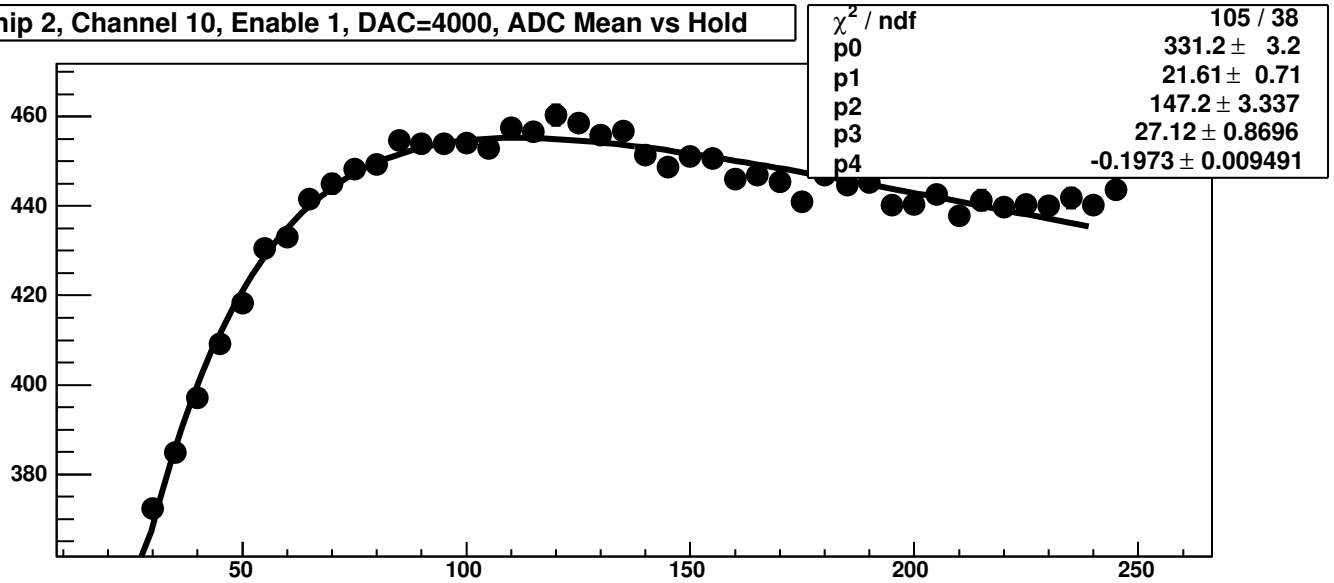



Chip 2, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 116 / 38
p0 325.6 ± 3.005
p1 20.33 ± 0.6622
p2 145.5 ± 3.108
p3 24.79 ± 0.7262
p4 -0.1729 ± 0.008119
460
450
440
430
420
410
400
390
380
370
50
100
150
200
250

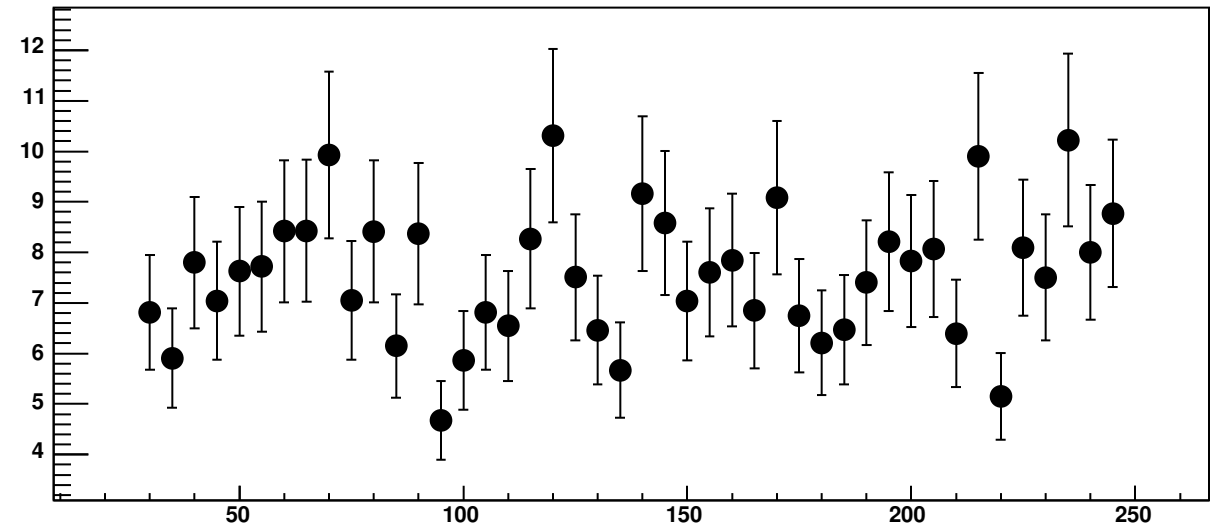

Chip 2, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
50
100
150
200
250


Chip 2, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

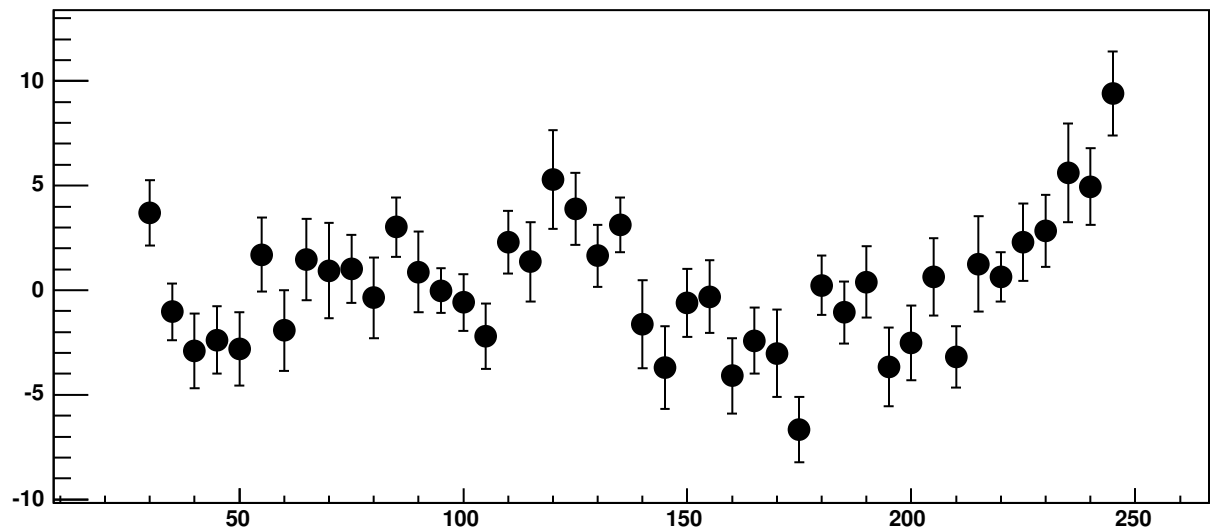
Chip 2, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold

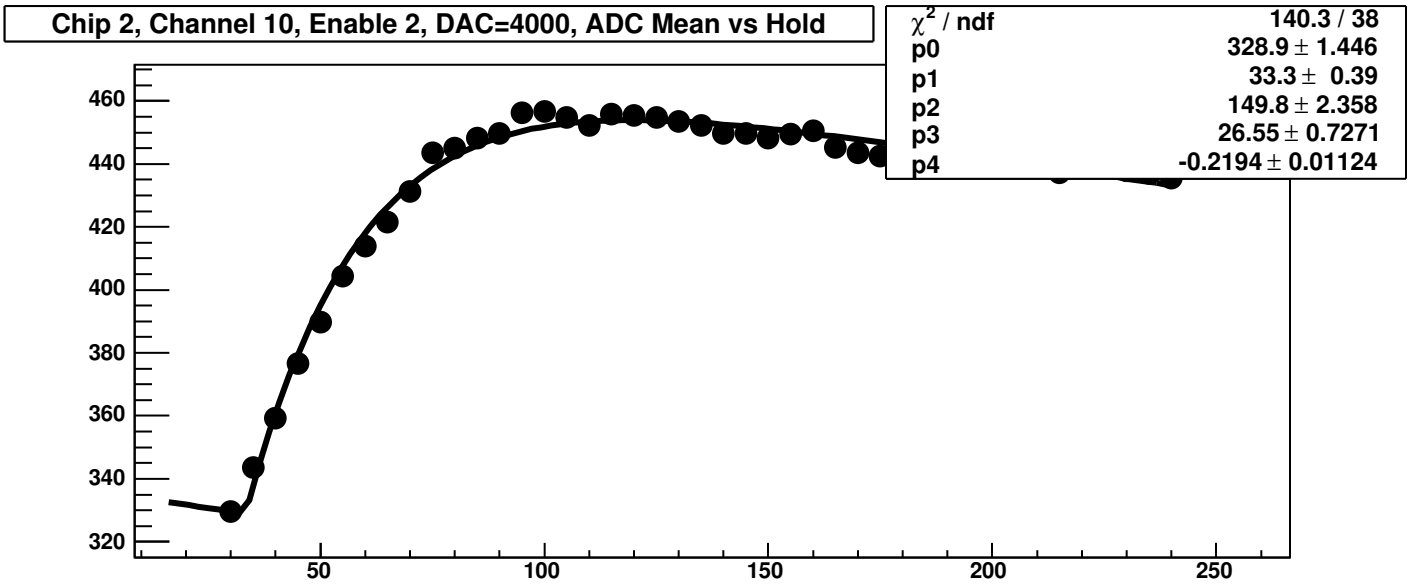


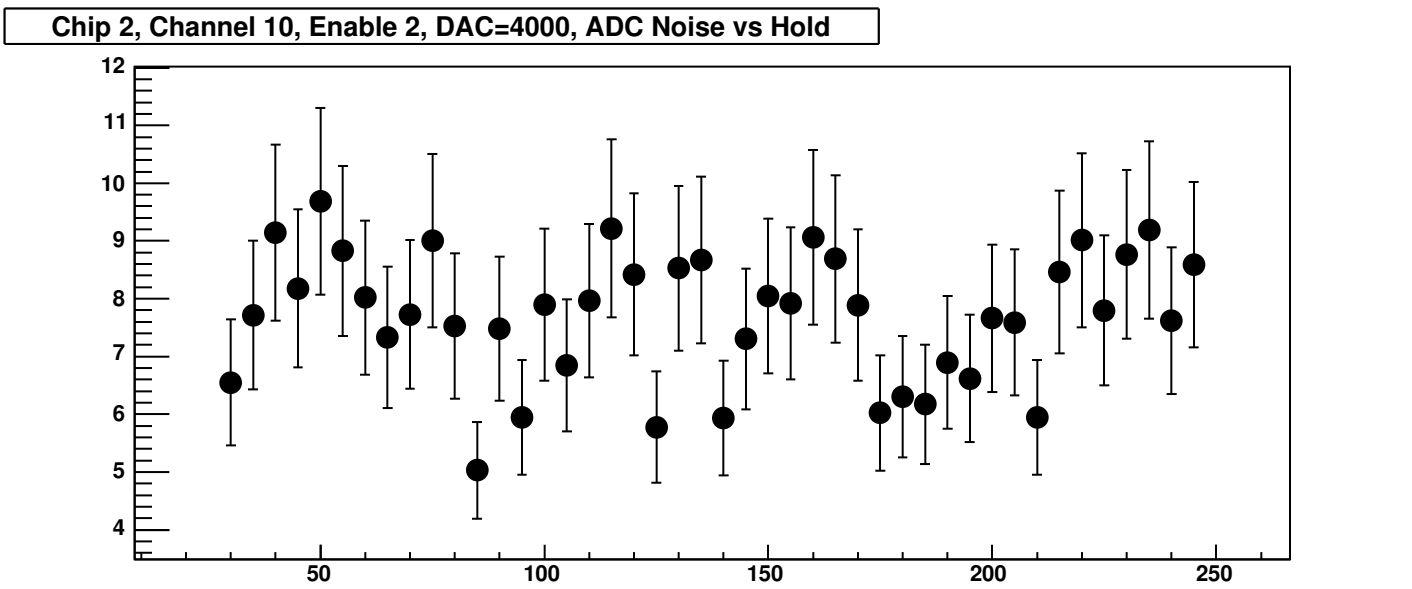
Chip 2, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold

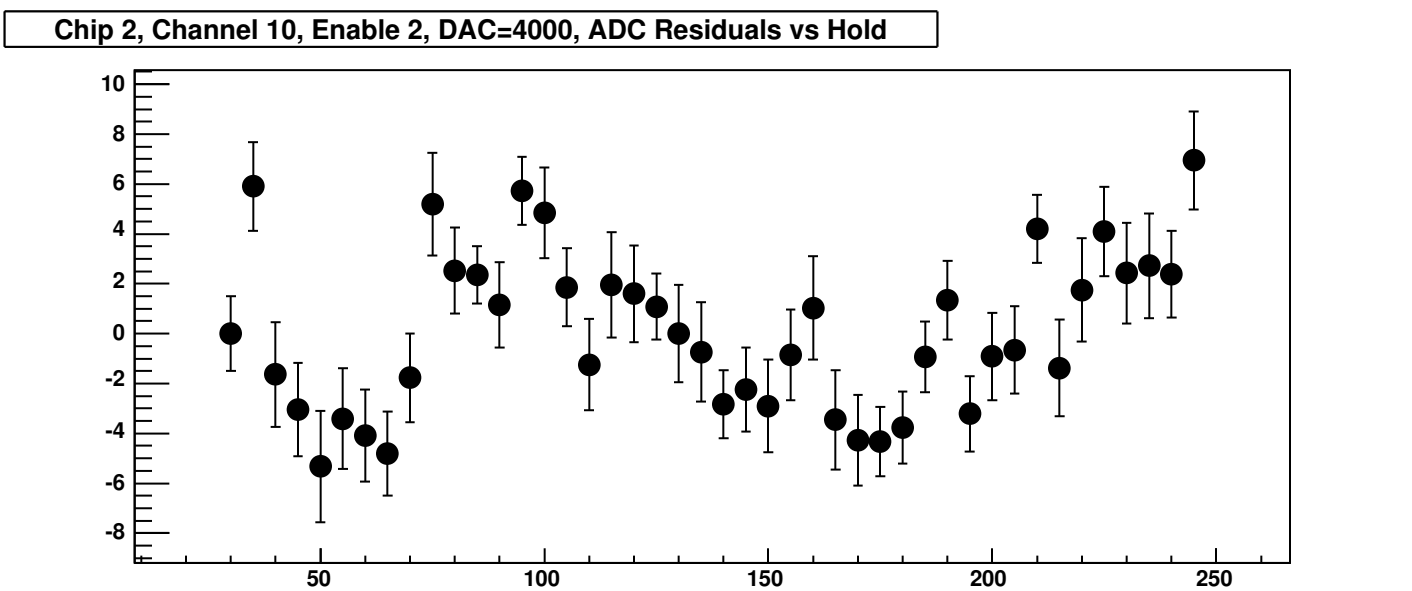


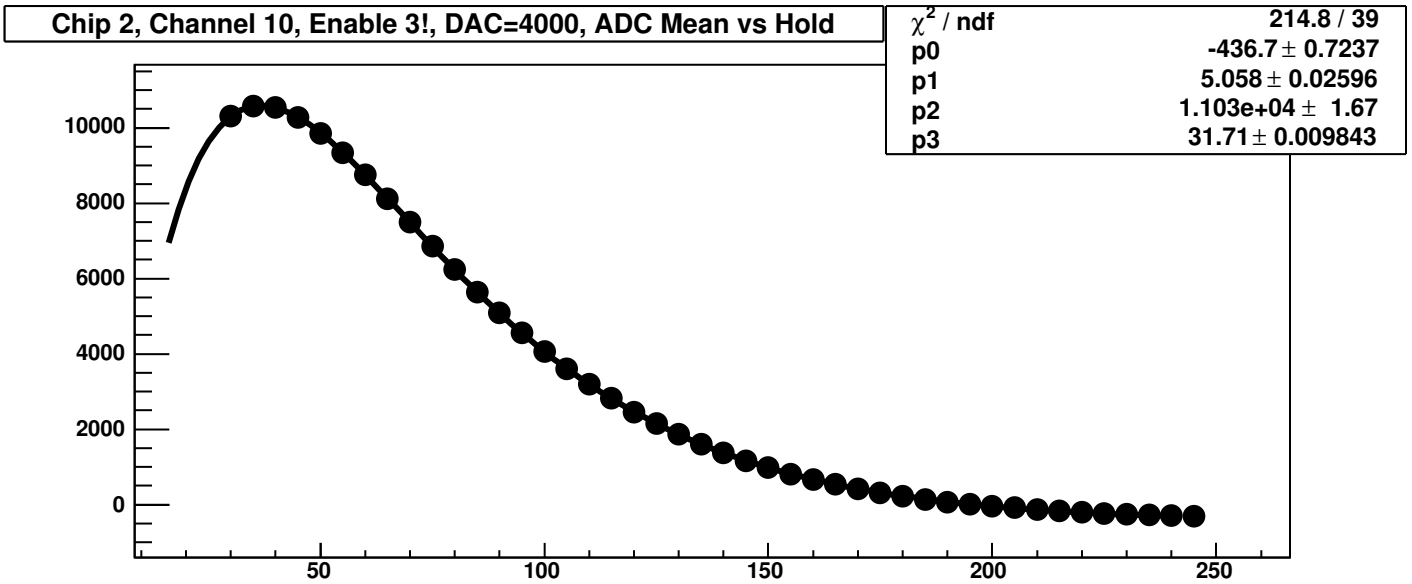
Chip 2, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold

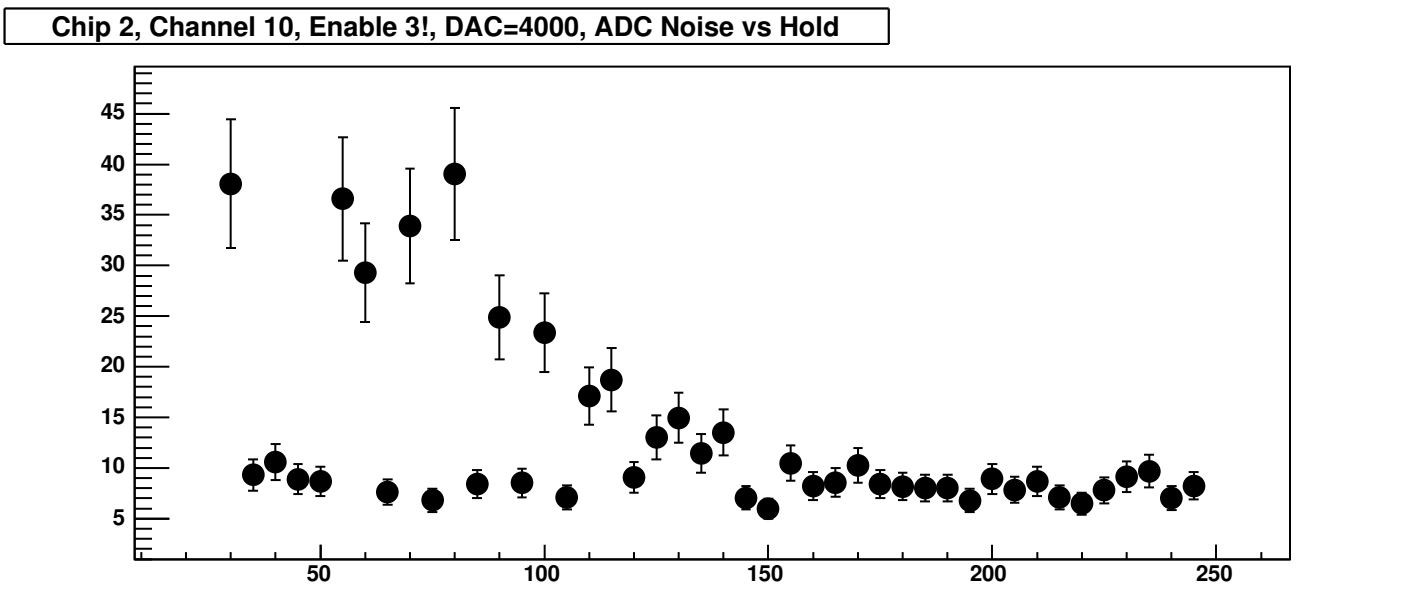


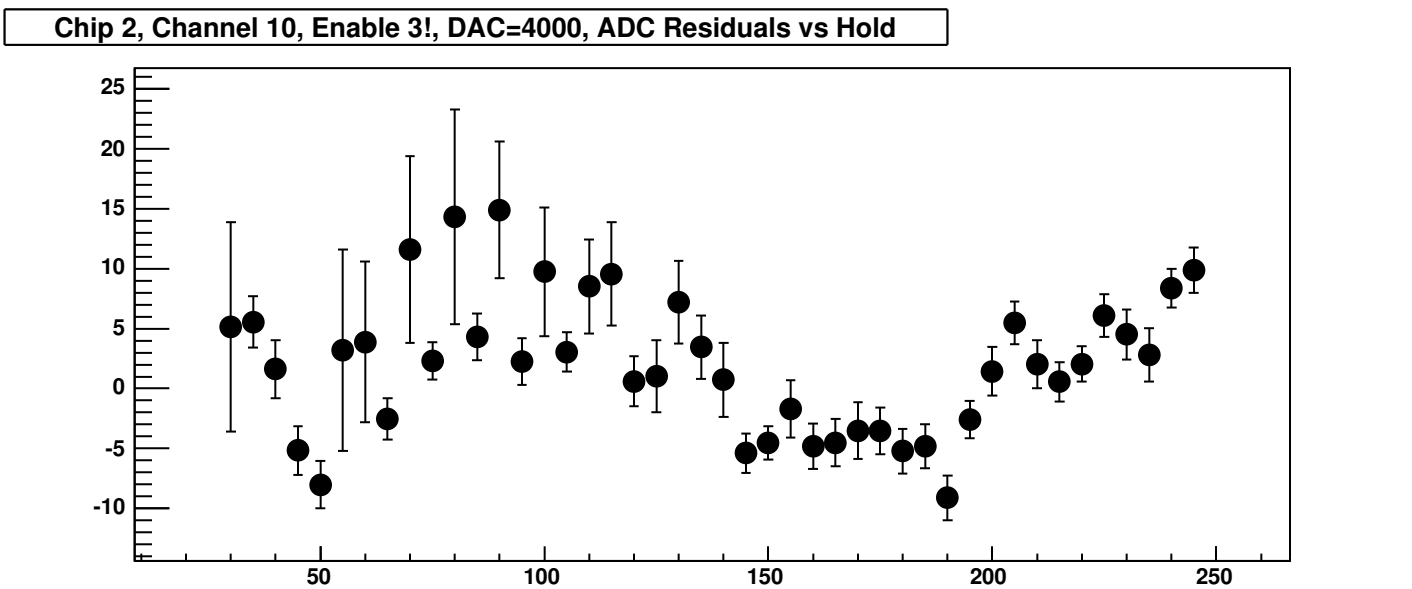

Chip 2, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 140.3 / 38
p0 328.9 ± 1.446
p1 33.3 ± 0.39
p2 149.8 ± 2.358
p3 26.55 ± 0.7271
p4 -0.2194 ± 0.01124
460
440
420
400
380
360
340
320
50
100
150
200
250


Chip 2, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

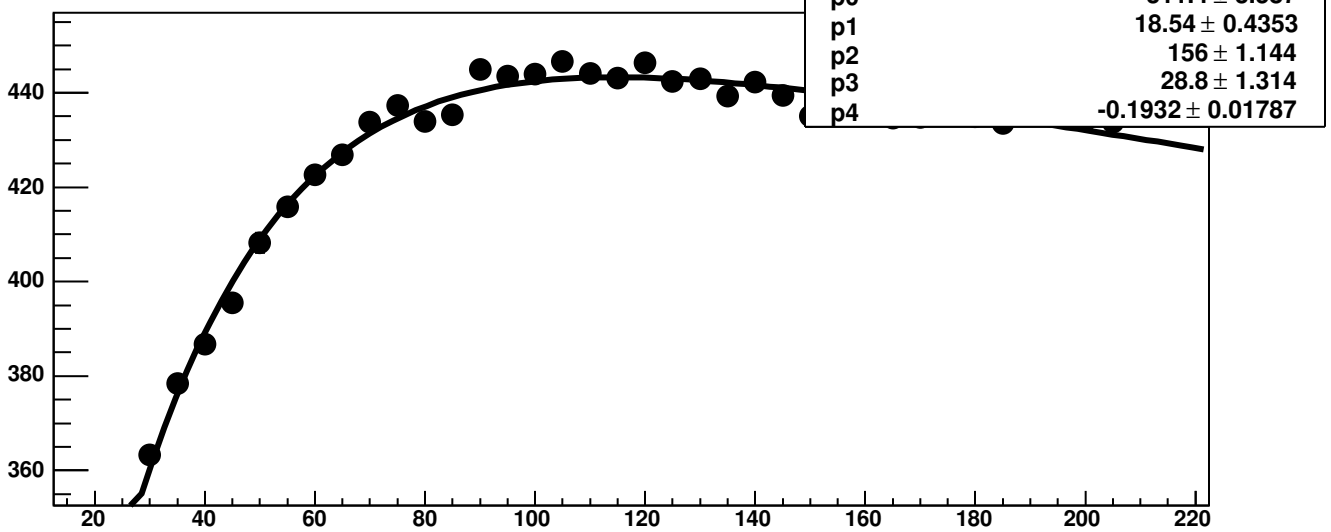

Chip 2, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250


Chip 2, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 214.8 / 39
p0 -436.7 ± 0.7237
p1 5.058 ± 0.02596
p2 1.103e+04 ± 1.67
p3 31.71 ± 0.009843
10000
8000
6000
4000
2000
0
50
100
150
200
250

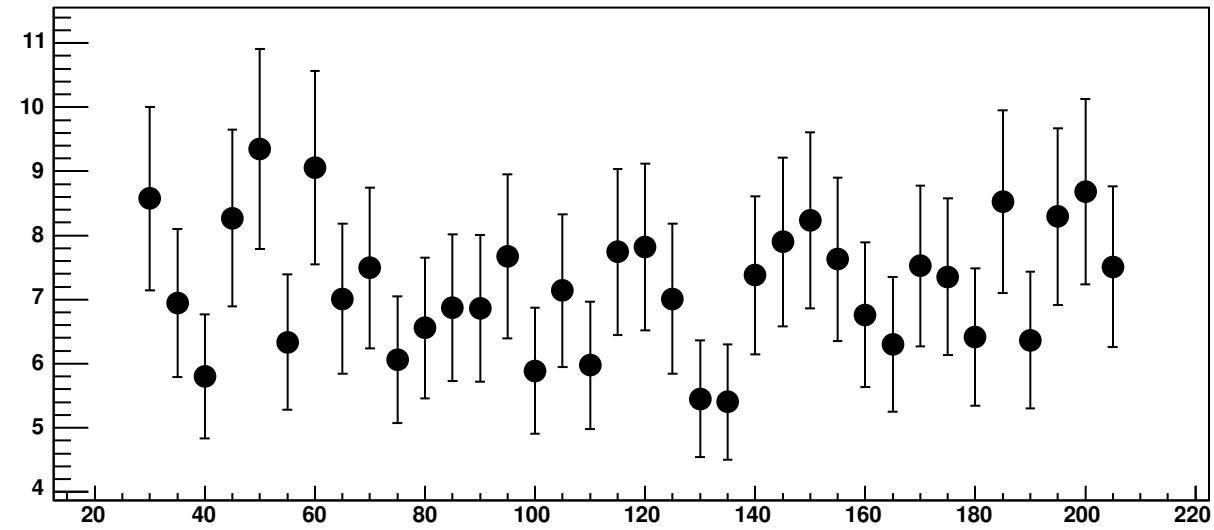

Chip 2, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold
45
40
35
30
25
20
15
10
5
50
100
150
200
250


Chip 2, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
50
100
150
200
250

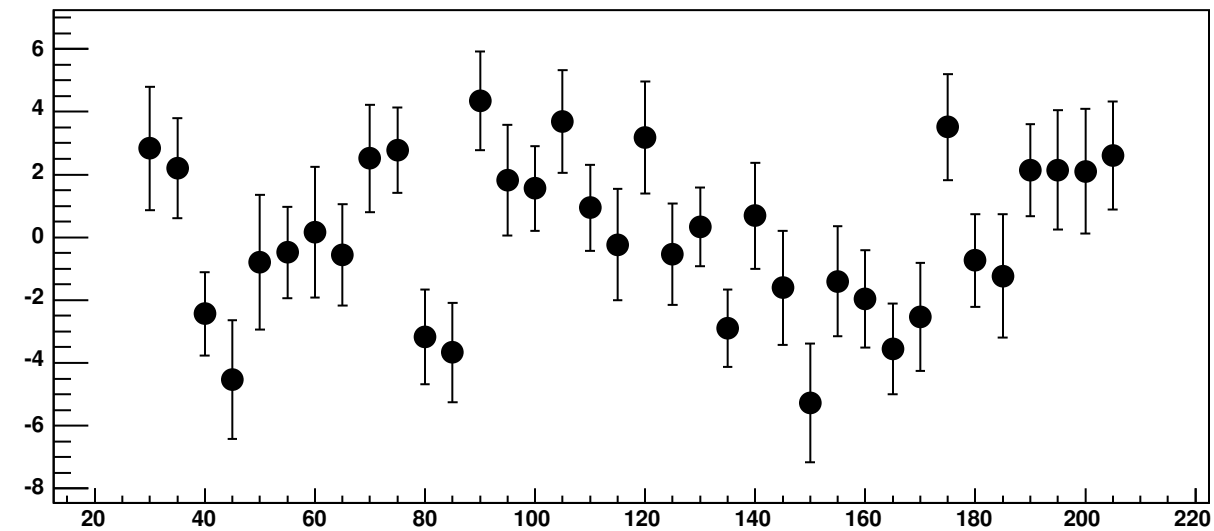
Chip 2, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold



Chip 2, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold

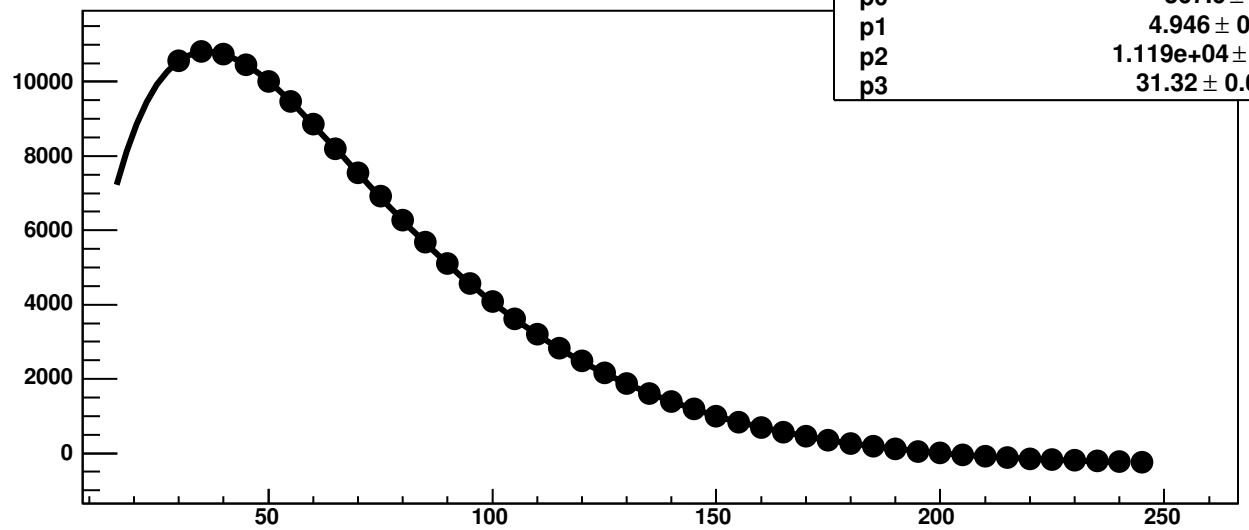


Chip 2, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

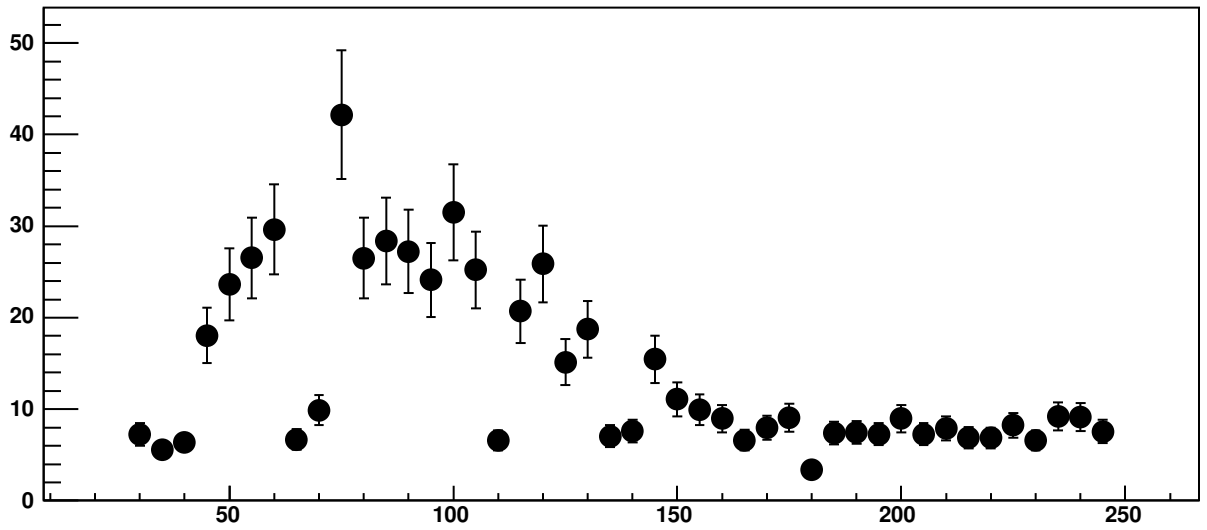


Chip 2, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold

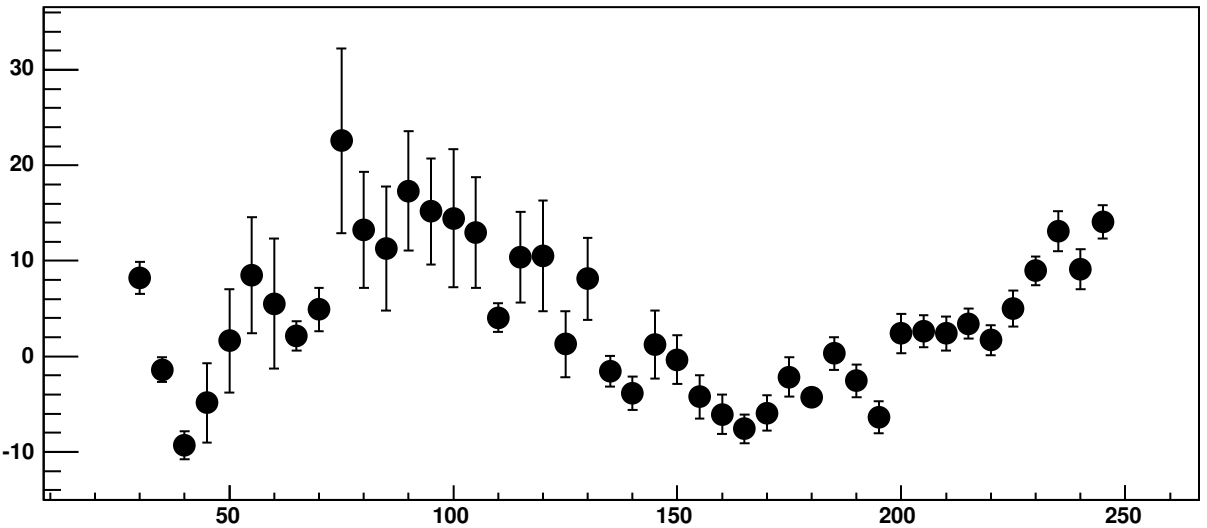
χ^2 / ndf	347 / 39
p0	-367.6 ± 0.6719
p1	4.946 ± 0.01816
p2	1.119e+04 ± 0.9381
p3	31.32 ± 0.008662

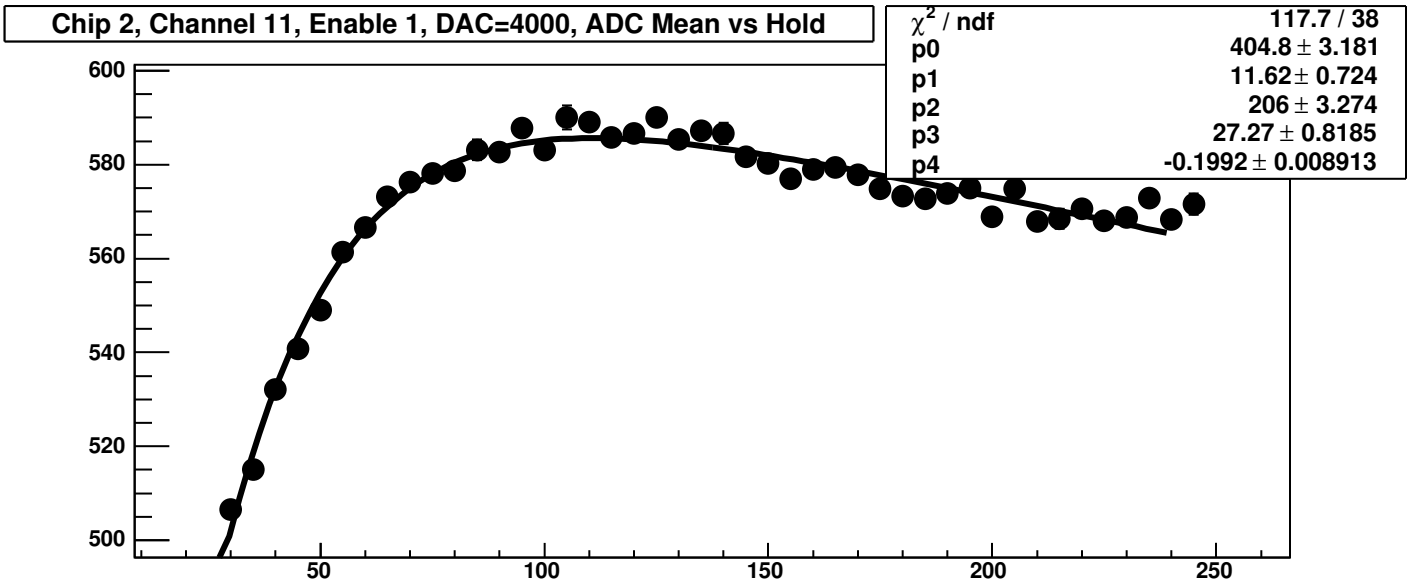


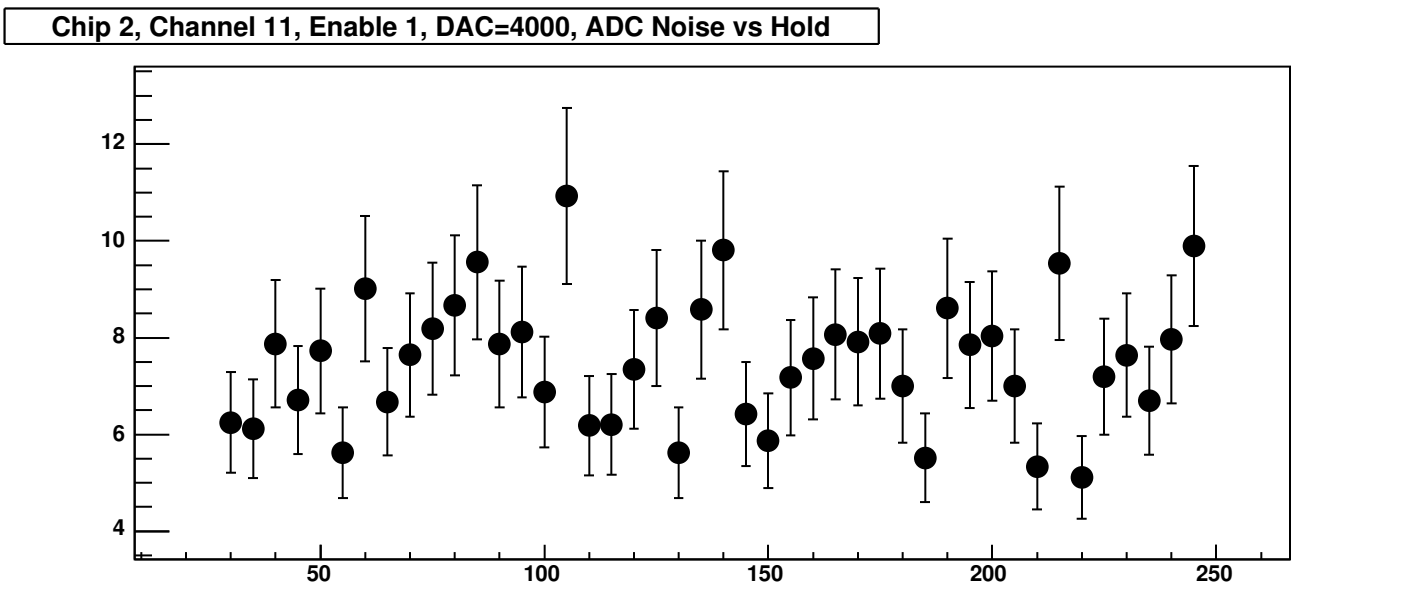
Chip 2, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold

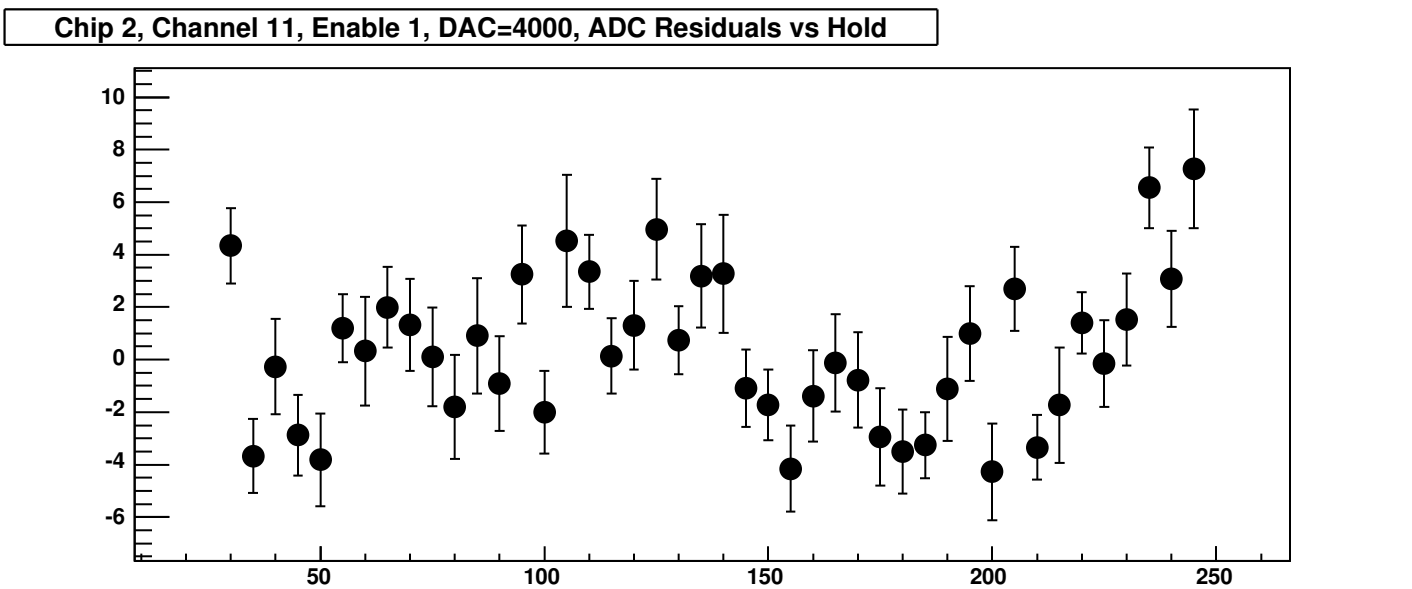


Chip 2, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold

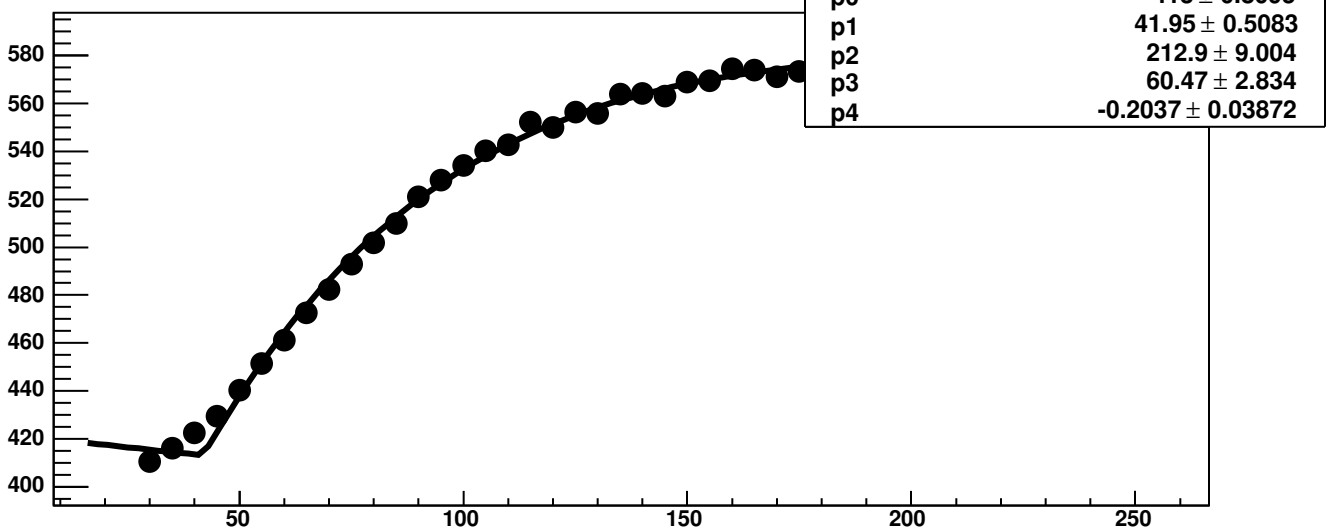



Chip 2, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 117.7 / 38
p0 404.8 ± 3.181
p1 11.62 ± 0.724
p2 206 ± 3.274
p3 27.27 ± 0.8185
p4 -0.1992 ± 0.008913
600
580
560
540
520
500
50
100
150
200
250

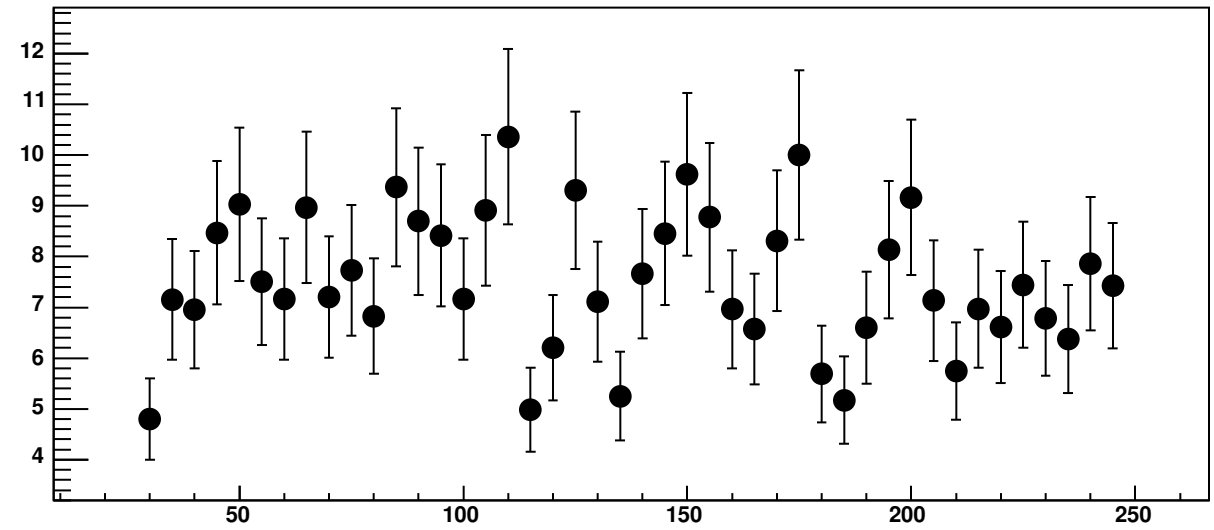

Chip 2, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250


Chip 2, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
50
100
150
200
250

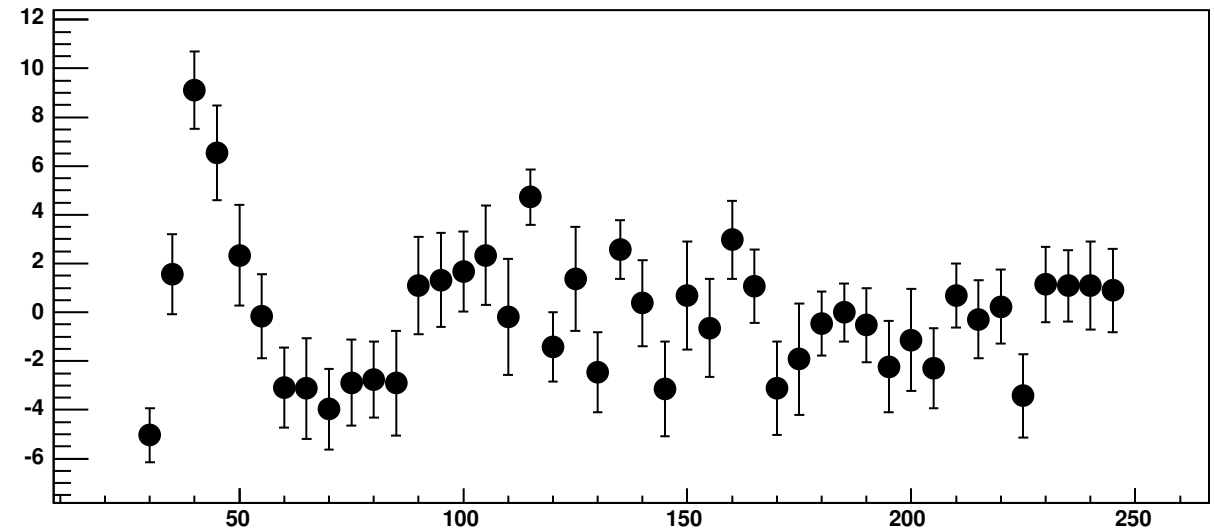
Chip 2, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold



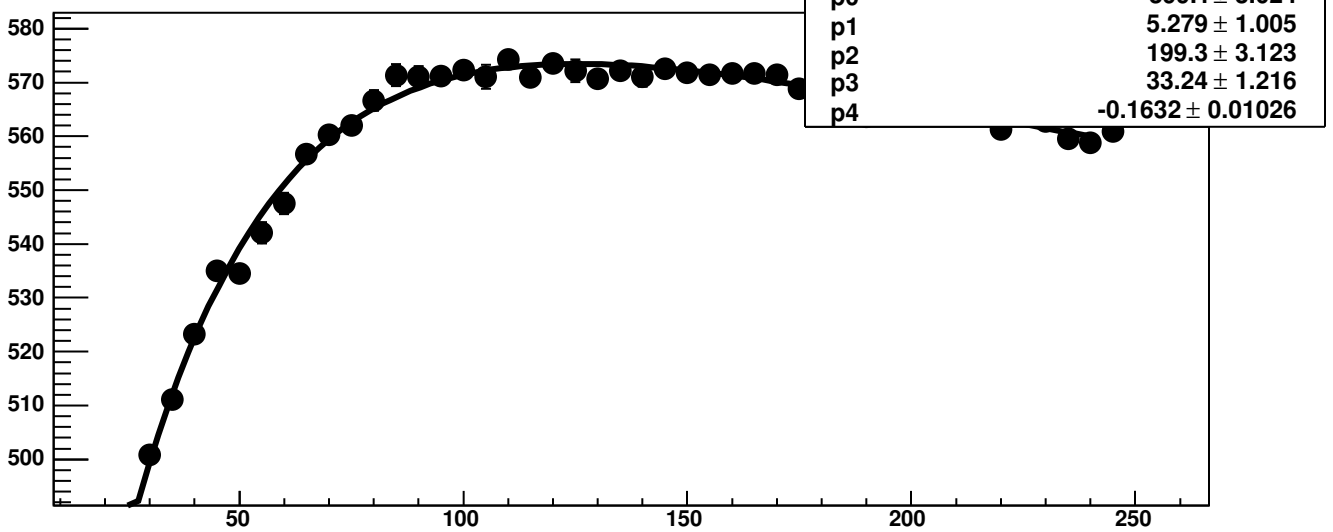
Chip 2, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



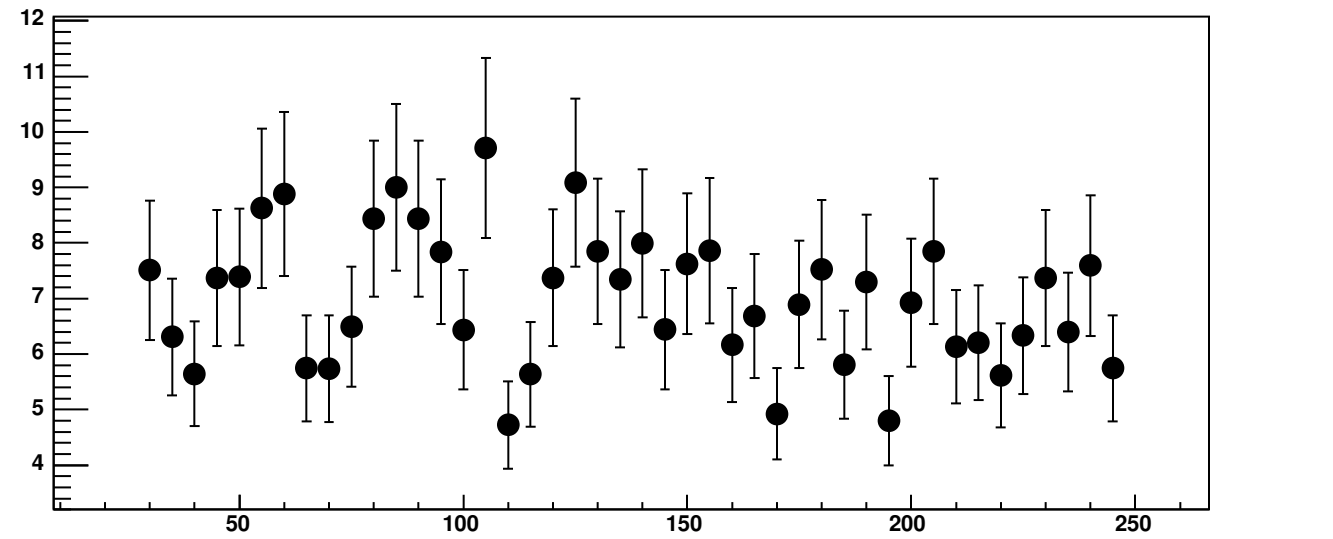
Chip 2, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold



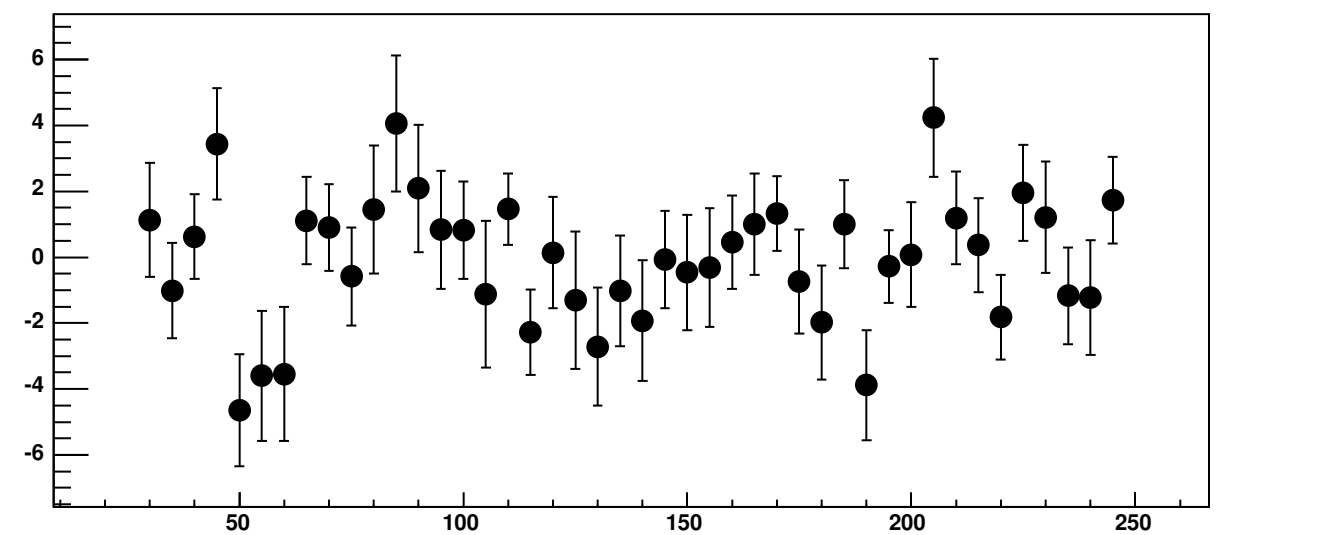
Chip 2, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold

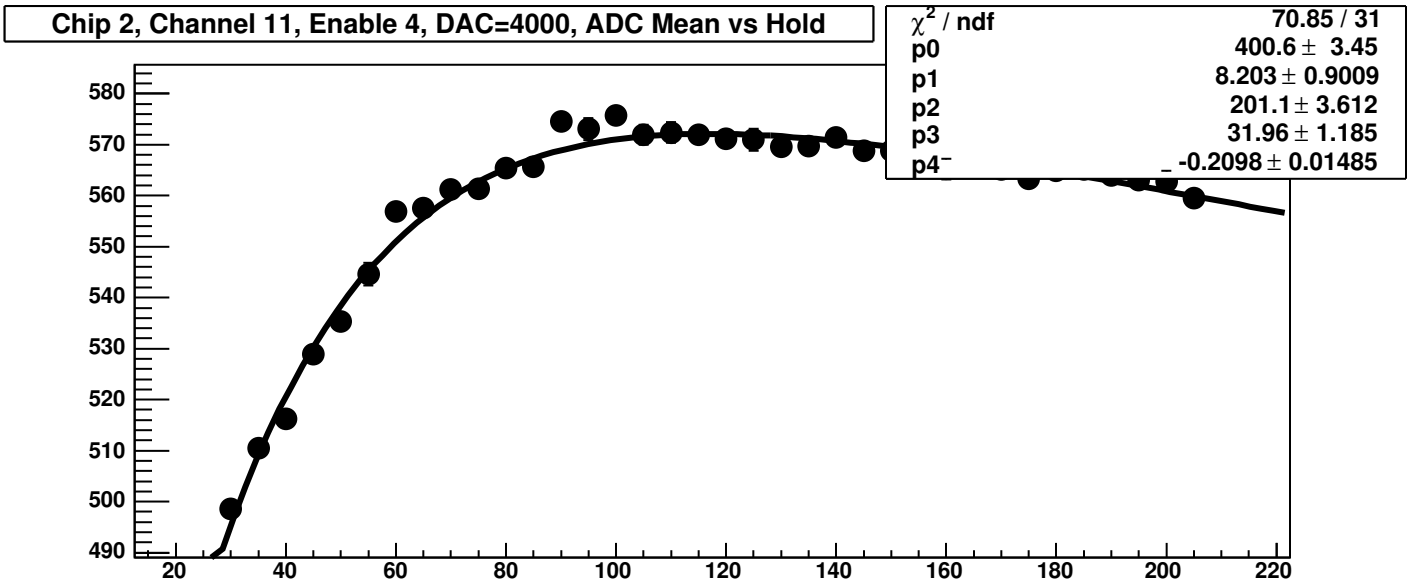


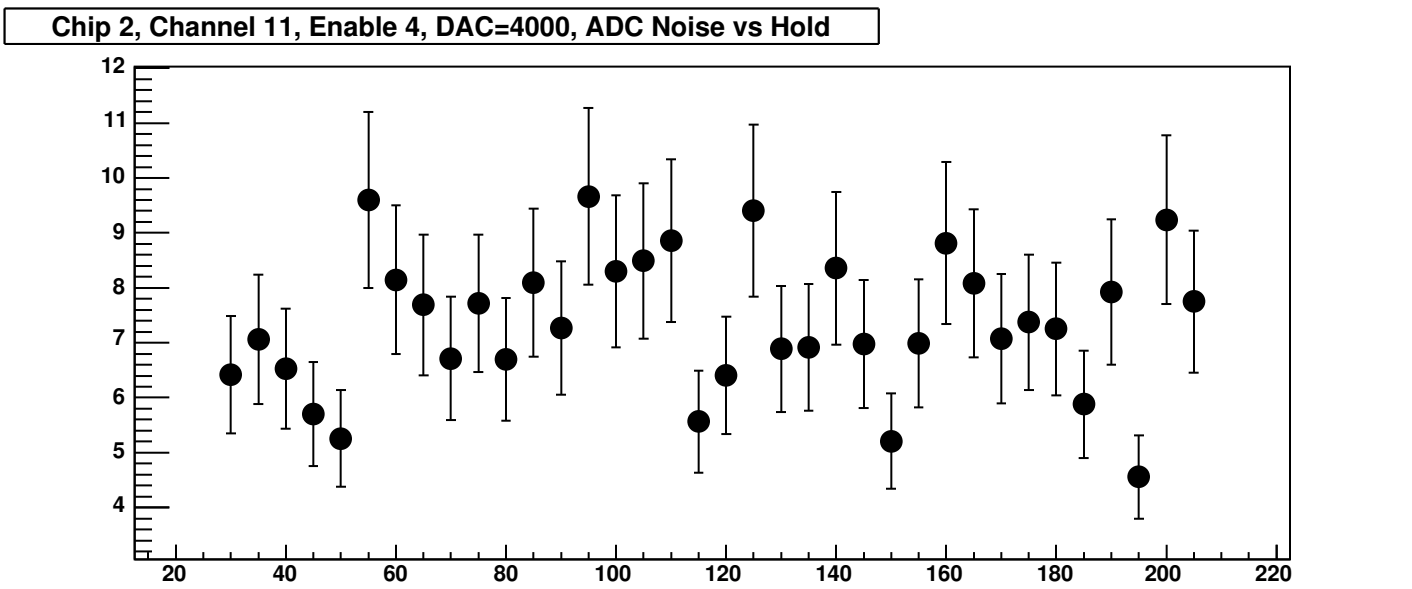
Chip 2, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold

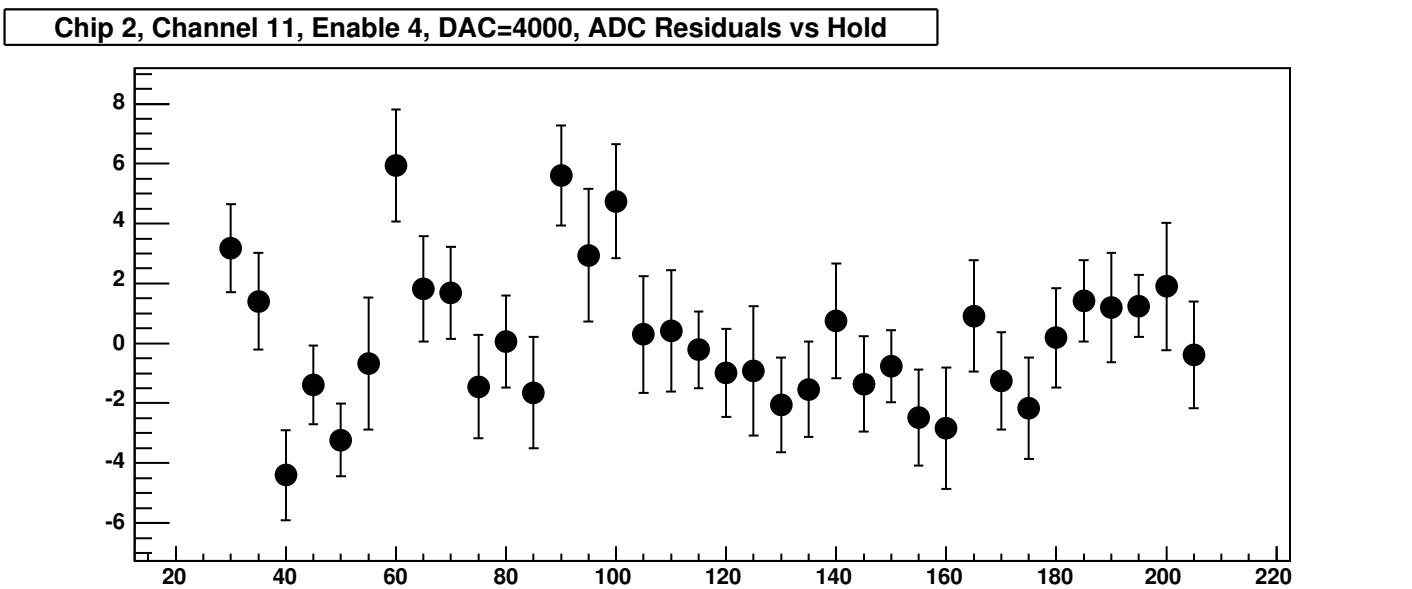


Chip 2, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold

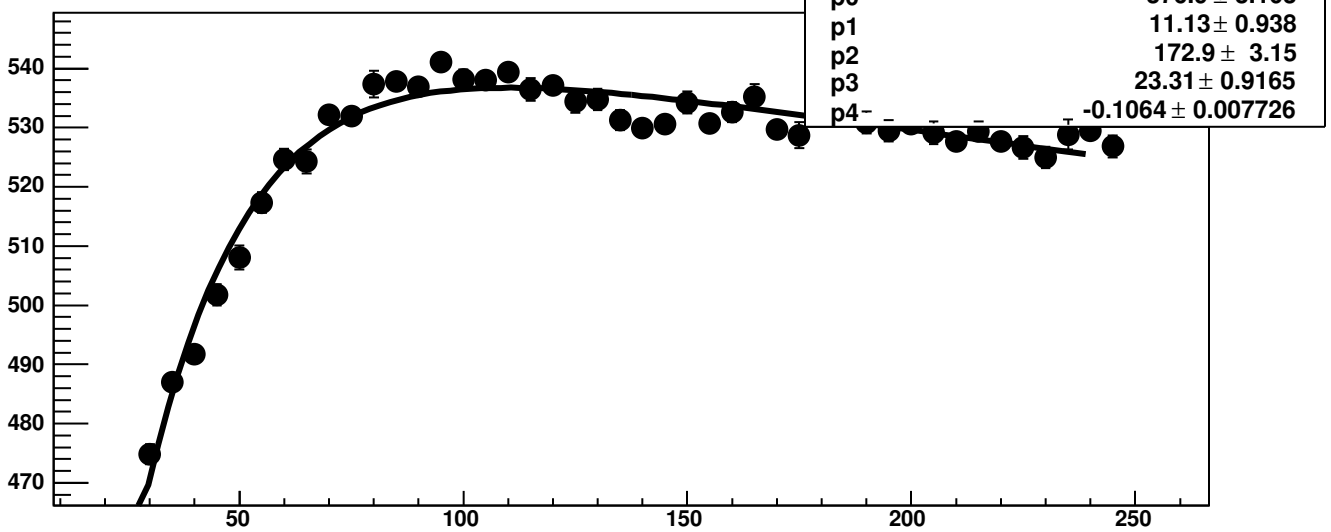



Chip 2, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 70.85 / 31
p0 400.6 ± 3.45
p1 8.203 ± 0.9009
p2 201.1 ± 3.612
p3 31.96 ± 1.185
p4 -0.2098 ± 0.01485
580
570
560
550
540
530
520
510
500
490
20
40
60
80
100
120
140
160
180
200
220

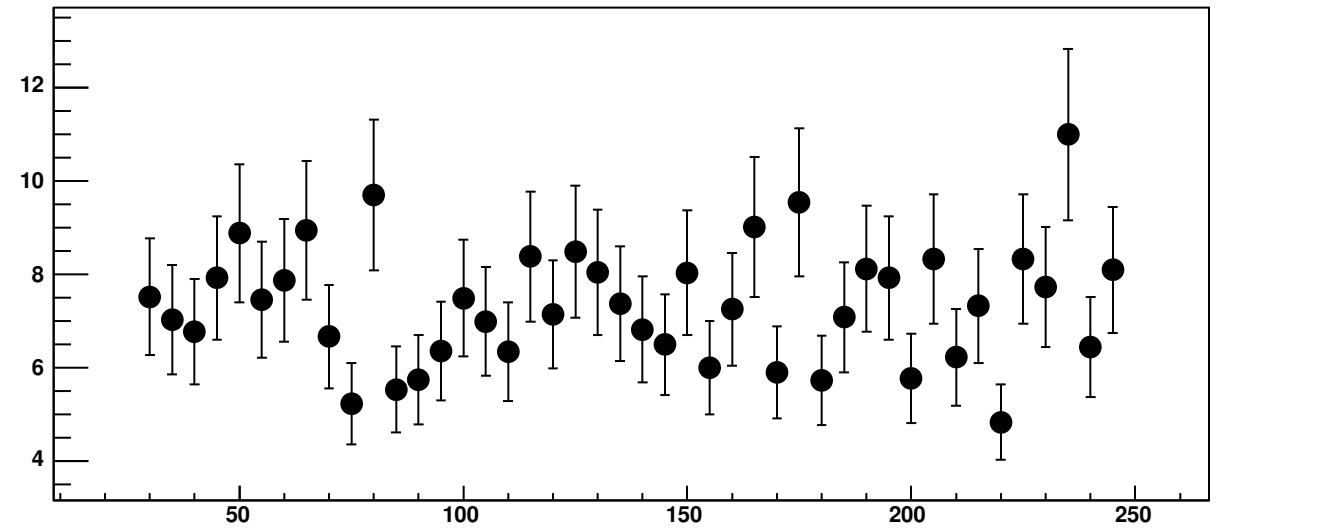

Chip 2, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
20
40
60
80
100
120
140
160
180
200
220


Chip 2, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
20
40
60
80
100
120
140
160
180
200
220

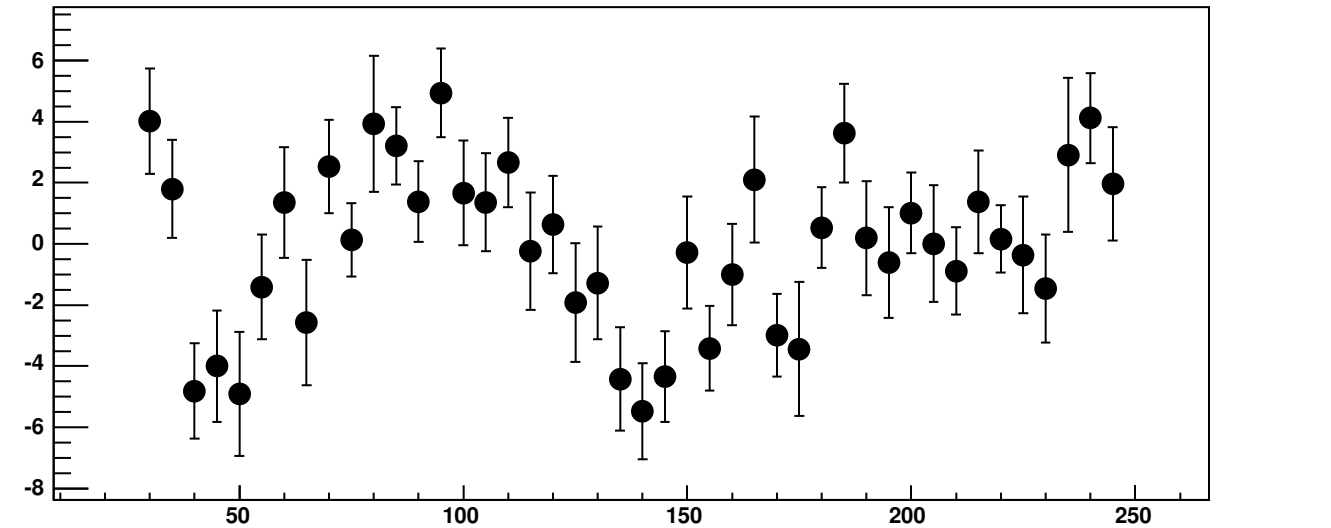
Chip 2, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold



Chip 2, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold

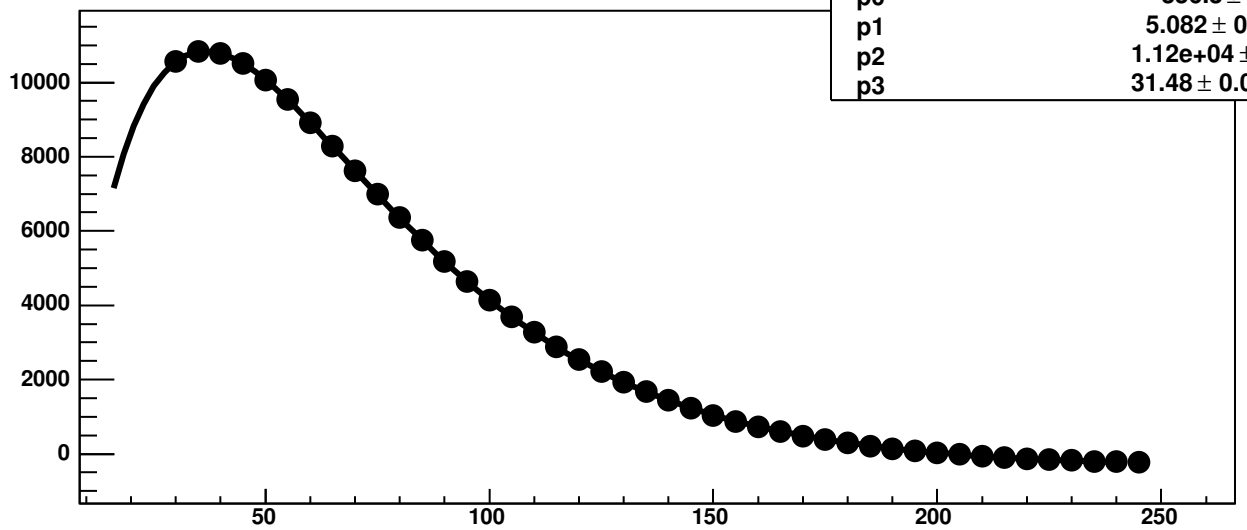


Chip 2, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

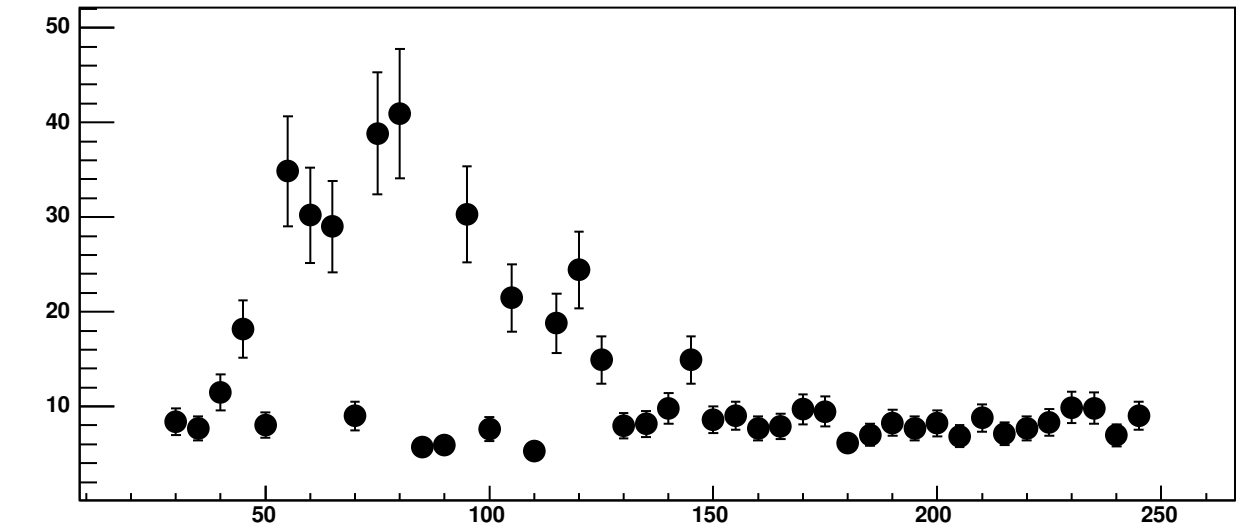


Chip 2, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold

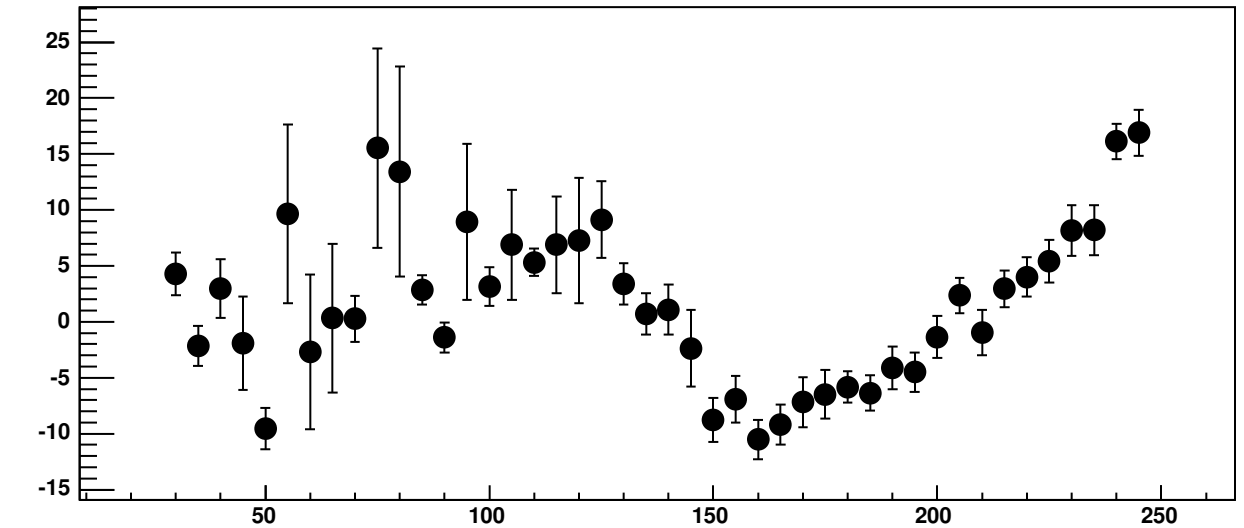
χ^2 / ndf	392.4 / 39
p0	-356.3 ± 0.6757
p1	5.082 ± 0.01874
p2	1.12e+04 ± 1.092
p3	31.48 ± 0.008205



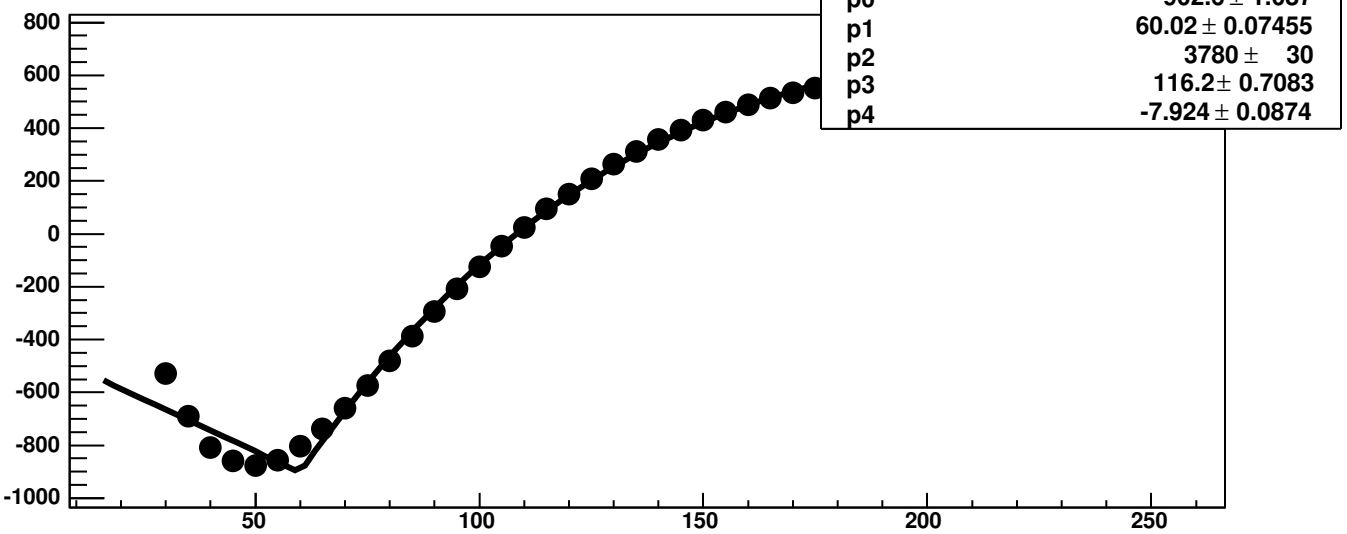
Chip 2, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold



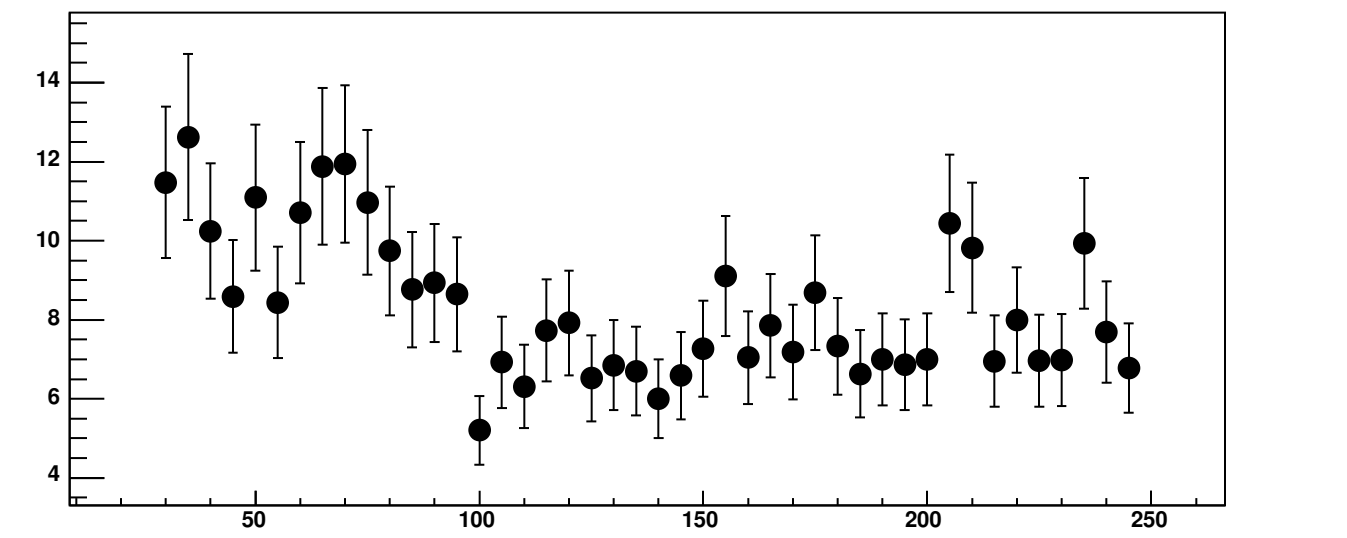
Chip 2, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold



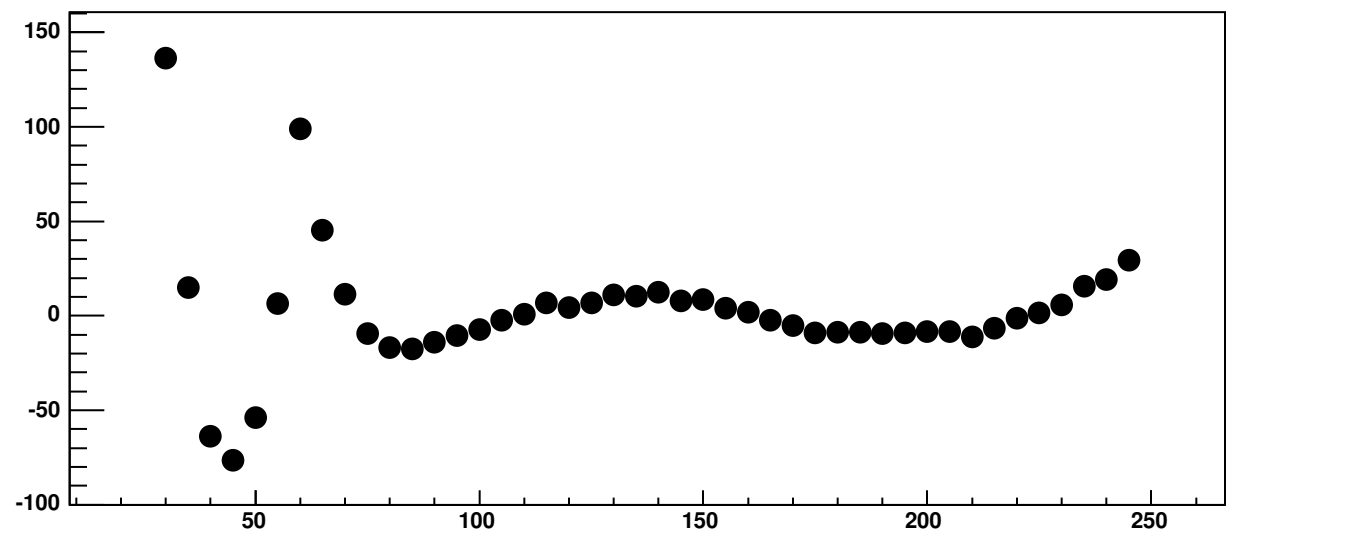
Chip 2, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

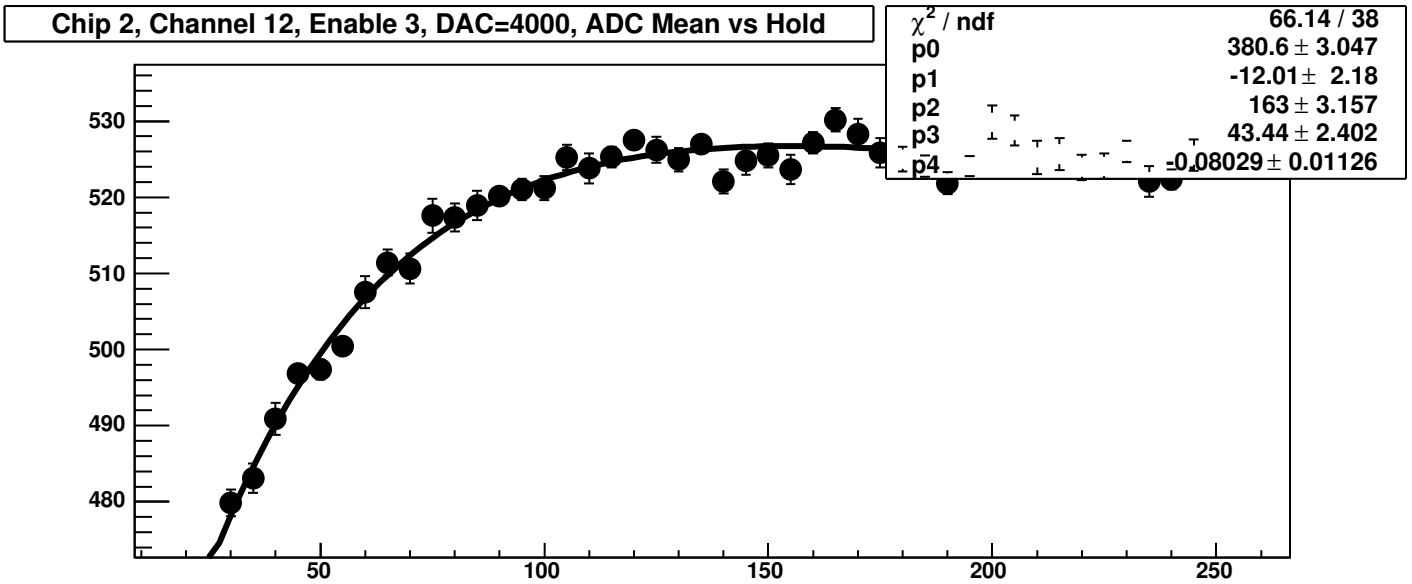


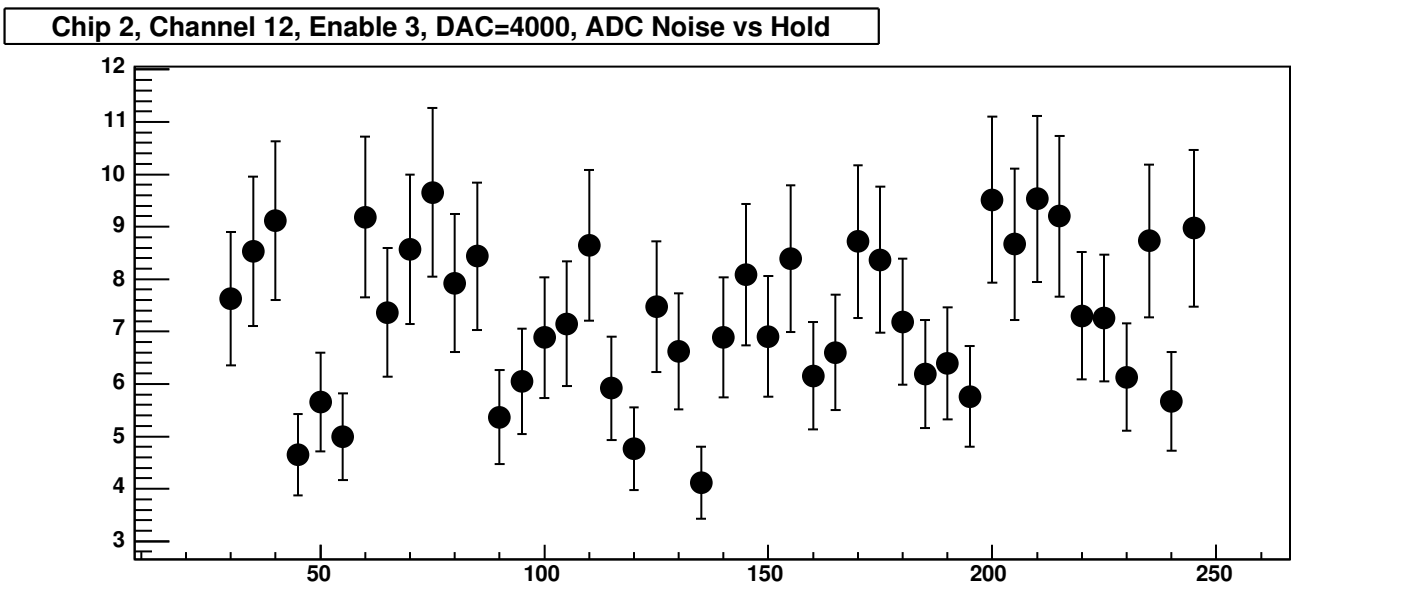
Chip 2, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold

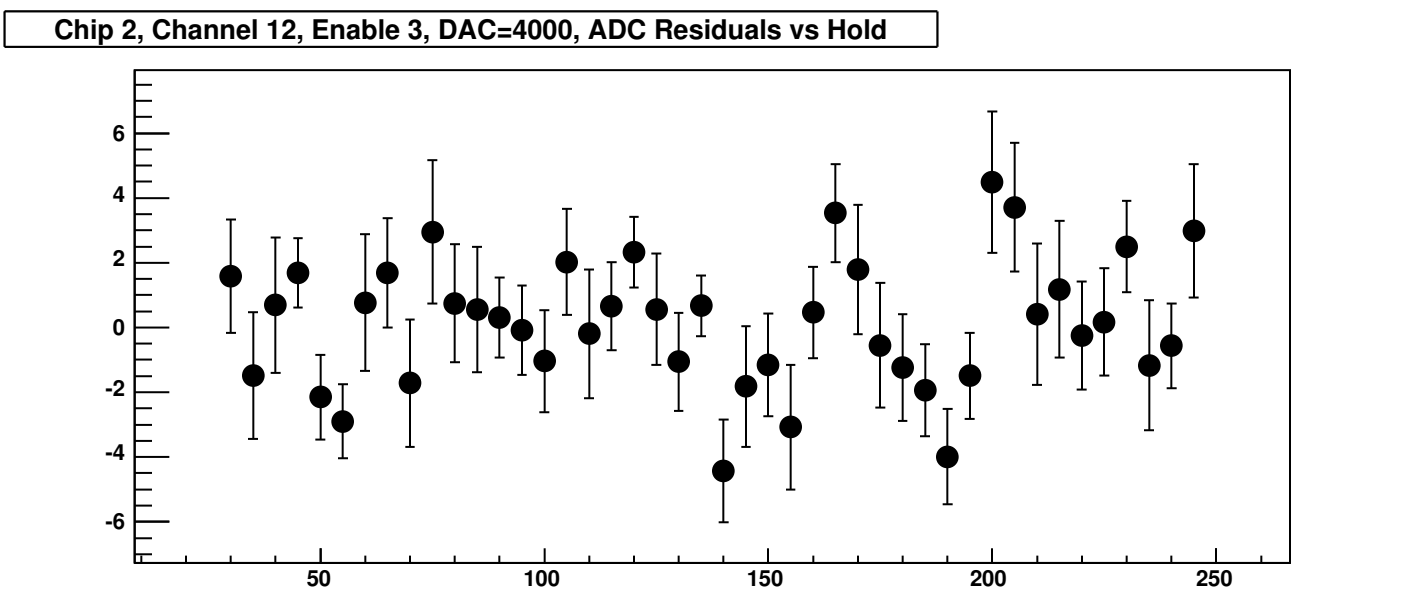


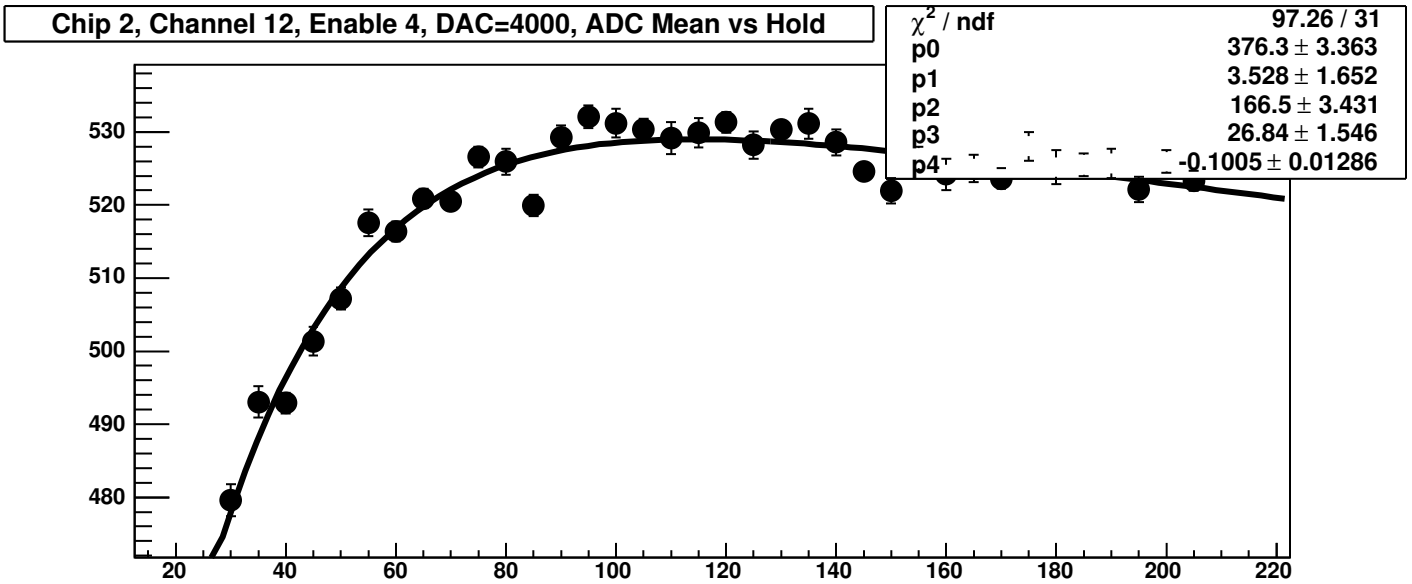
Chip 2, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold

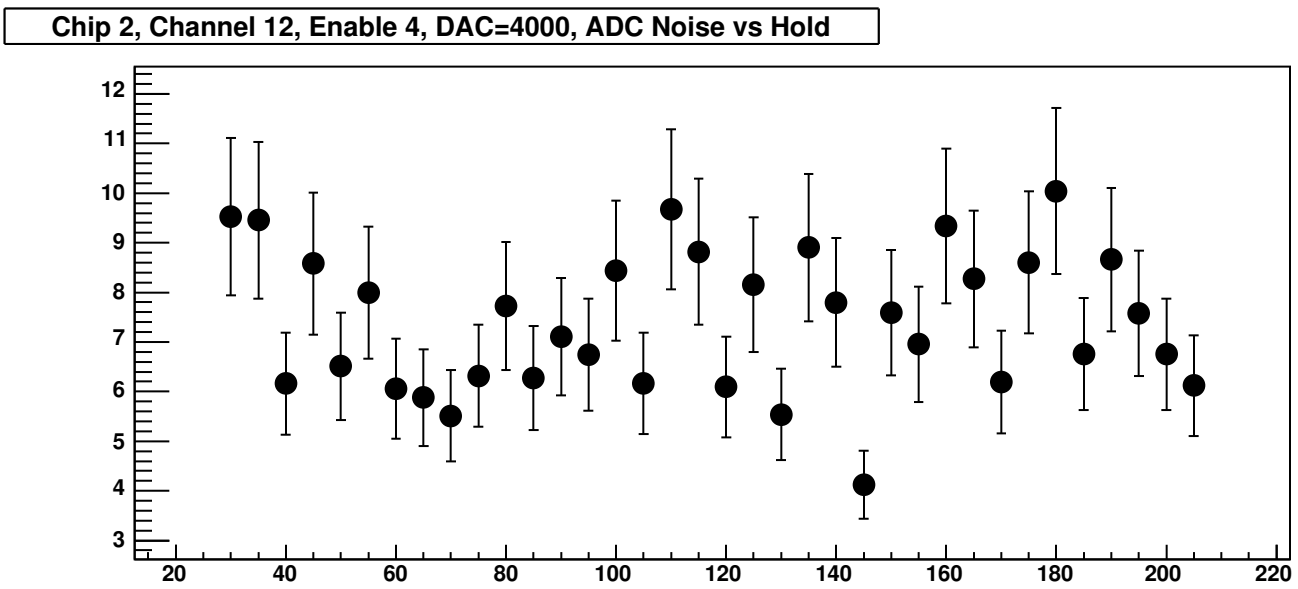


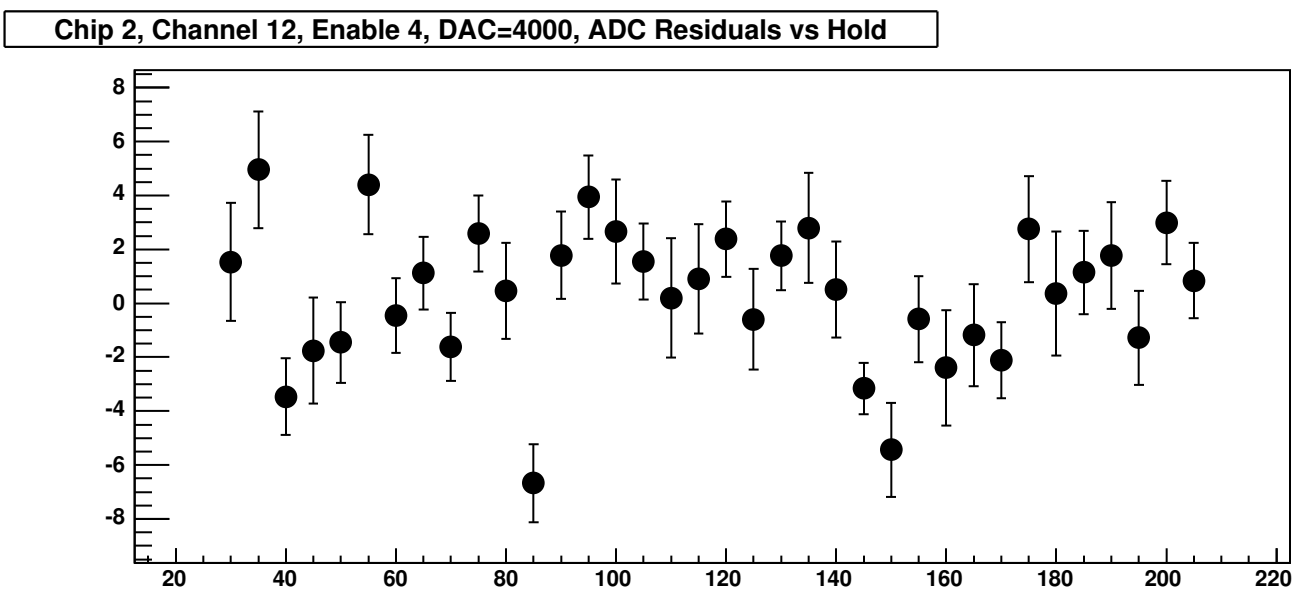

Chip 2, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 66.14 / 38
p0 380.6 ± 3.047
p1 -12.01 ± 2.18
p2 163 ± 3.157
p3 43.44 ± 2.402
p4 -0.08029 ± 0.01126
530
520
510
500
490
480
50
100
150
200
250

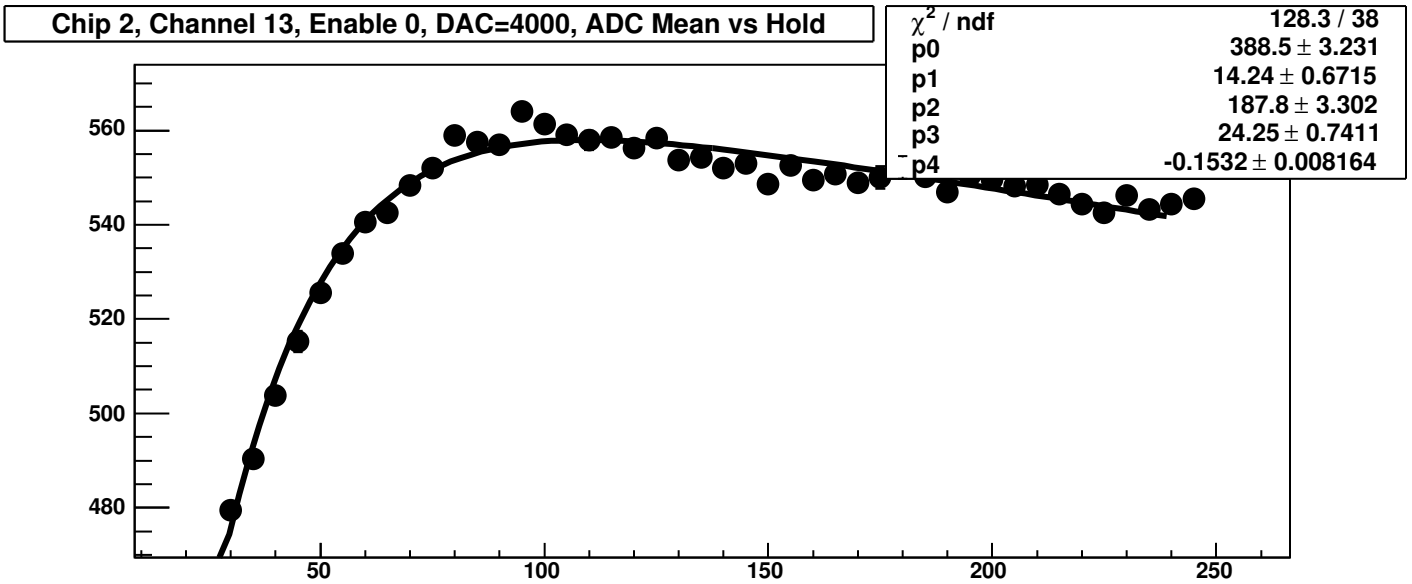

Chip 2, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250

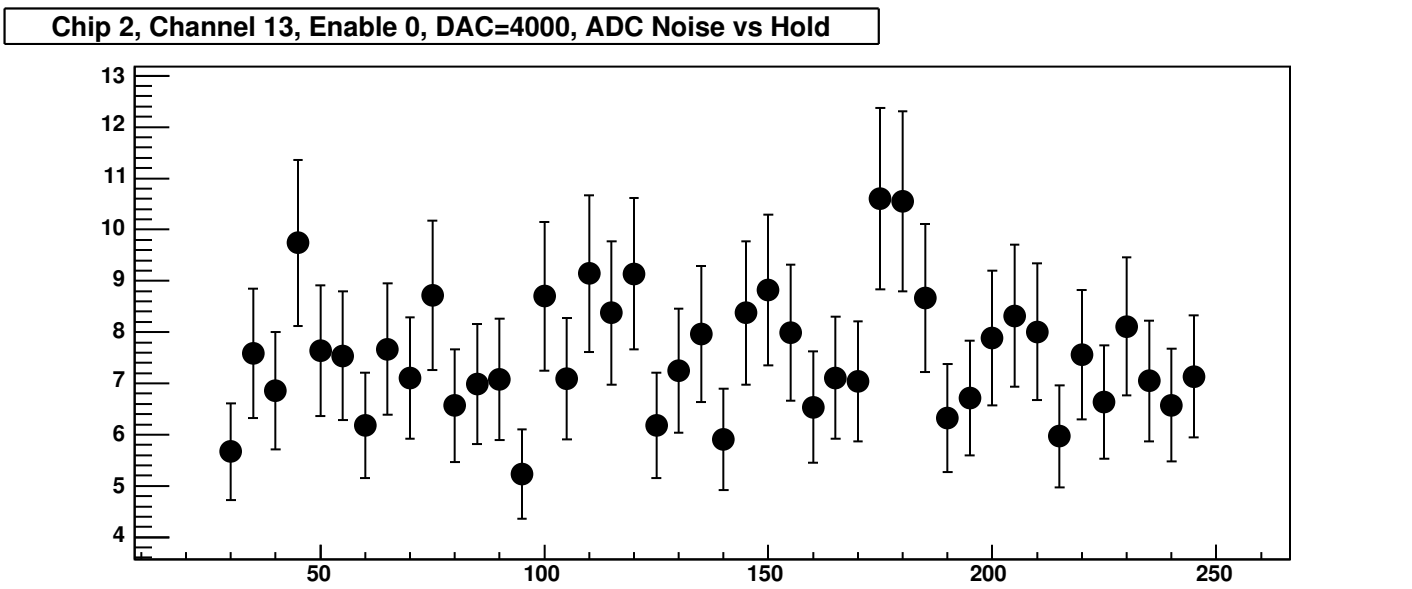

Chip 2, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
50
100
150
200
250

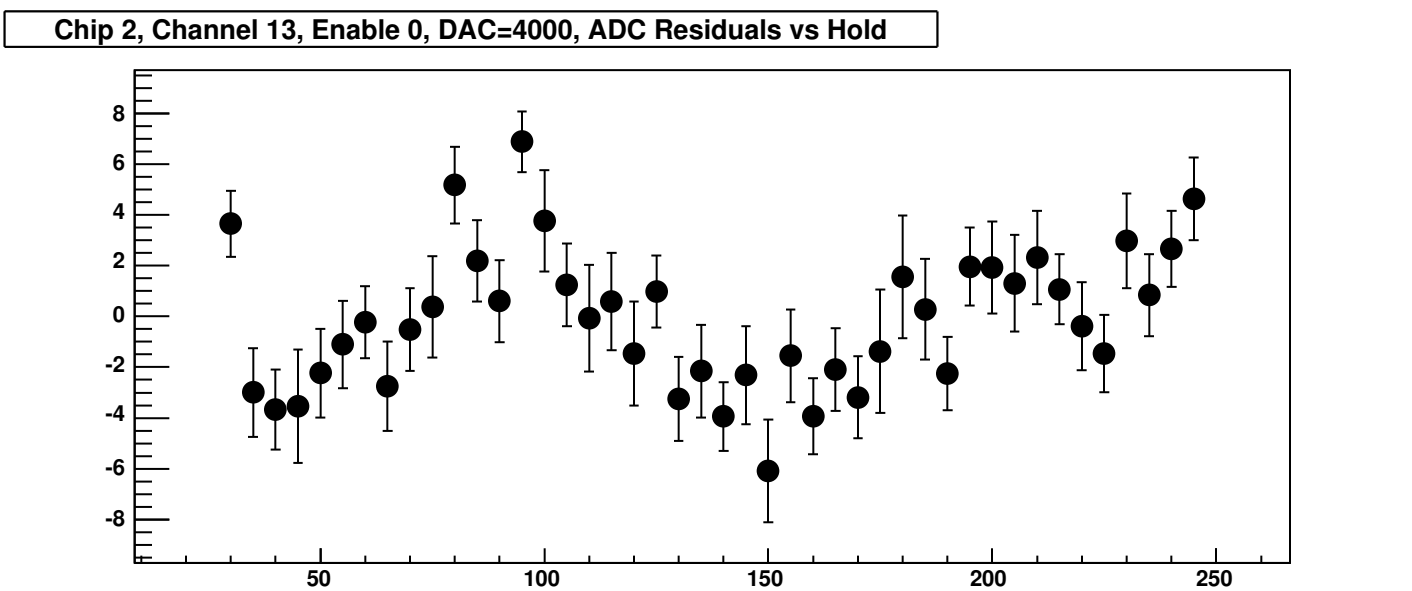

Chip 2, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 97.26 / 31
p0 376.3 ± 3.363
p1 3.528 ± 1.652
p2 166.5 ± 3.431
p3 26.84 ± 1.546
p4 -0.1005 ± 0.01286
530
520
510
500
490
480
20
40
60
80
100
120
140
160
180
200
220


Chip 2, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
20
40
60
80
100
120
140
160
180
200
220

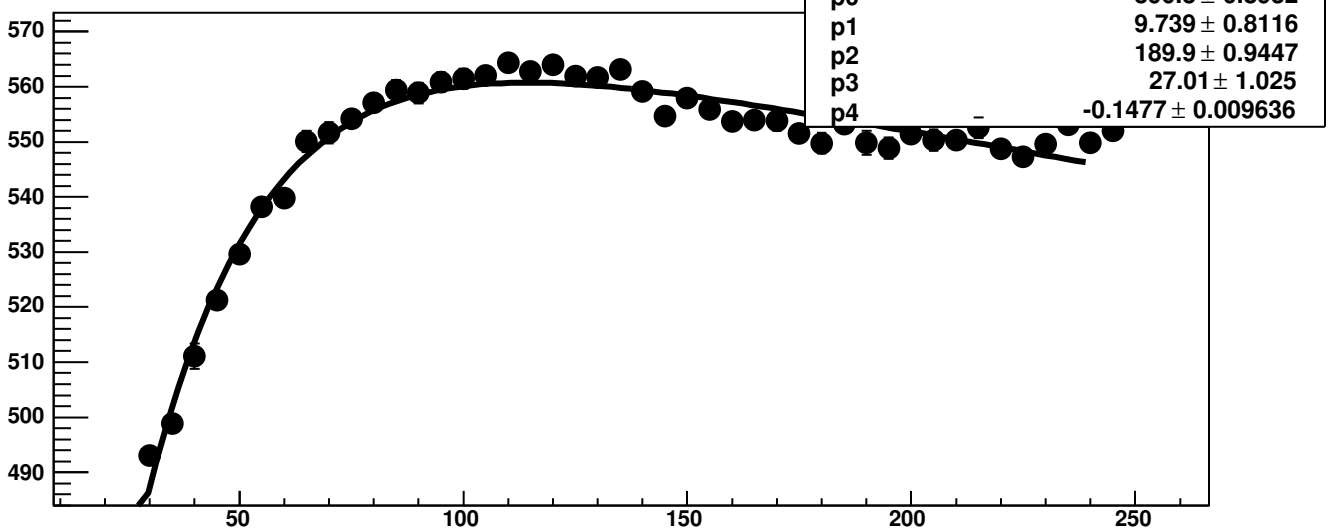

Chip 2, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
20
40
60
80
100
120
140
160
180
200
220


Chip 2, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 128.3 / 38
p0 388.5 ± 3.231
p1 14.24 ± 0.6715
p2 187.8 ± 3.302
p3 24.25 ± 0.7411
p4 -0.1532 ± 0.008164
560
540
520
500
480
50
100
150
200
250

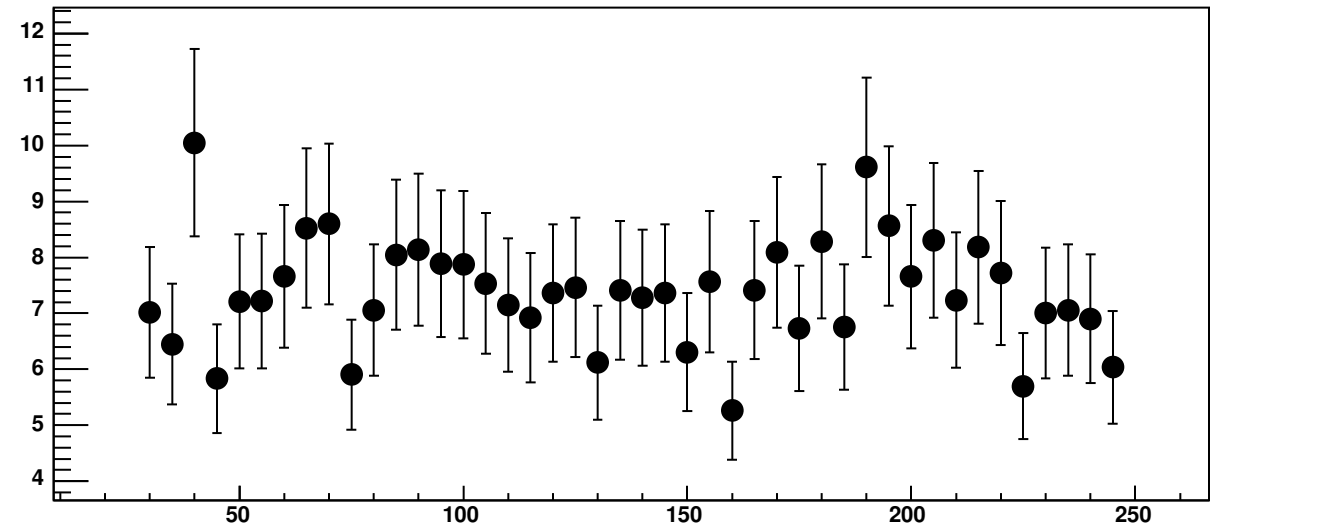

Chip 2, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 2, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

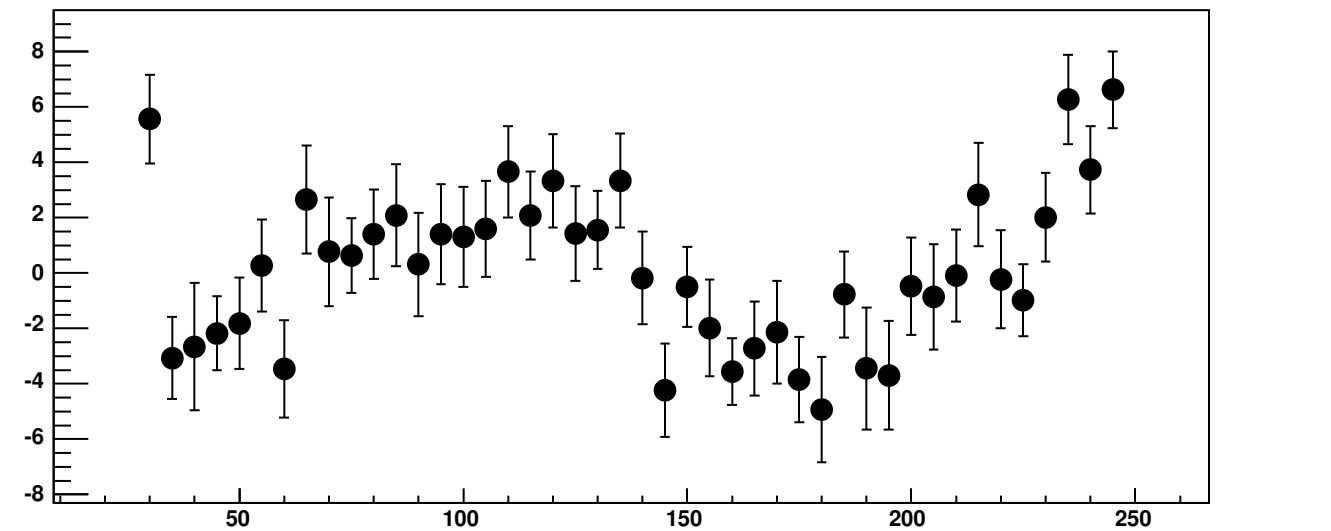
Chip 2, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold

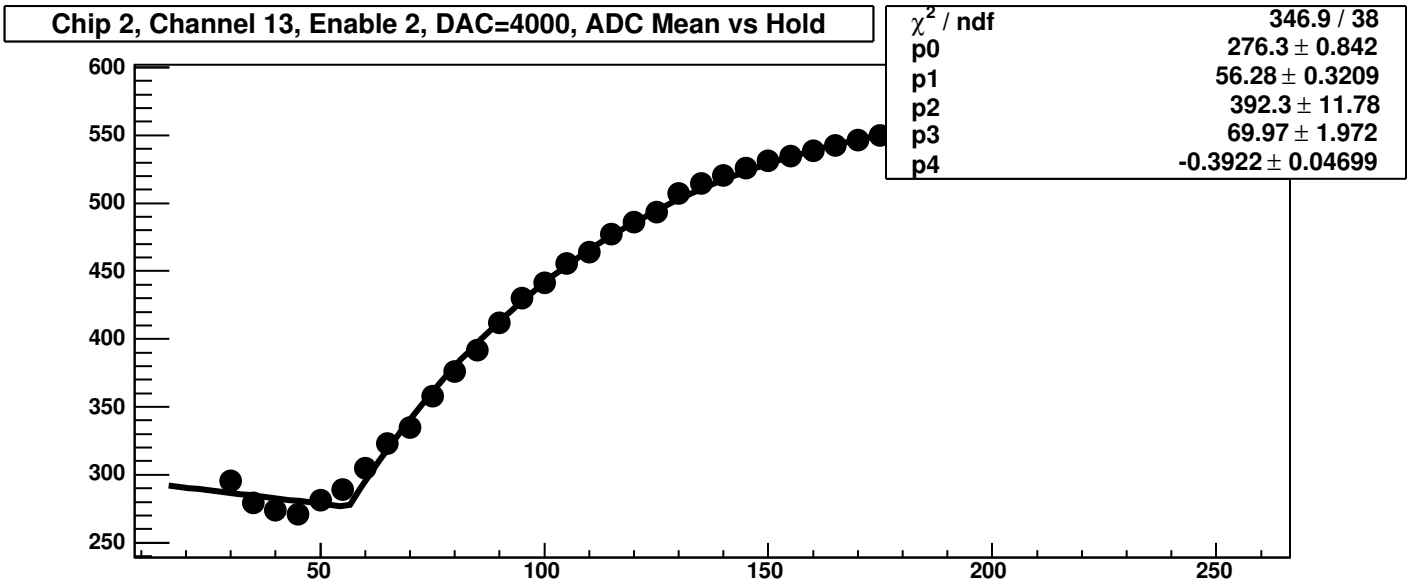


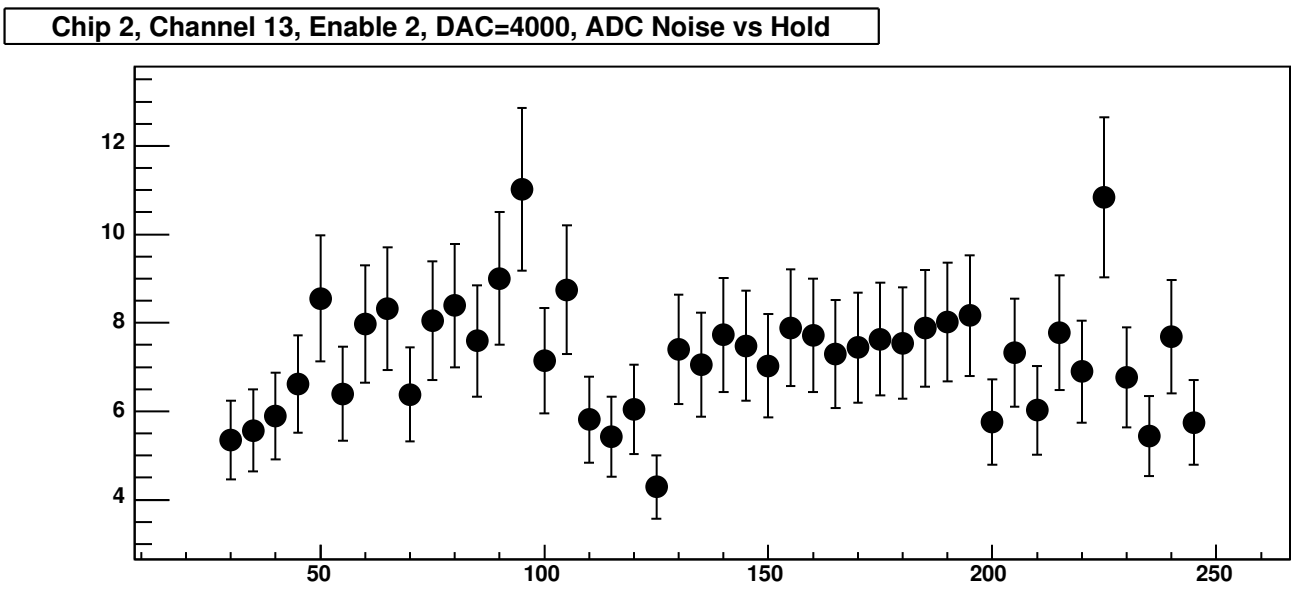
Chip 2, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold

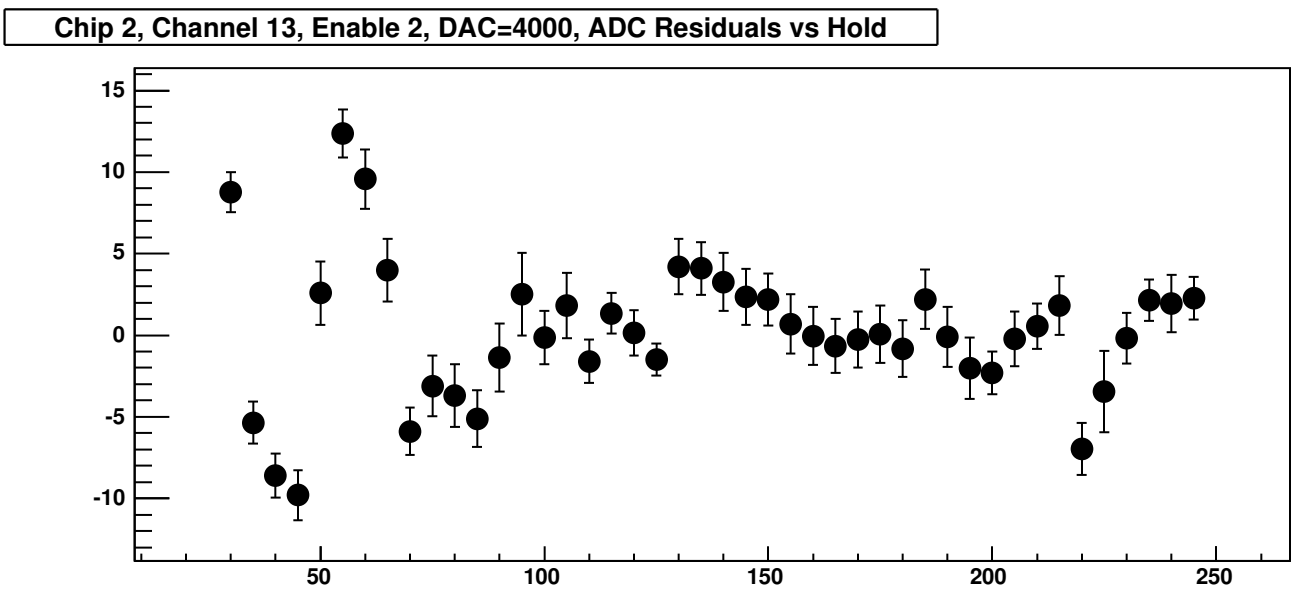


Chip 2, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold

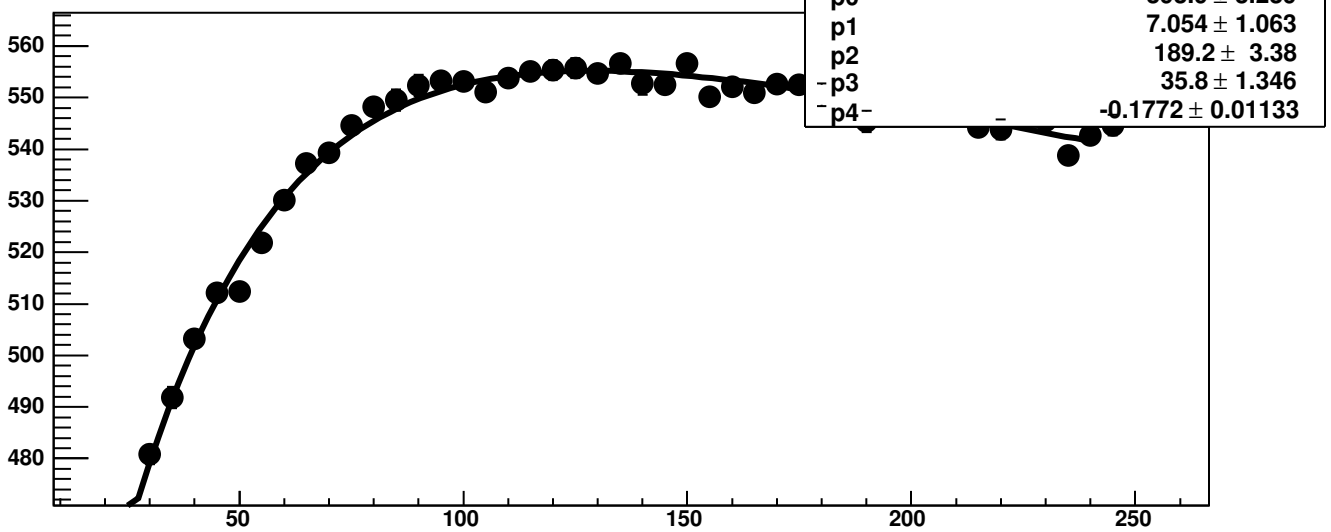



Chip 2, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 346.9 / 38
p0 276.3 ± 0.842
p1 56.28 ± 0.3209
p2 392.3 ± 11.78
p3 69.97 ± 1.972
p4 -0.3922 ± 0.04699
600
550
500
450
400
350
300
250
50
100
150
200
250

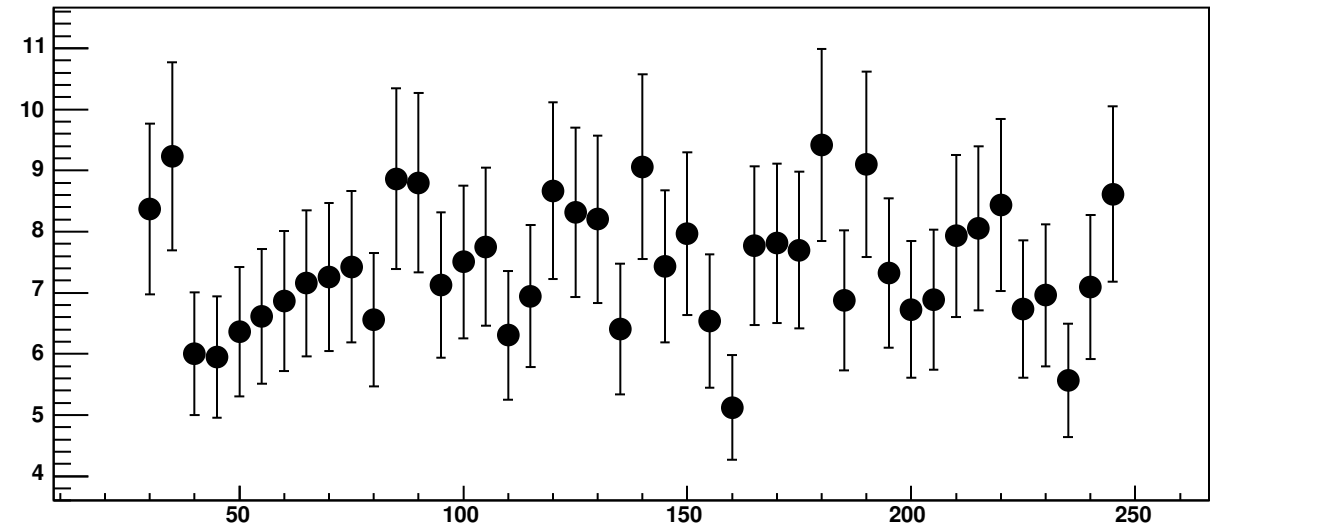

Chip 2, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250


Chip 2, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
50
100
150
200
250

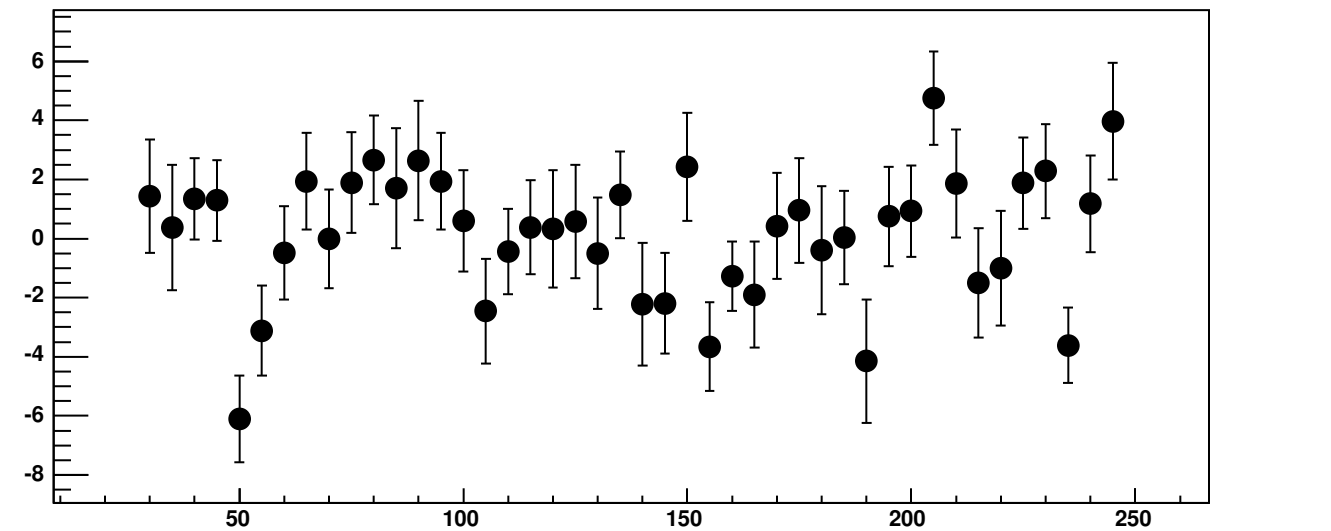
Chip 2, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

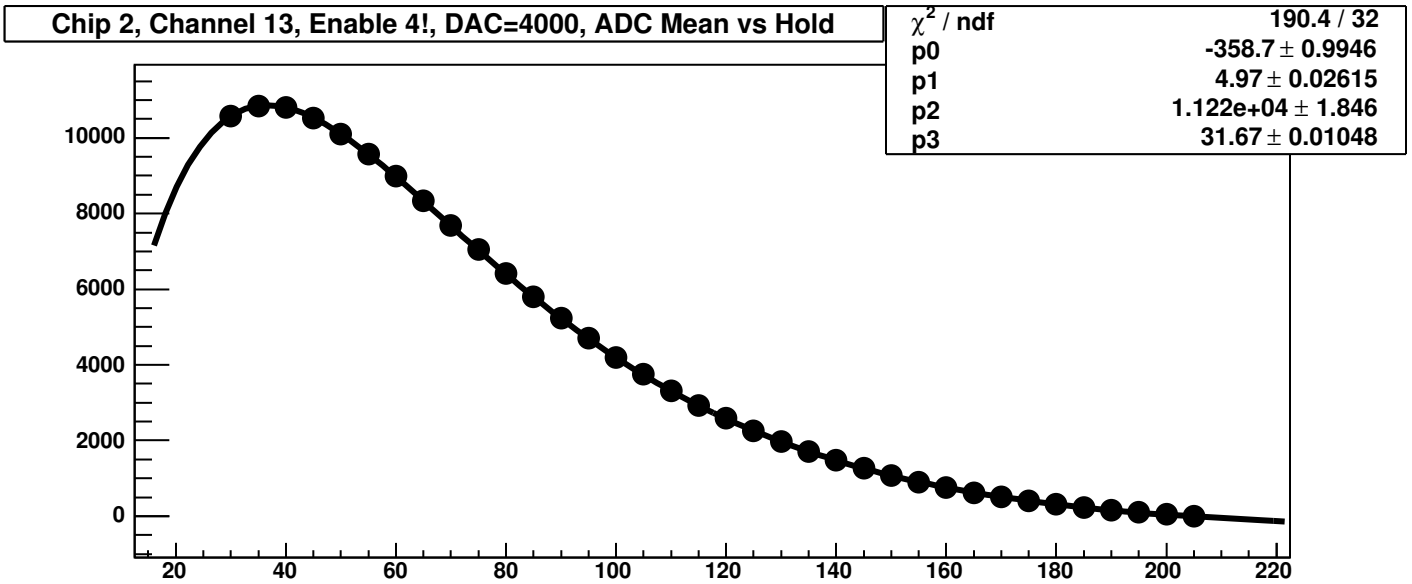


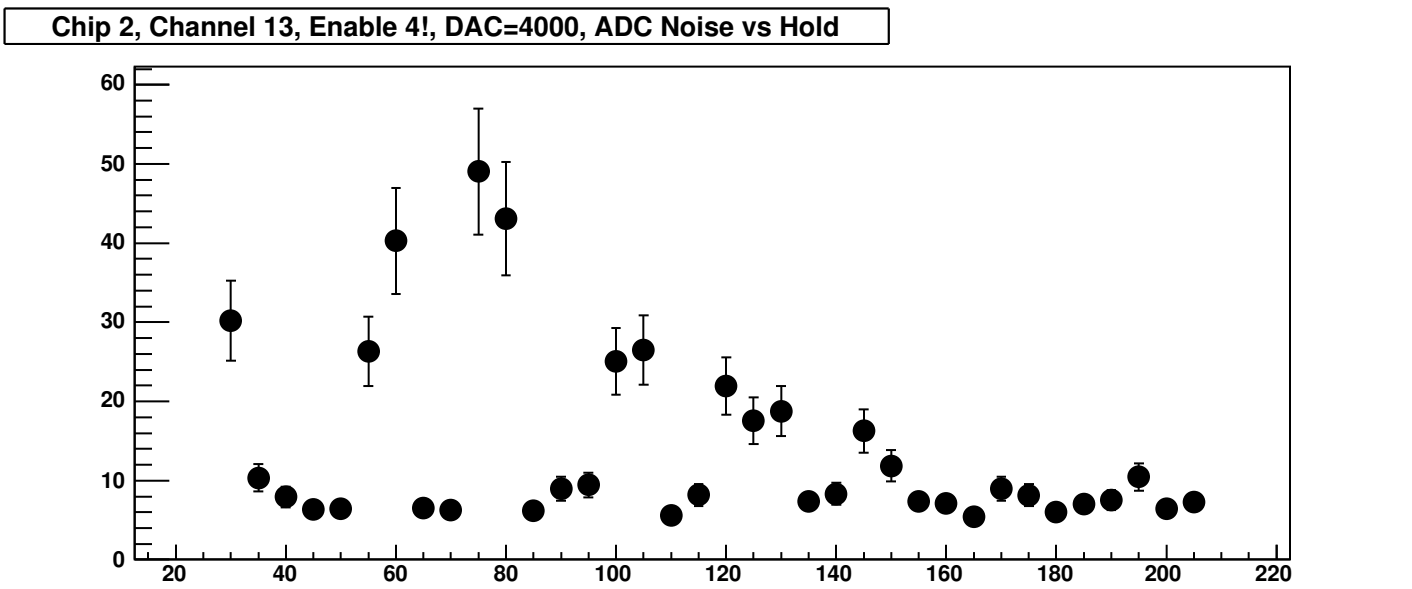
Chip 2, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold

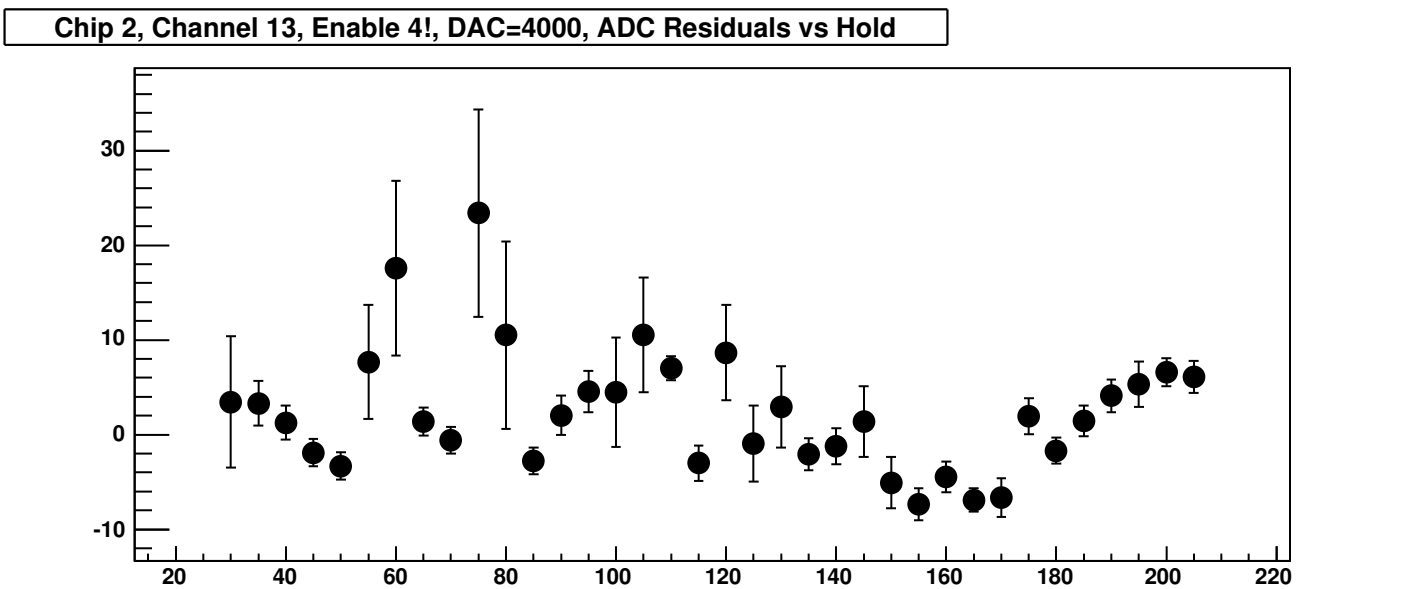


Chip 2, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

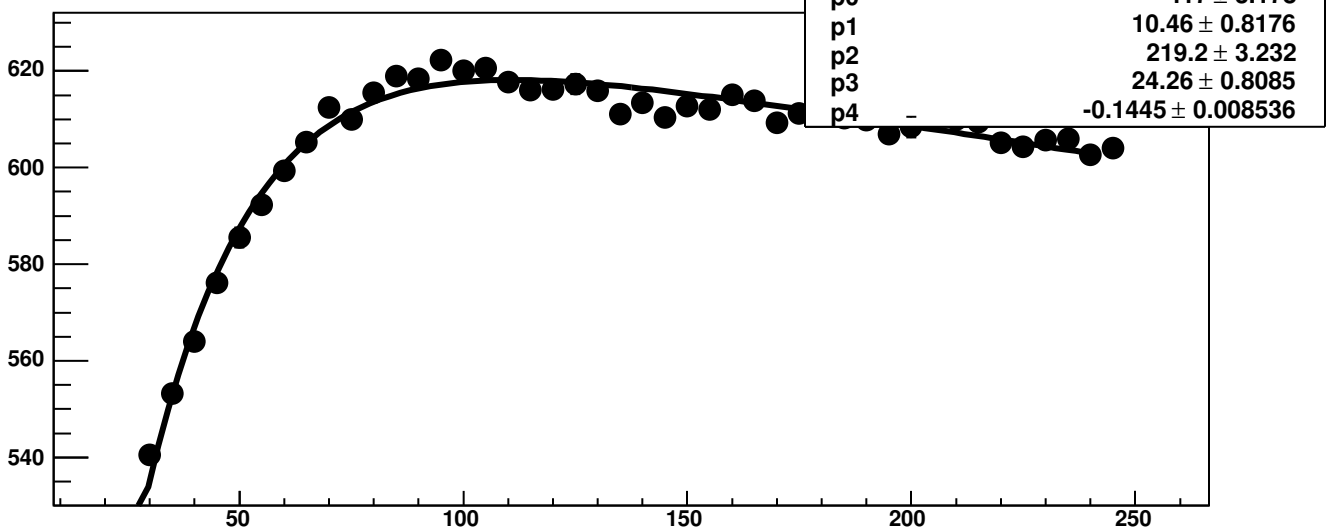



Chip 2, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 190.4 / 32
p0 -358.7 ± 0.9946
p1 4.97 ± 0.02615
p2 1.122e+04 ± 1.846
p3 31.67 ± 0.01048
10000
8000
6000
4000
2000
0
20
40
60
80
100
120
140
160
180
200
220

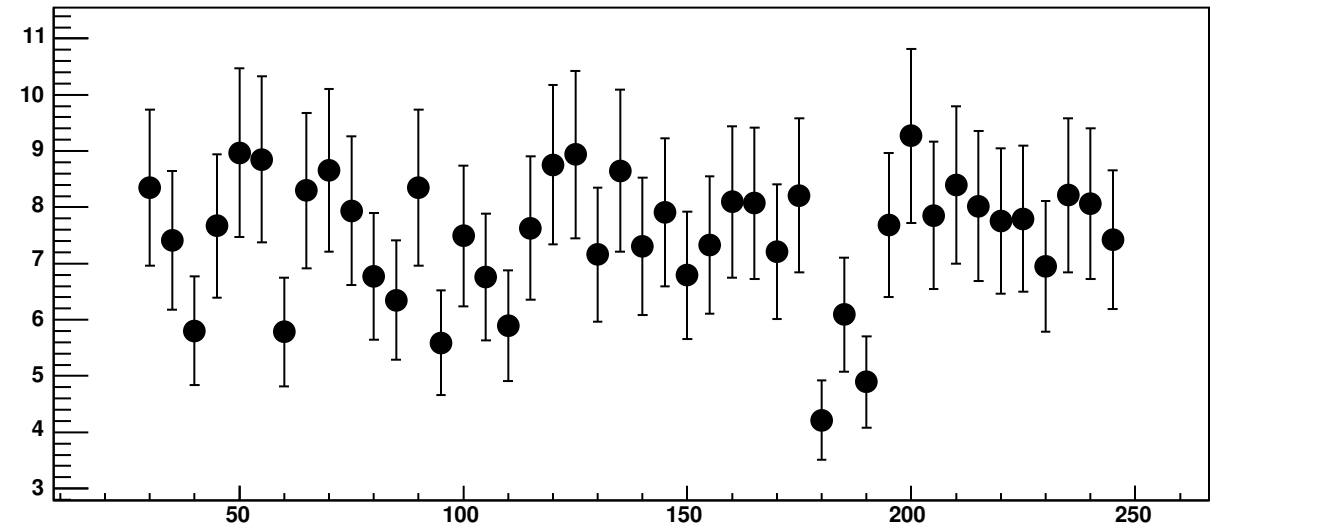

Chip 2, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold
60
50
40
30
20
10
0
20
40
60
80
100
120
140
160
180
200
220


Chip 2, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
20
40
60
80
100
120
140
160
180
200
220

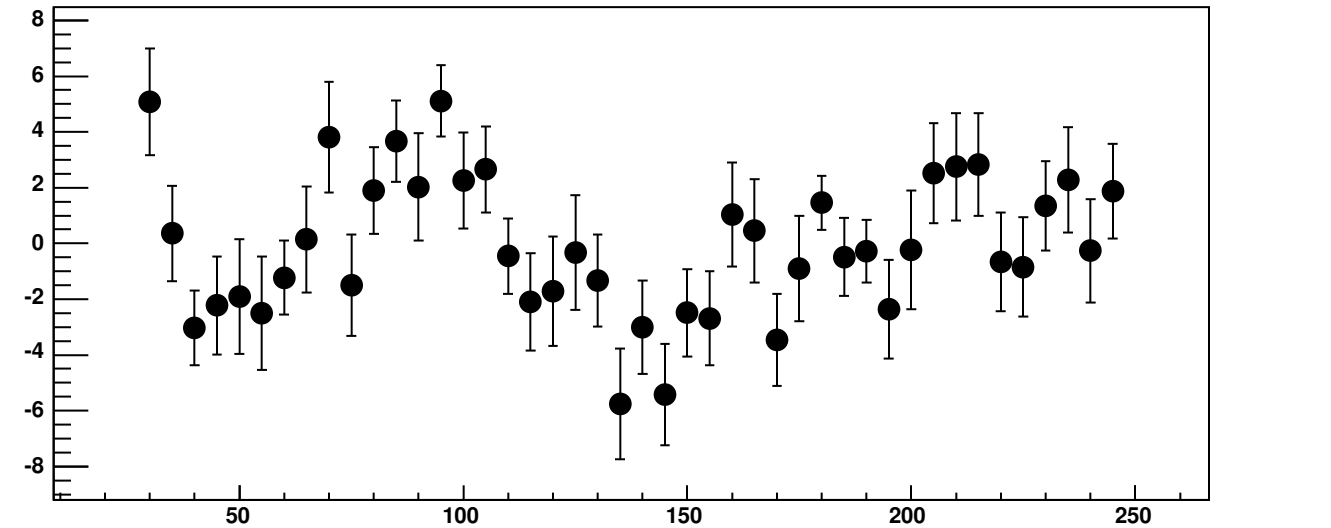
Chip 2, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold

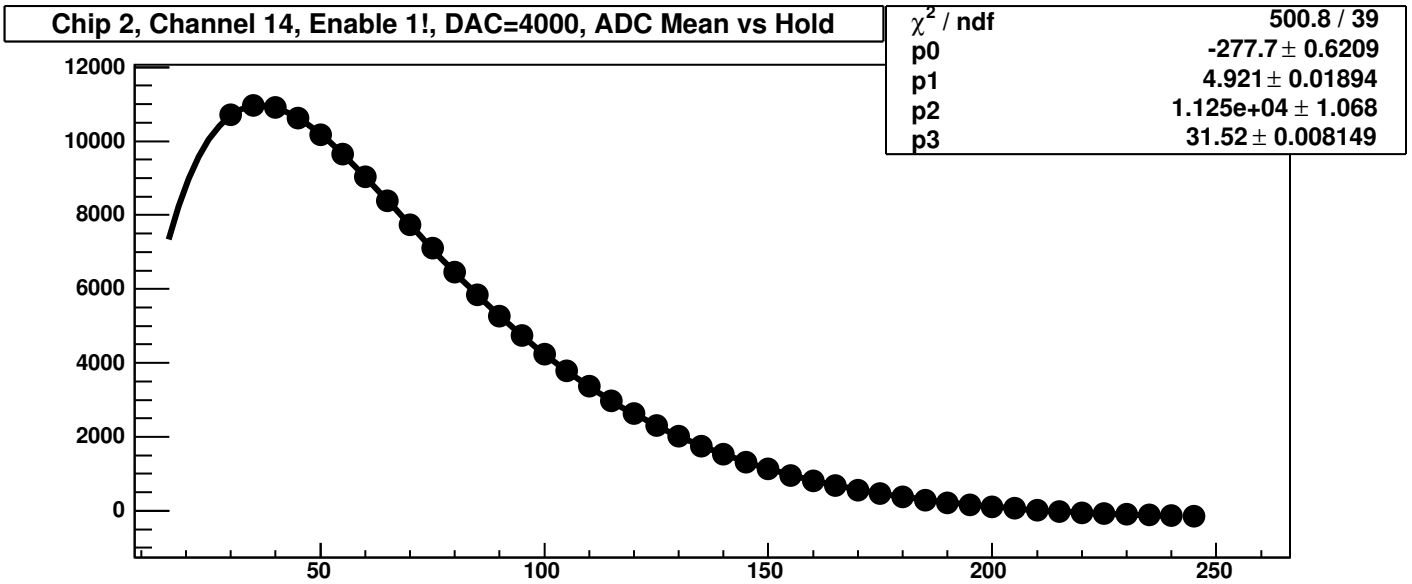


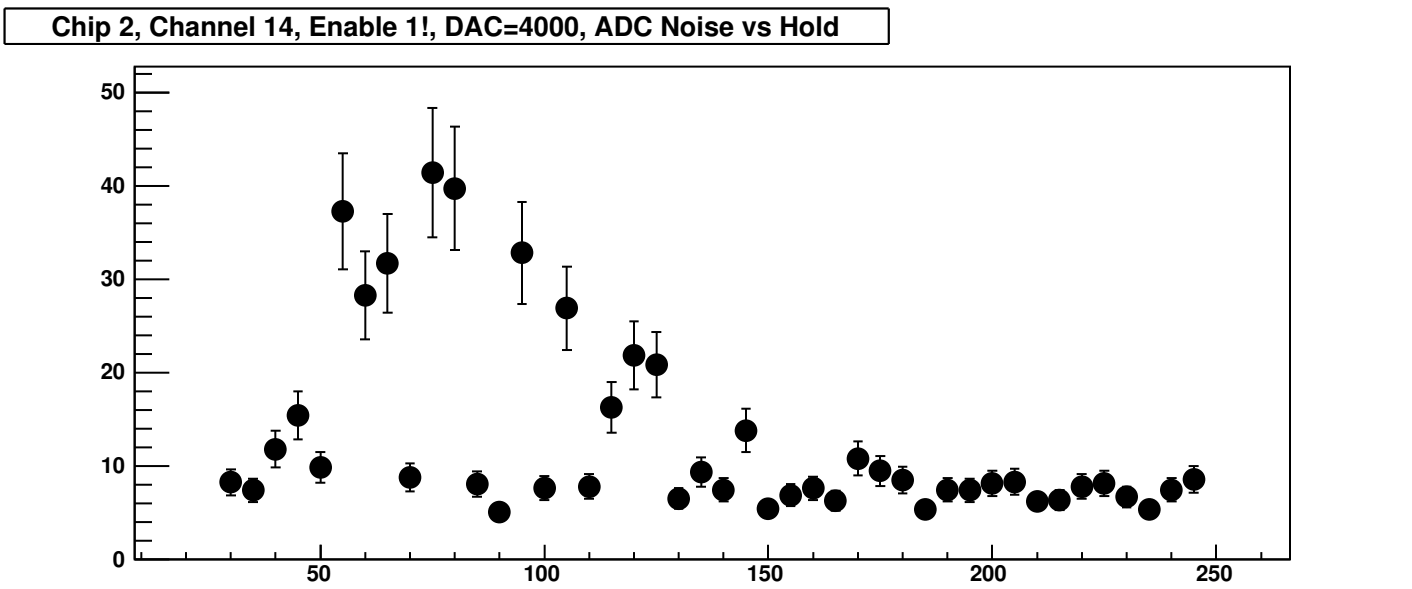
Chip 2, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold

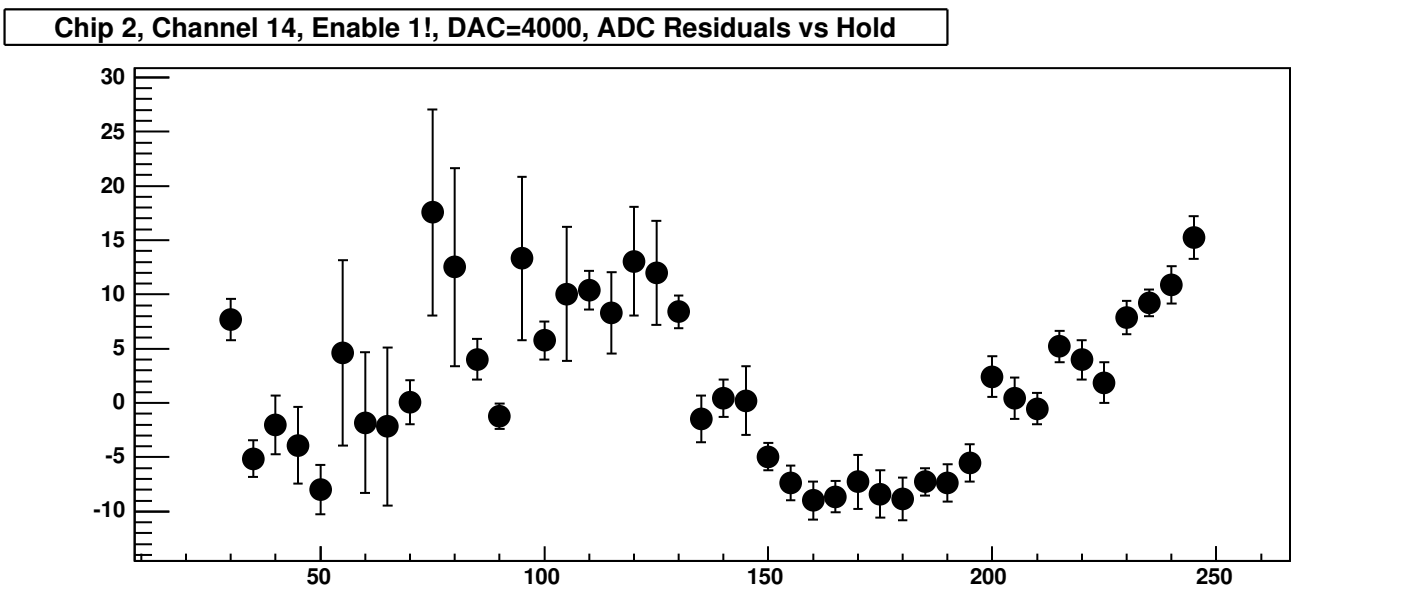


Chip 2, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

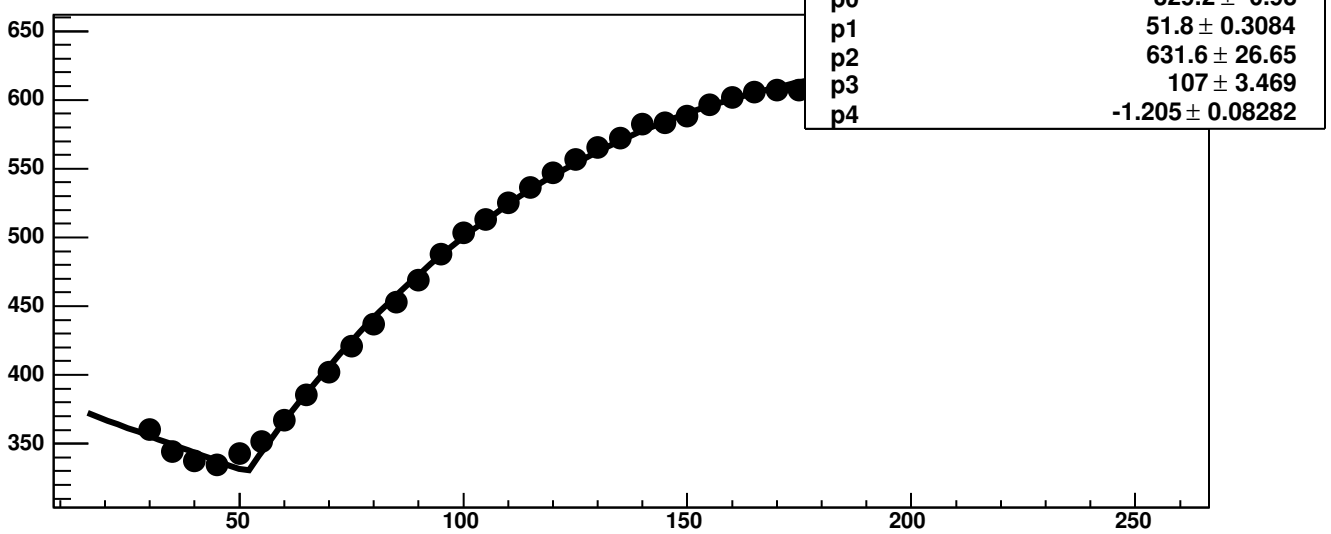



Chip 2, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 500.8 / 39
p0 -277.7 ± 0.6209
p1 4.921 ± 0.01894
p2 1.125e+04 ± 1.068
p3 31.52 ± 0.008149
12000
10000
8000
6000
4000
2000
0
50
100
150
200
250

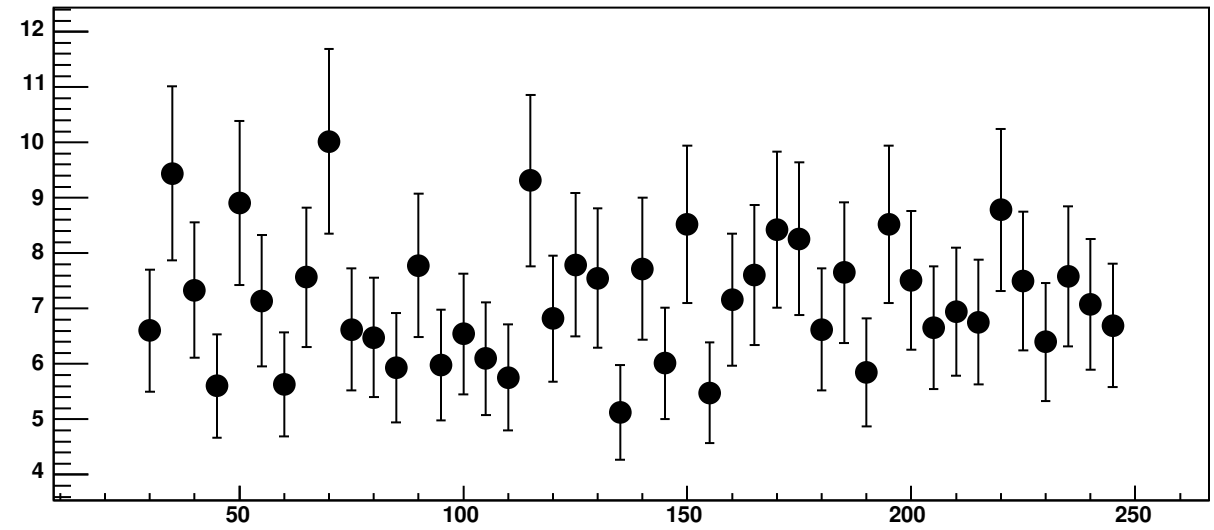

Chip 2, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
0
50
100
150
200
250


Chip 2, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
50
100
150
200
250

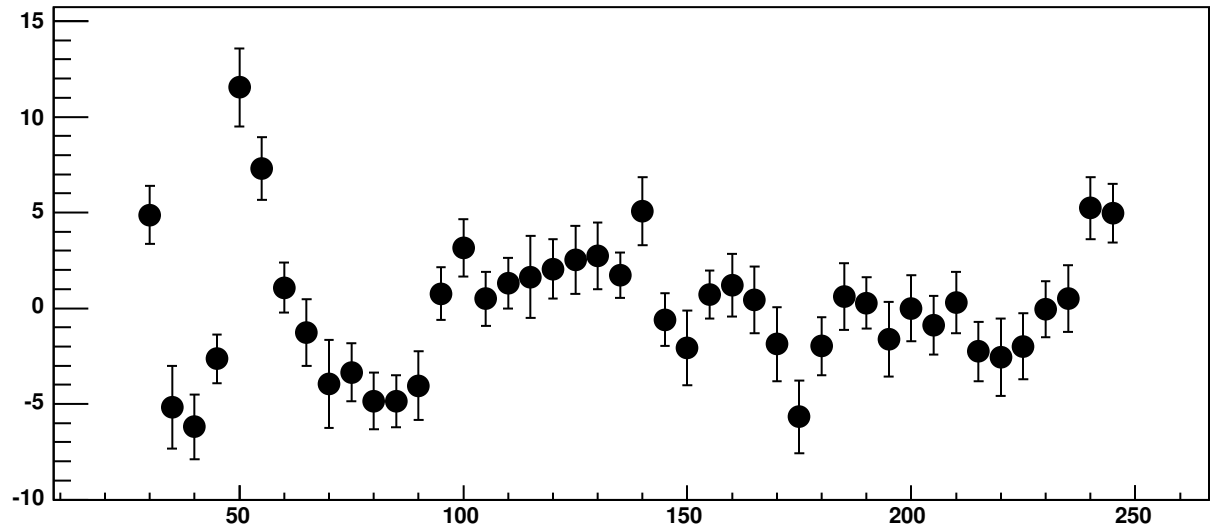
Chip 2, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

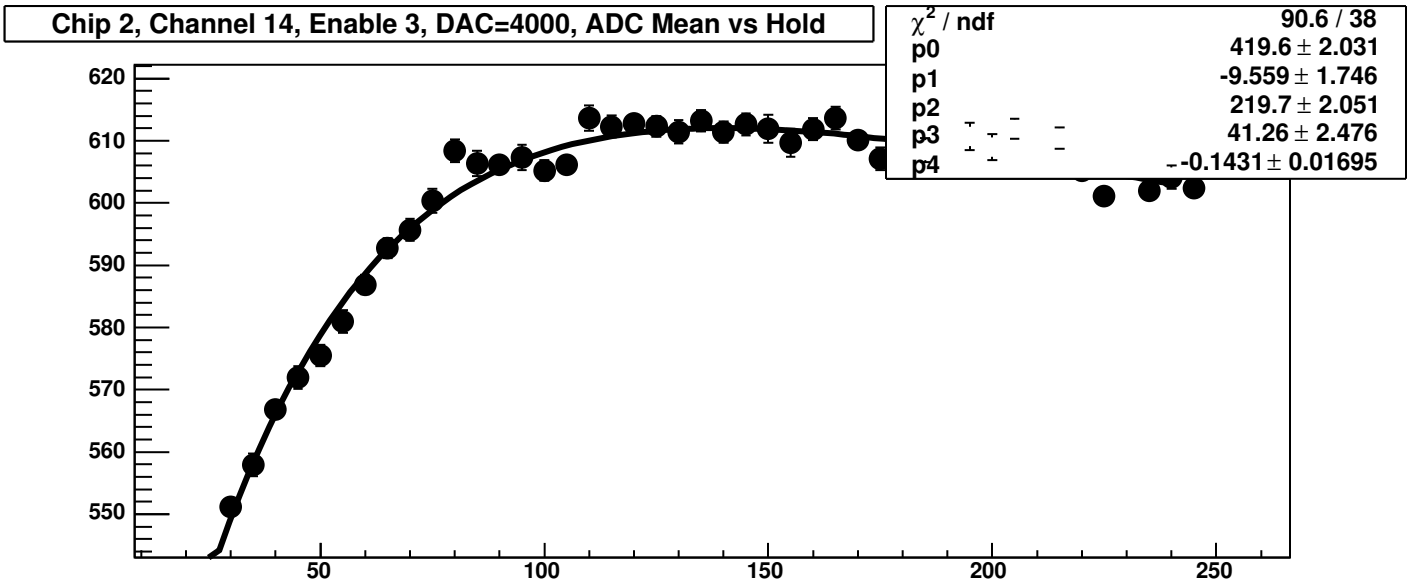


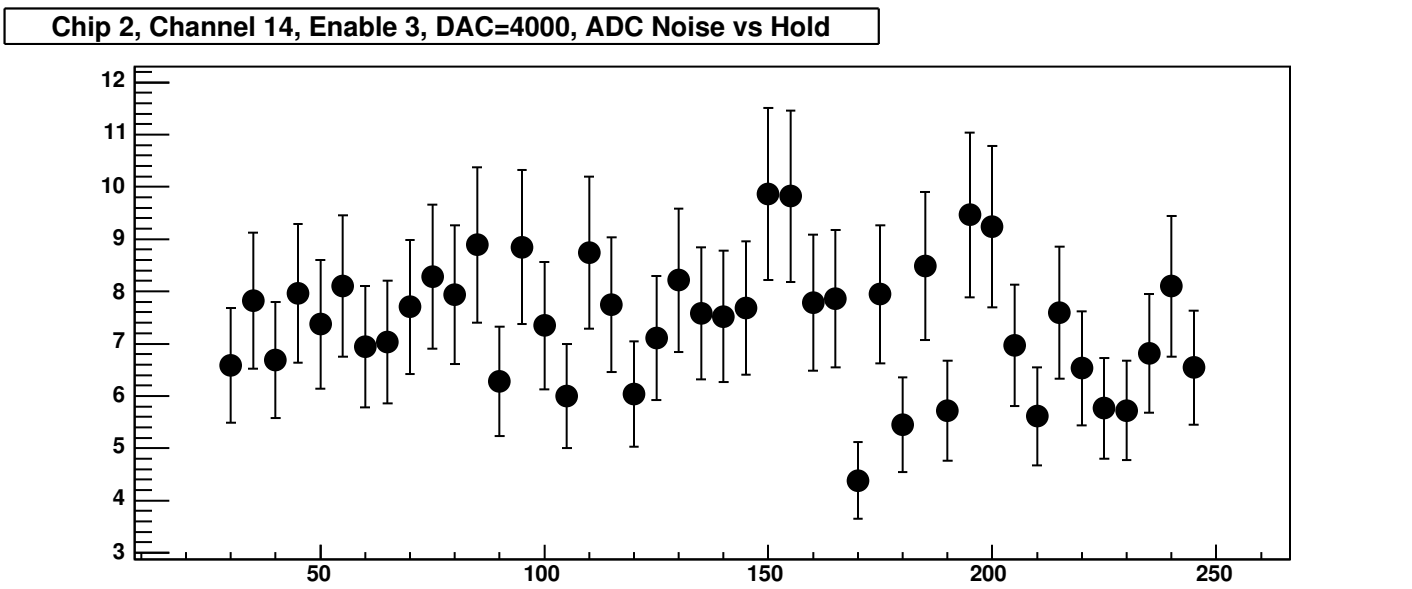
Chip 2, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold

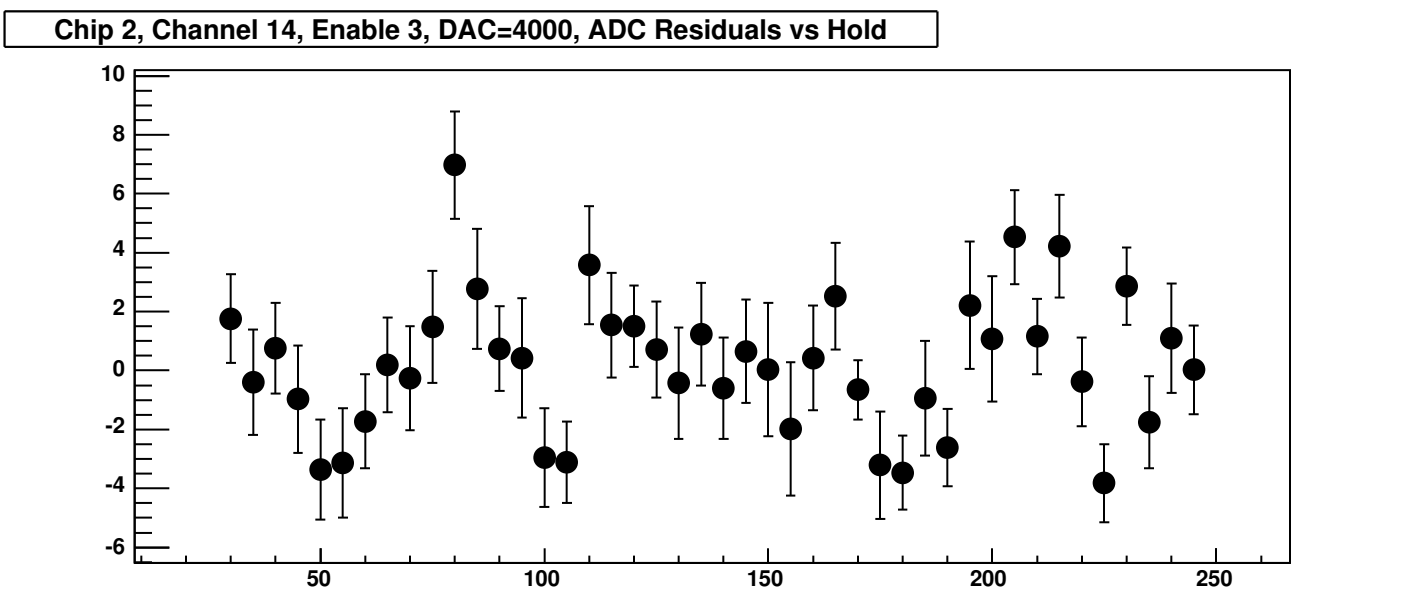


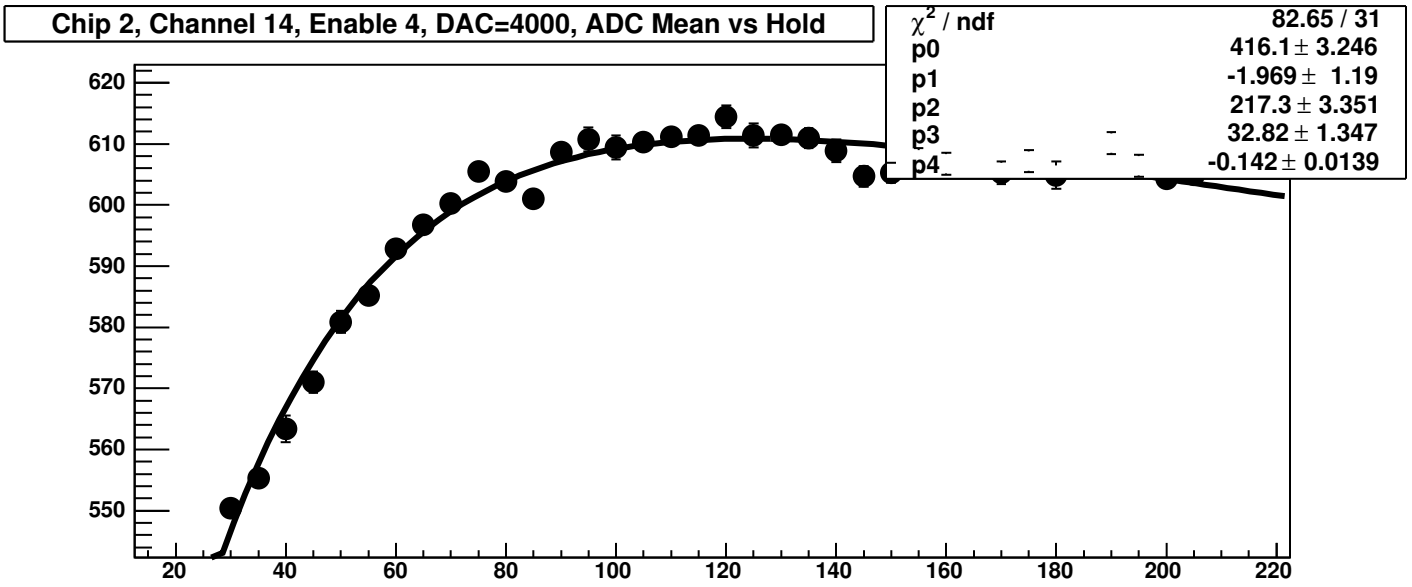
Chip 2, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold

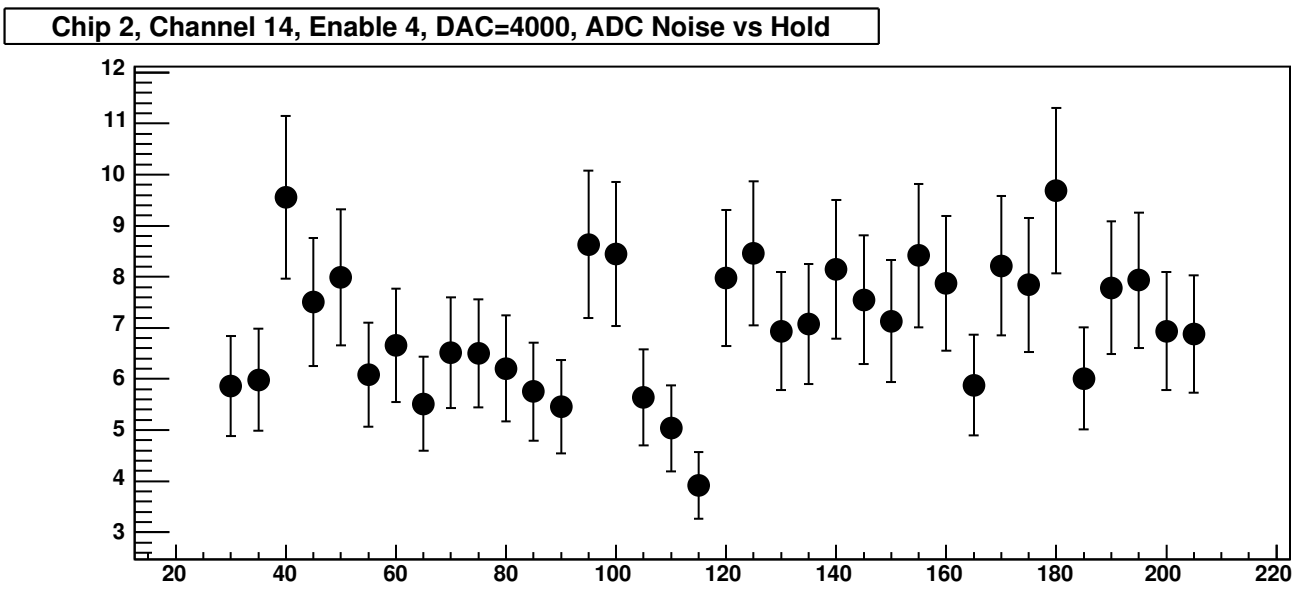


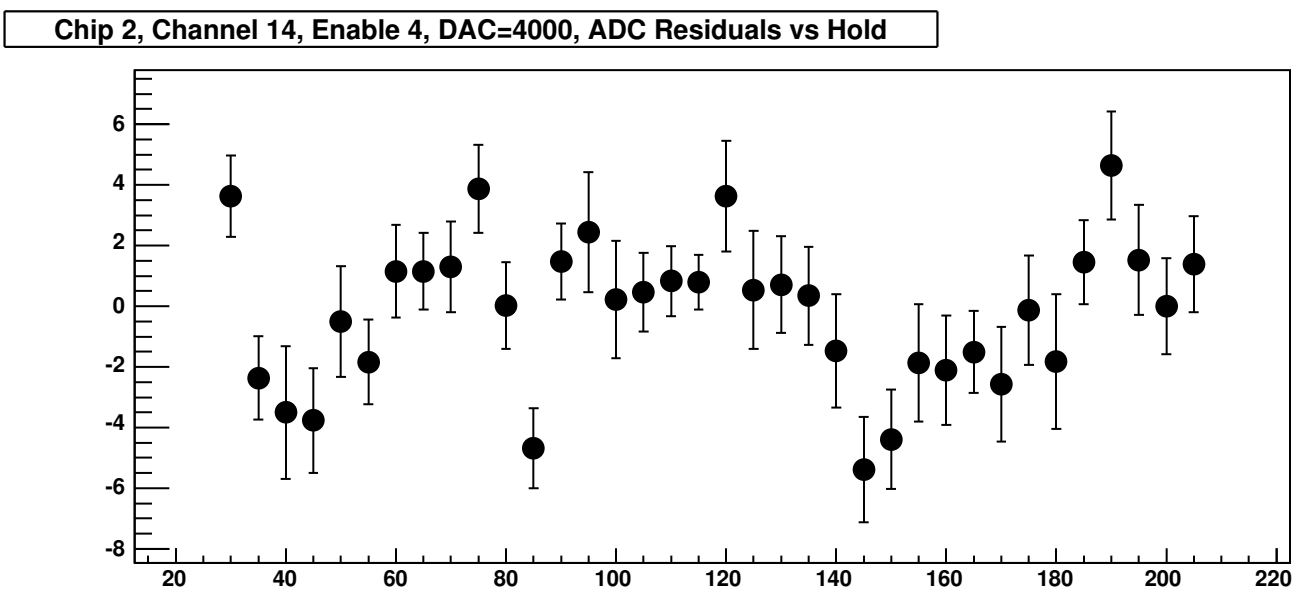

Chip 2, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 90.6 / 38
p0 419.6 ± 2.031
p1 -9.559 ± 1.746
p2 219.7 ± 2.051
p3 41.26 ± 2.476
p4 -0.1431 ± 0.01695
620
610
600
590
580
570
560
550
50
100
150
200
250


Chip 2, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250

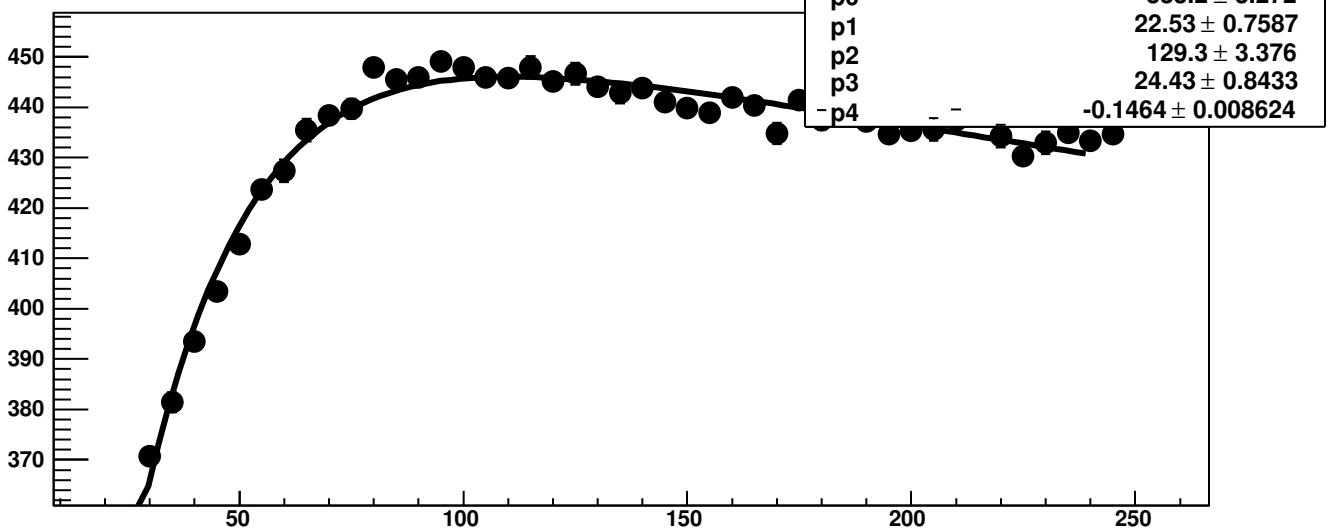

Chip 2, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
50
100
150
200
250


Chip 2, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 82.65 / 31
p0 416.1 ± 3.246
p1 -1.969 ± 1.19
p2 217.3 ± 3.351
p3 32.82 ± 1.347
p4 -0.142 ± 0.0139

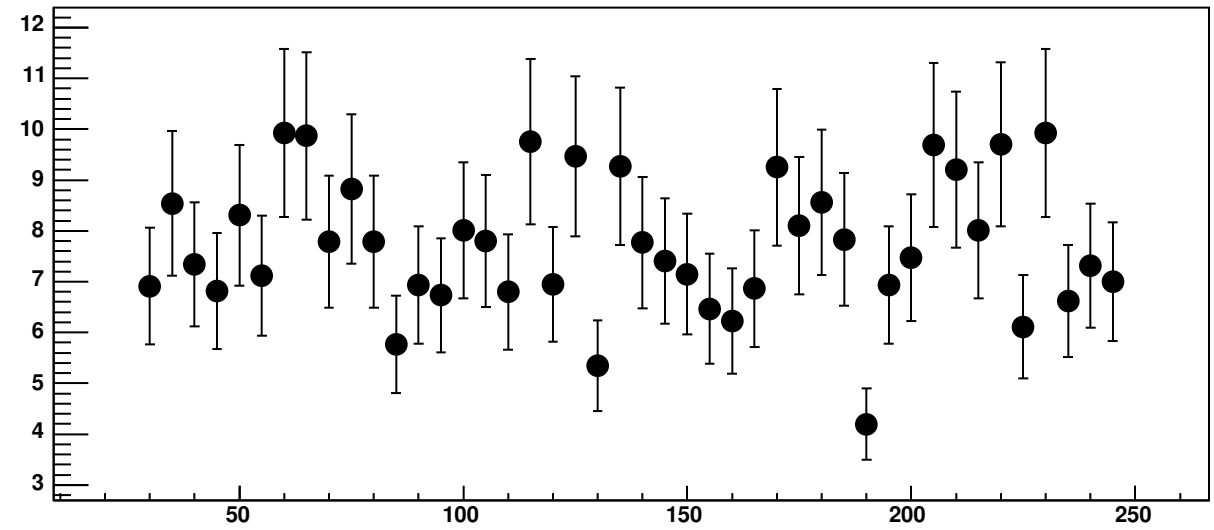

Chip 2, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold


Chip 2, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold

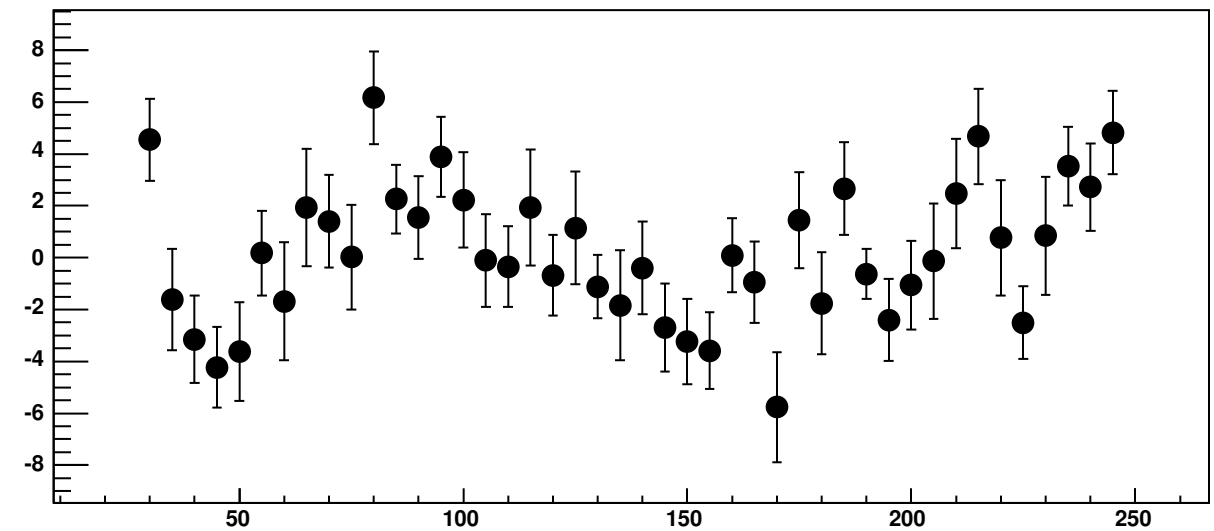
Chip 2, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold



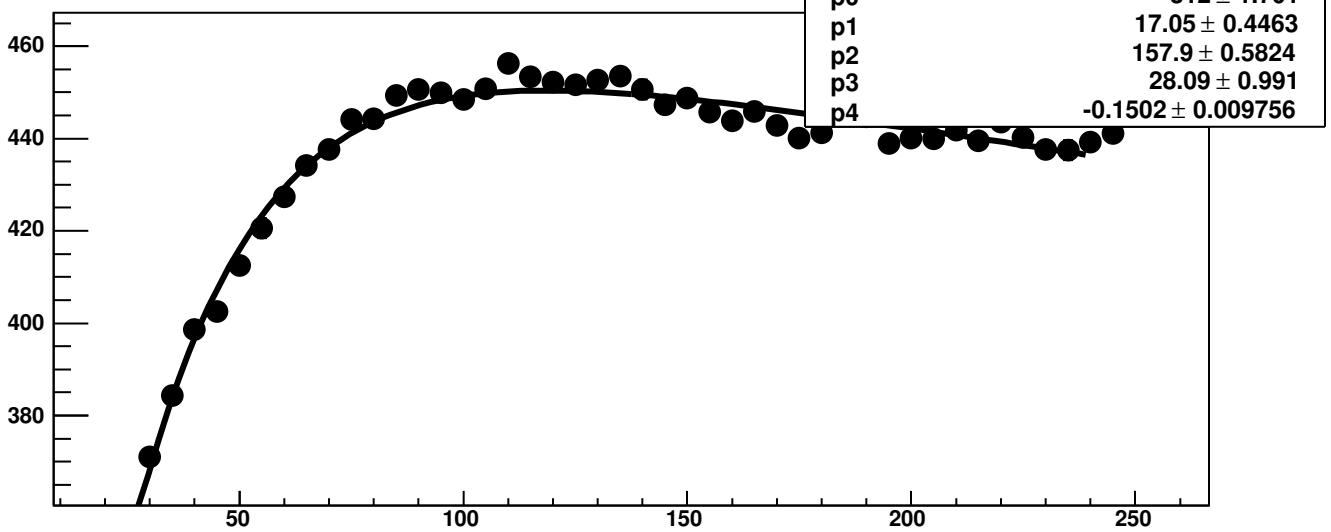
Chip 2, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



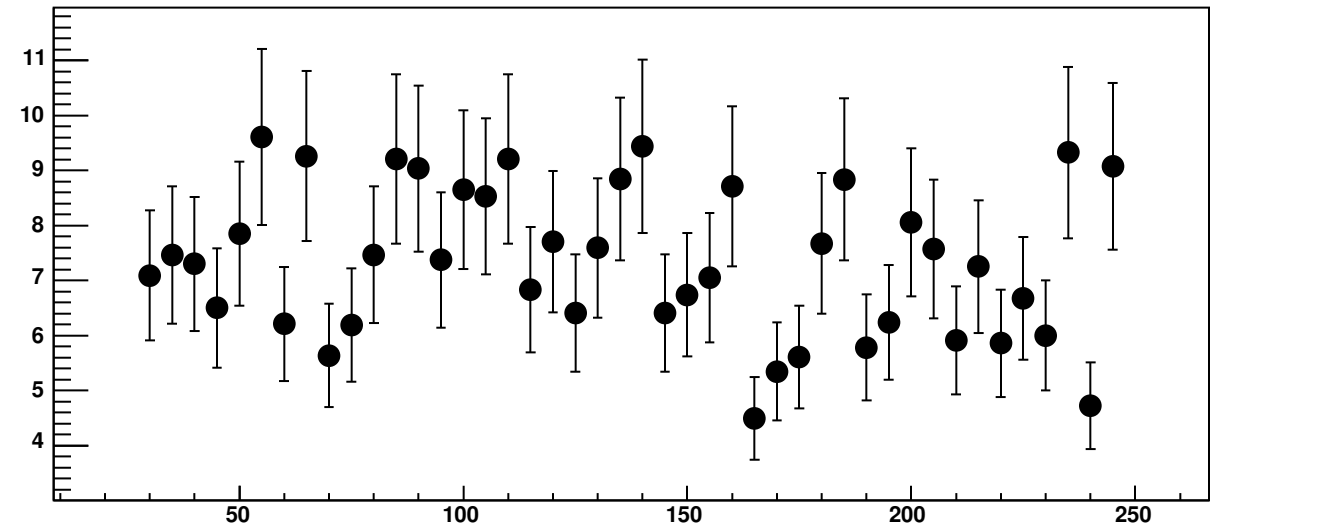
Chip 2, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold



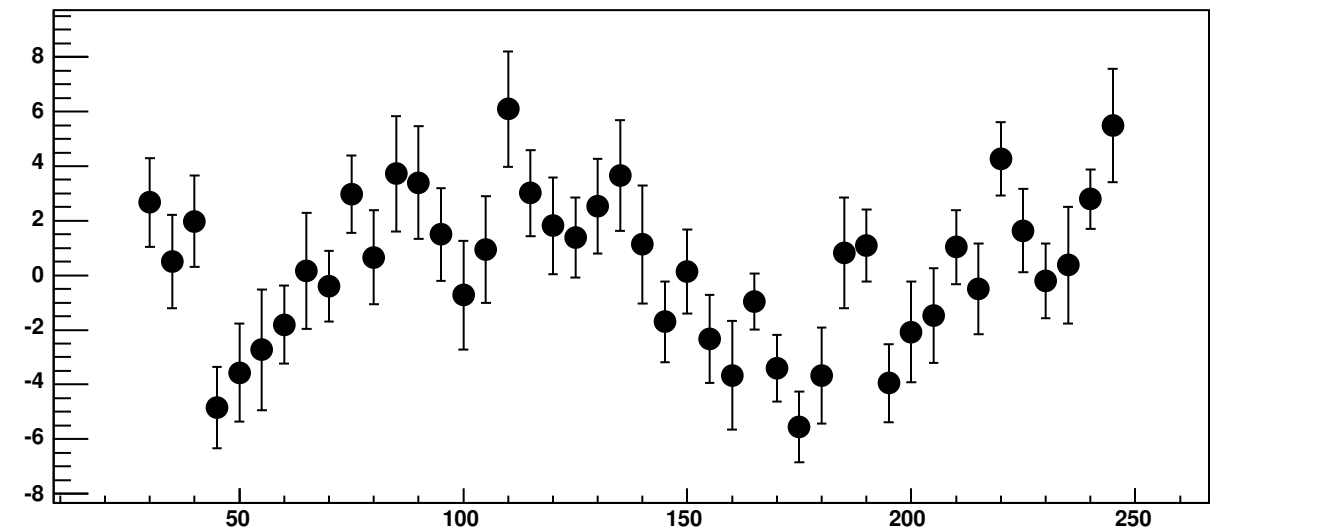
Chip 2, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

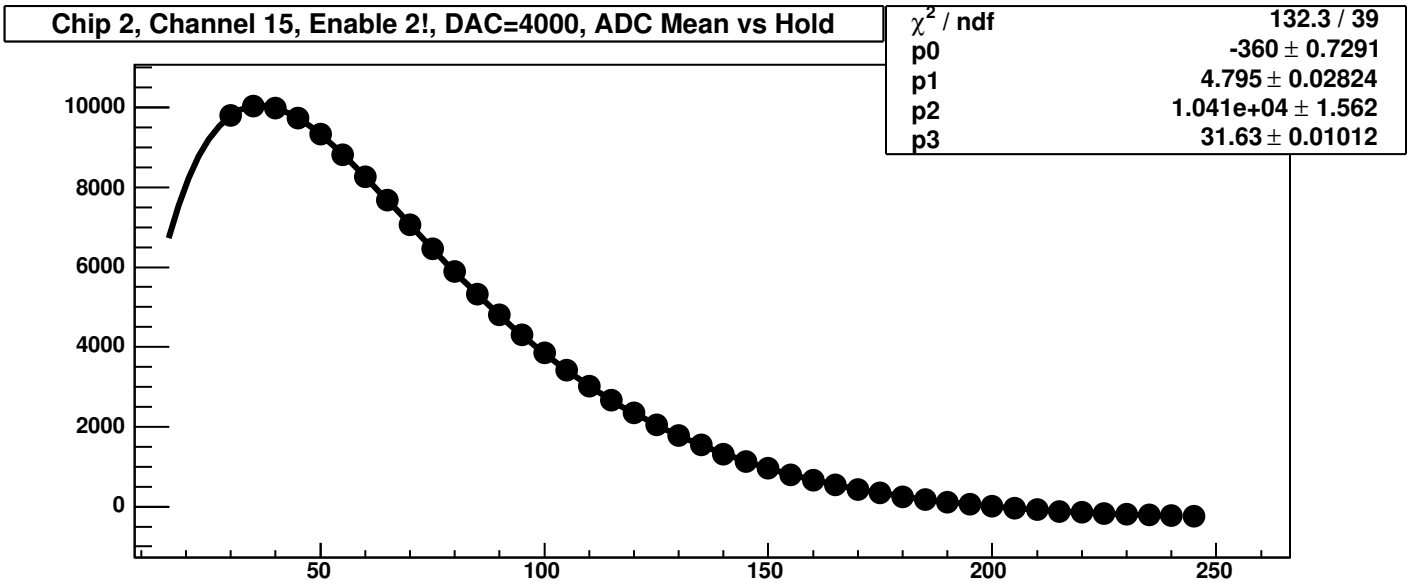


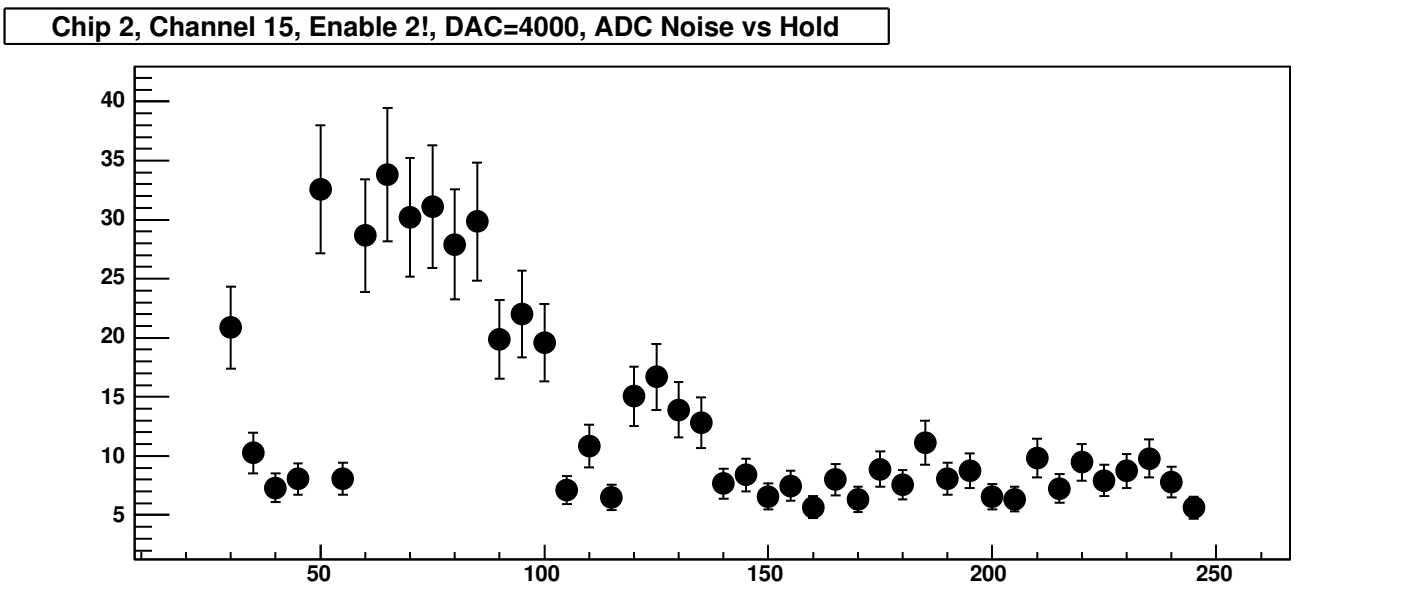
Chip 2, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold

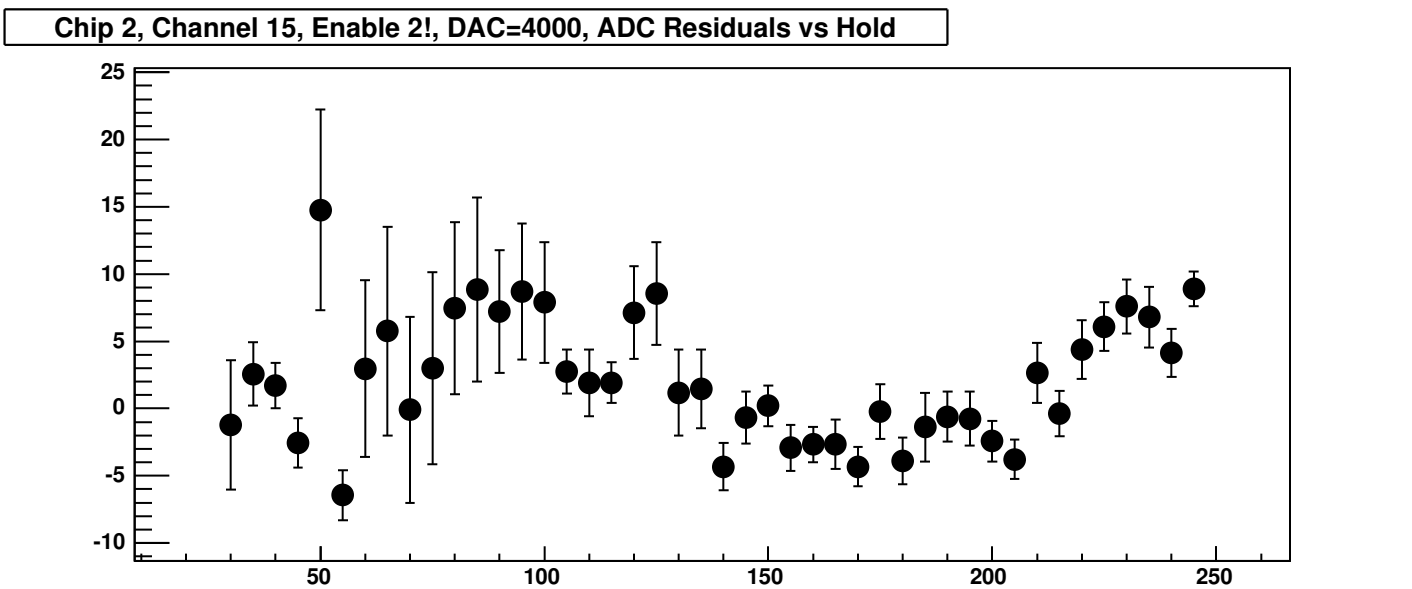


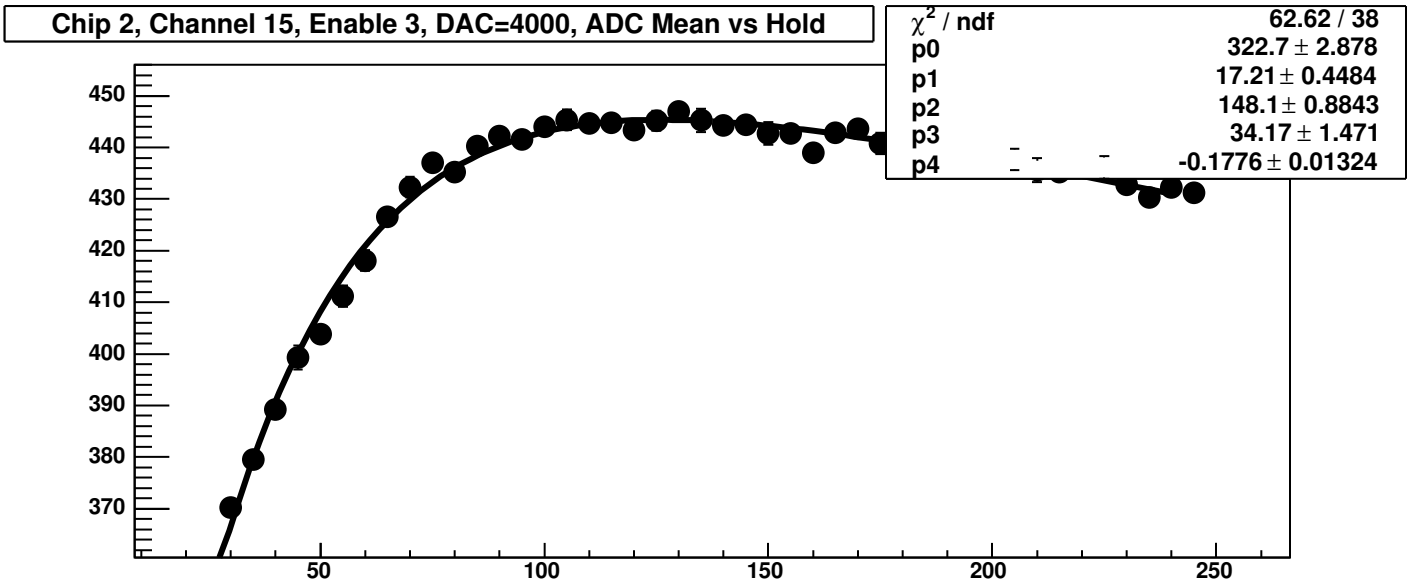
Chip 2, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

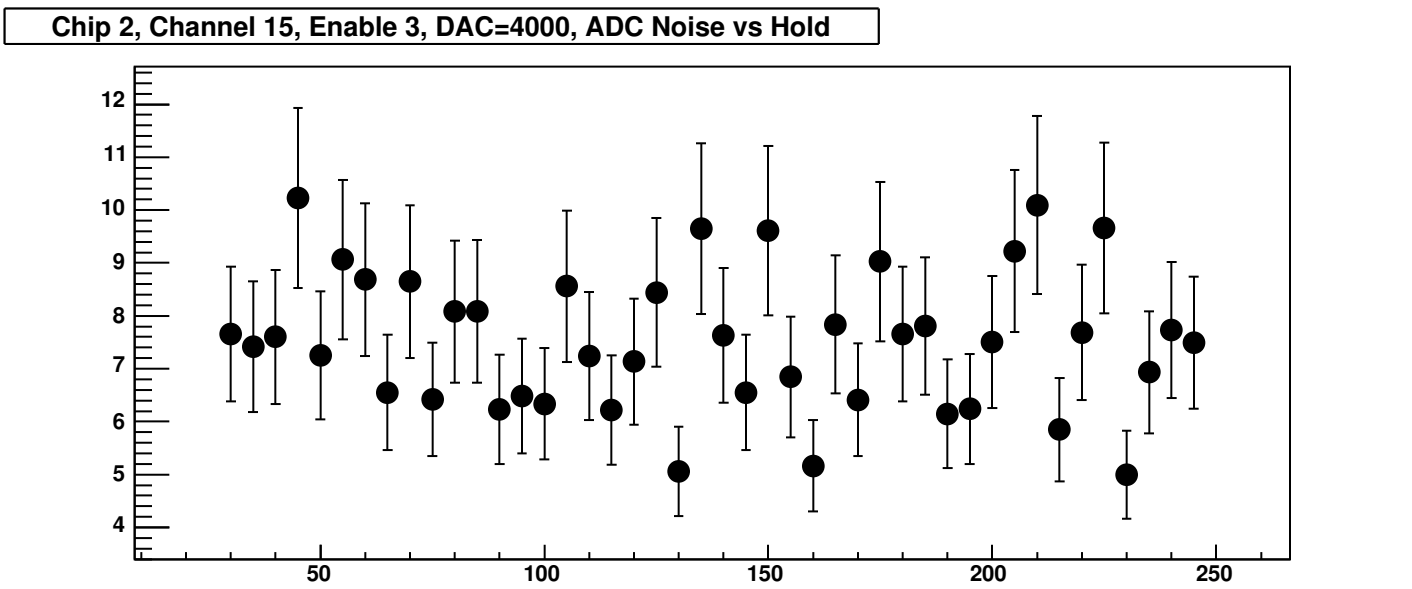


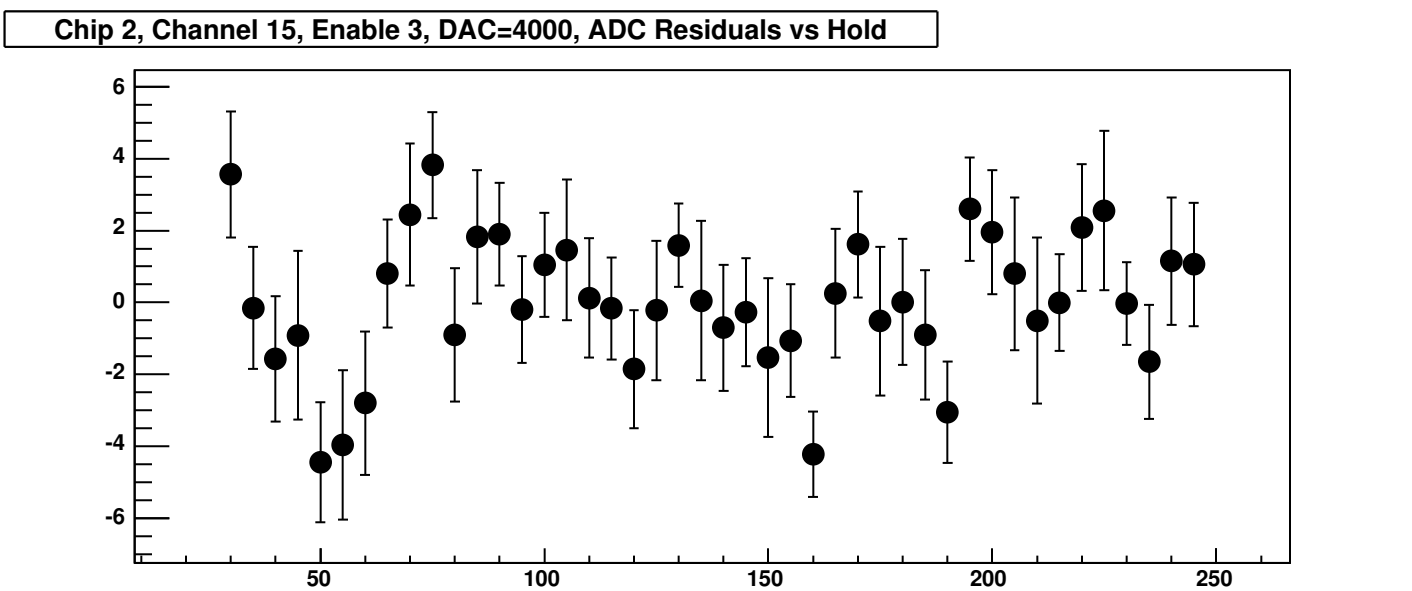

Chip 2, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 132.3 / 39
p0 -360 ± 0.7291
p1 4.795 ± 0.02824
p2 1.041e+04 ± 1.562
p3 31.63 ± 0.01012
10000
8000
6000
4000
2000
0
50
100
150
200
250


Chip 2, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold
40
35
30
25
20
15
10
5
50
100
150
200
250

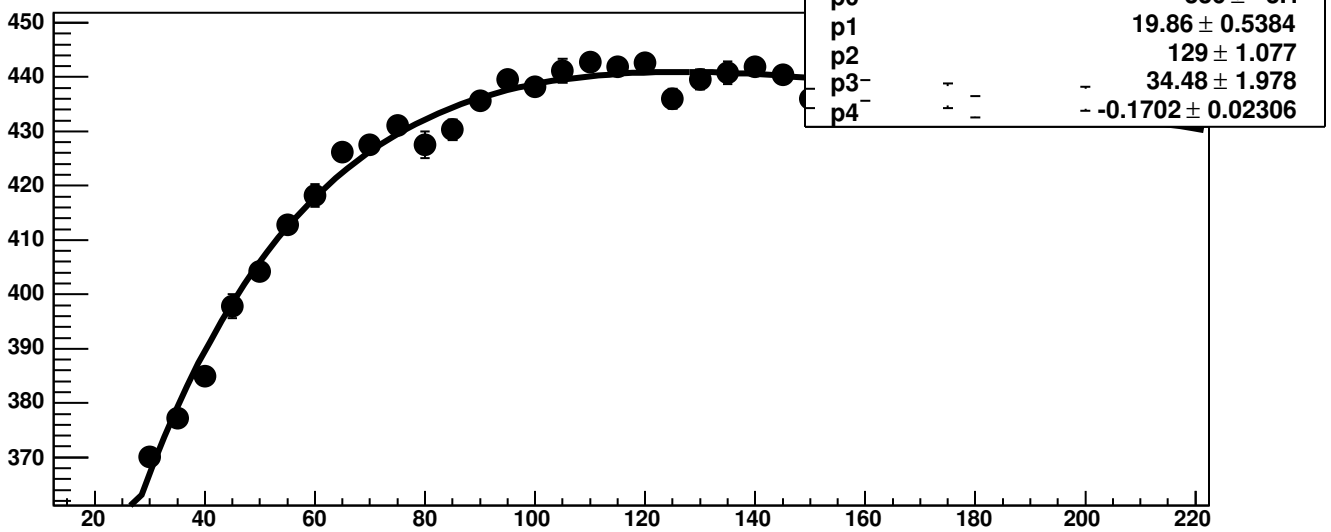

Chip 2, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
50
100
150
200
250


Chip 2, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 62.62 / 38
p0 322.7 ± 2.878
p1 17.21 ± 0.4484
p2 148.1 ± 0.8843
p3 34.17 ± 1.471
p4 -0.1776 ± 0.01324
450
440
430
420
410
400
390
380
370
50
100
150
200
250

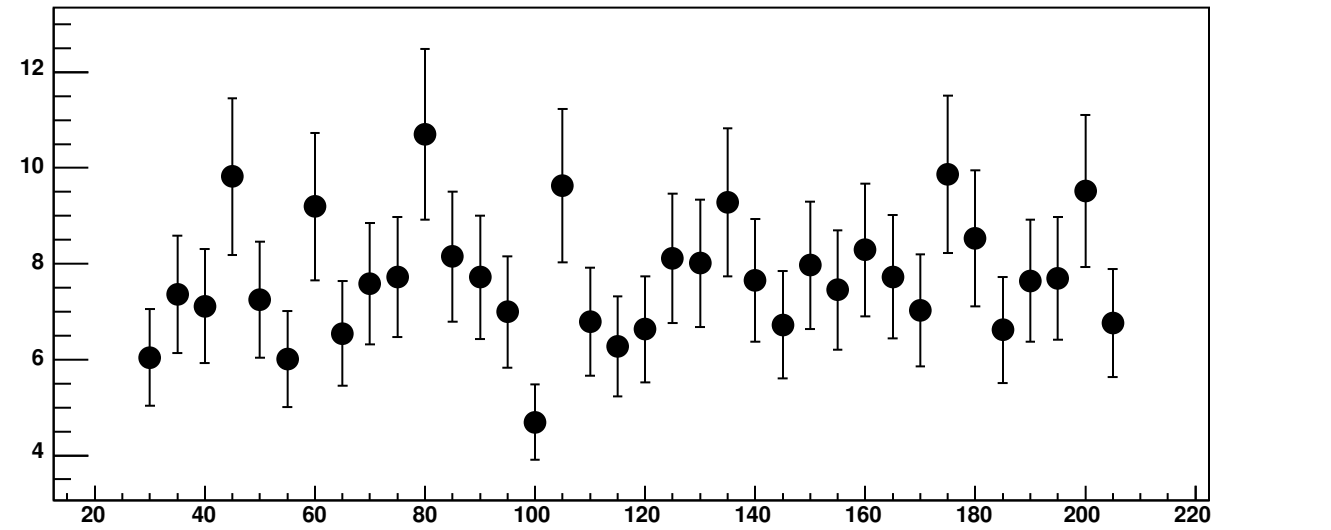

Chip 2, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 2, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
50
100
150
200
250

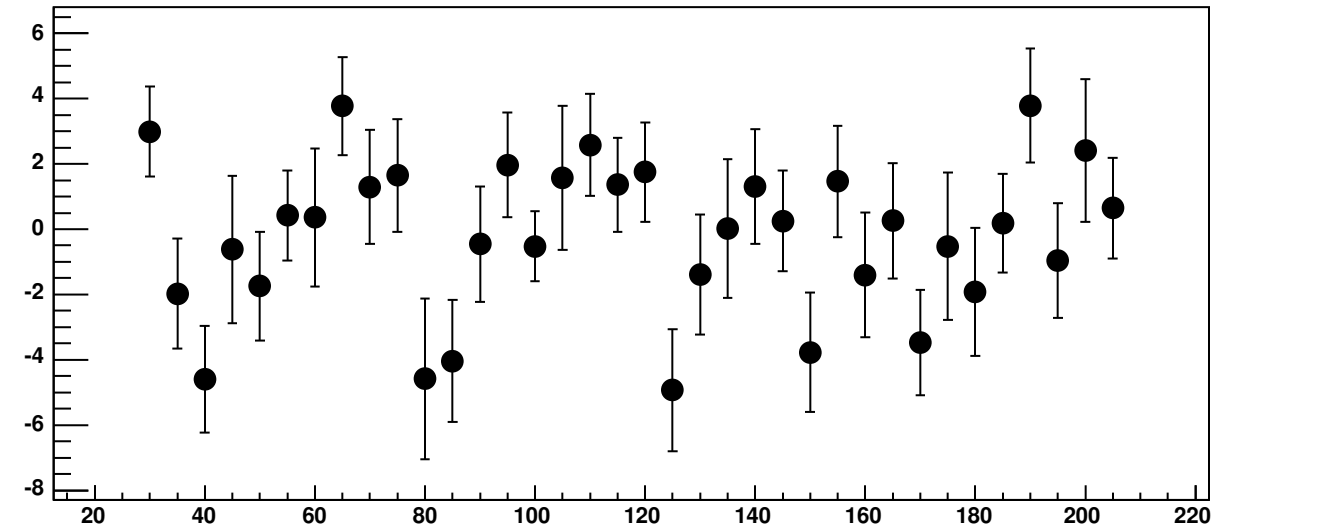
Chip 2, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold



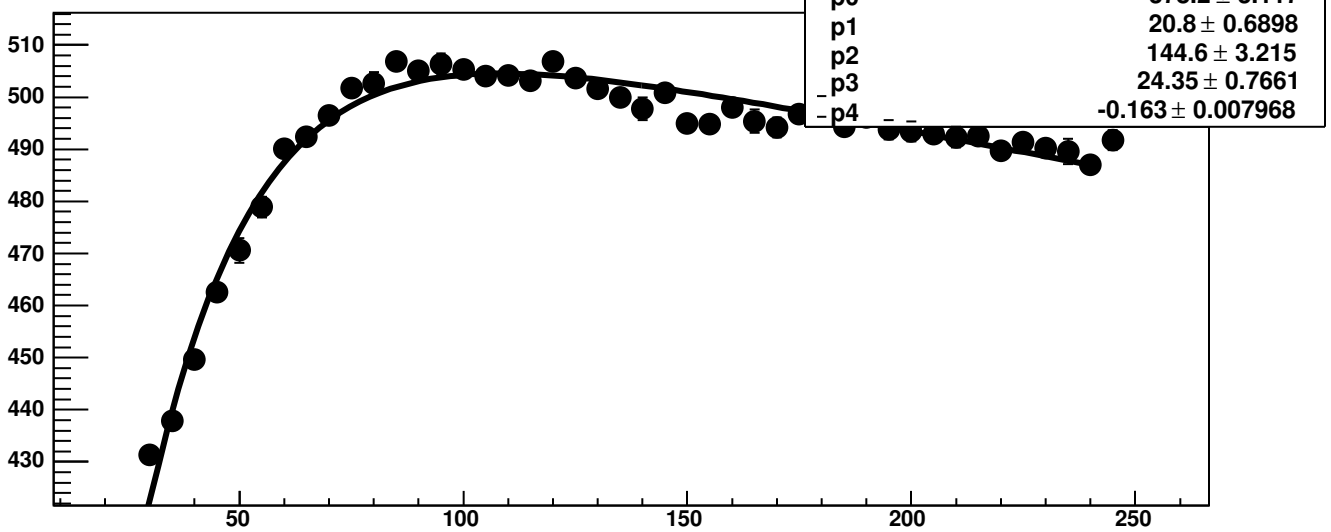
Chip 2, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



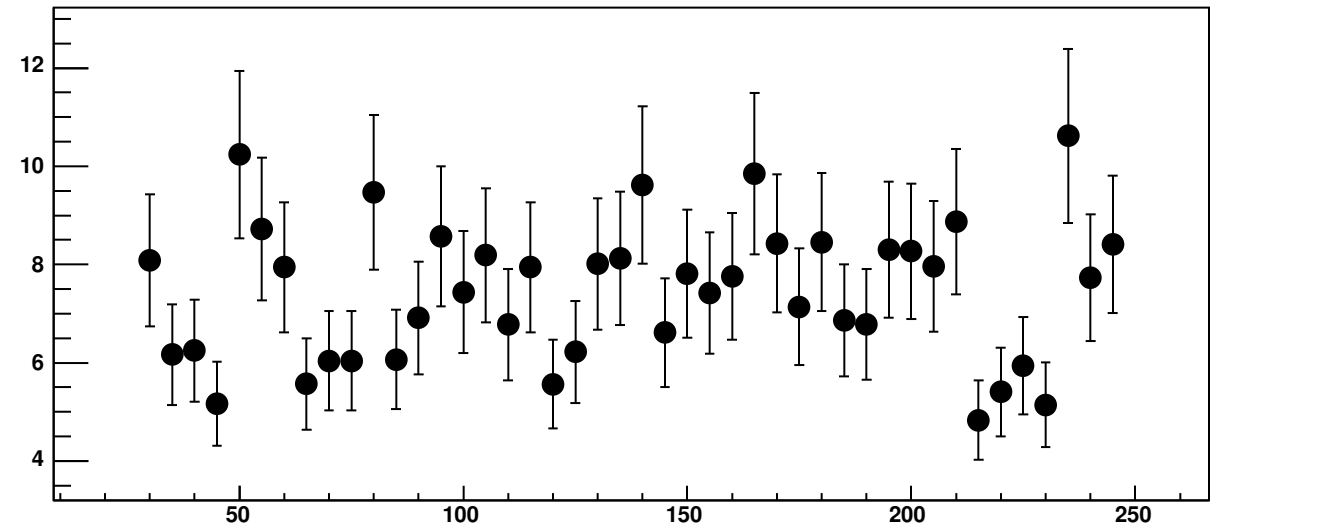
Chip 2, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold



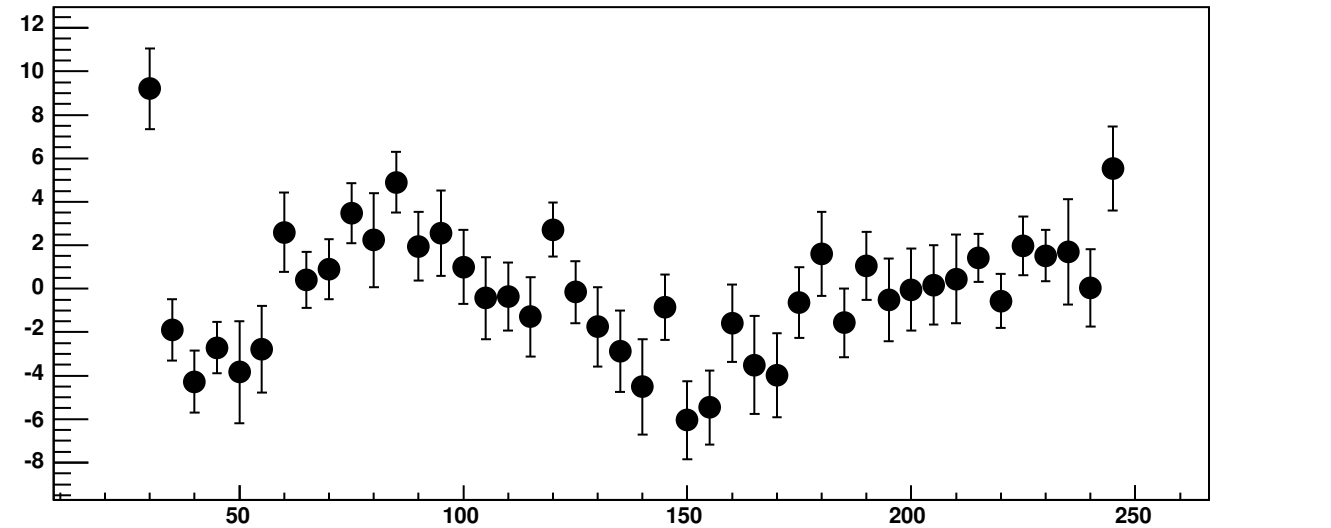
Chip 2, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold



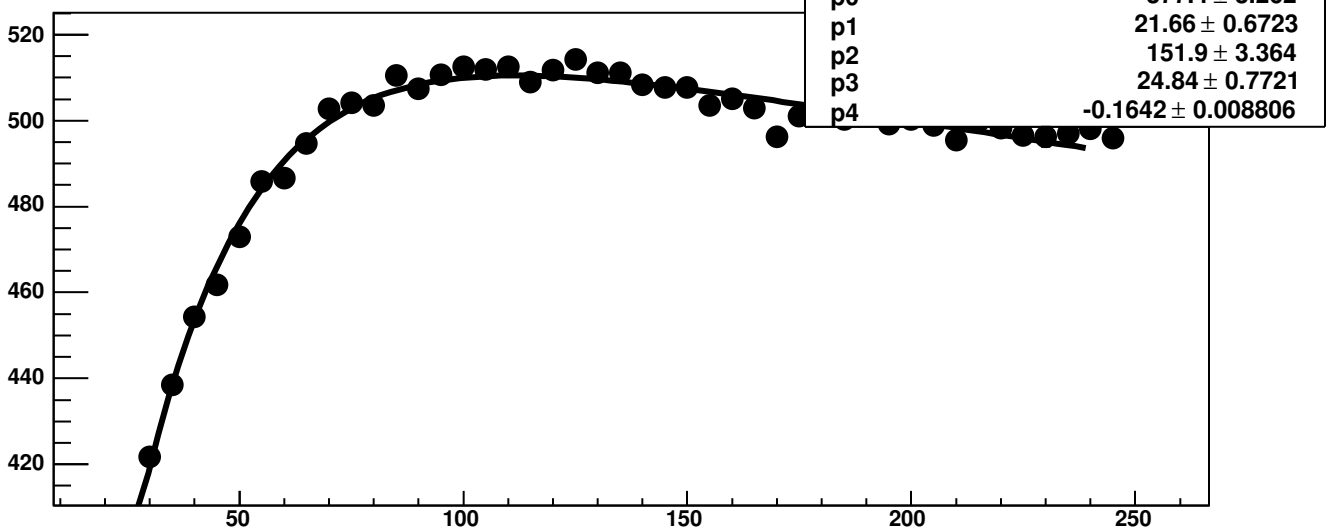
Chip 2, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



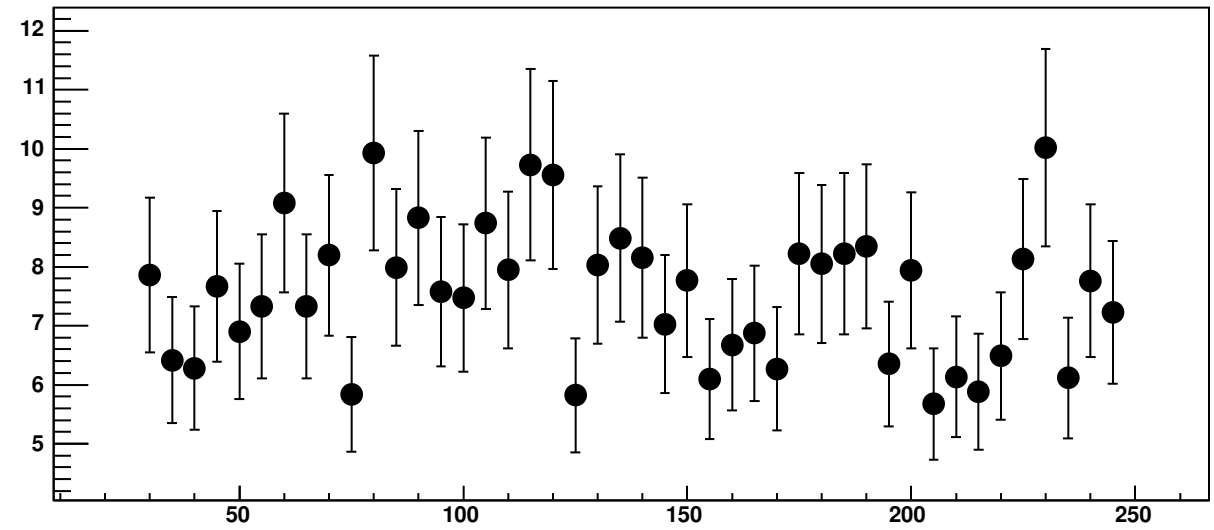
Chip 2, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold



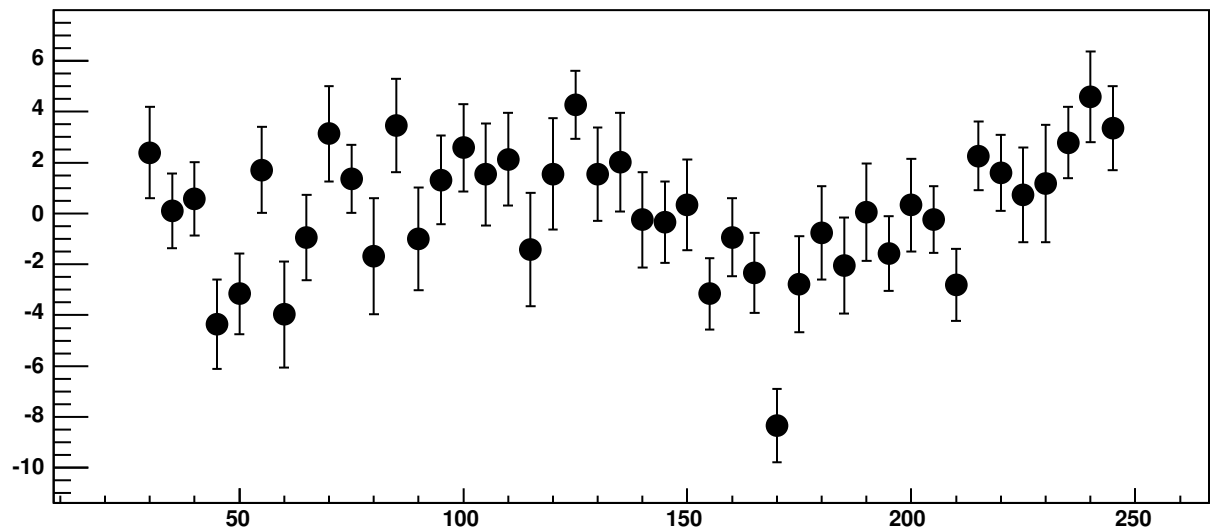
Chip 2, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold



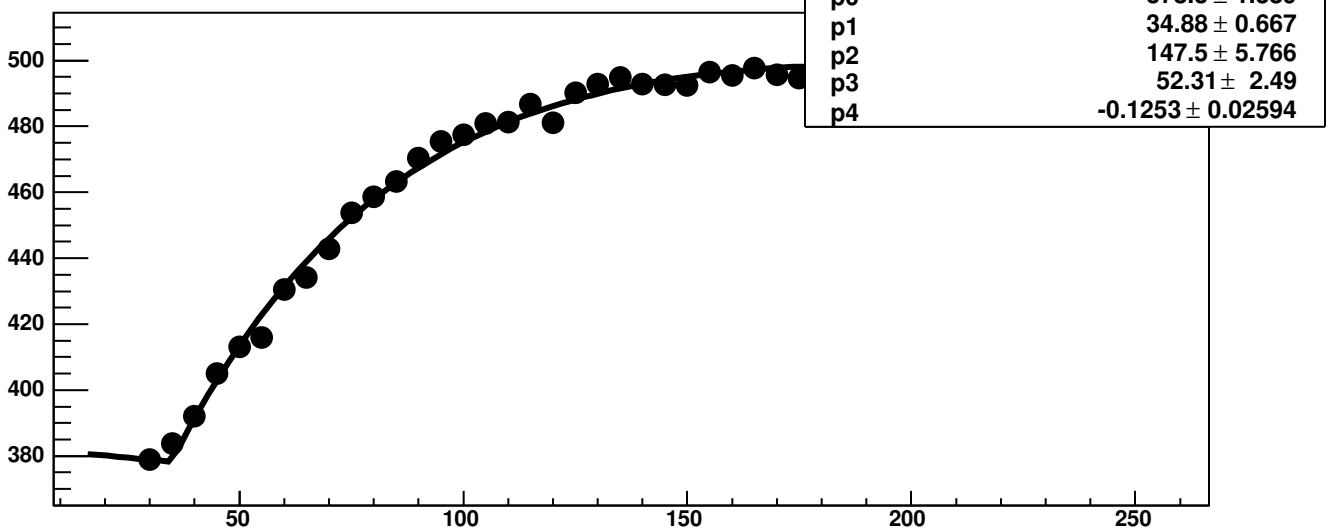
Chip 2, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



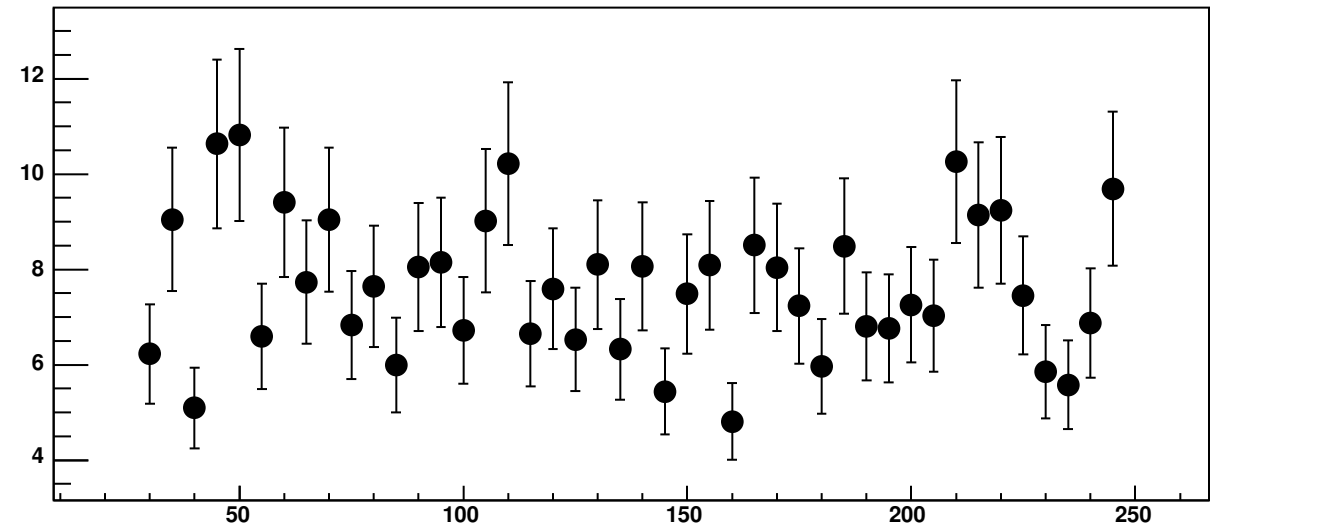
Chip 2, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold



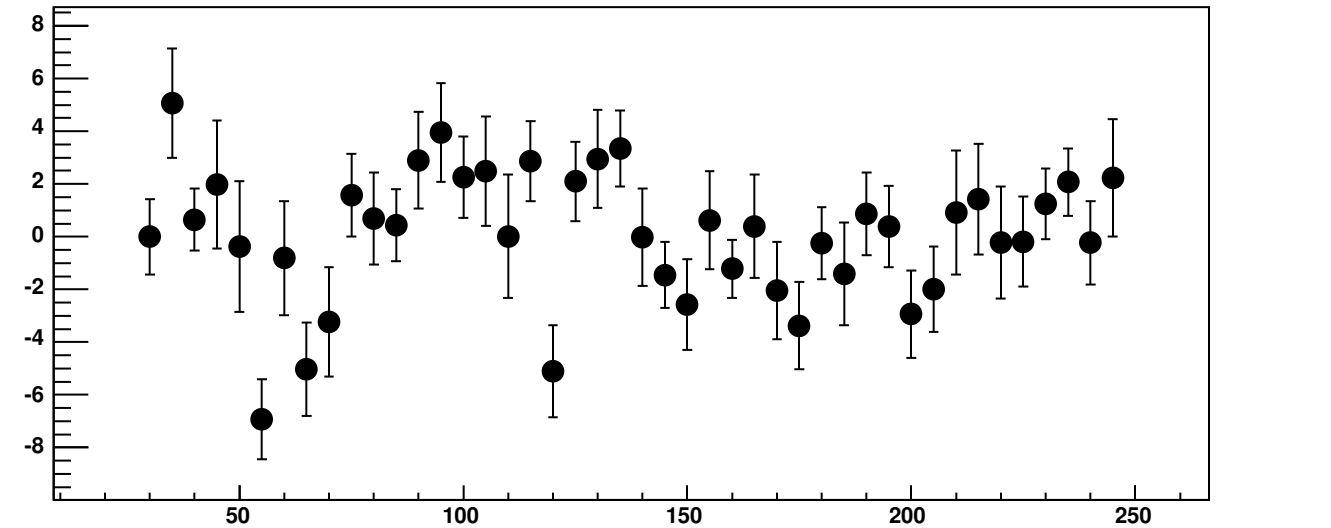
Chip 2, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold

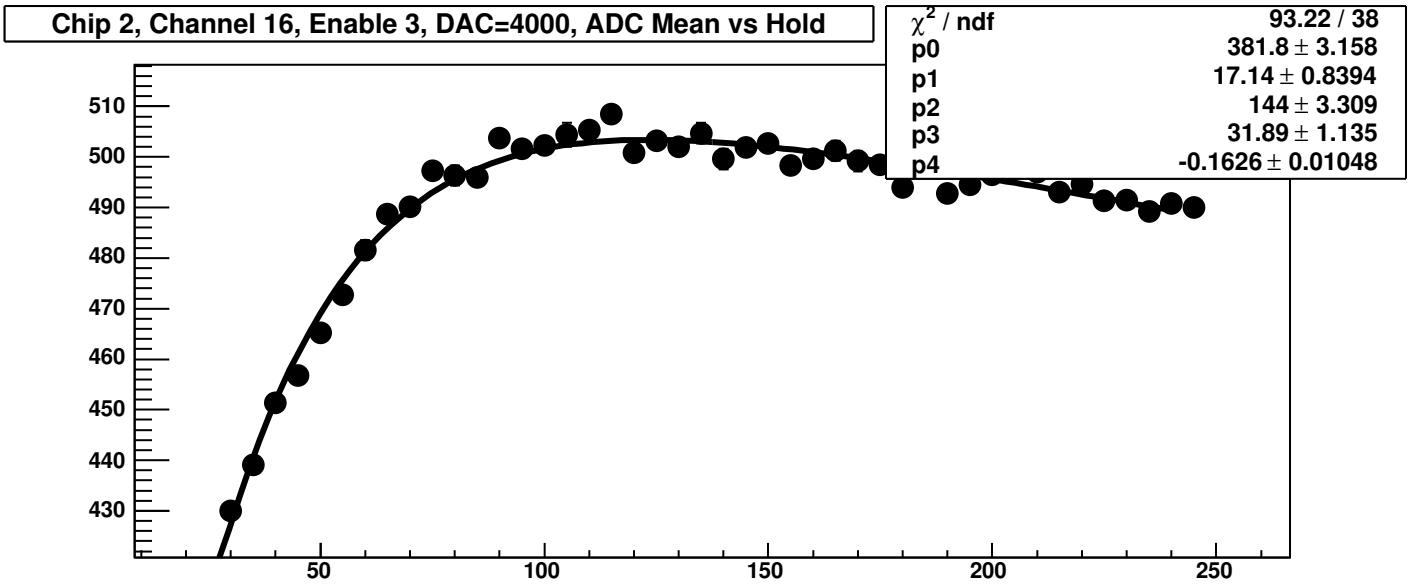


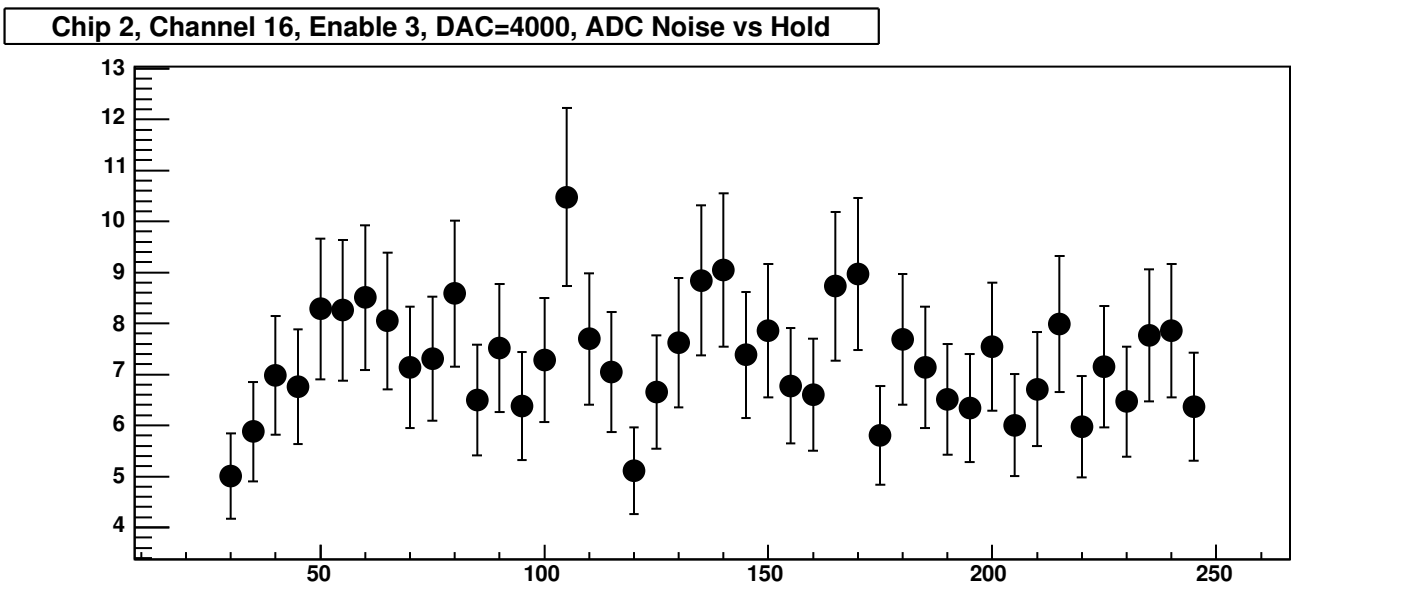
Chip 2, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold

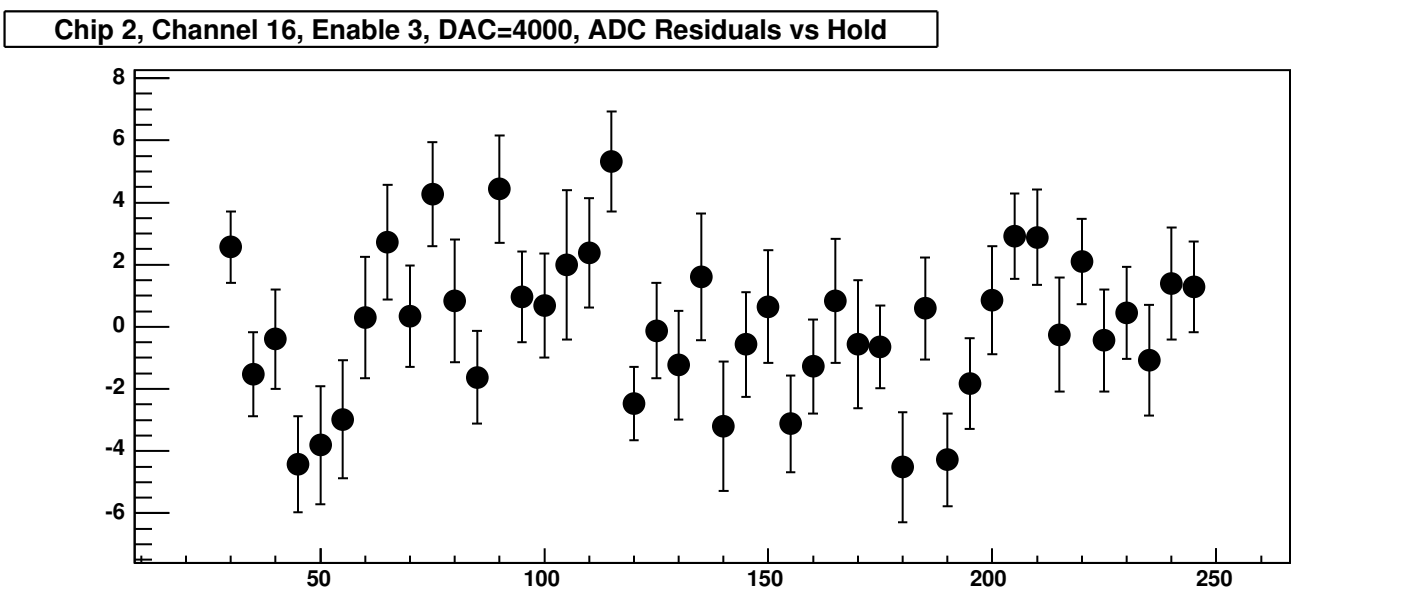


Chip 2, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold

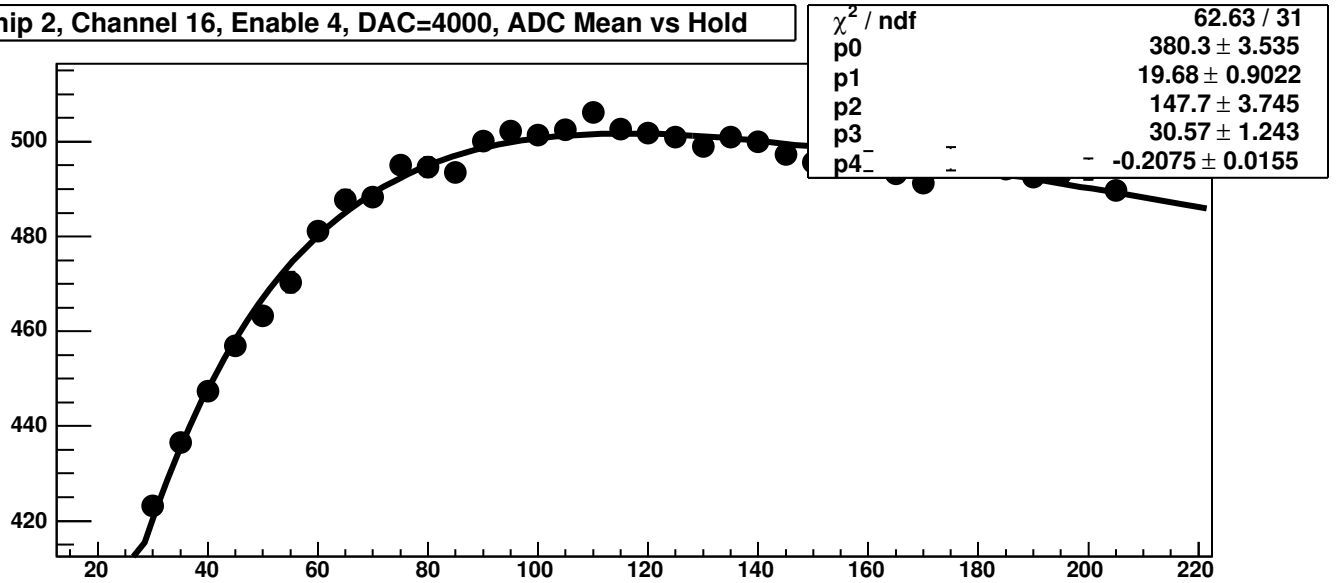



Chip 2, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 93.22 / 38
p0 381.8 ± 3.158
p1 17.14 ± 0.8394
p2 144 ± 3.309
p3 31.89 ± 1.135
p4 -0.1626 ± 0.01048
510
500
490
480
470
460
450
440
430
50
100
150
200
250

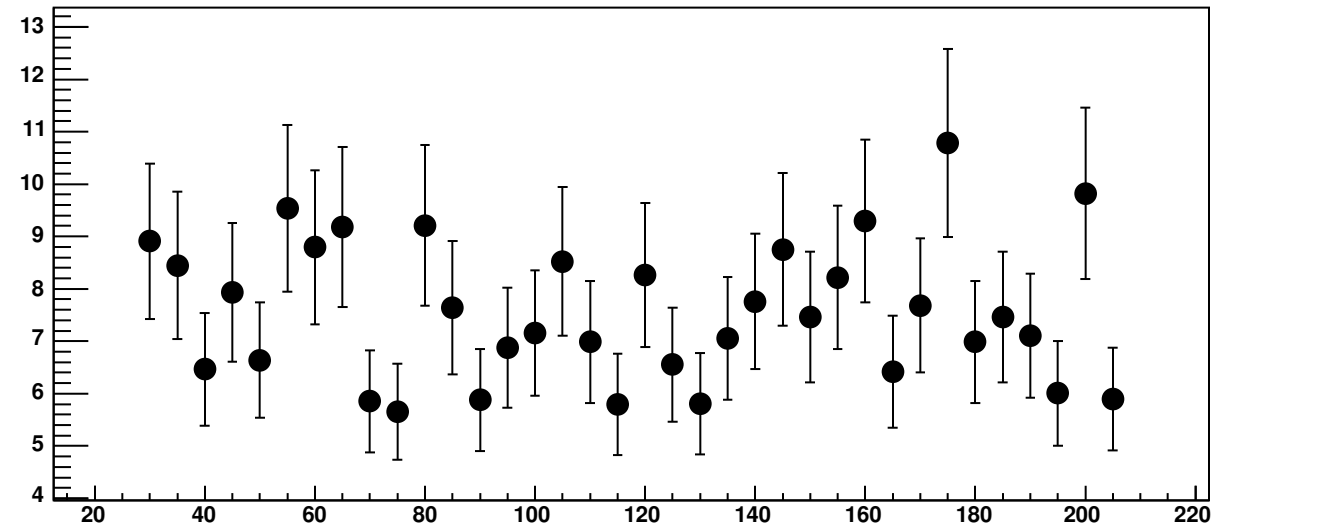

Chip 2, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 2, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
50
100
150
200
250

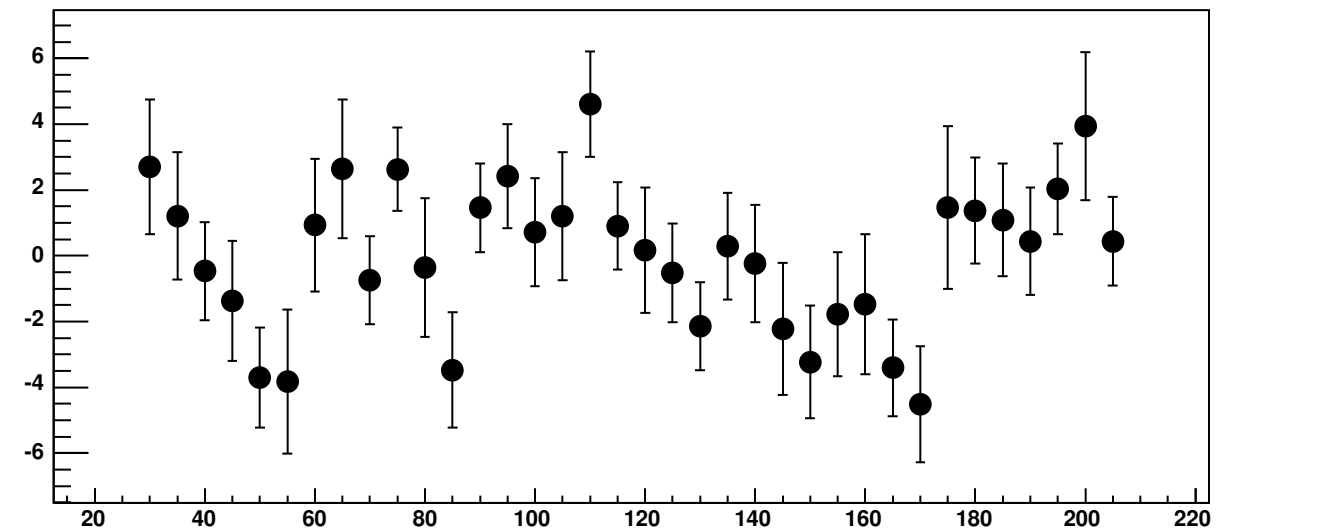
Chip 2, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold



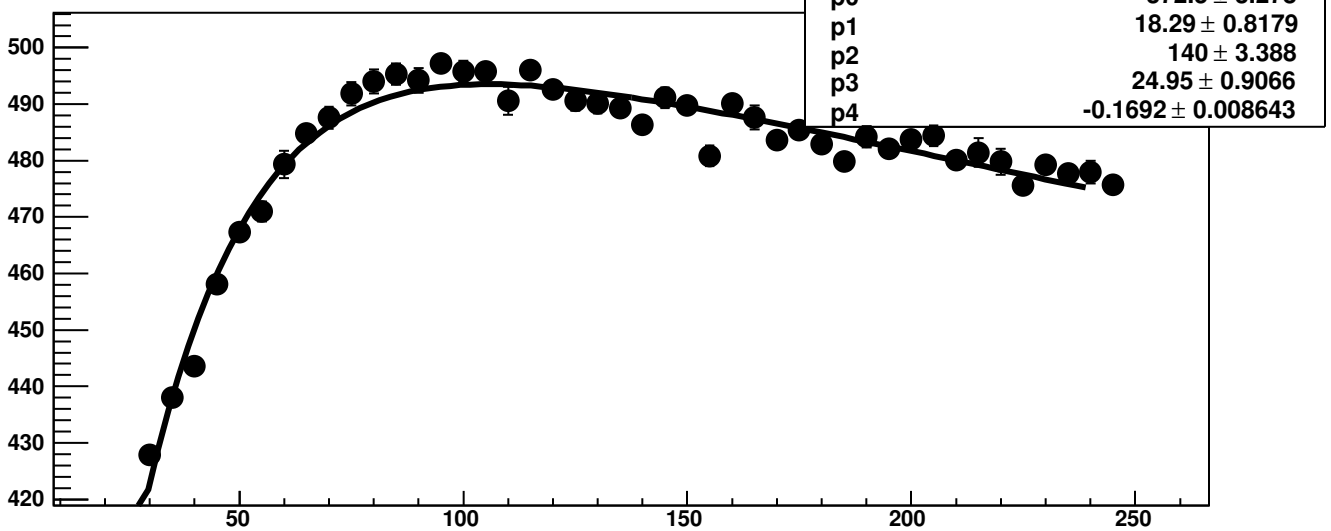
Chip 2, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



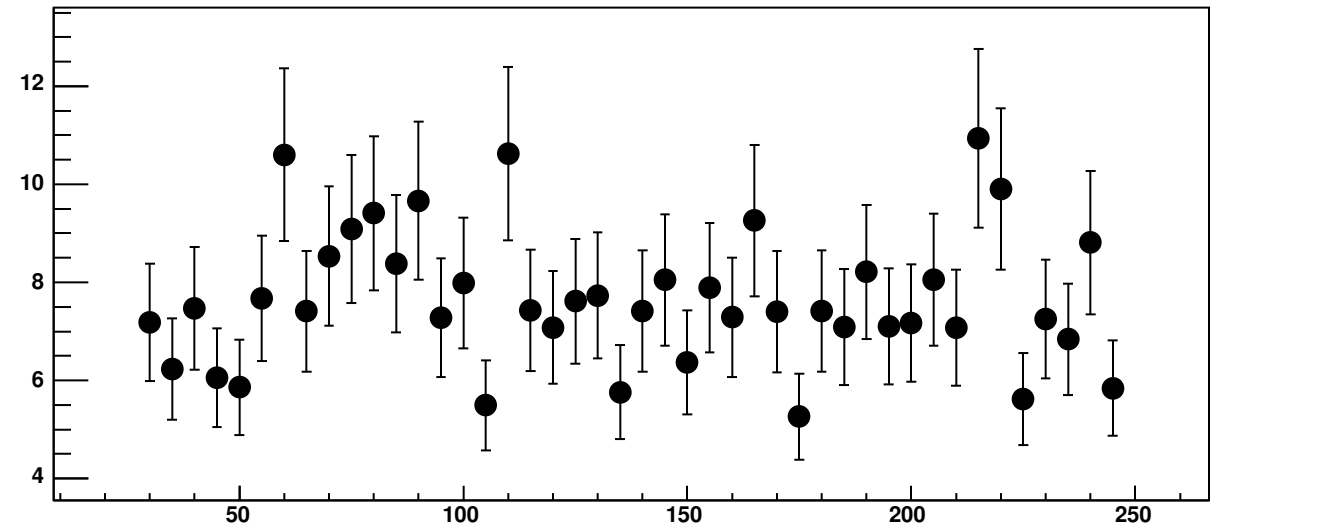
Chip 2, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold



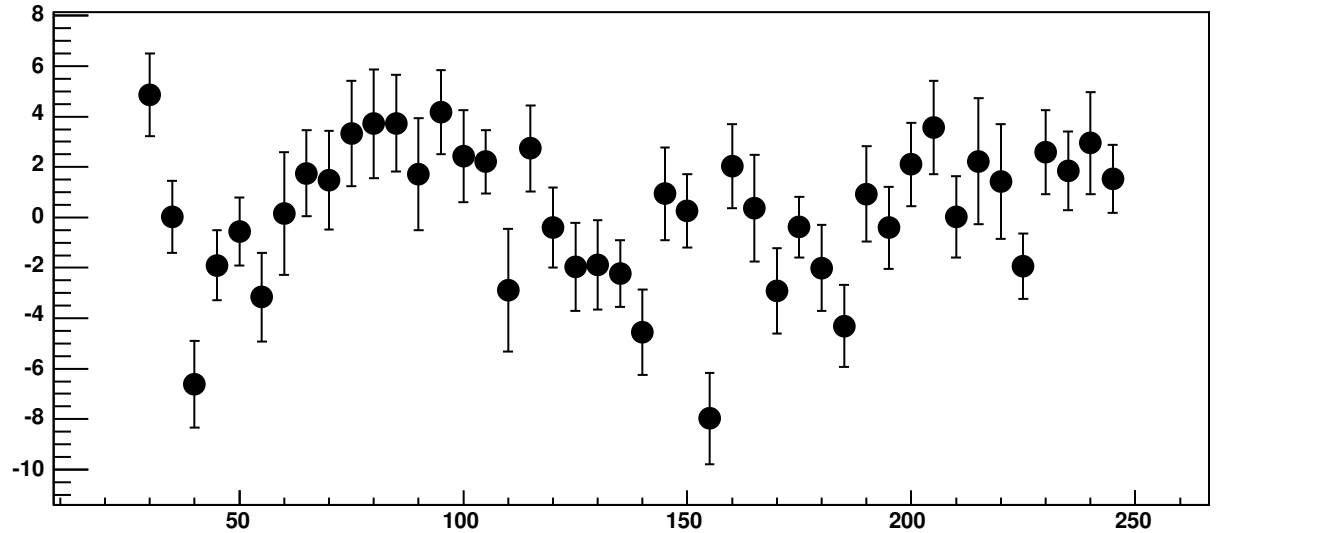
Chip 2, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold



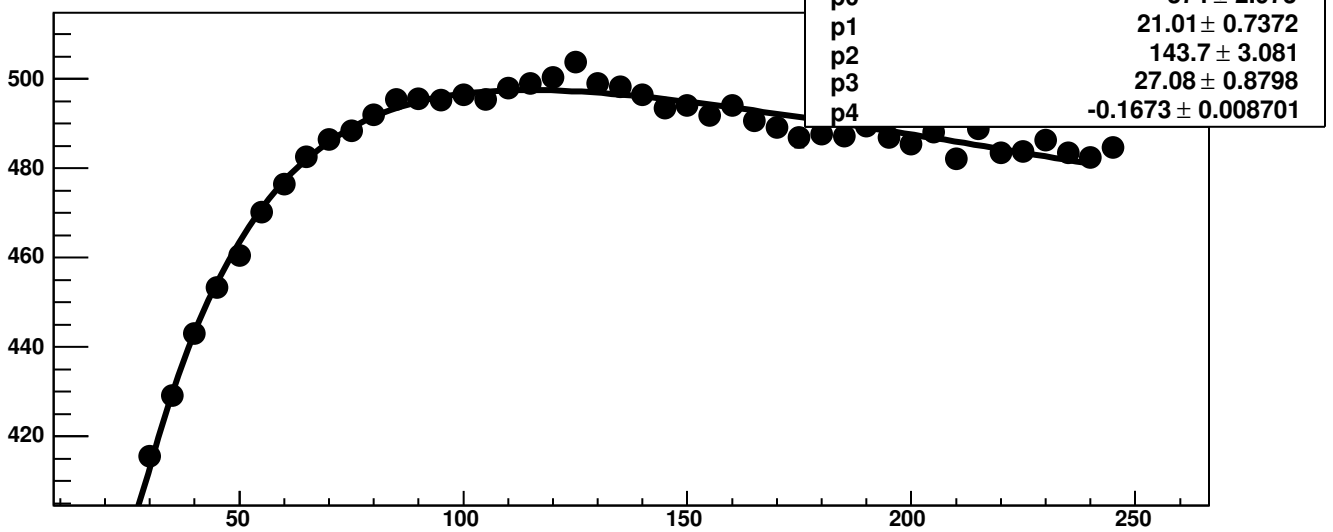
Chip 2, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



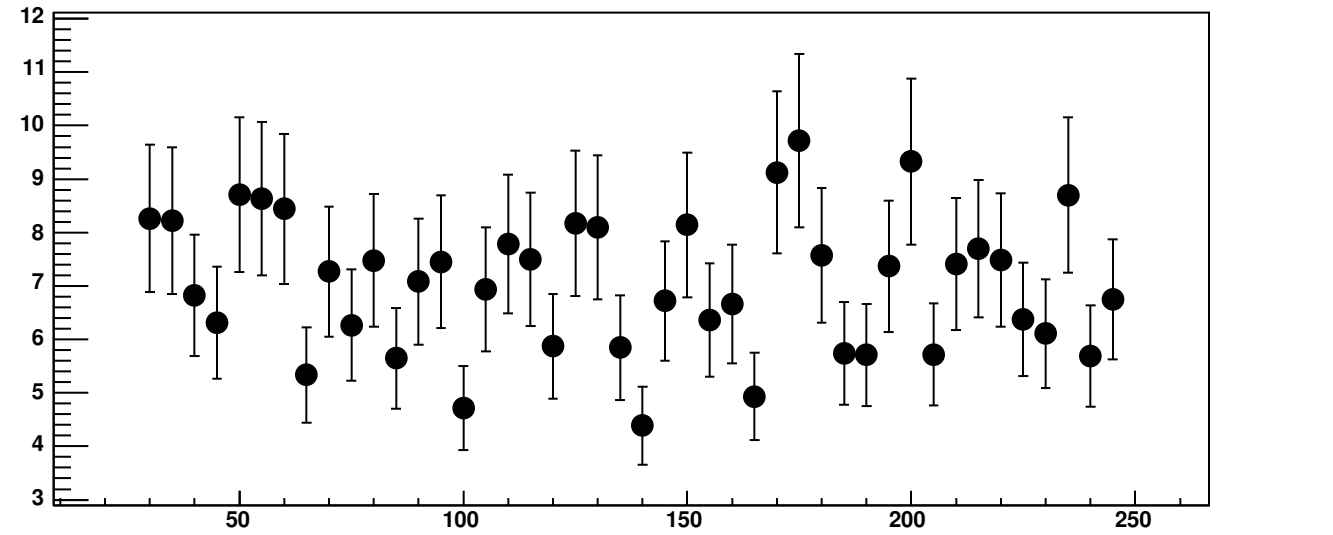
Chip 2, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold



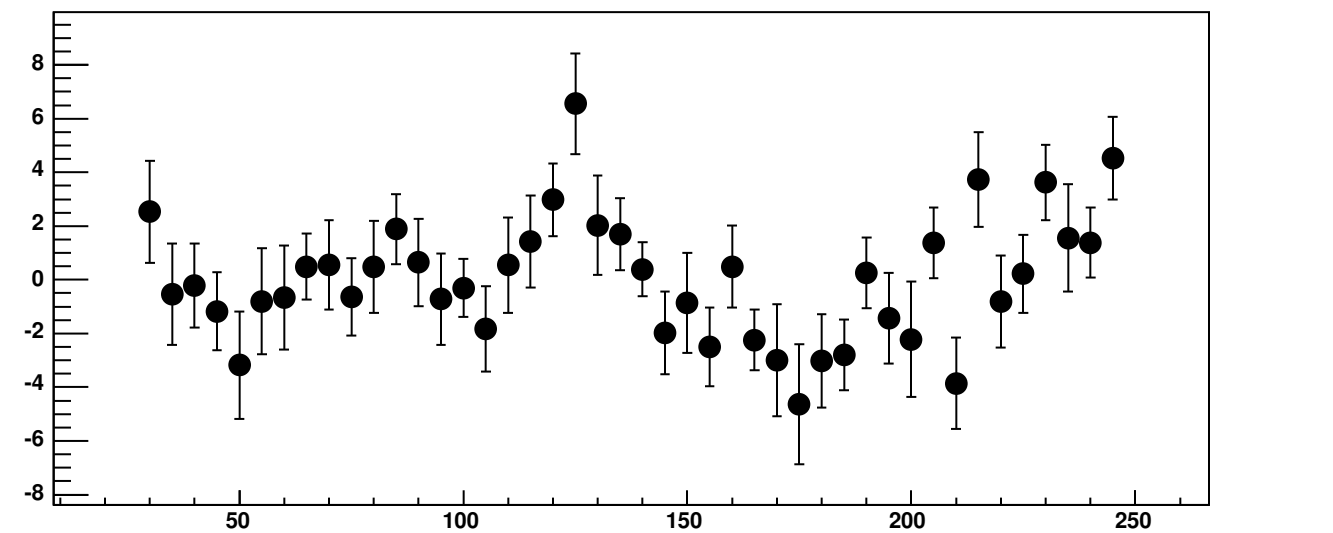
Chip 2, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

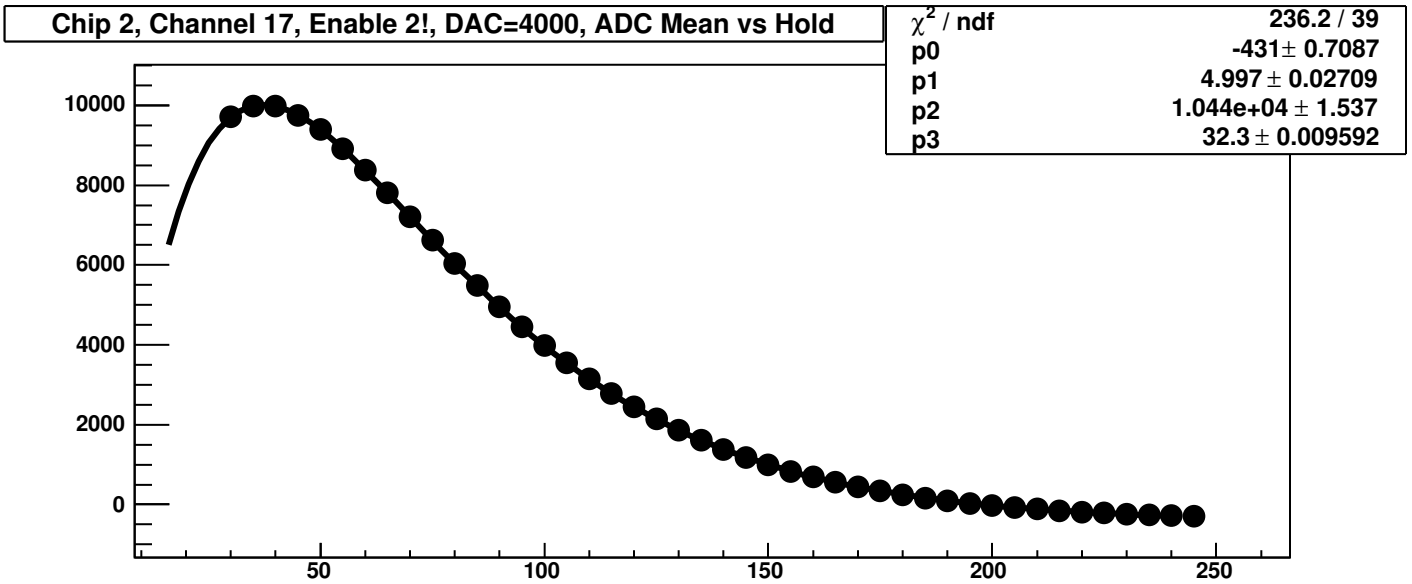


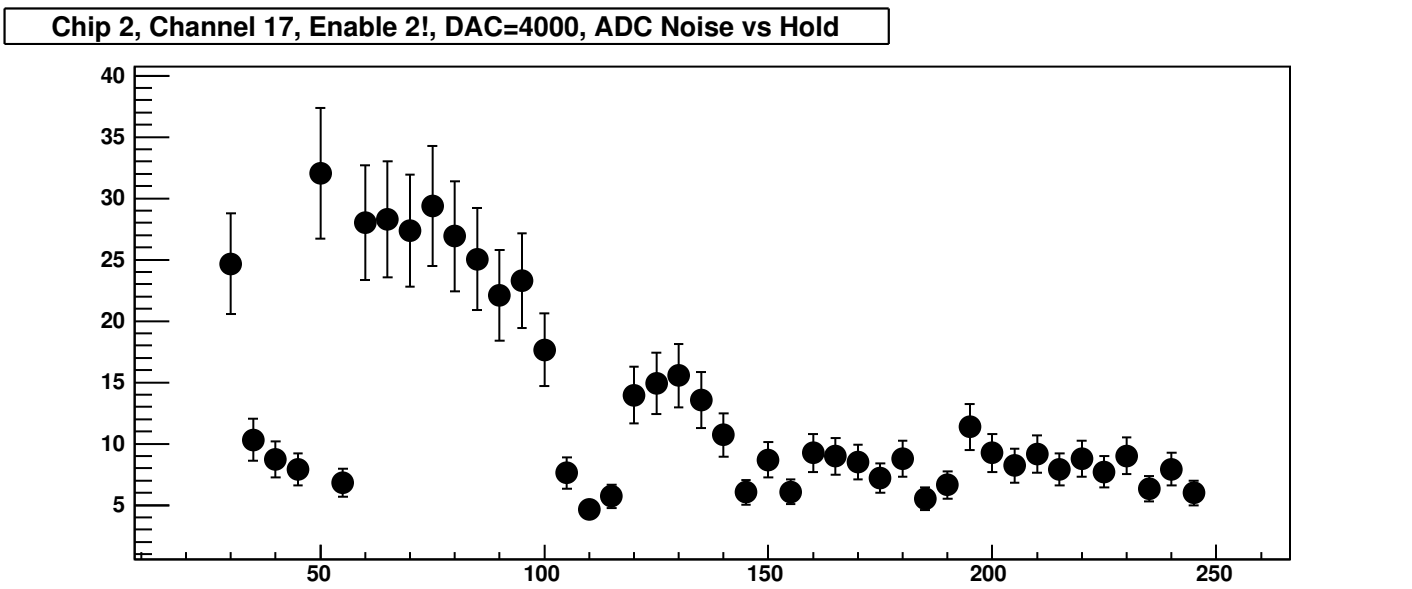
Chip 2, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold

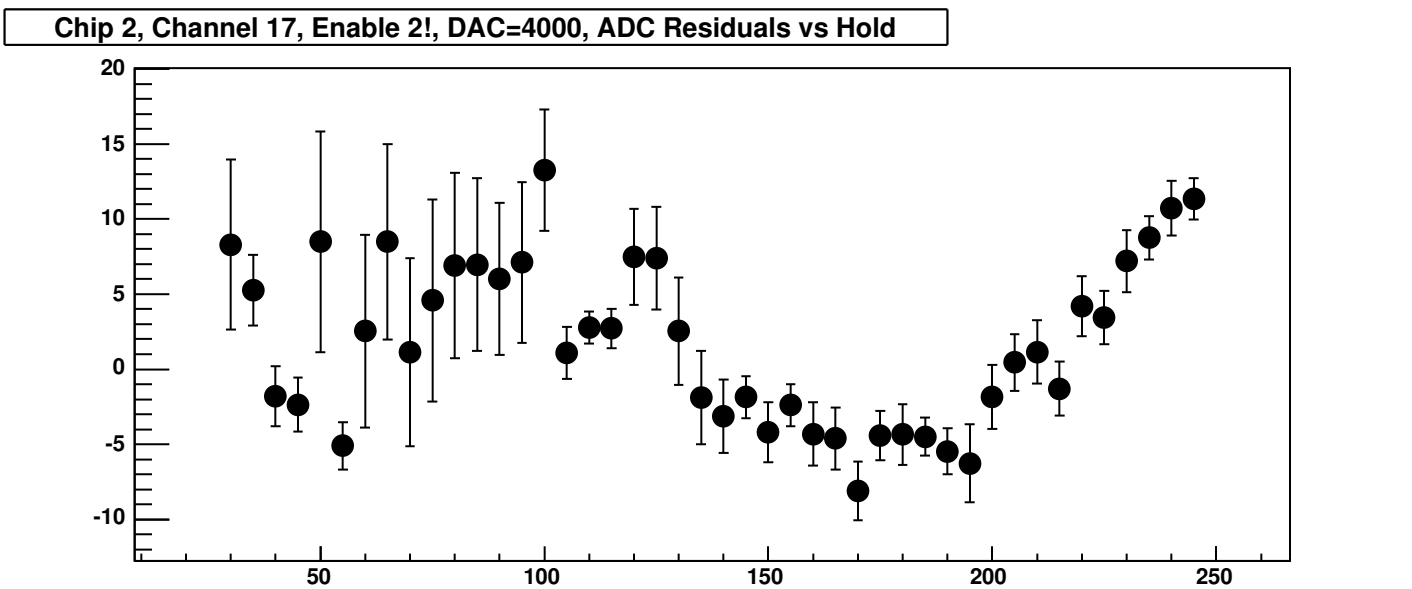


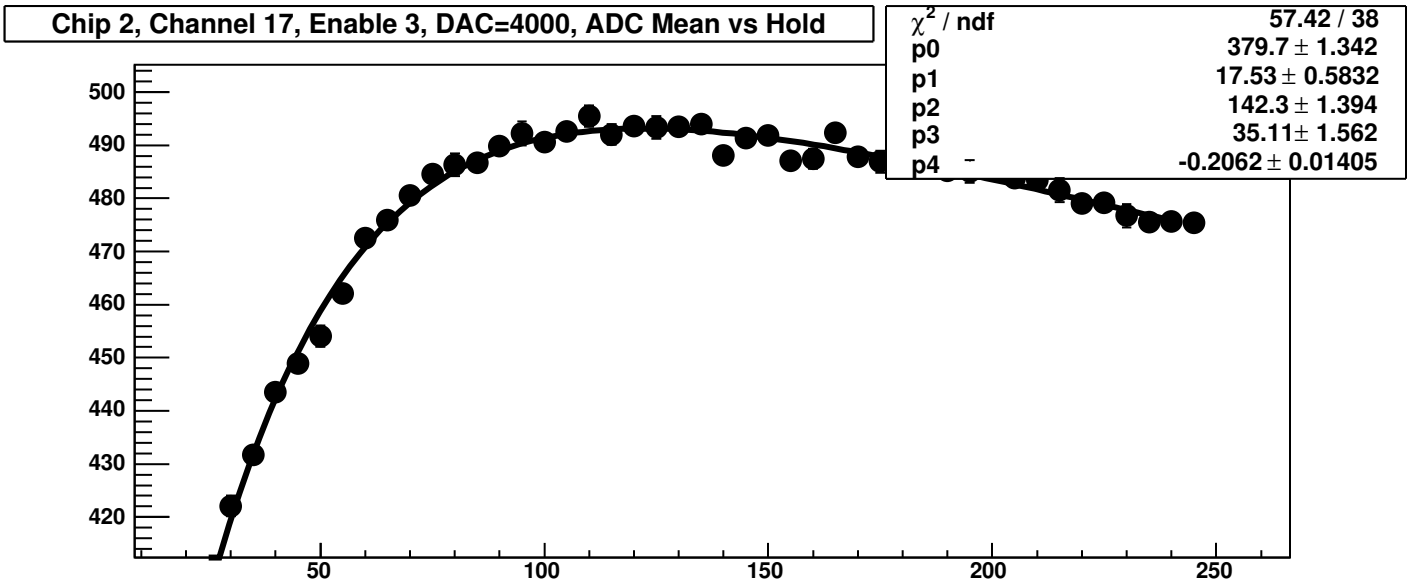
Chip 2, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

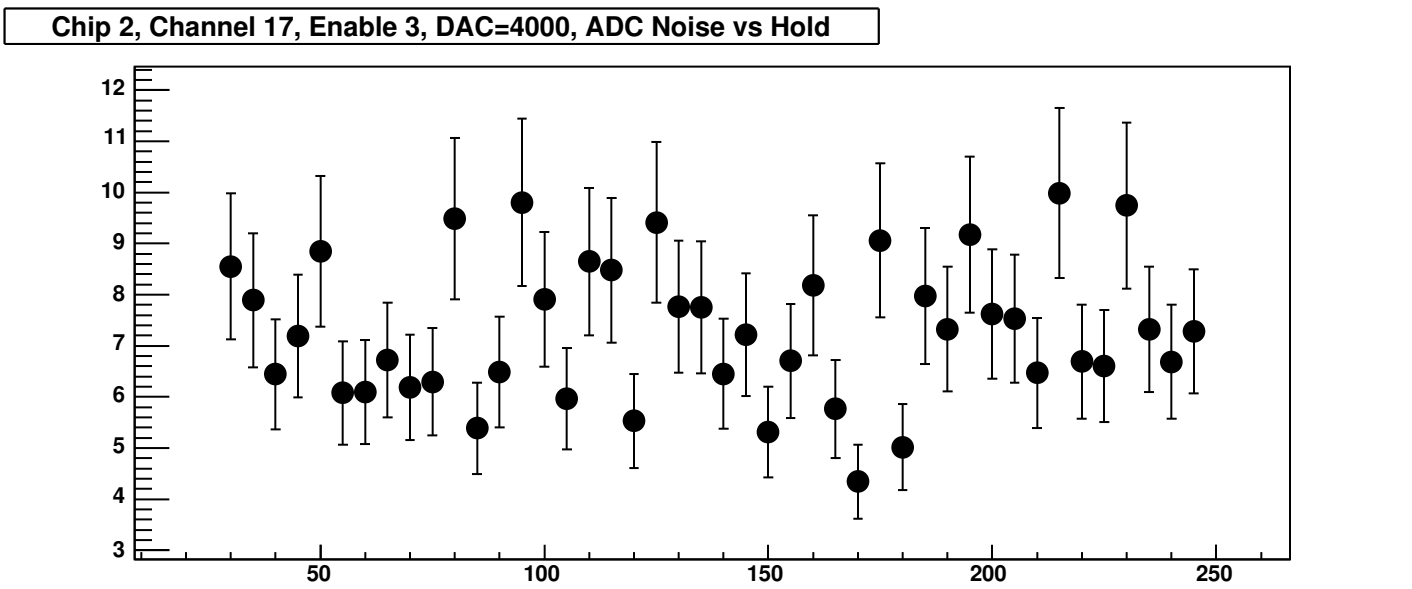


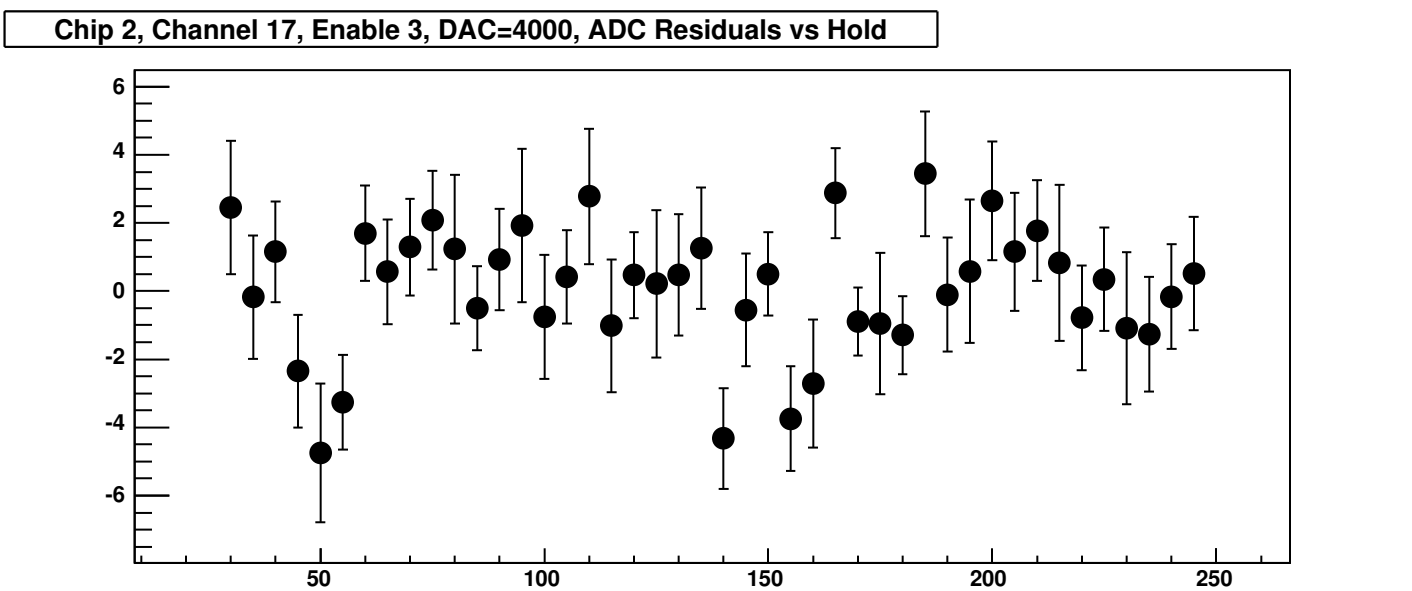

Chip 2, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 236.2 / 39
p0 -431 ± 0.7087
p1 4.997 ± 0.02709
p2 1.044e+04 ± 1.537
p3 32.3 ± 0.009592
10000
8000
6000
4000
2000
0
50
100
150
200
250

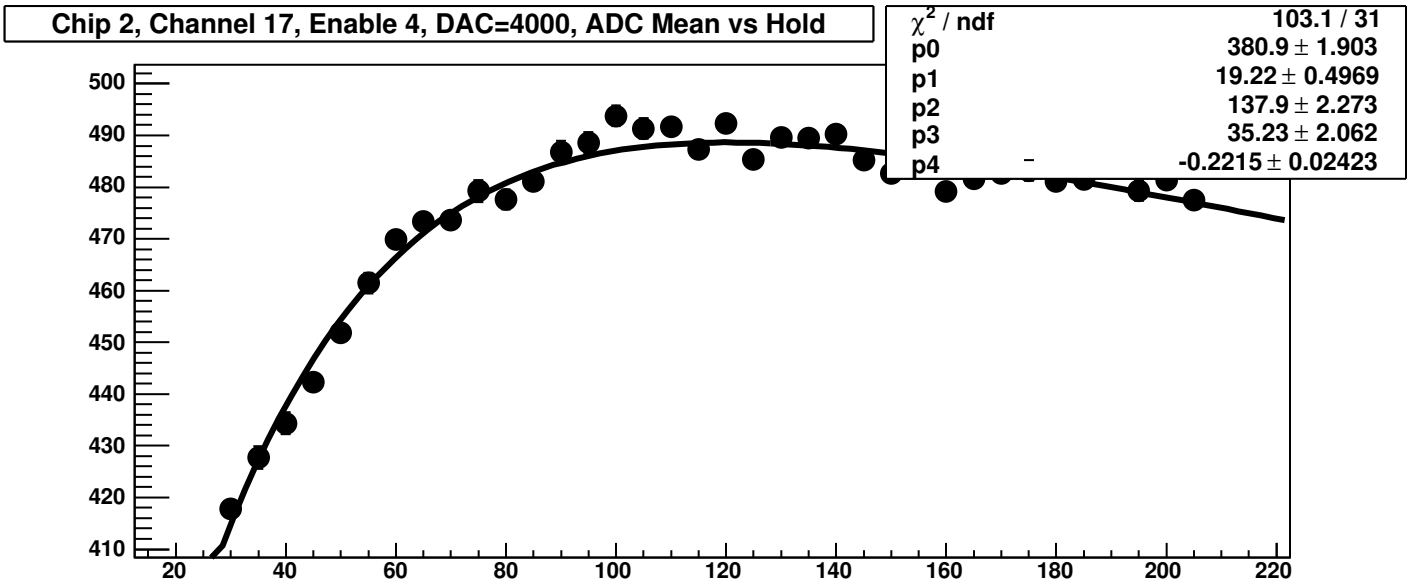

Chip 2, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold
40
35
30
25
20
15
10
5
50
100
150
200
250

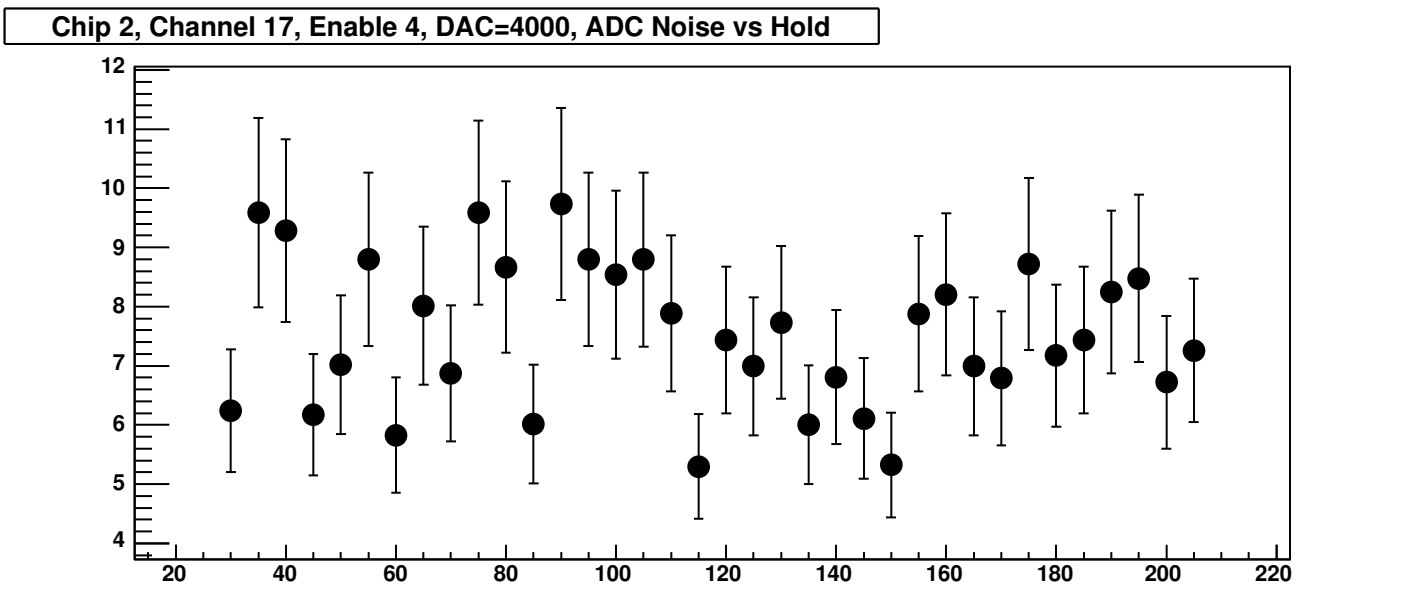

Chip 2, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
50
100
150
200
250

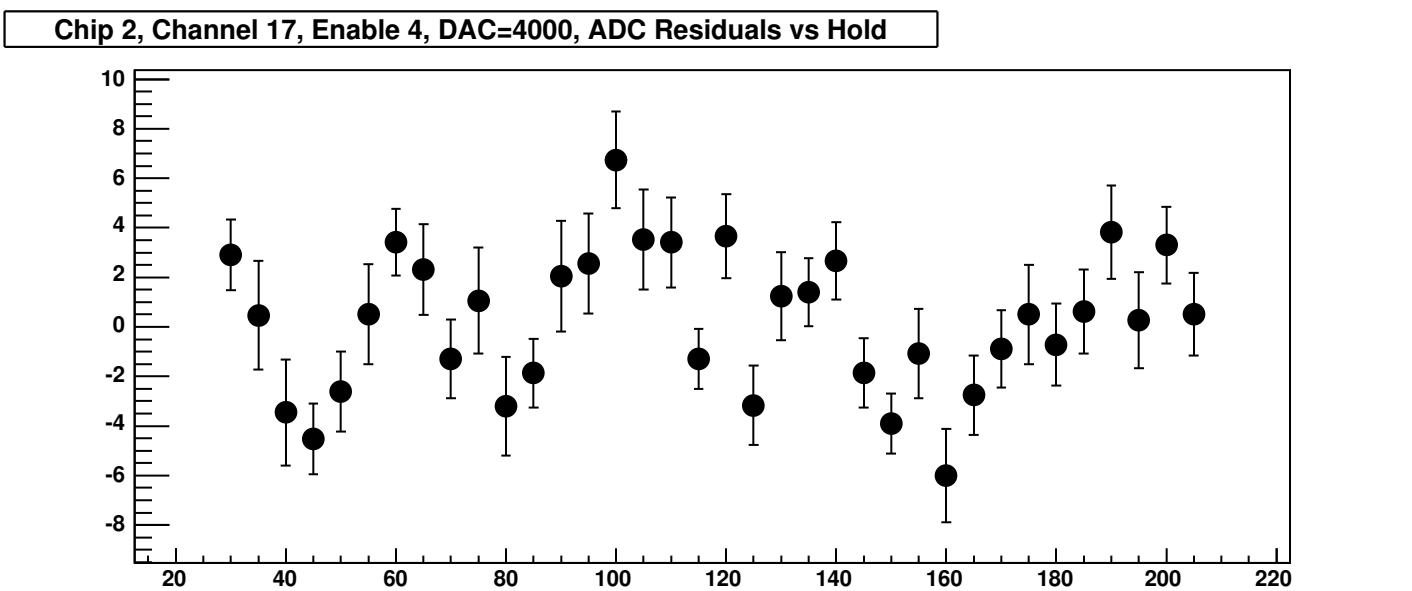

Chip 2, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 57.42 / 38
p0 379.7 ± 1.342
p1 17.53 ± 0.5832
p2 142.3 ± 1.394
p3 35.11 ± 1.562
p4 -0.2062 ± 0.01405
500
490
480
470
460
450
440
430
420
50
100
150
200
250

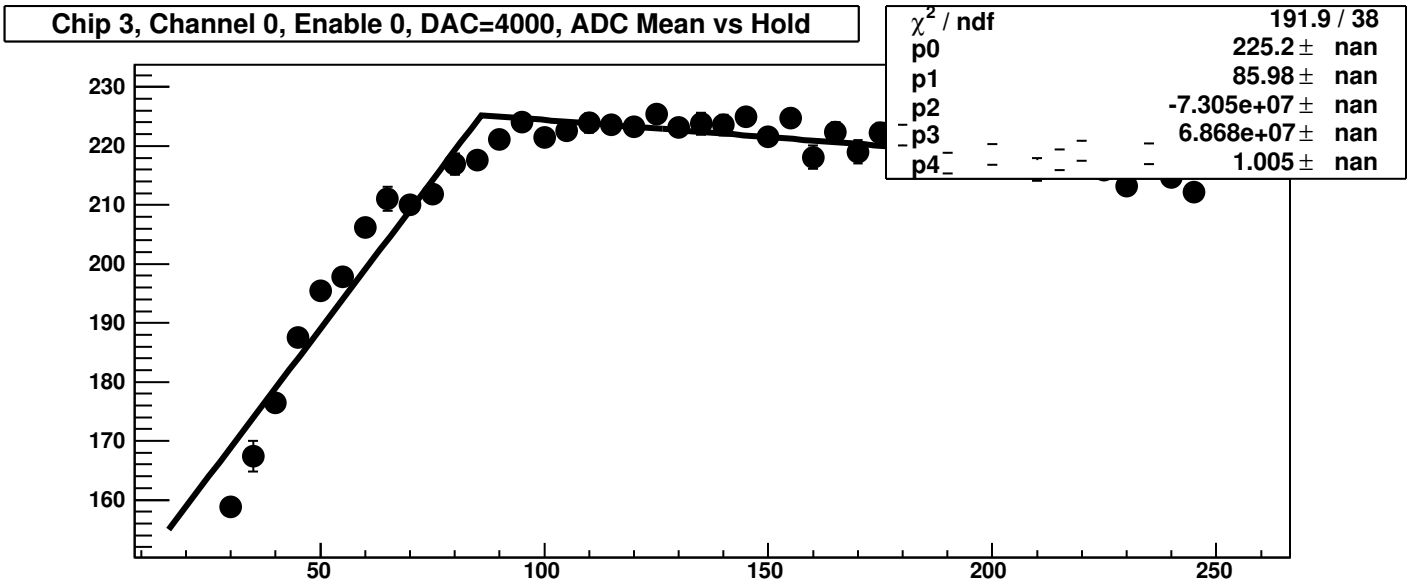

Chip 2, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250

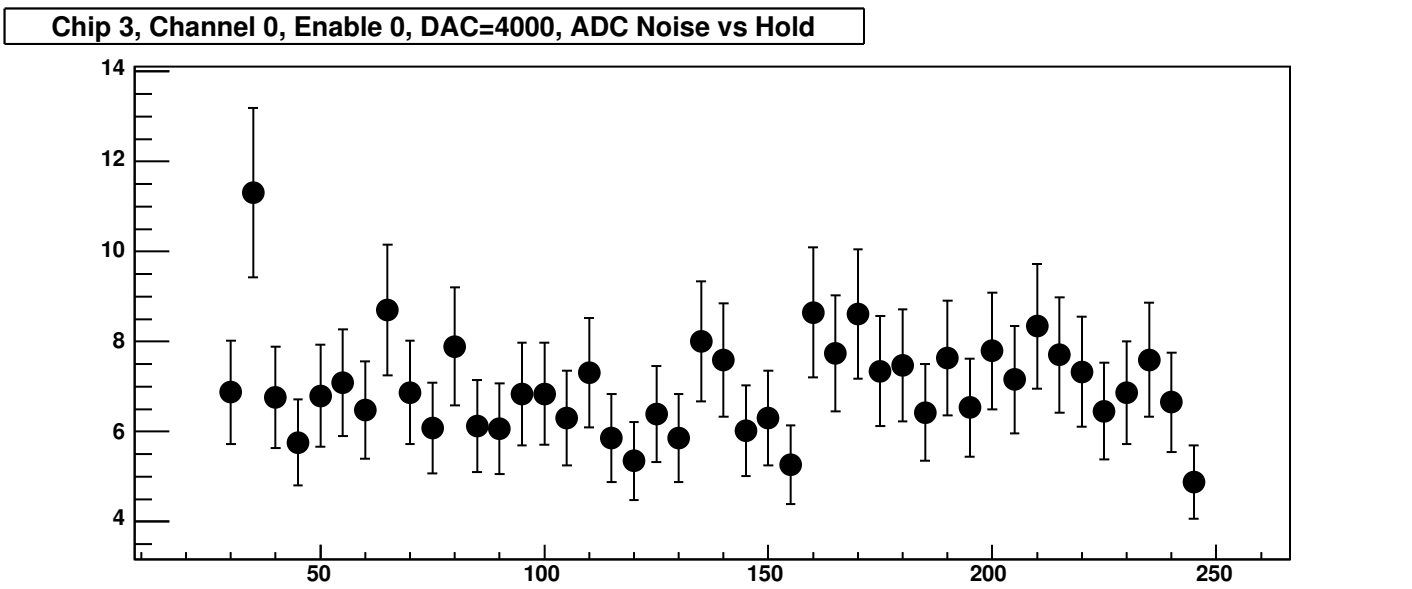

Chip 2, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
50
100
150
200
250

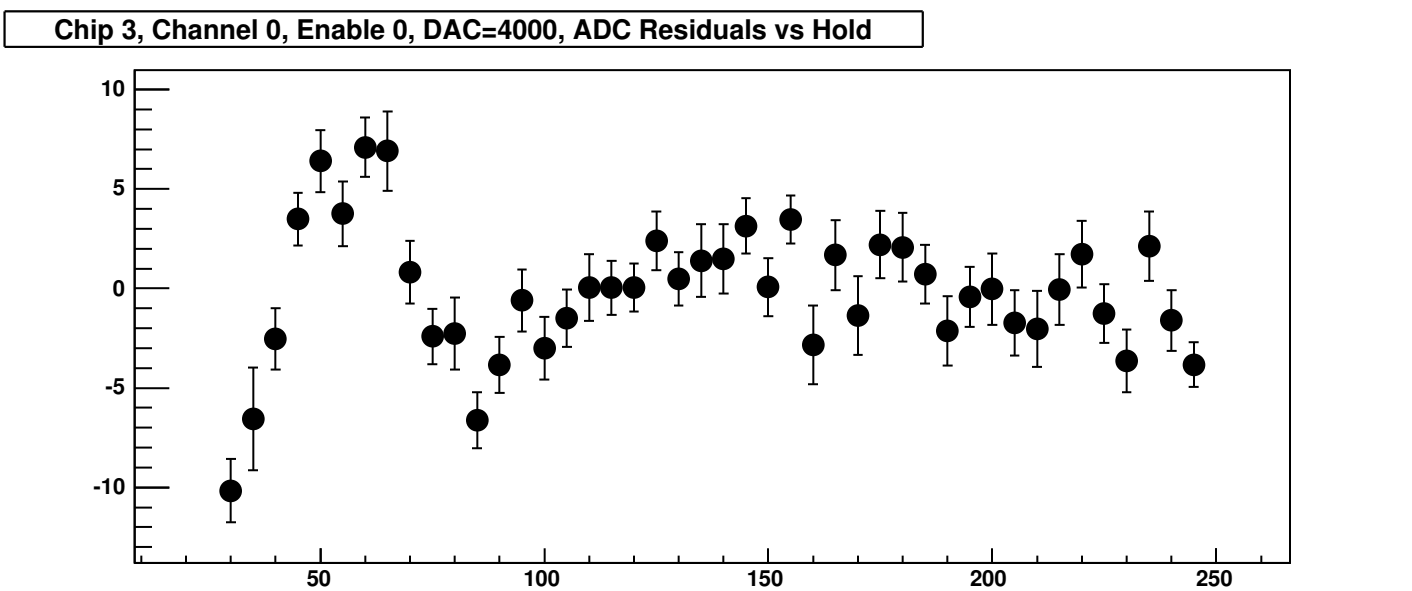

Chip 2, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 103.1 / 31
p0 380.9 ± 1.903
p1 19.22 ± 0.4969
p2 137.9 ± 2.273
p3 35.23 ± 2.062
p4 -0.2215 ± 0.02423
500
490
480
470
460
450
440
430
420
410
20
40
60
80
100
120
140
160
180
200
220

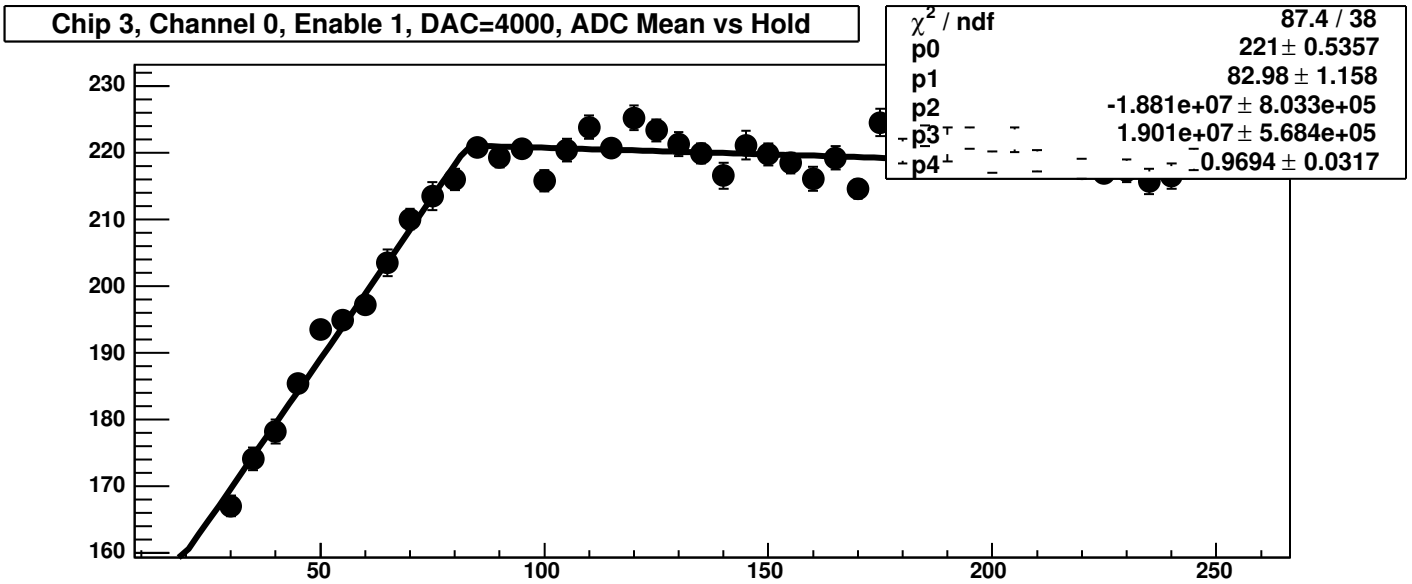

Chip 2, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
20
40
60
80
100
120
140
160
180
200
220

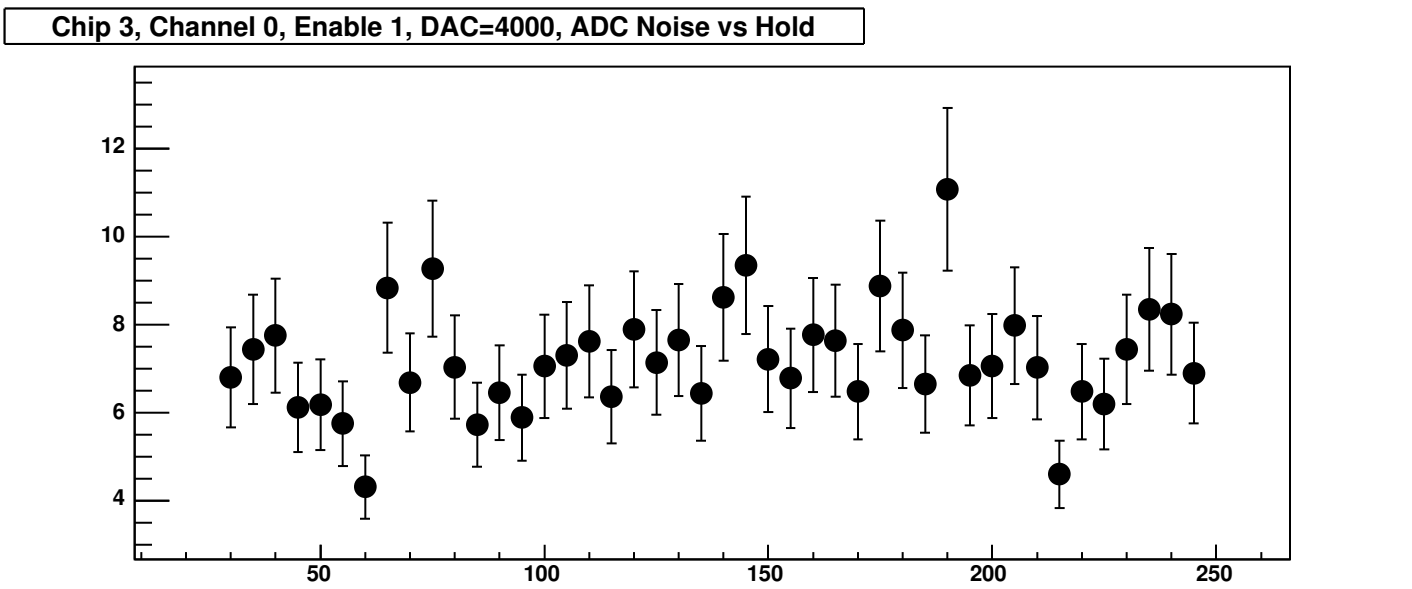

Chip 2, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
-8
20
40
60
80
100
120
140
160
180
200
220

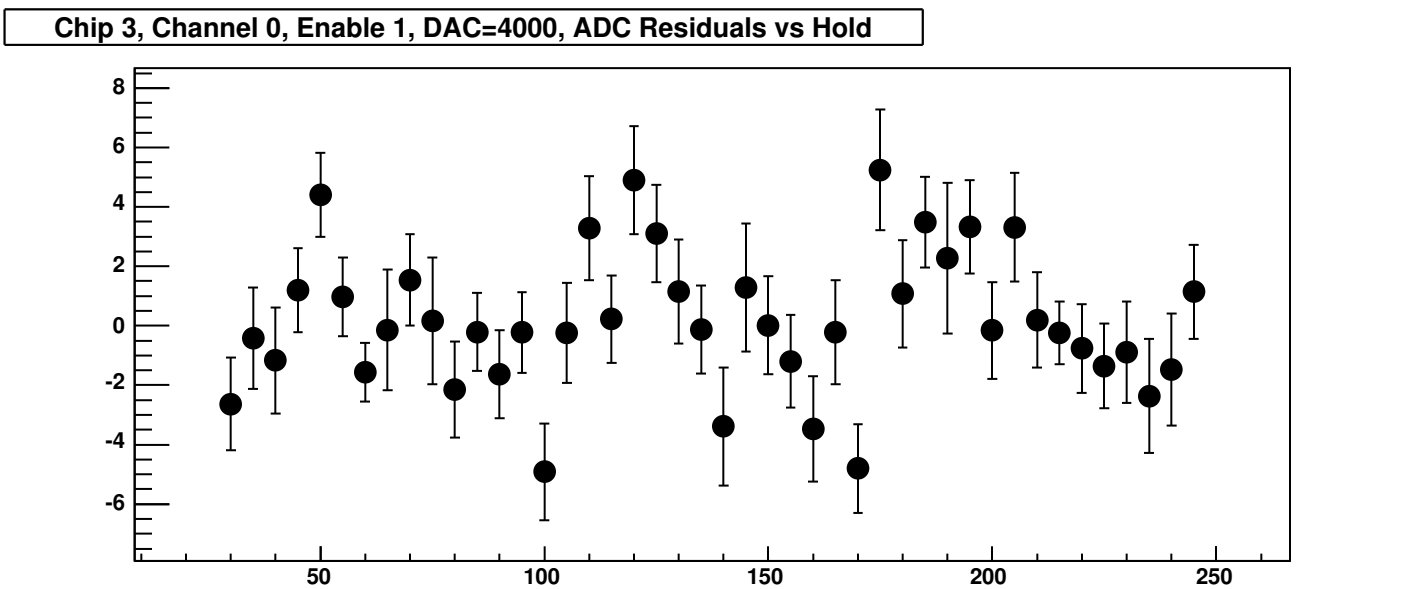

Chip 3, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 191.9 / 38
p0 225.2 ± nan
p1 85.98 ± nan
p2 -7.305e+07 ± nan
p3 6.868e+07 ± nan
p4 1.005 ± nan
230
220
210
200
190
180
170
160
50
100
150
200
250

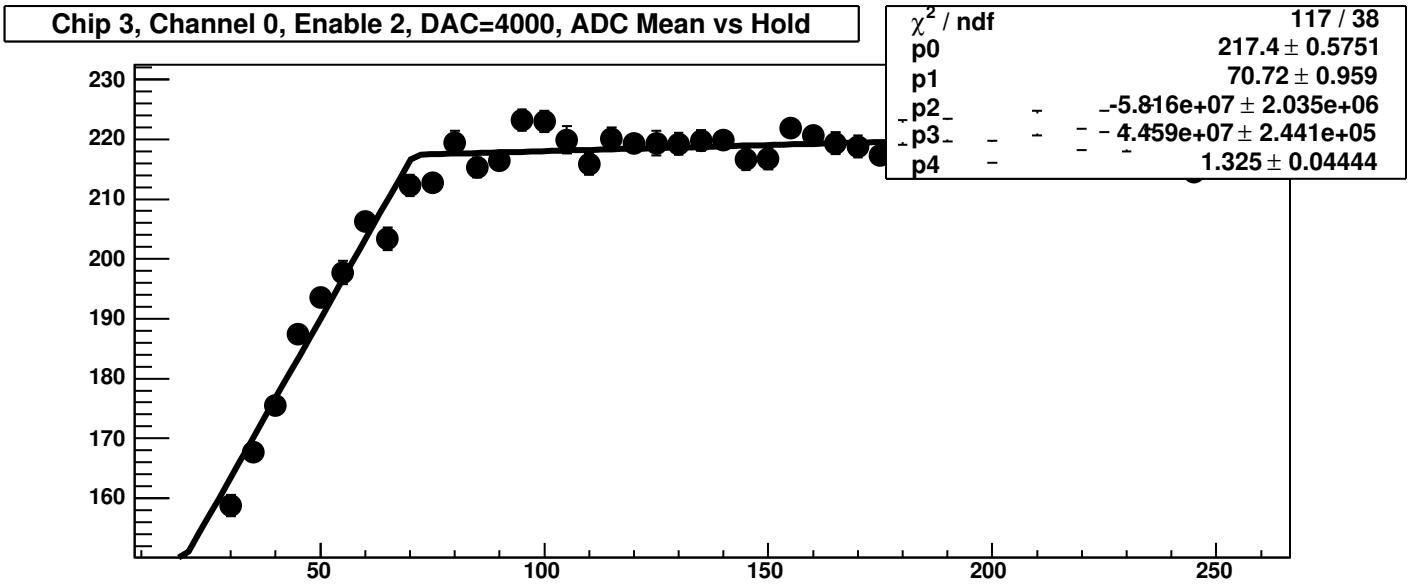

Chip 3, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250

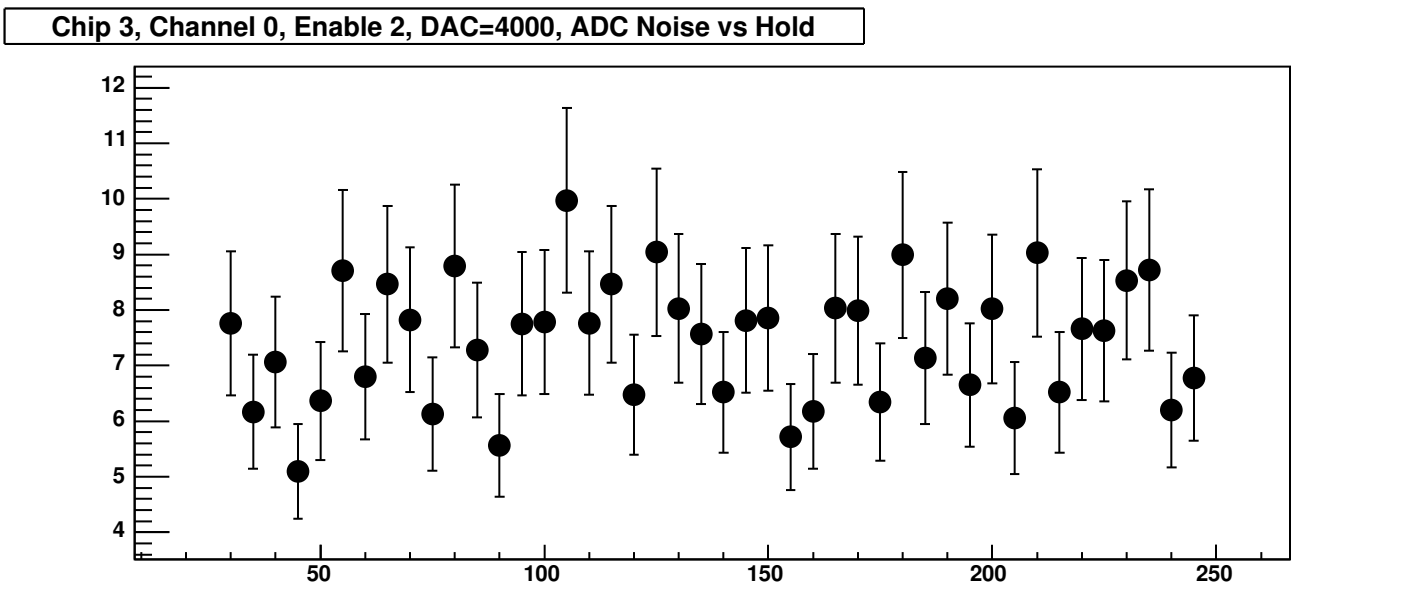

Chip 3, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

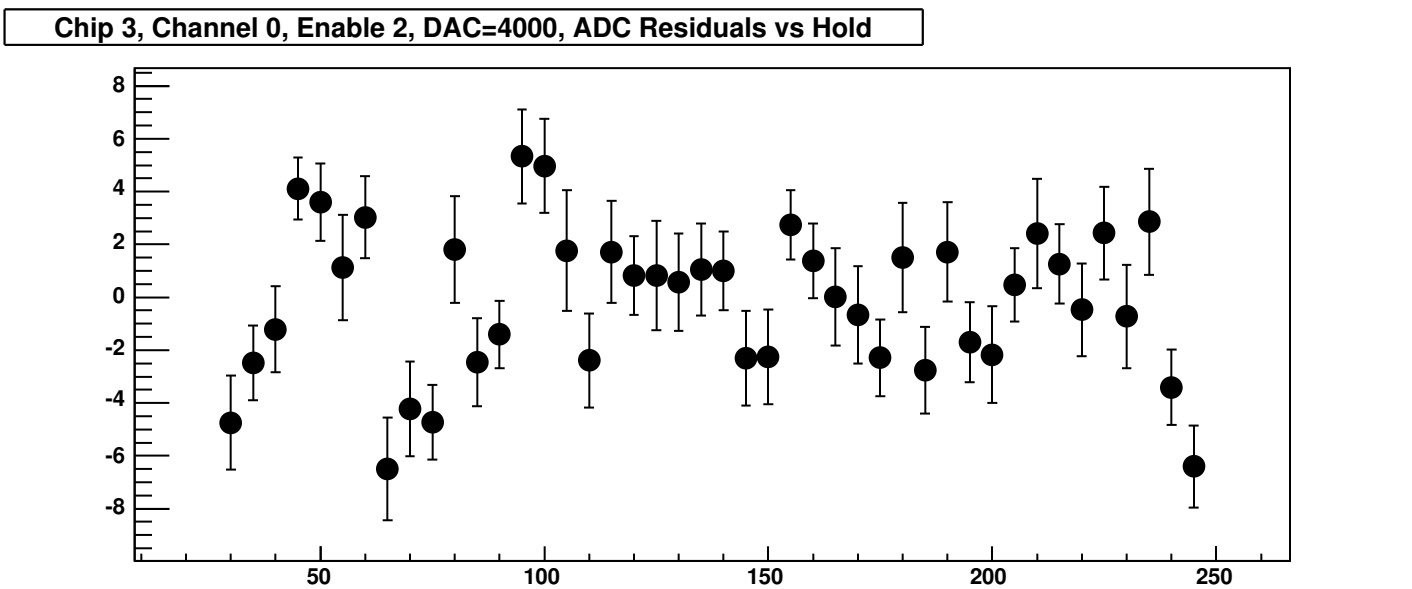

Chip 3, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 87.4 / 38
p0 221 ± 0.5357
p1 82.98 ± 1.158
p2 -1.881e+07 ± 8.033e+05
p3 1.901e+07 ± 5.684e+05
p4 0.9694 ± 0.0317
230
220
210
200
190
180
170
160
50
100
150
200
250

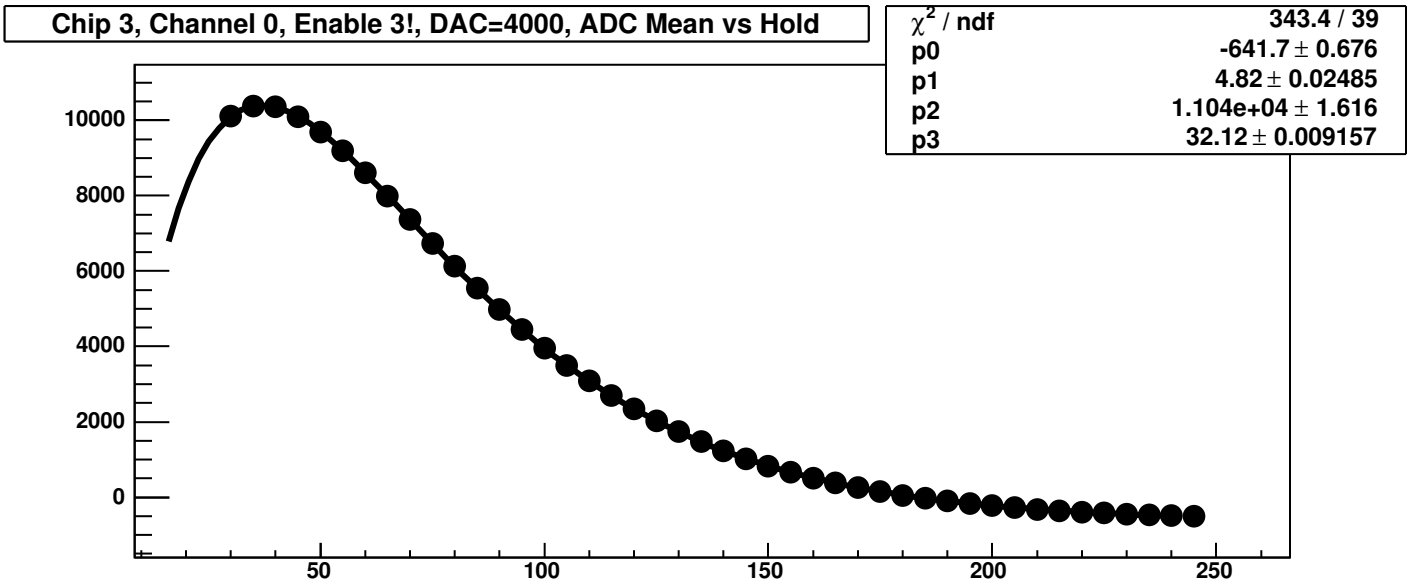

Chip 3, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250

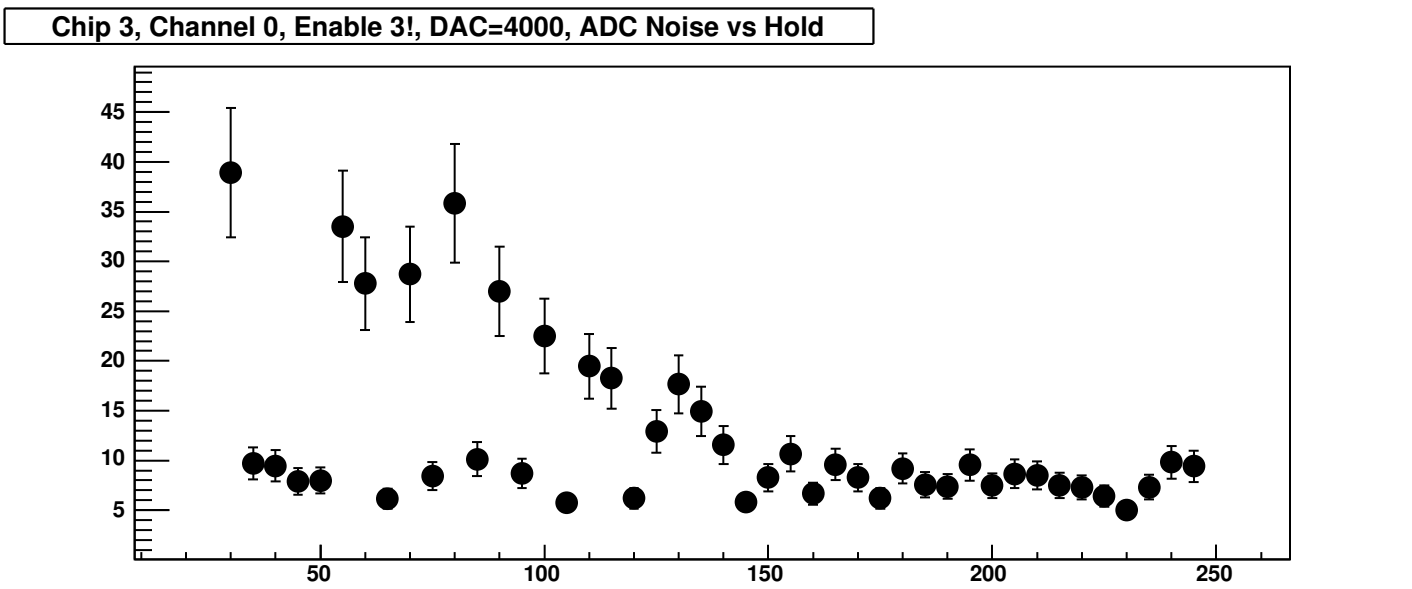

Chip 3, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
50
100
150
200
250

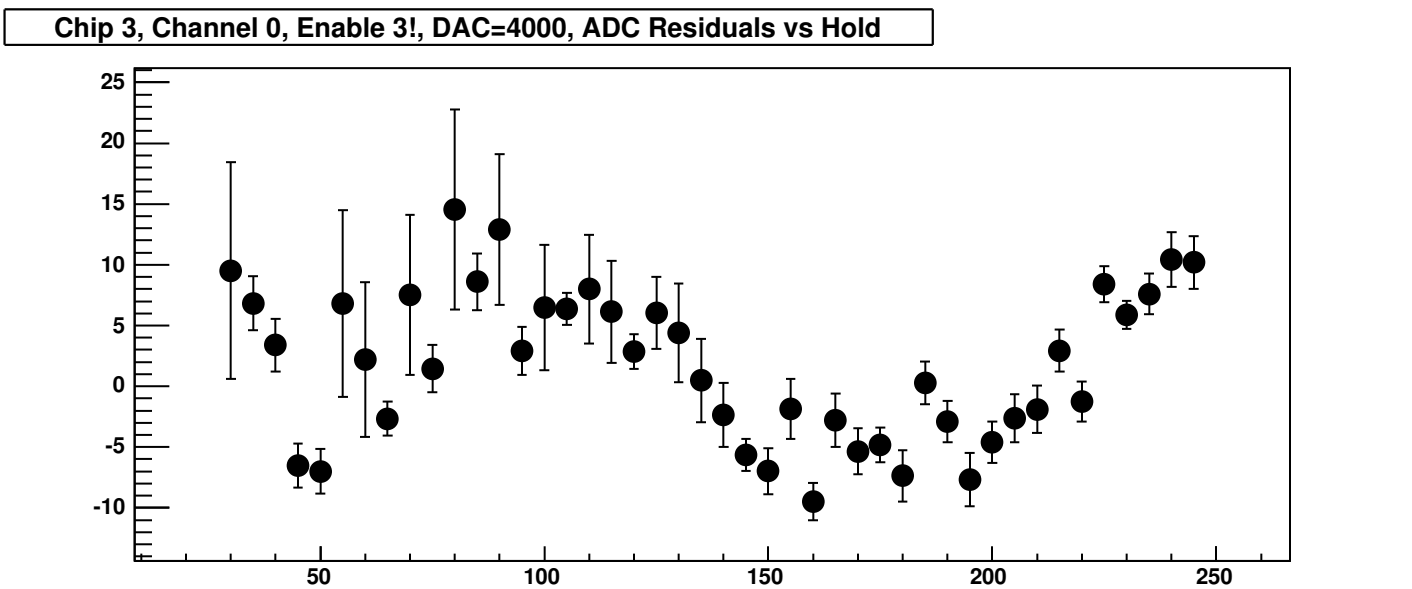

Chip 3, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 117 / 38
p0 217.4 ± 0.5751
p1 70.72 ± 0.959
p2 -5.816e+07 ± 2.035e+06
p3 4.459e+07 ± 2.441e+05
p4 1.325 ± 0.04444
230
220
210
200
190
180
170
160
50
100
150
200
250

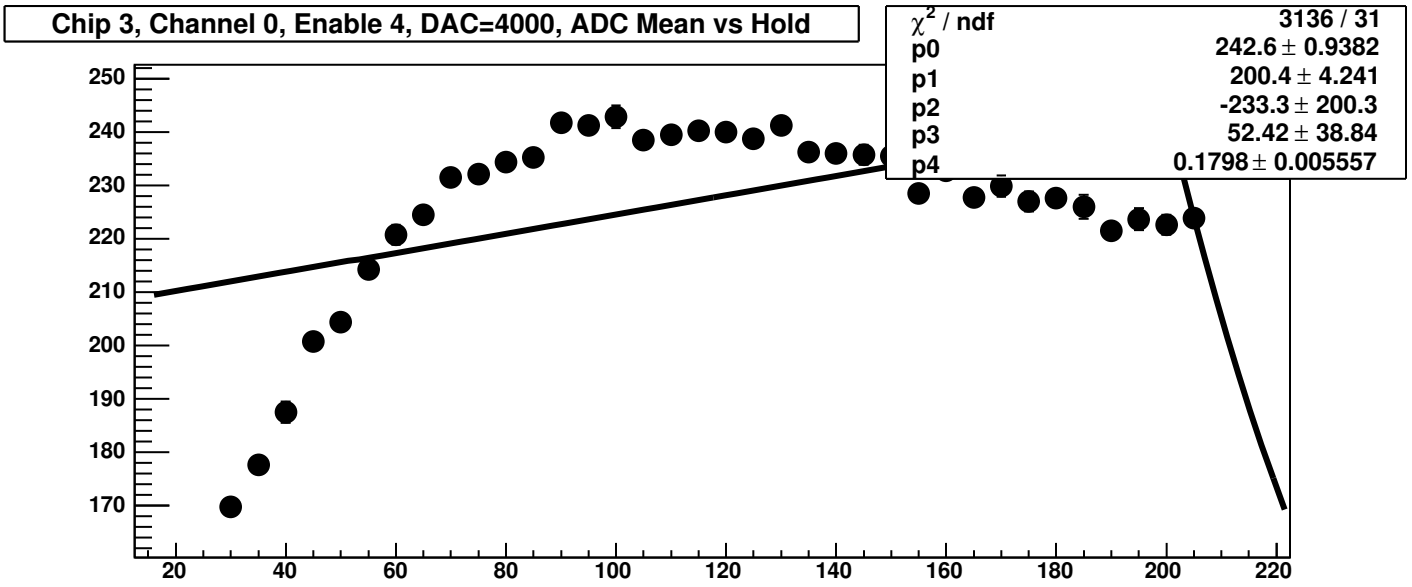

Chip 3, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

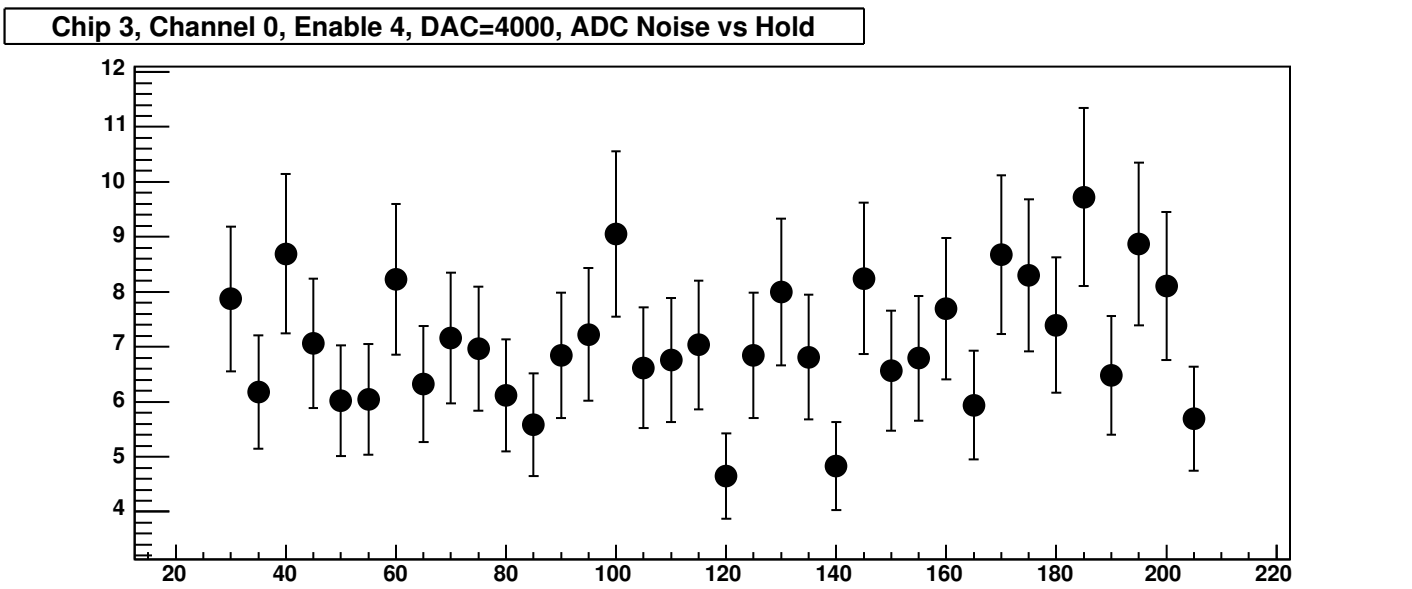

Chip 3, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

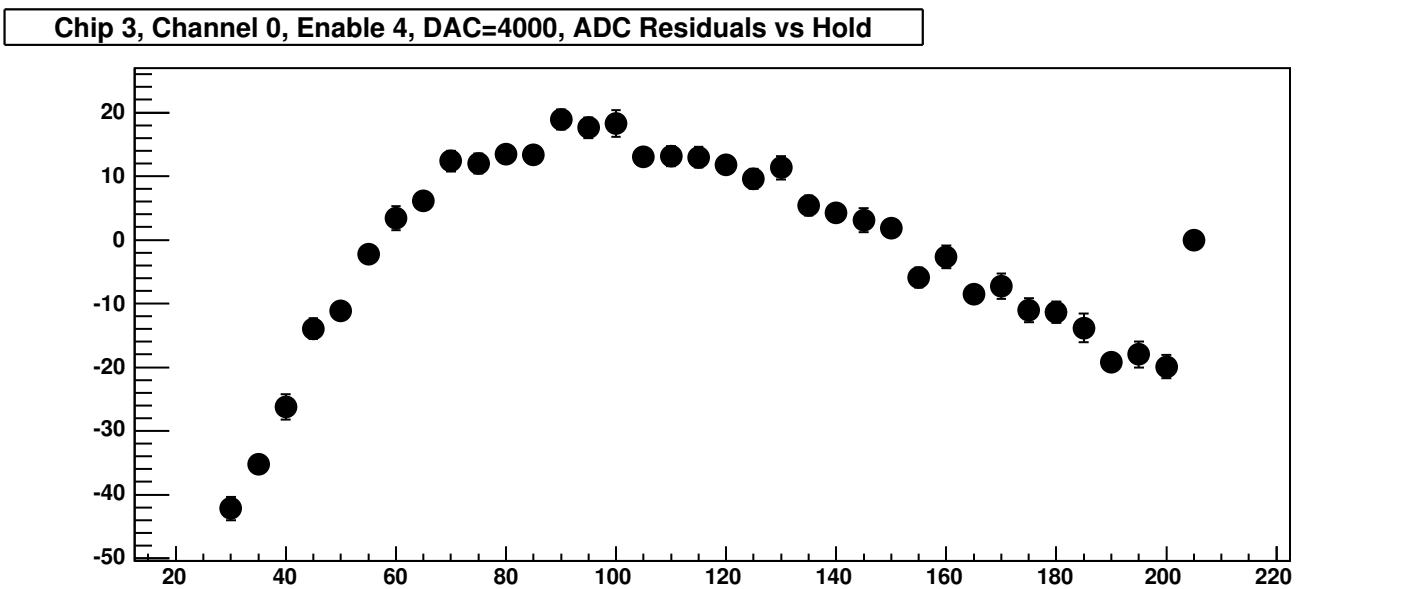

Chip 3, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 343.4 / 39
p0 -641.7 ± 0.676
p1 4.82 ± 0.02485
p2 1.104e+04 ± 1.616
p3 32.12 ± 0.009157
10000
8000
6000
4000
2000
0
50
100
150
200
250

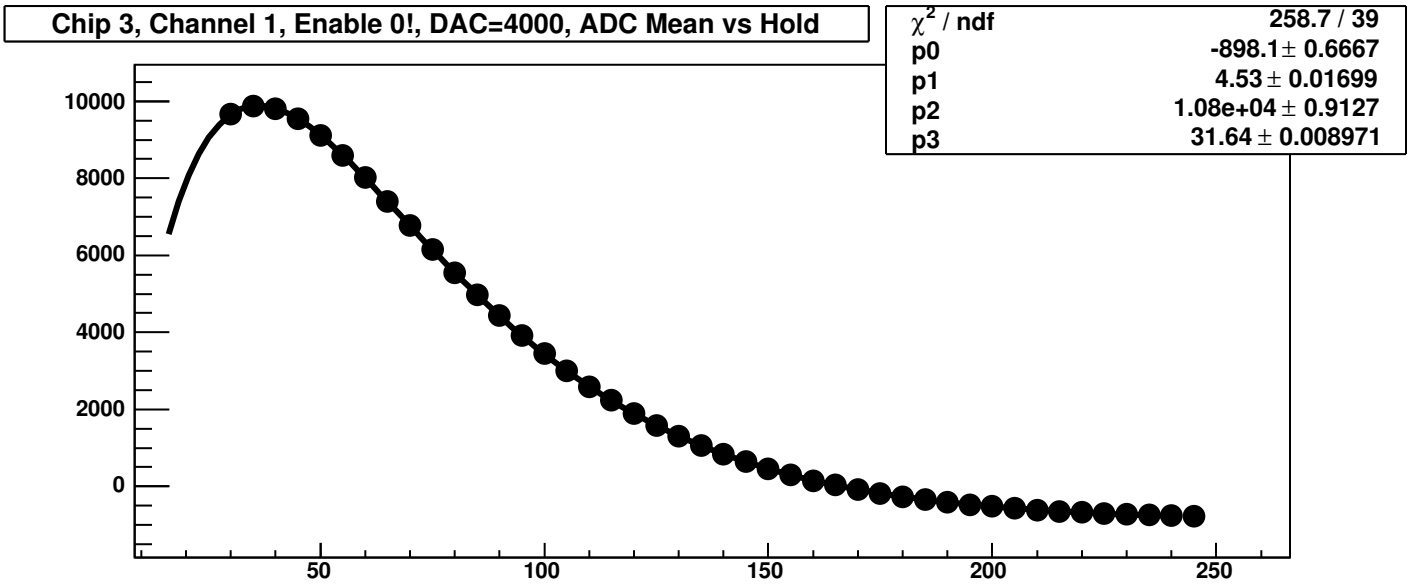

Chip 3, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold
45
40
35
30
25
20
15
10
5
50
100
150
200
250

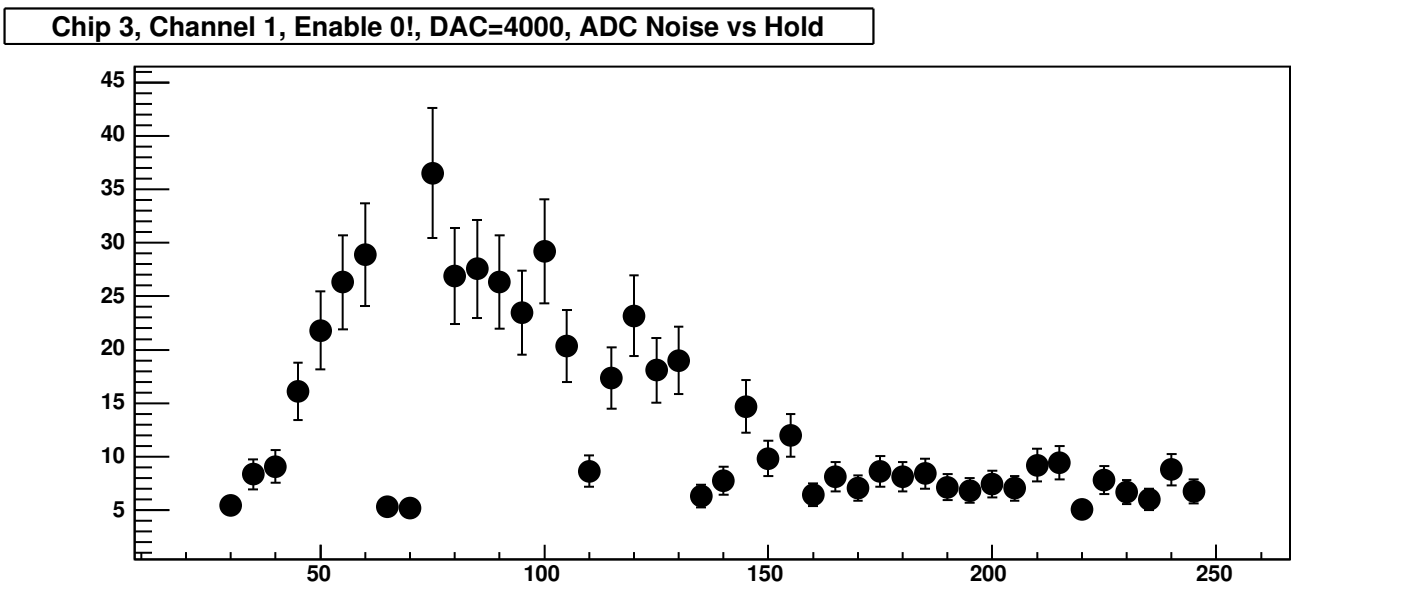

Chip 3, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
50
100
150
200
250

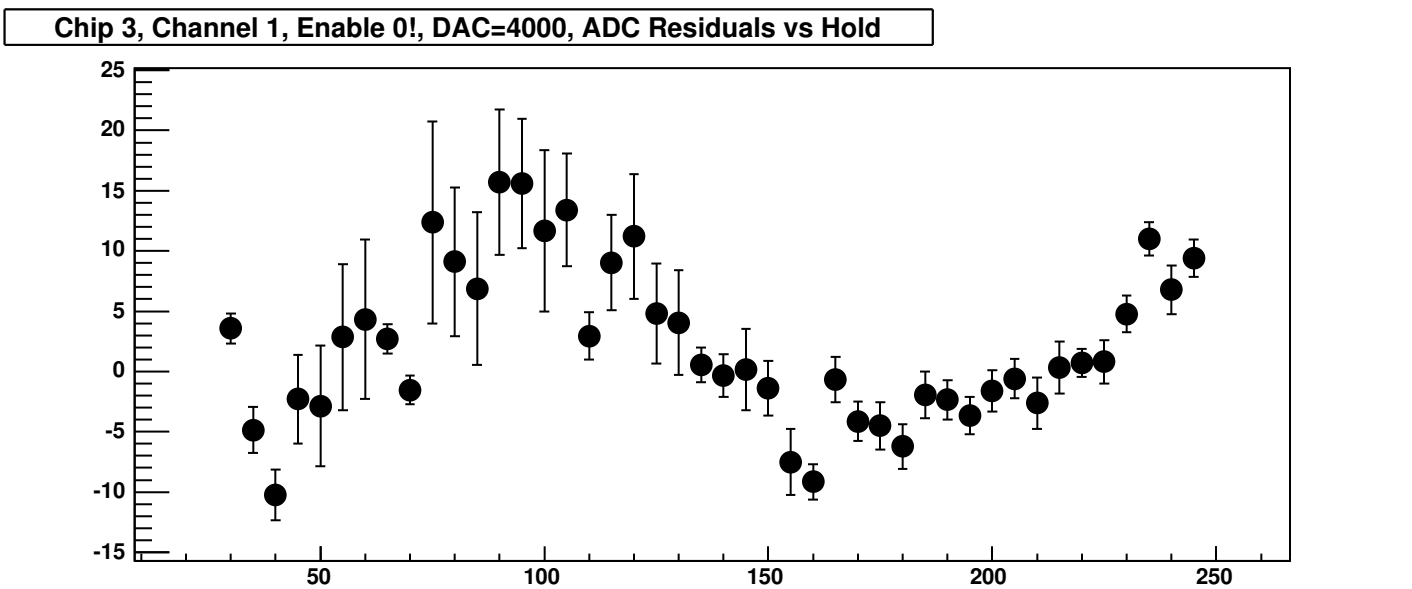

Chip 3, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 3136 / 31
p0 242.6 ± 0.9382
p1 200.4 ± 4.241
p2 -233.3 ± 200.3
p3 52.42 ± 38.84
p4 0.1798 ± 0.005557
250
240
230
220
210
200
190
180
170
20
40
60
80
100
120
140
160
180
200
220


Chip 3, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
20
40
60
80
100
120
140
160
180
200
220

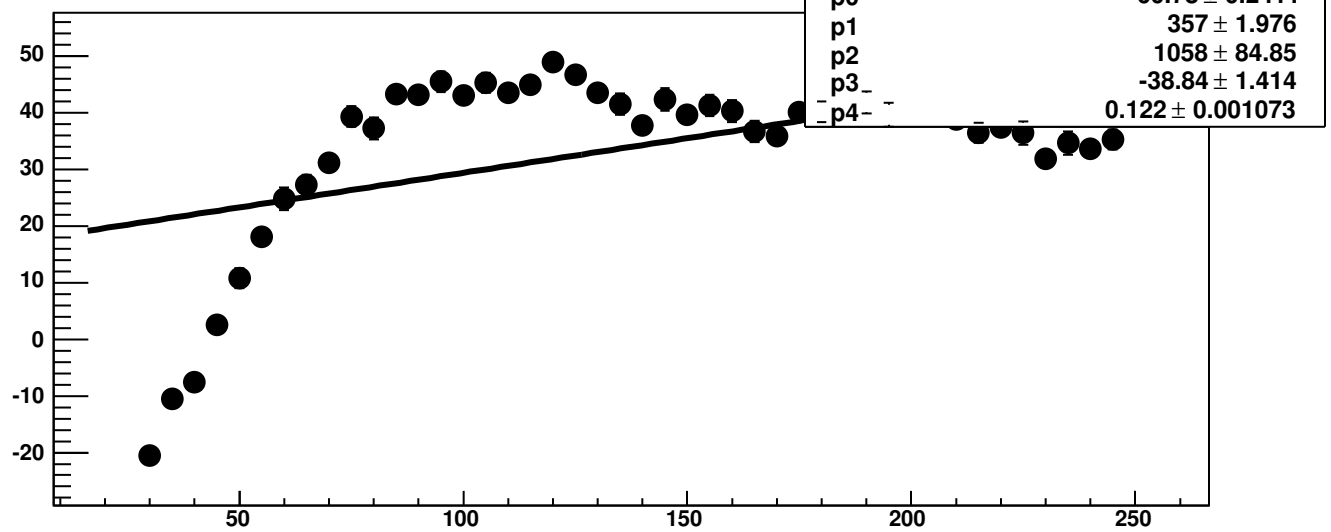

Chip 3, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
-50
20
40
60
80
100
120
140
160
180
200
220


Chip 3, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 258.7 / 39
p0 -898.1 ± 0.6667
p1 4.53 ± 0.01699
p2 1.08e+04 ± 0.9127
p3 31.64 ± 0.008971
10000
8000
6000
4000
2000
0
50
100
150
200
250

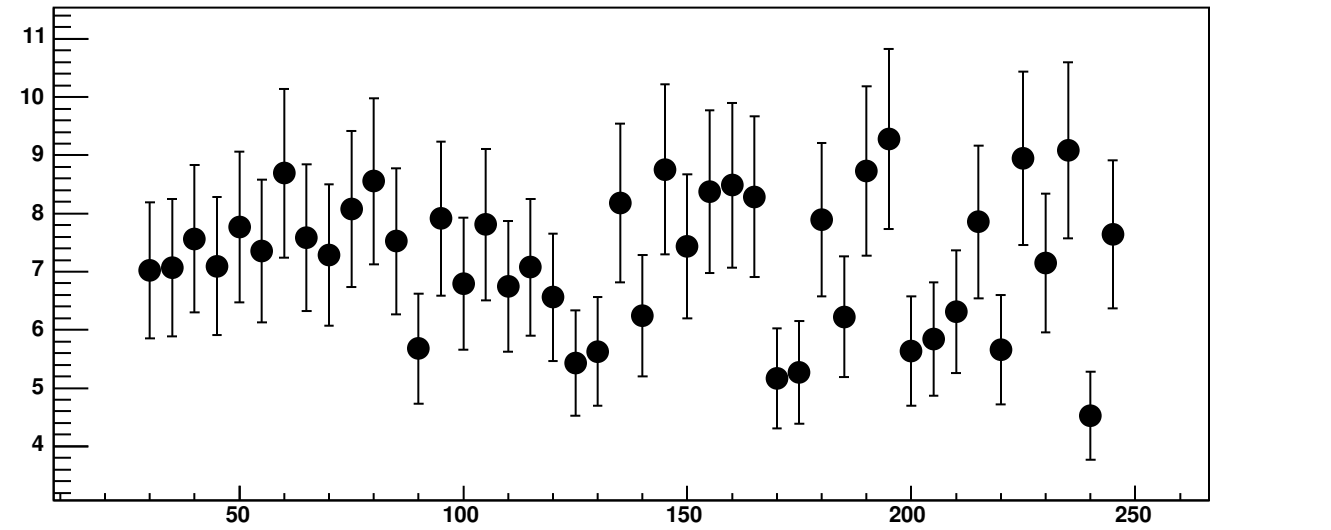

Chip 3, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold
45
40
35
30
25
20
15
10
5
50
100
150
200
250


Chip 3, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
-15
50
100
150
200
250

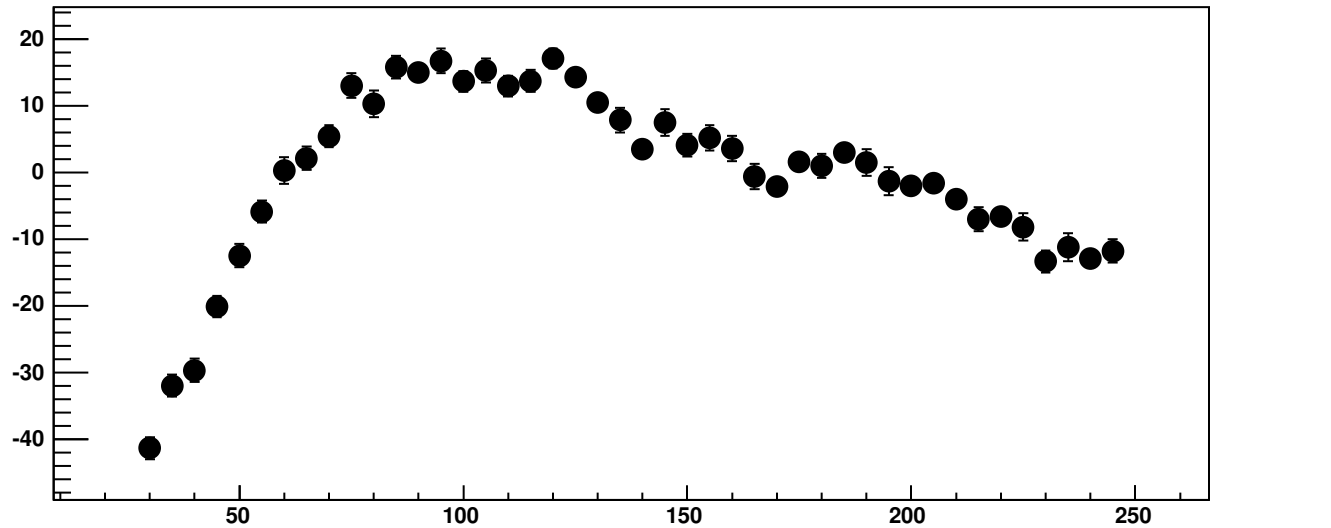
Chip 3, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold



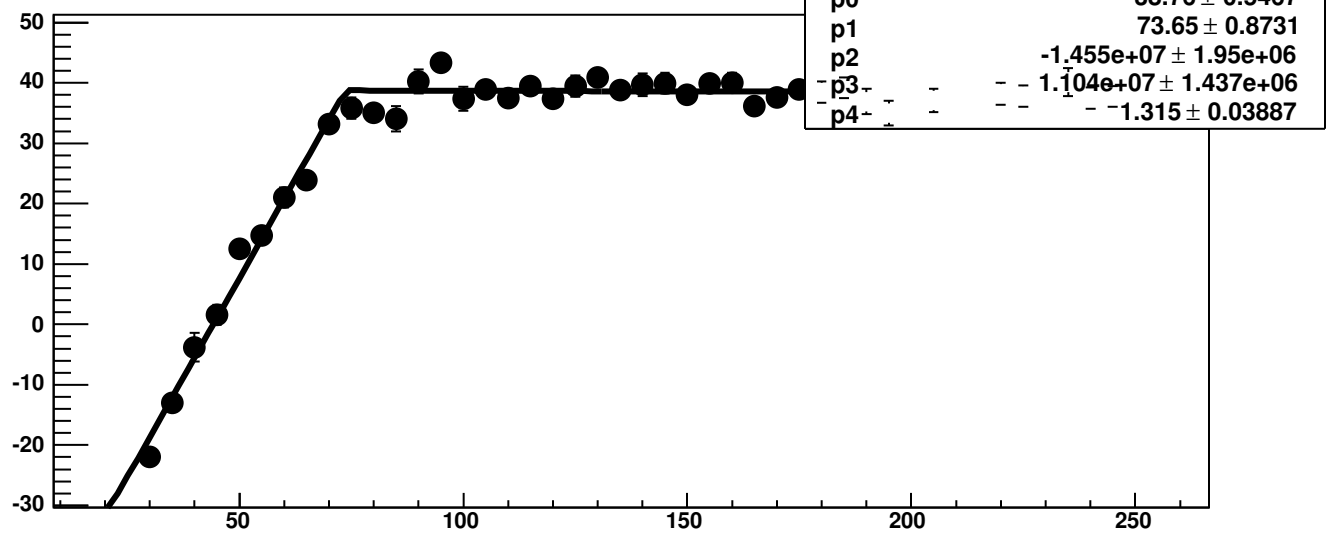
Chip 3, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



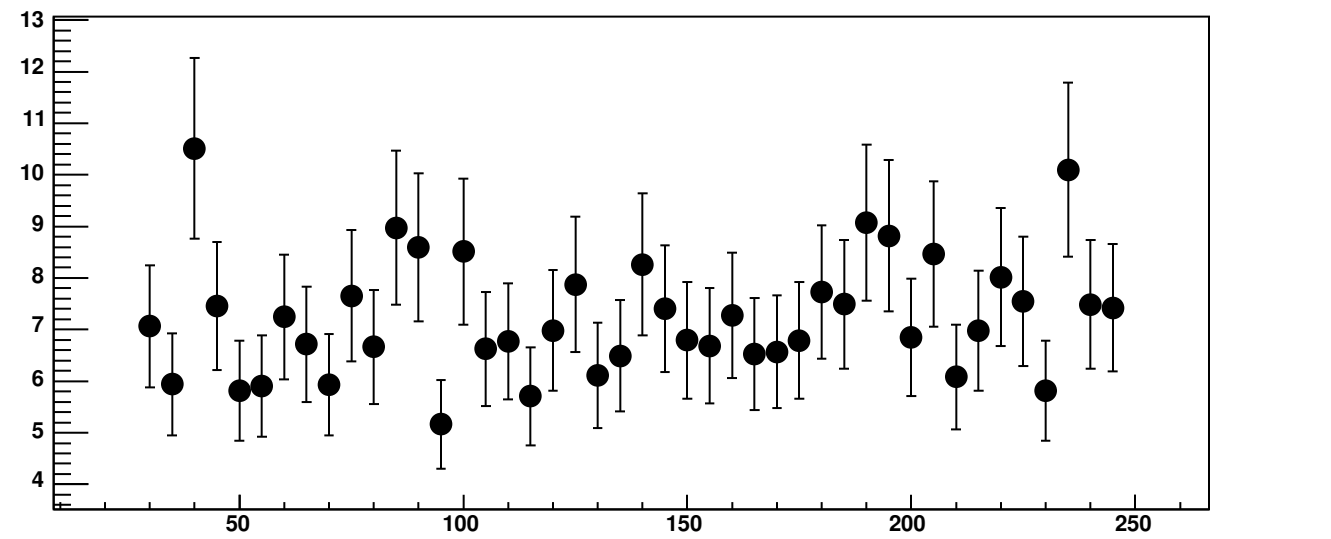
Chip 3, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold



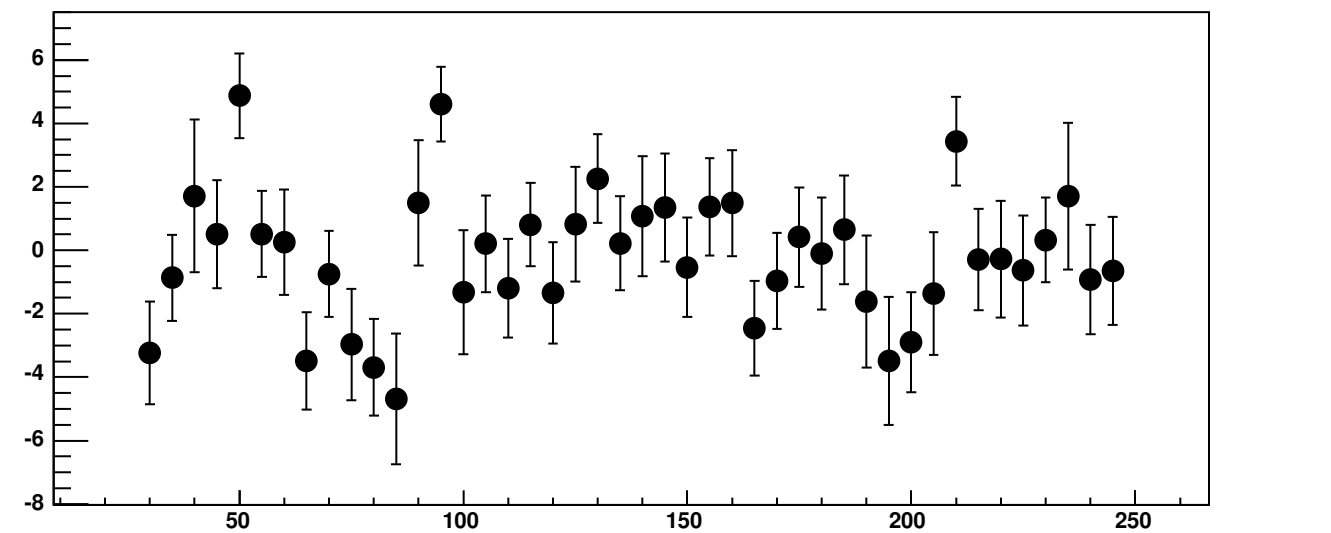
Chip 3, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

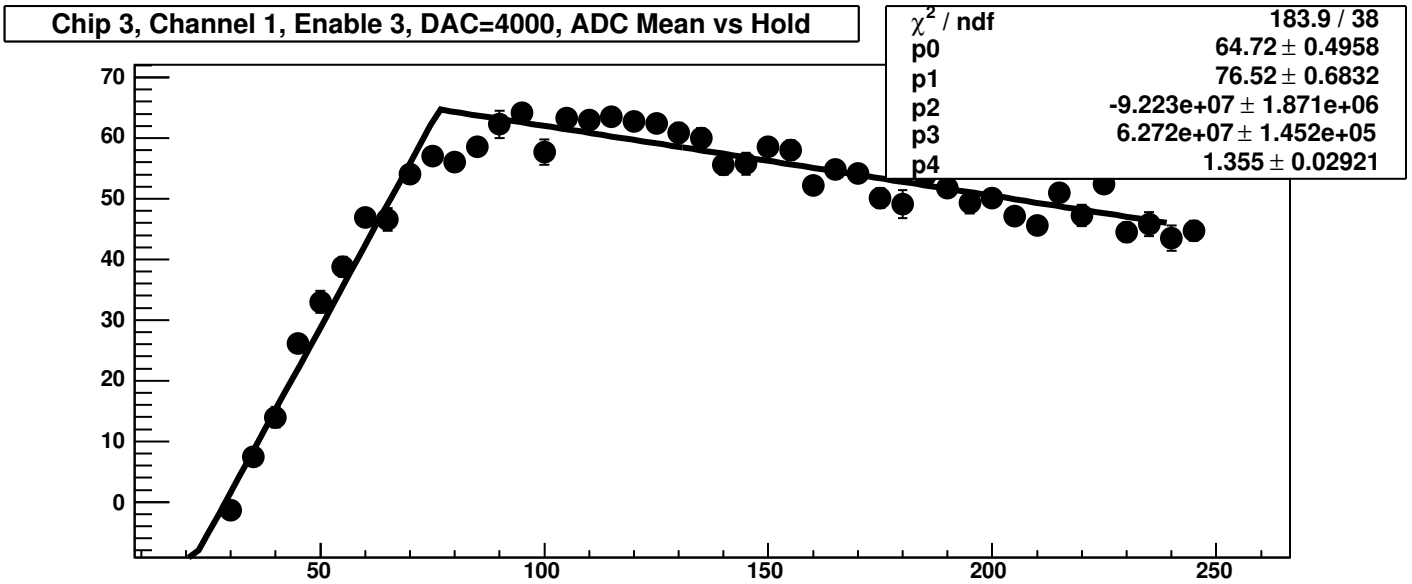


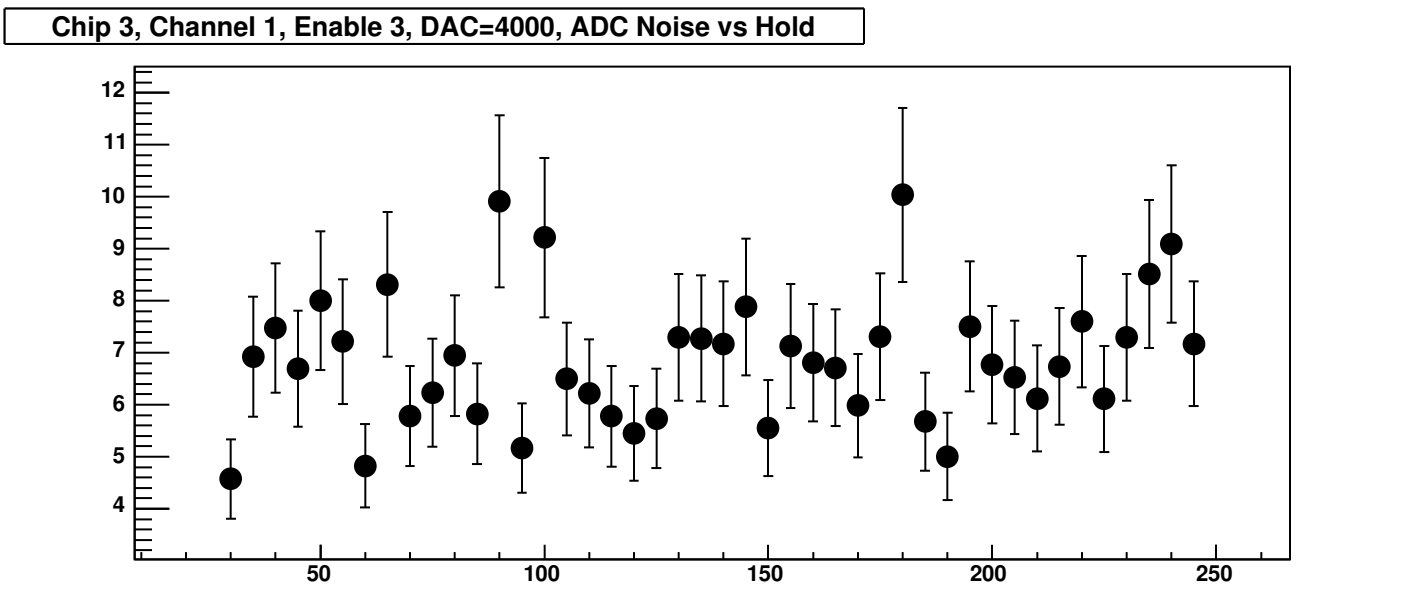
Chip 3, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold

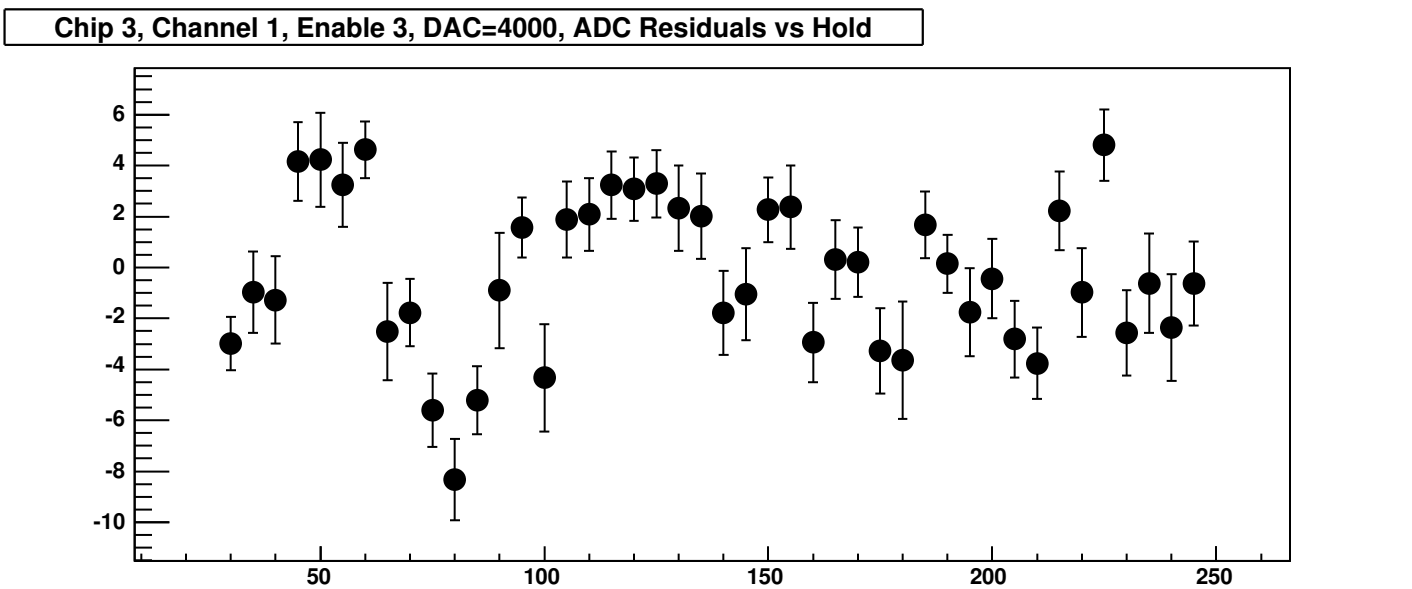


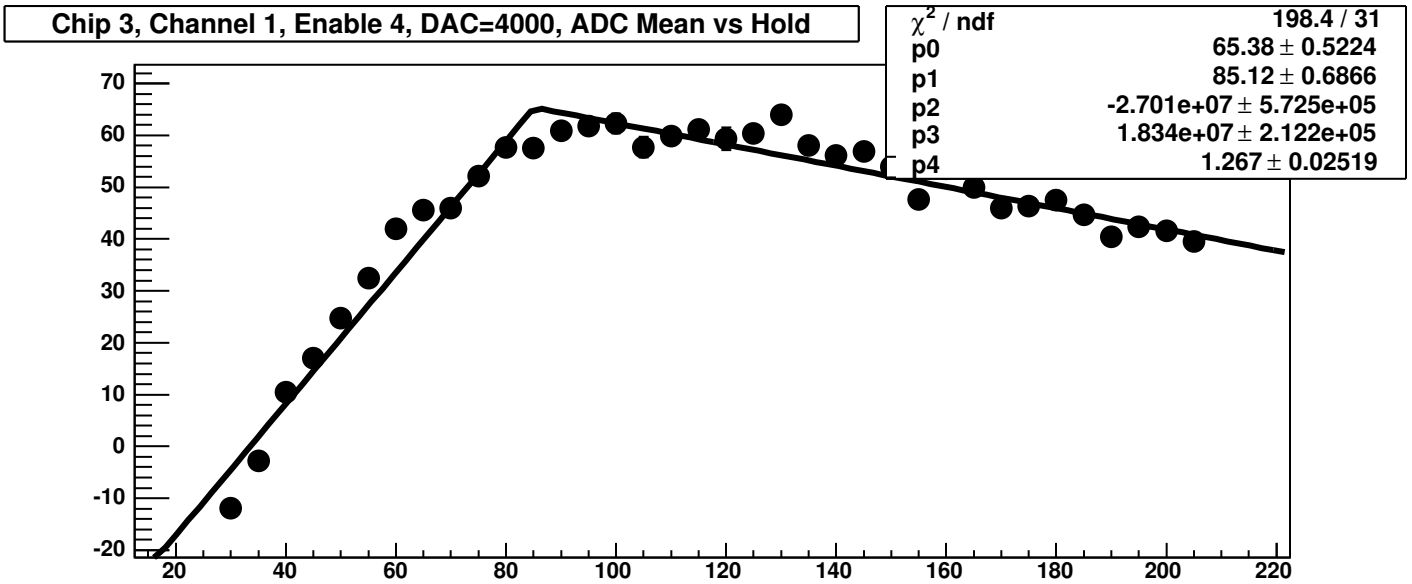
Chip 3, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

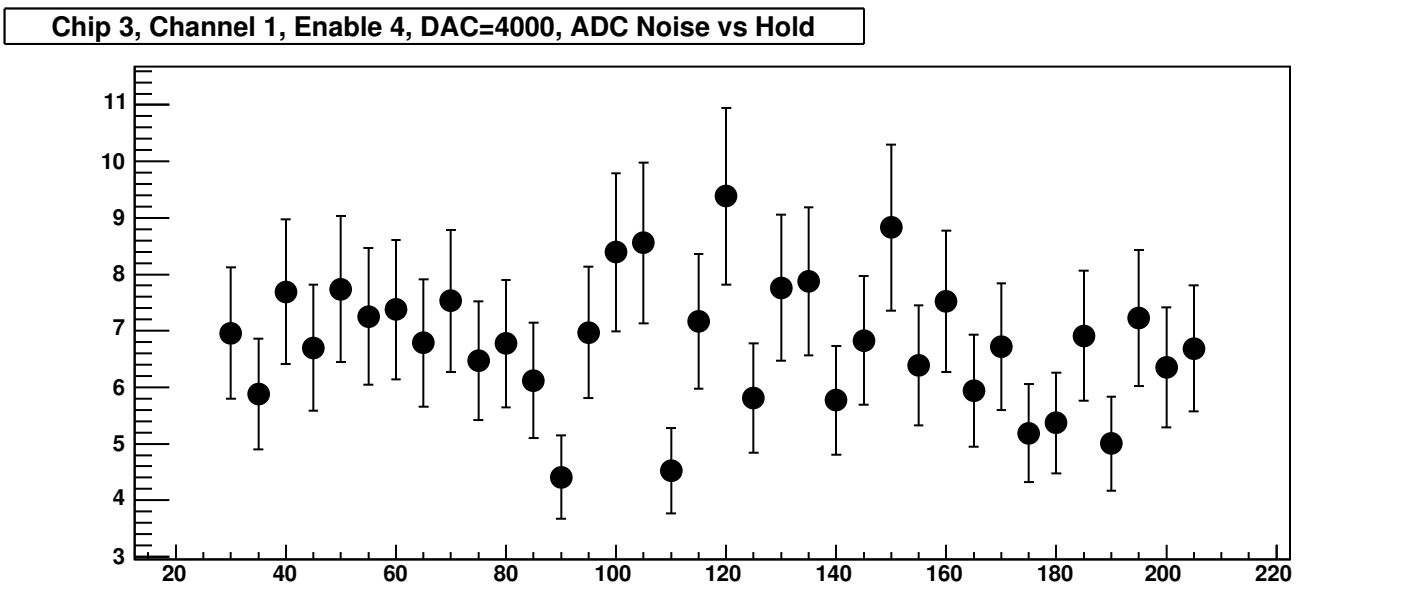


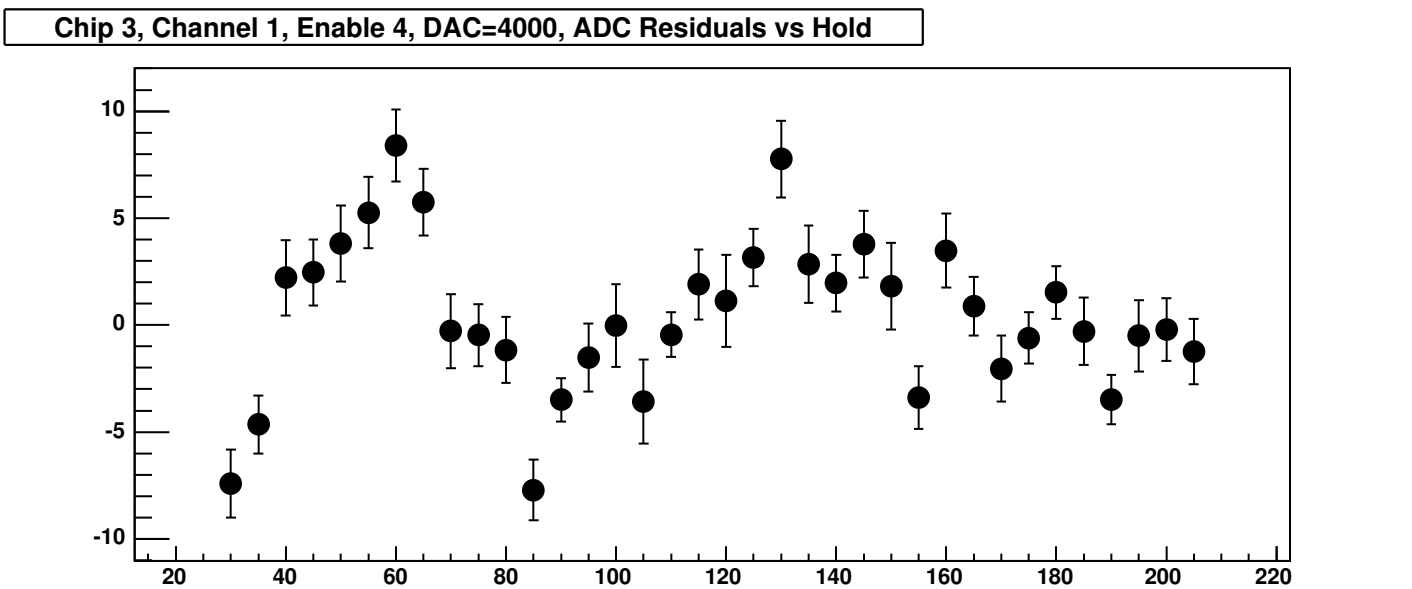

Chip 3, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 183.9 / 38
p0 64.72 ± 0.4958
p1 76.52 ± 0.6832
p2 -9.223e+07 ± 1.871e+06
p3 6.272e+07 ± 1.452e+05
p4 1.355 ± 0.02921
70
60
50
40
30
20
10
0
50
100
150
200
250

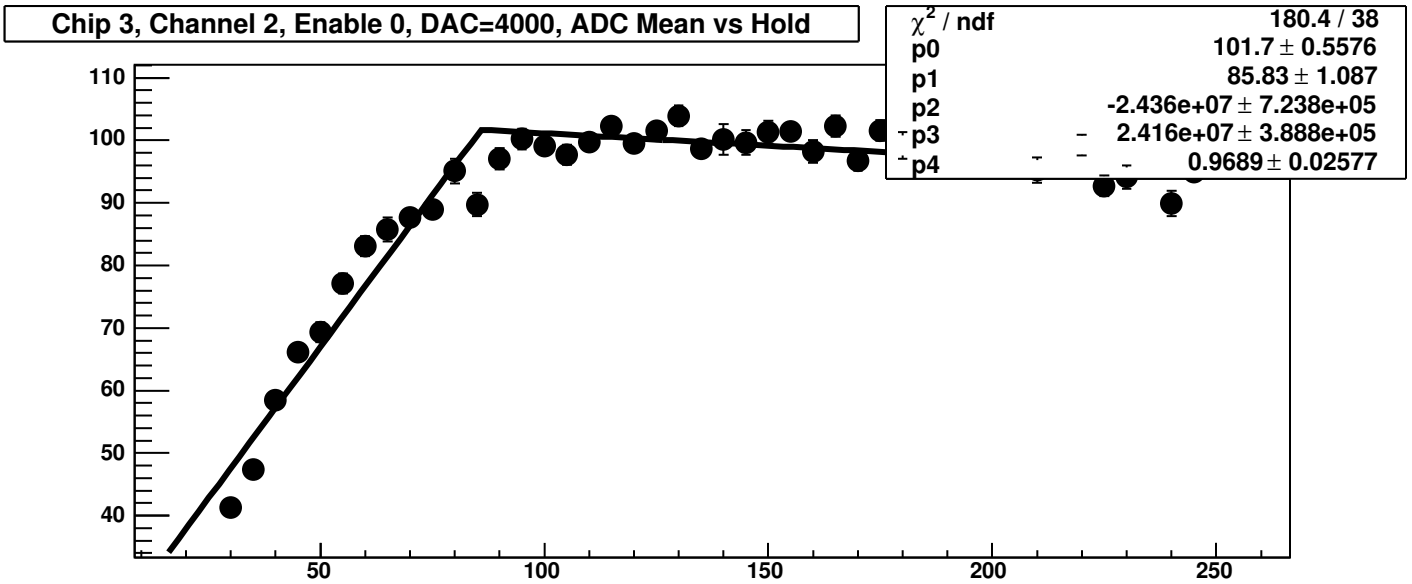

Chip 3, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

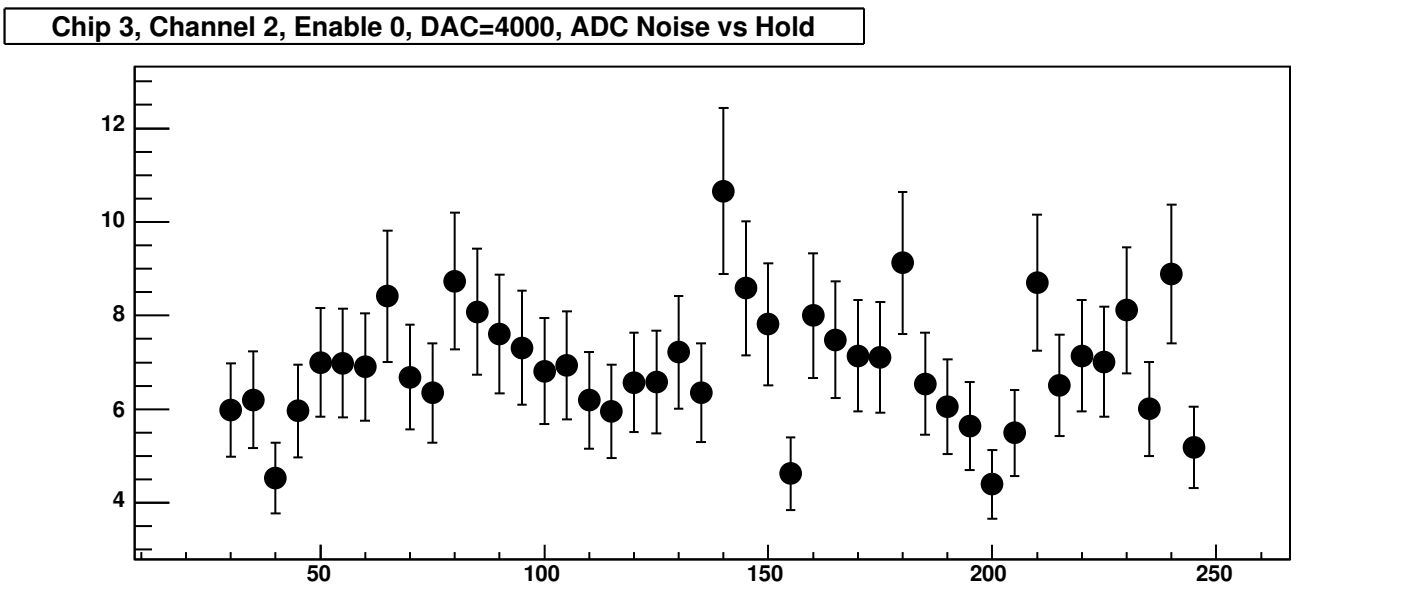

Chip 3, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
-8
-10
50
100
150
200
250

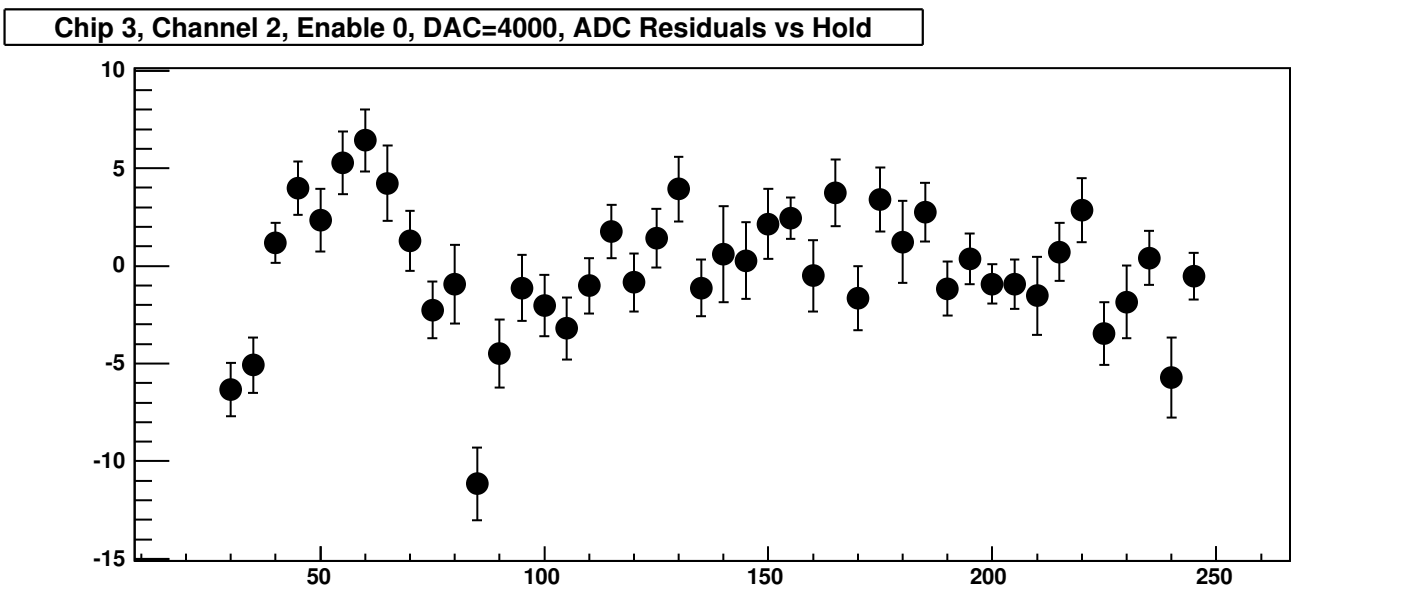

Chip 3, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 198.4 / 31
p0 65.38 ± 0.5224
p1 85.12 ± 0.6866
p2 -2.701e+07 ± 5.725e+05
p3 1.834e+07 ± 2.122e+05
p4 1.267 ± 0.02519

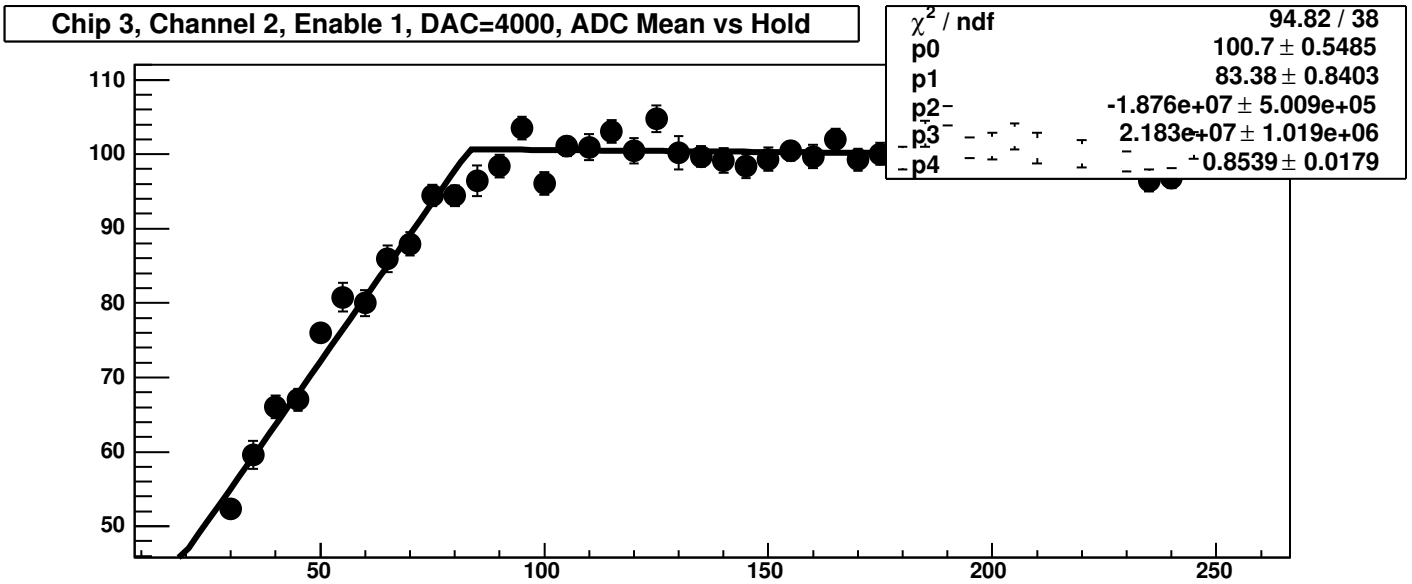

Chip 3, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold

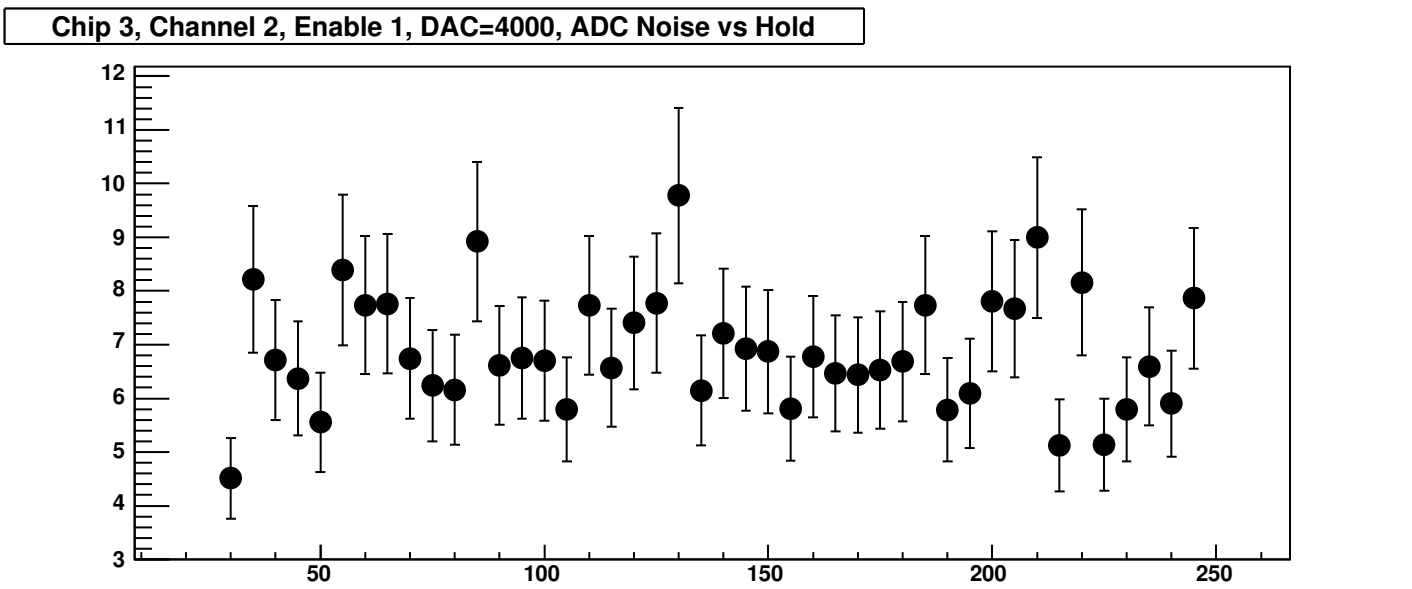

Chip 3, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

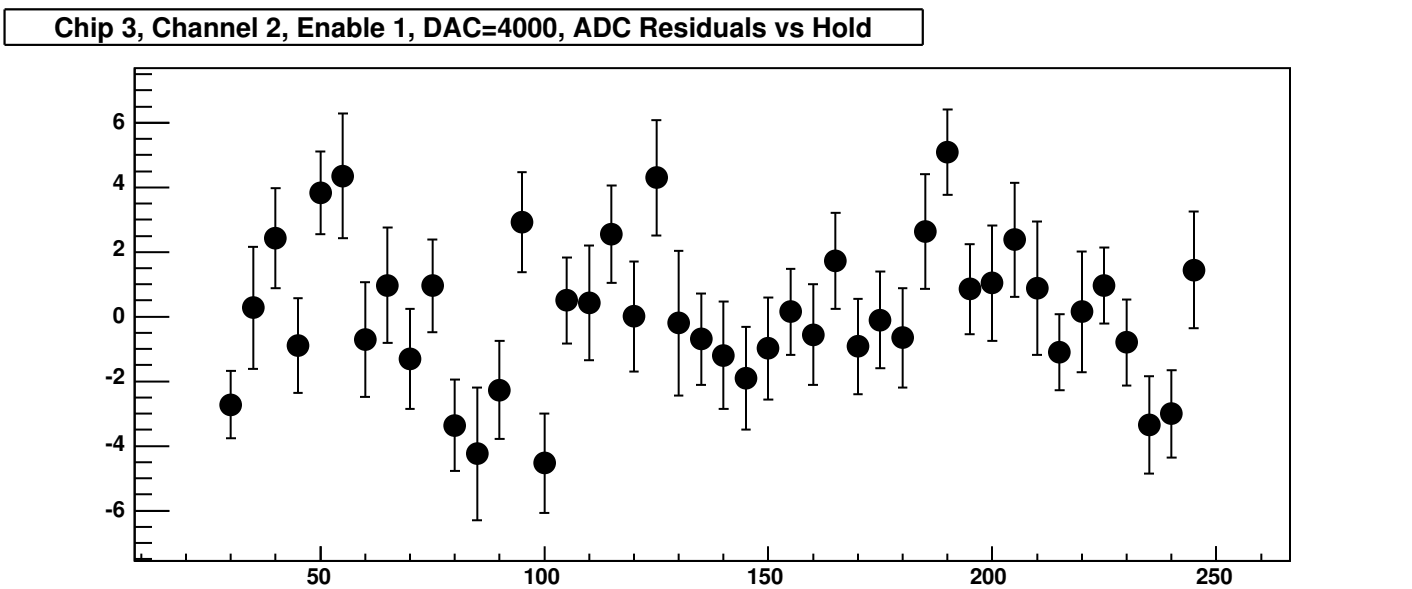

Chip 3, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 180.4 / 38
p0 101.7 ± 0.5576
p1 85.83 ± 1.087
p2 -2.436e+07 ± 7.238e+05
p3 2.416e+07 ± 3.888e+05
p4 0.9689 ± 0.02577
110
100
90
80
70
60
50
40
50
100
150
200
250

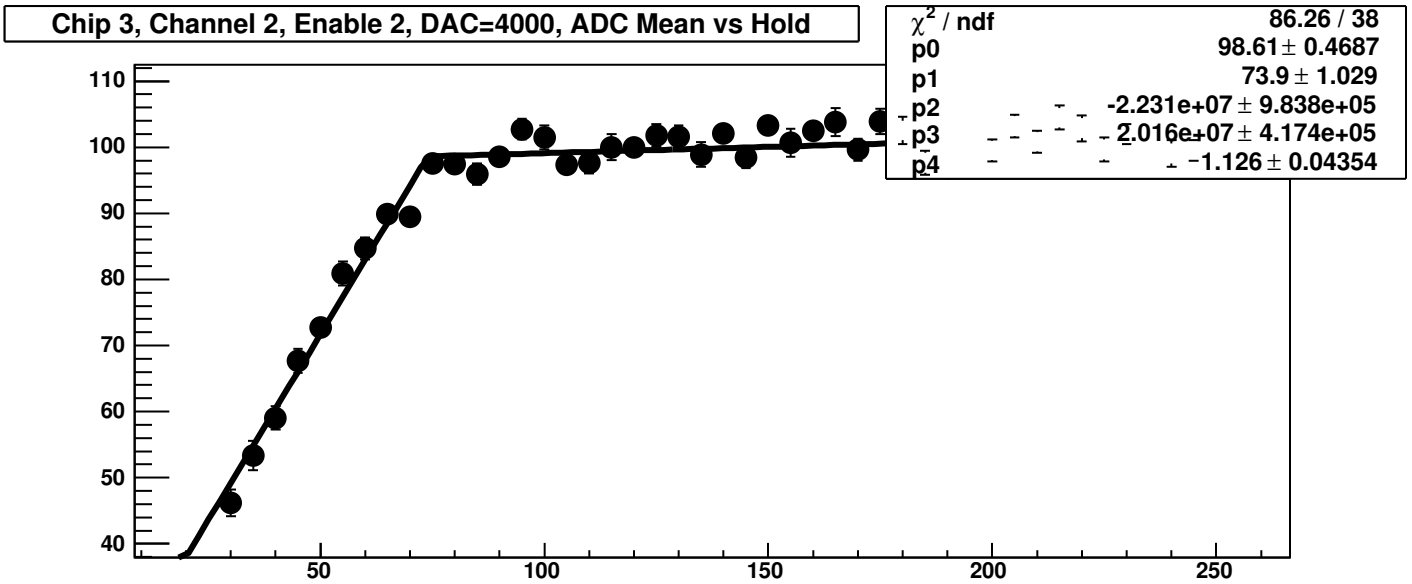

Chip 3, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250

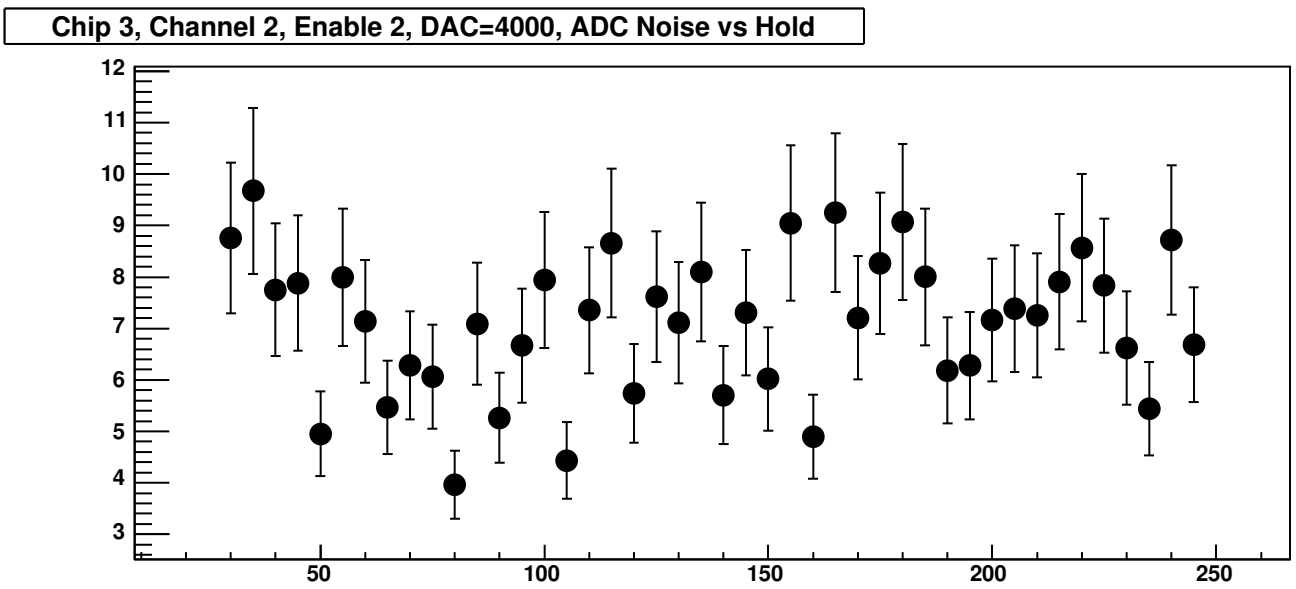

Chip 3, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
50
100
150
200
250

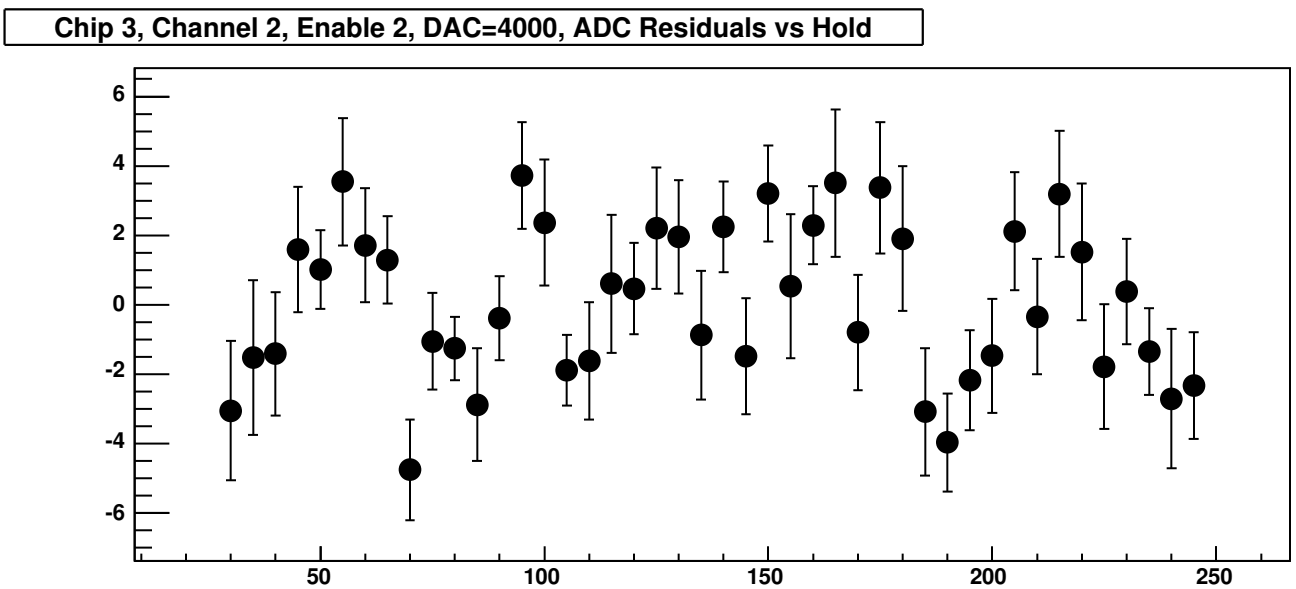

Chip 3, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 94.82 / 38
p0 100.7 ± 0.5485
p1 83.38 ± 0.8403
p2 -1.876e+07 ± 5.009e+05
p3 2.183e+07 ± 1.019e+06
p4 0.8539 ± 0.0179
110
100
90
80
70
60
50
50
100
150
200
250


Chip 3, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250

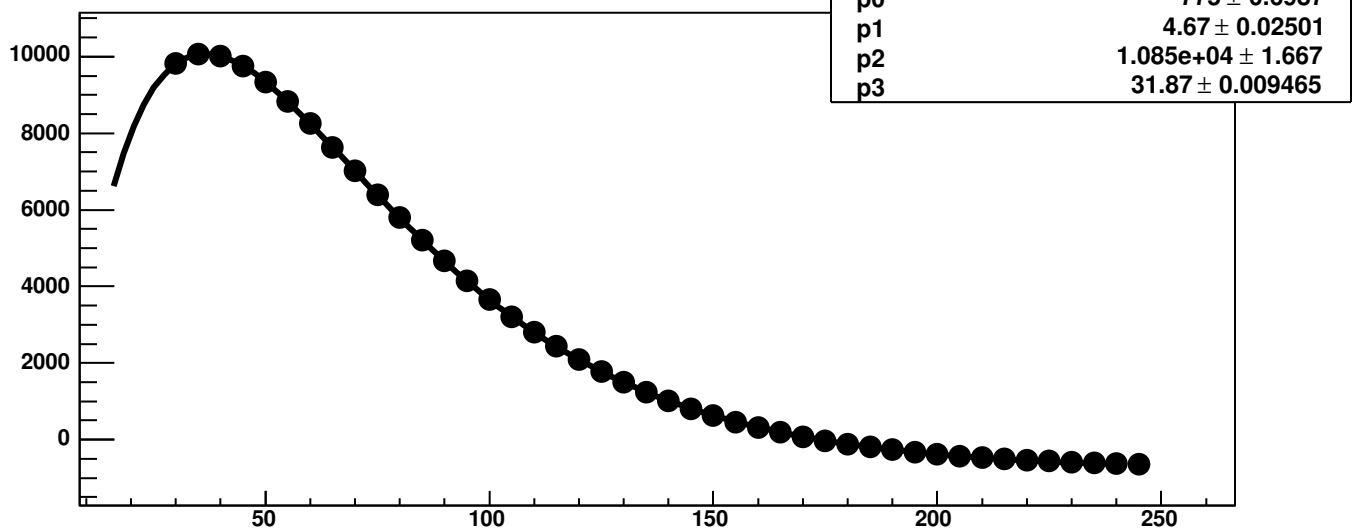

Chip 3, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
50
100
150
200
250


Chip 3, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 86.26 / 38
p0 98.61 ± 0.4687
p1 73.9 ± 1.029
p2 -2.231e+07 ± 9.838e+05
p3 2.016e+07 ± 4.174e+05
p4 −1.126 ± 0.04354
110
100
90
80
70
60
50
40
50
100
150
200
250

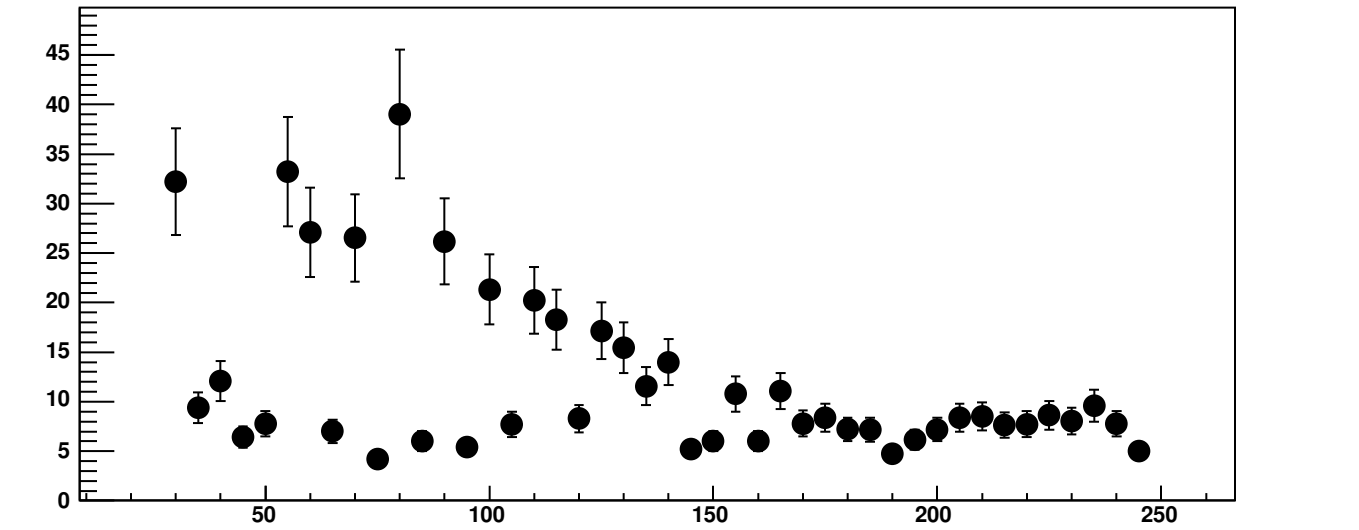

Chip 3, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250


Chip 3, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
50
100
150
200
250

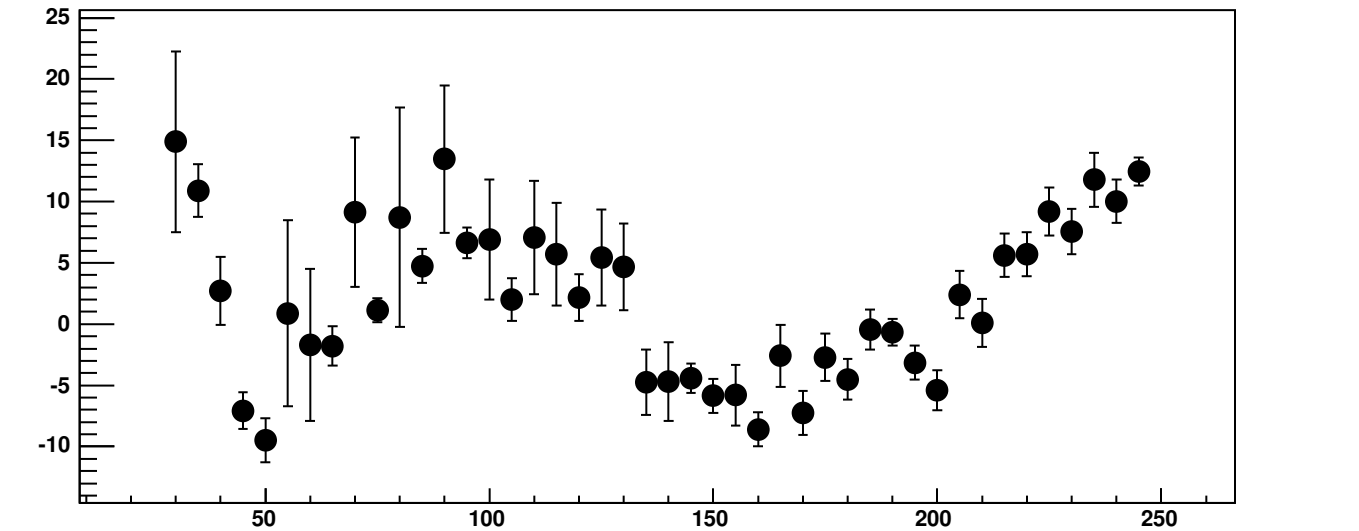
Chip 3, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold

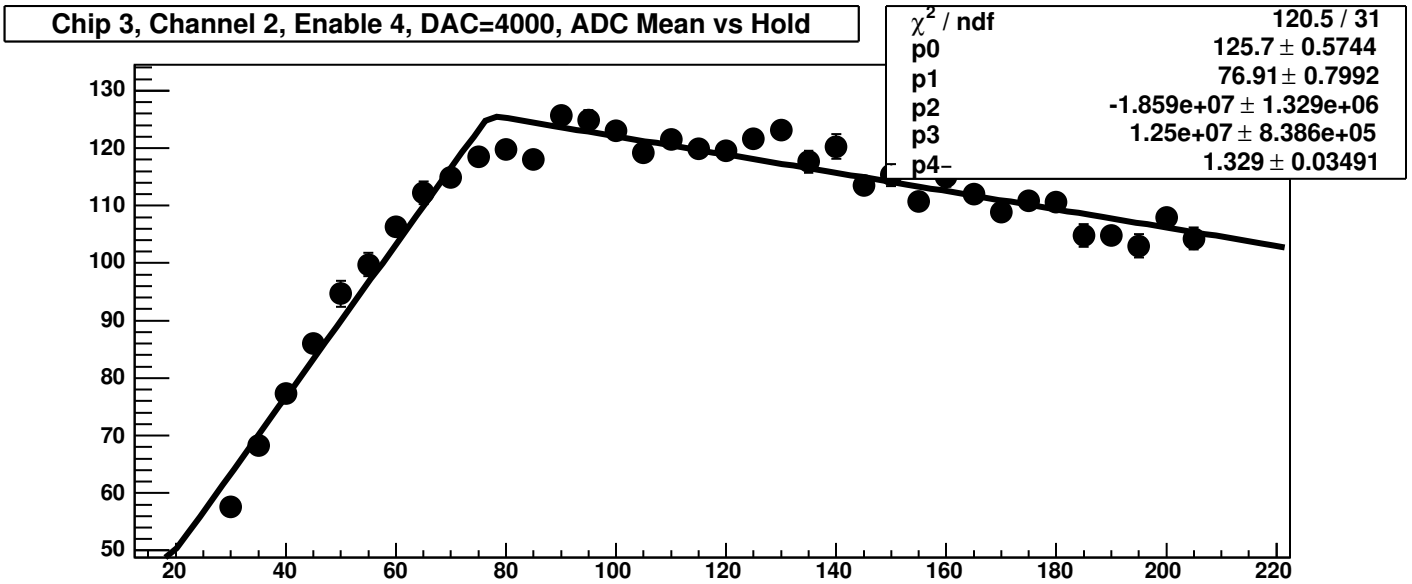


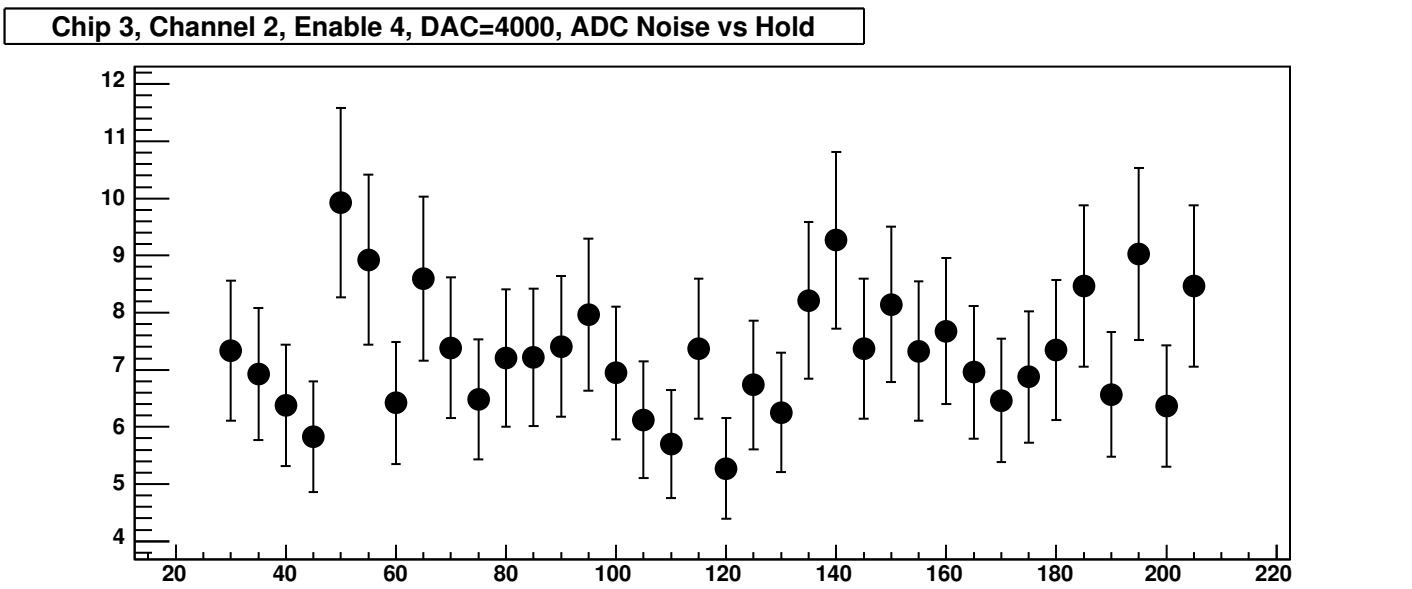
Chip 3, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold

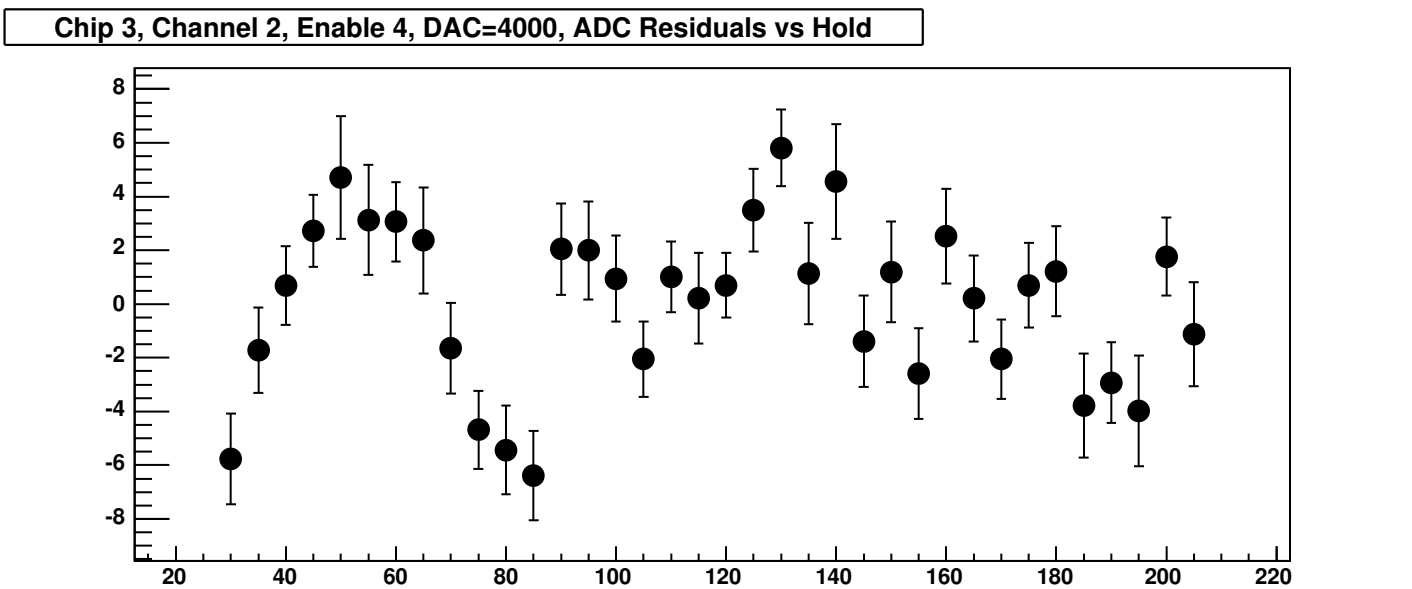


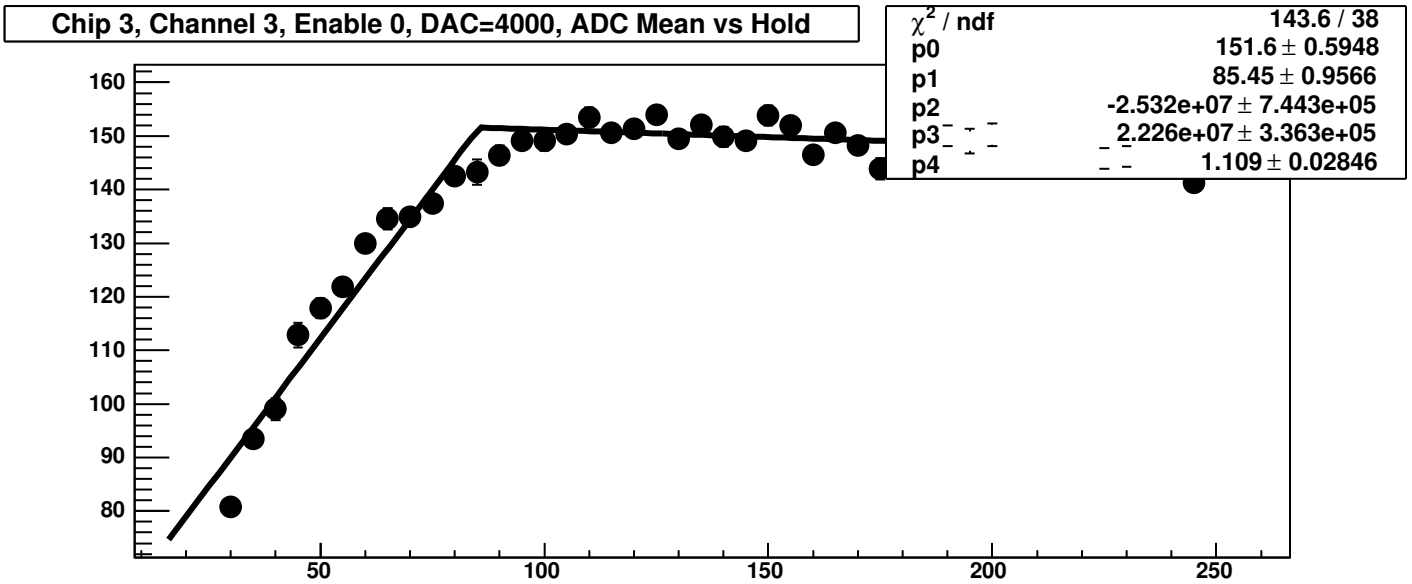
Chip 3, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold

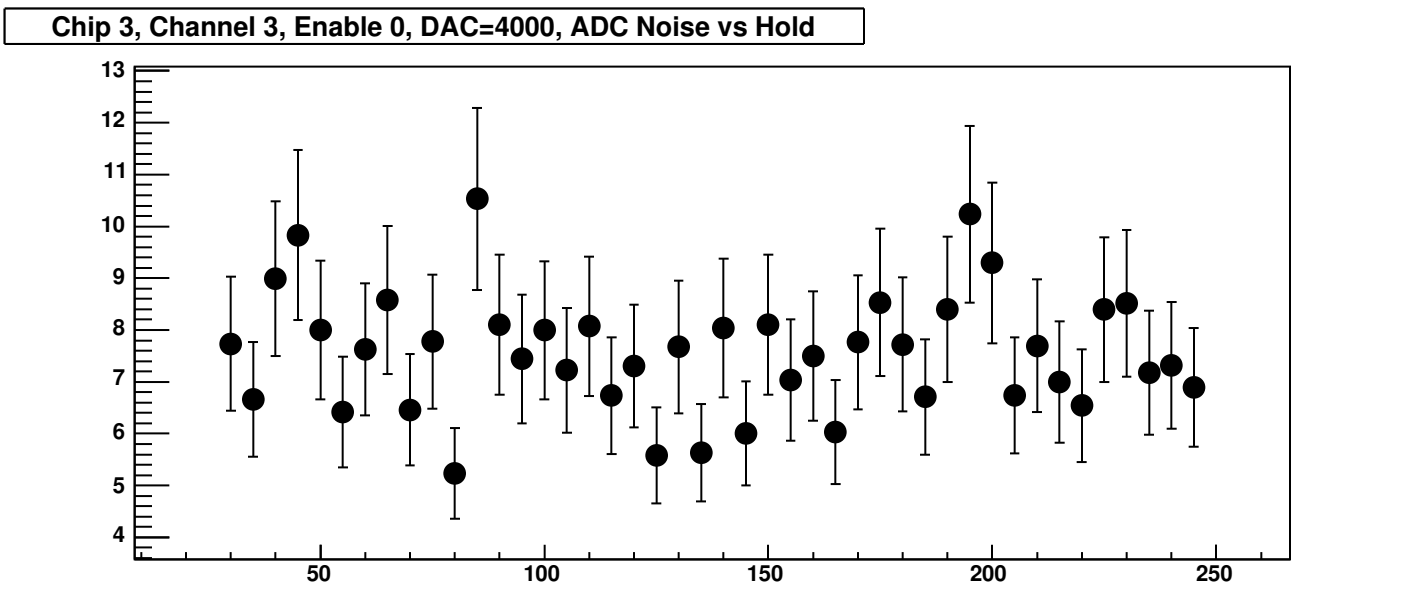


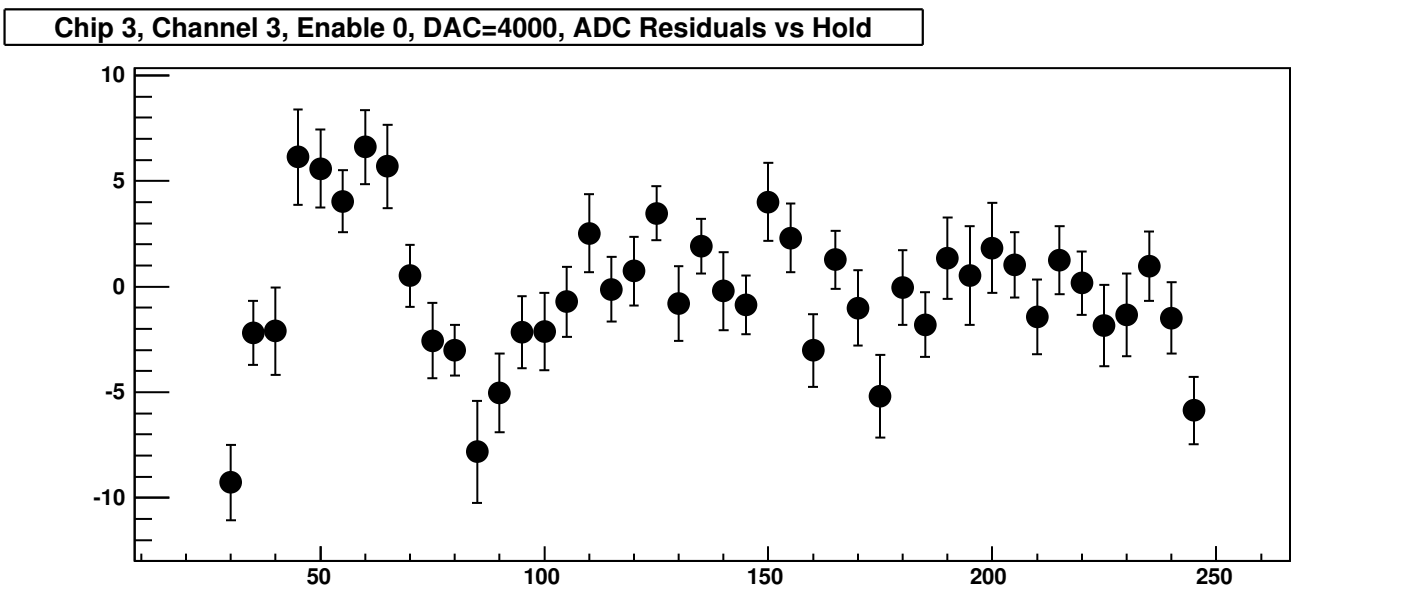

Chip 3, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 120.5 / 31
p0 125.7 ± 0.5744
p1 76.91 ± 0.7992
p2 -1.859e+07 ± 1.329e+06
p3 1.25e+07 ± 8.386e+05
p4 1.329 ± 0.03491


Chip 3, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold

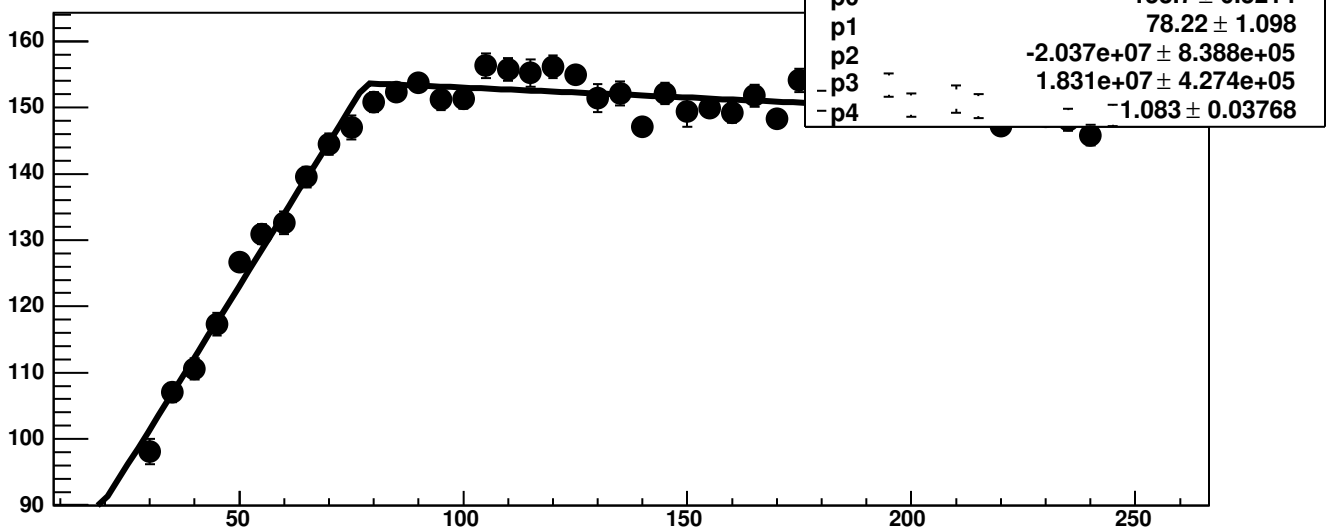

Chip 3, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold


Chip 3, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 143.6 / 38
p0 151.6 ± 0.5948
p1 85.45 ± 0.9566
p2 -2.532e+07 ± 7.443e+05
p3 2.226e+07 ± 3.363e+05
p4 1.109 ± 0.02846
160
150
140
130
120
110
100
90
80
50
100
150
200
250

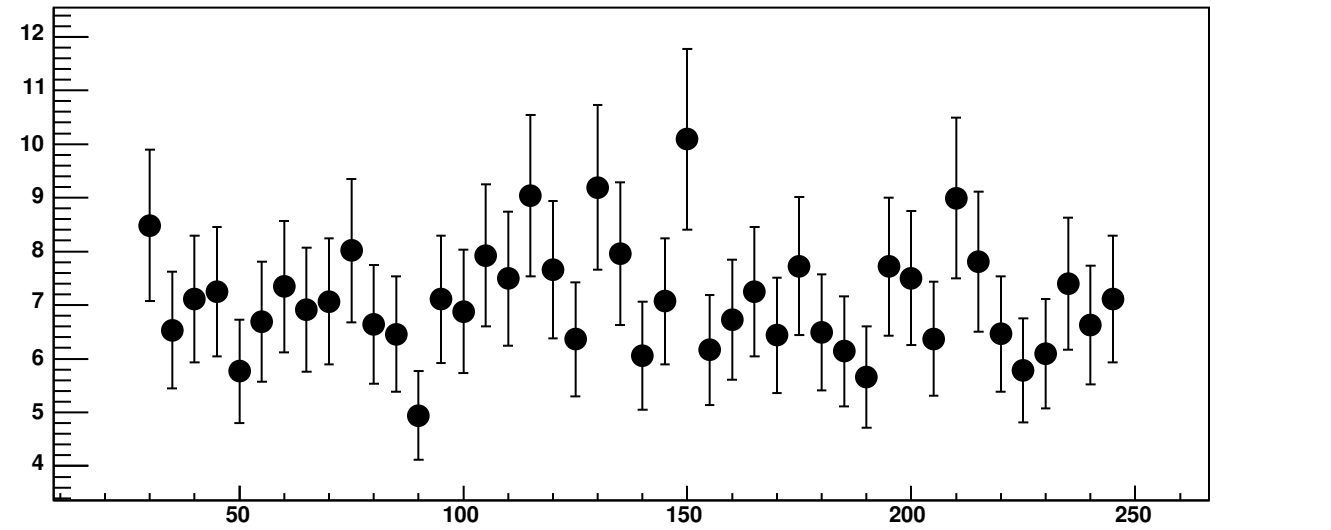

Chip 3, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 3, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

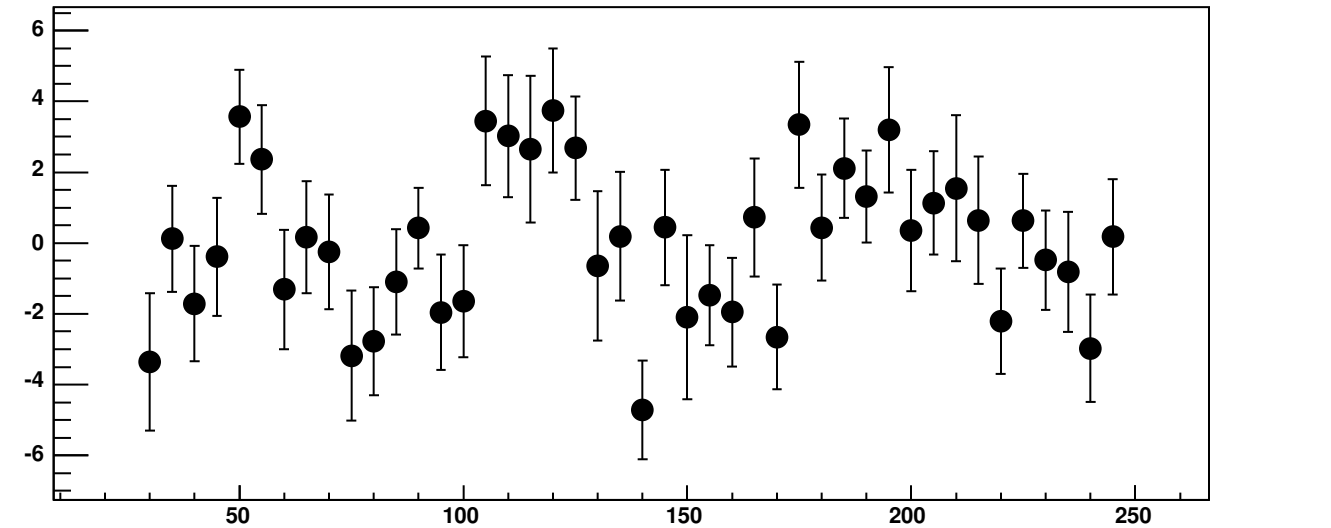
Chip 3, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold

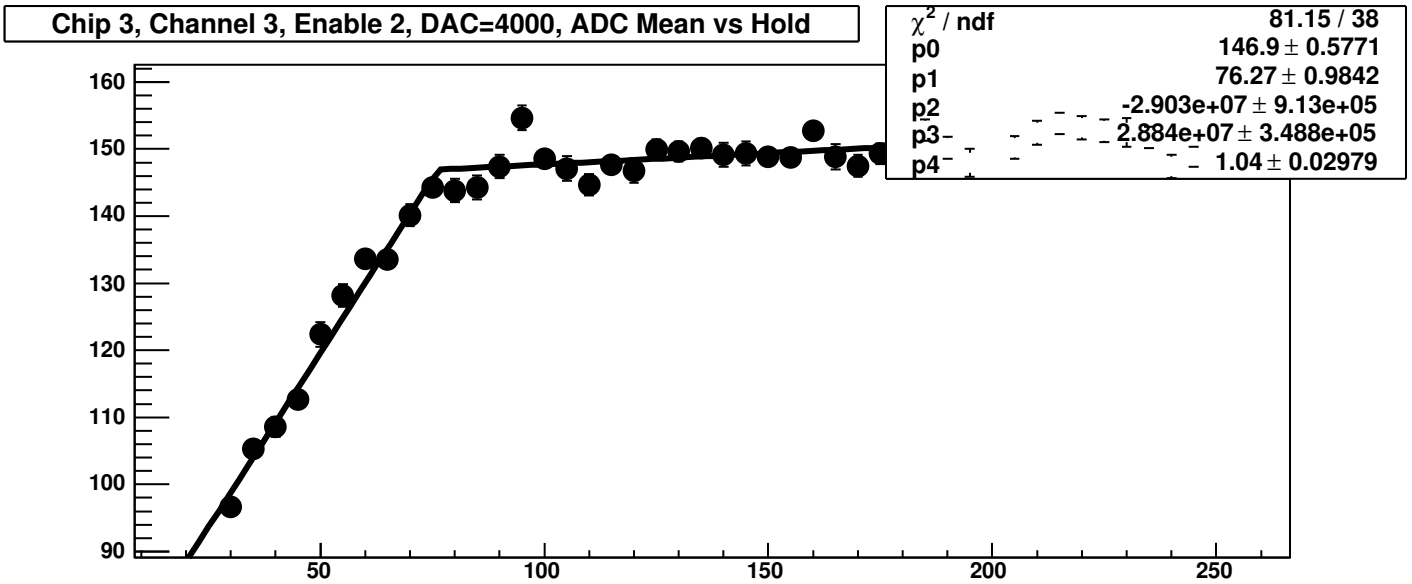


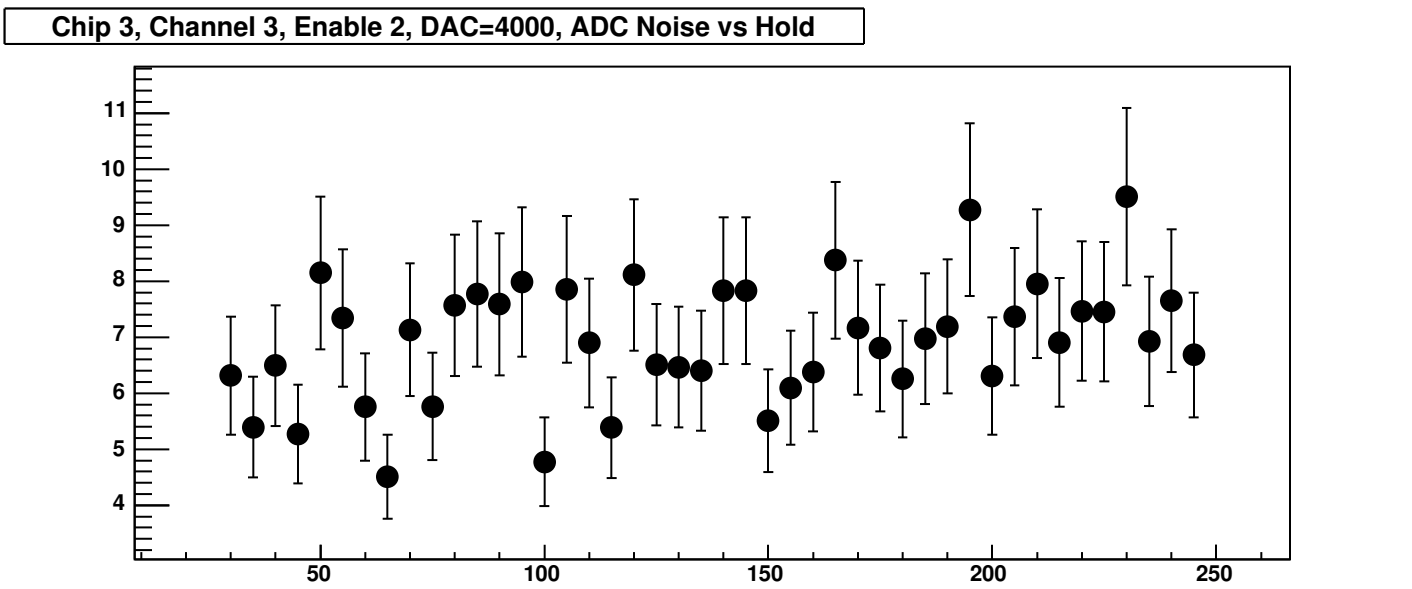
Chip 3, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold

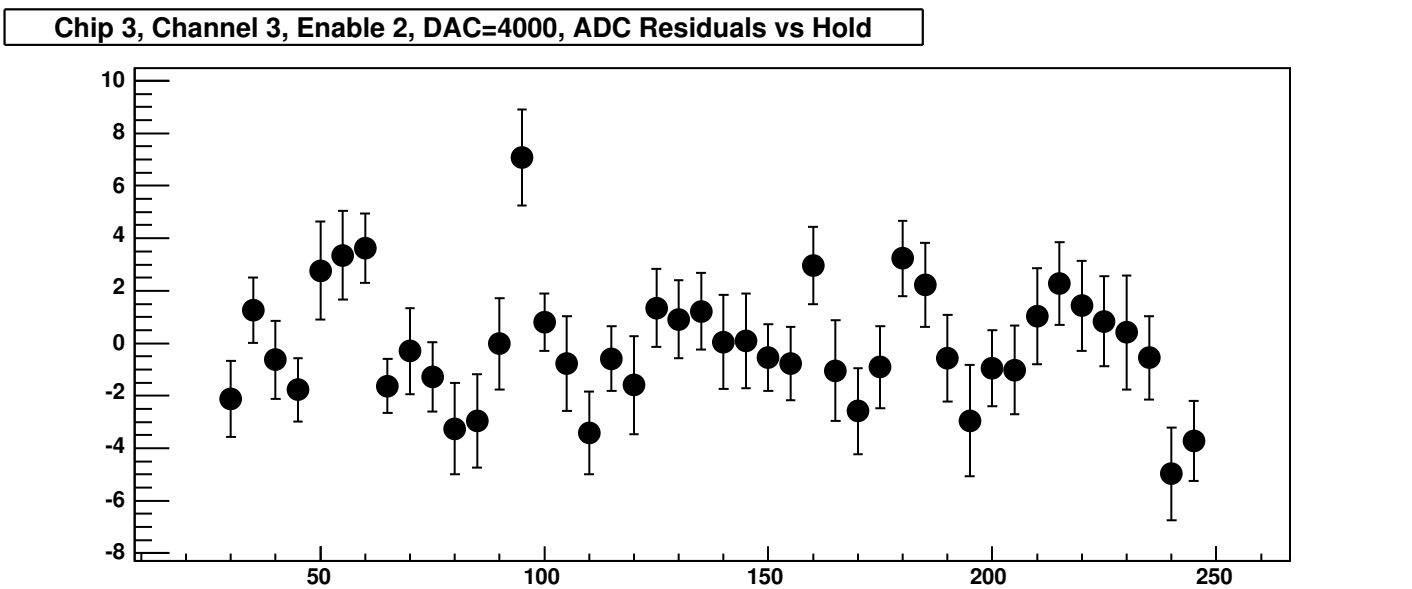


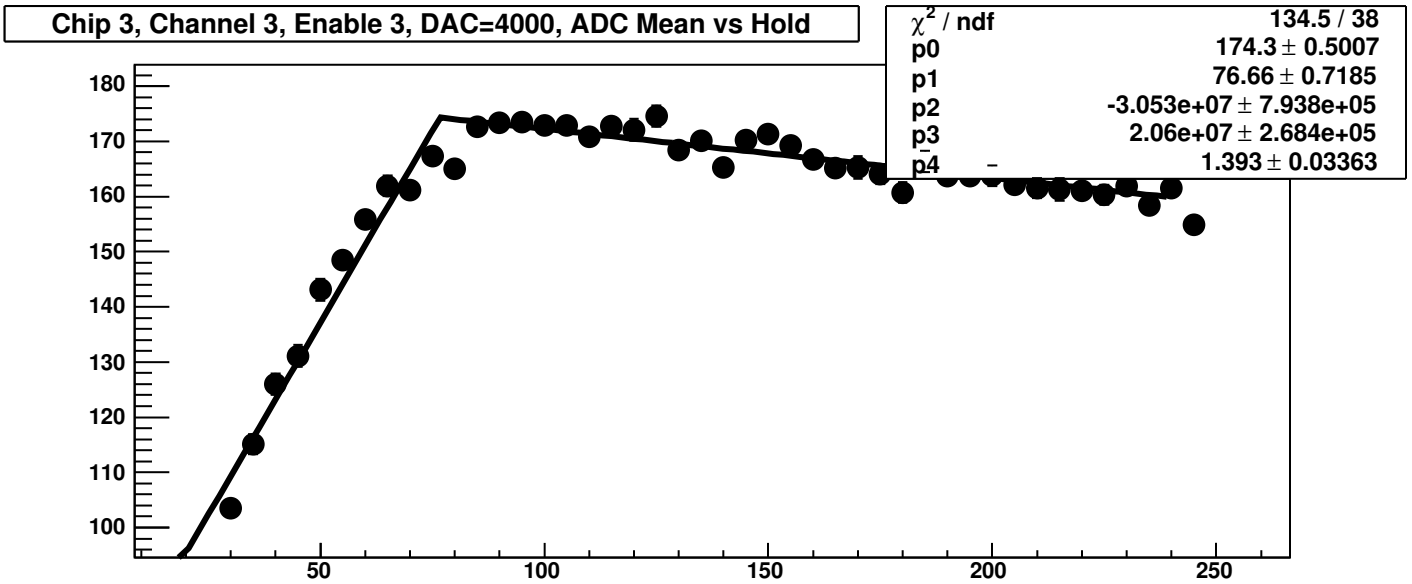
Chip 3, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold

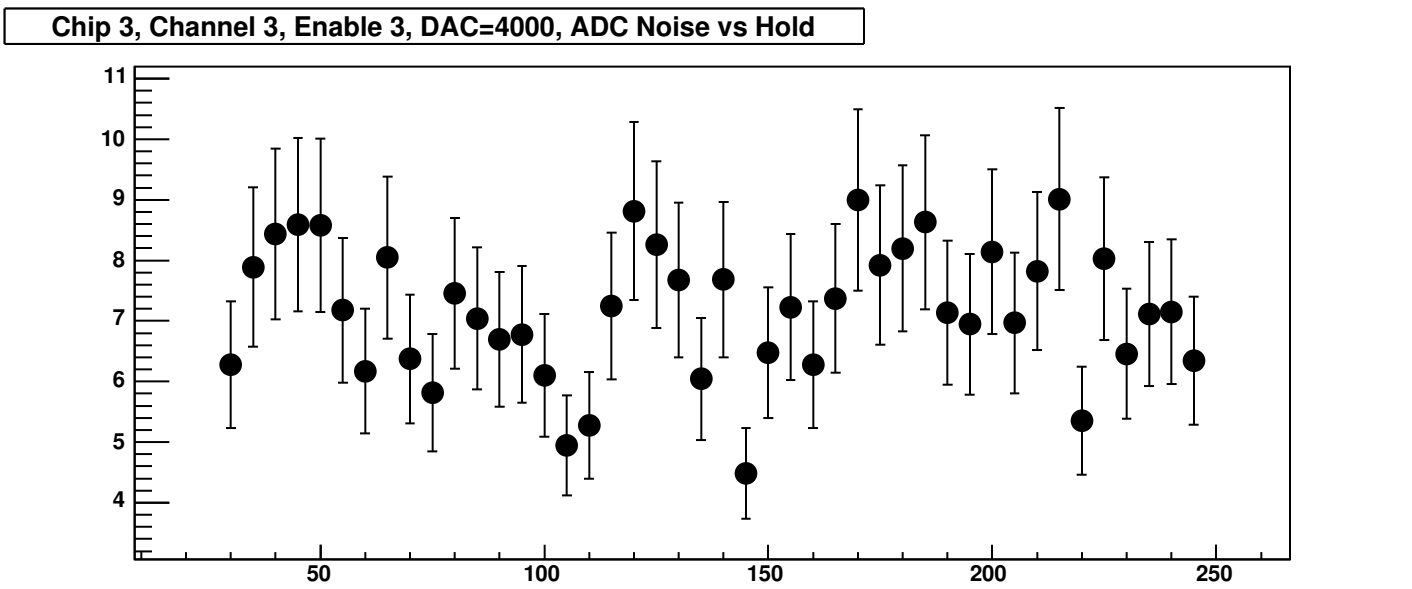


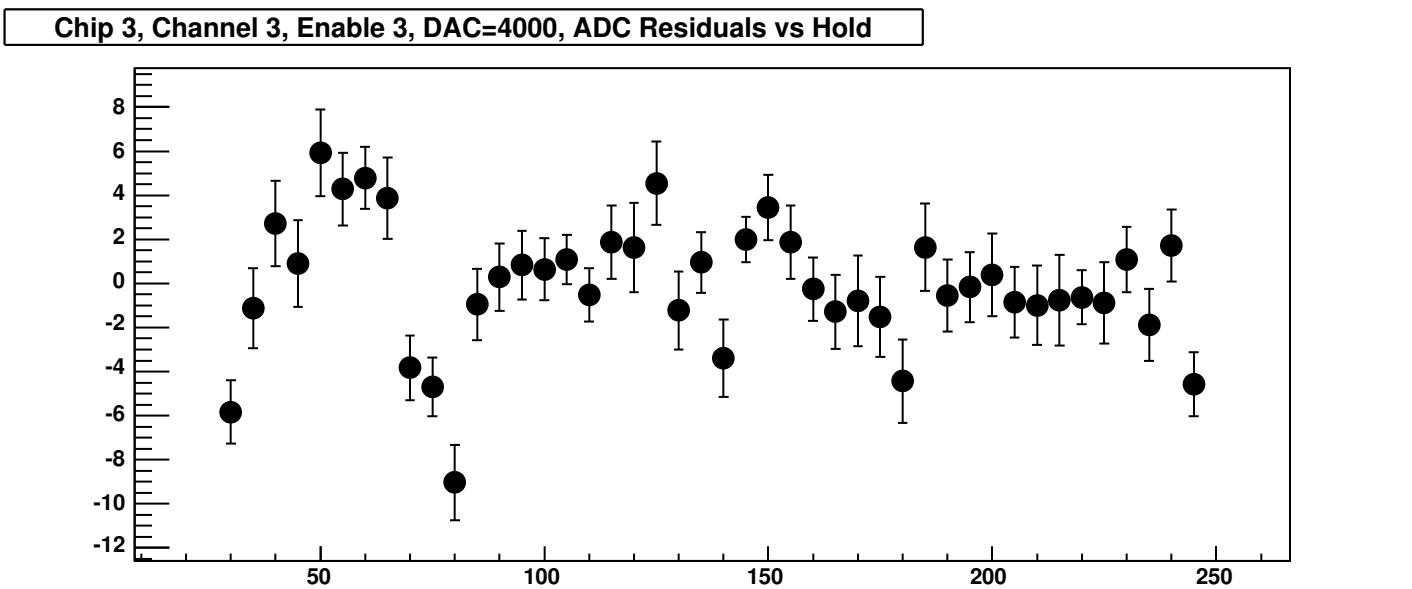

Chip 3, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 81.15 / 38
p0 146.9 ± 0.5771
p1 76.27 ± 0.9842
p2 -2.903e+07 ± 9.13e+05
p3 2.884e+07 ± 3.488e+05
p4 1.04 ± 0.02979
160
150
140
130
120
110
100
90
50
100
150
200
250

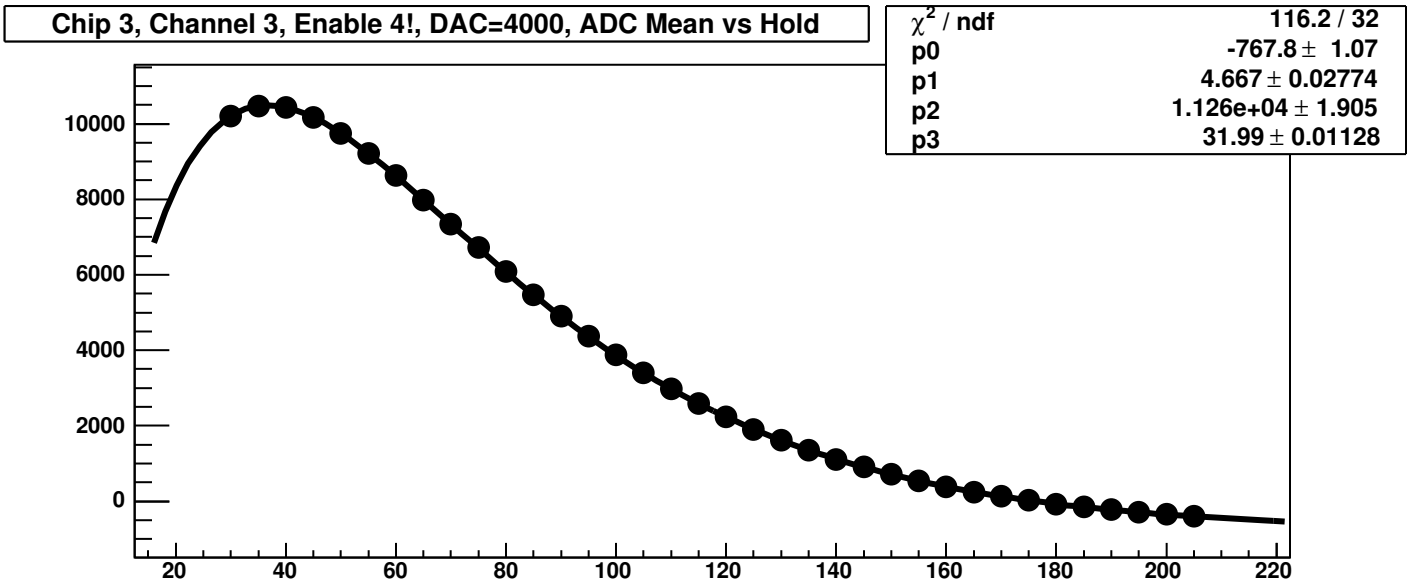

Chip 3, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250

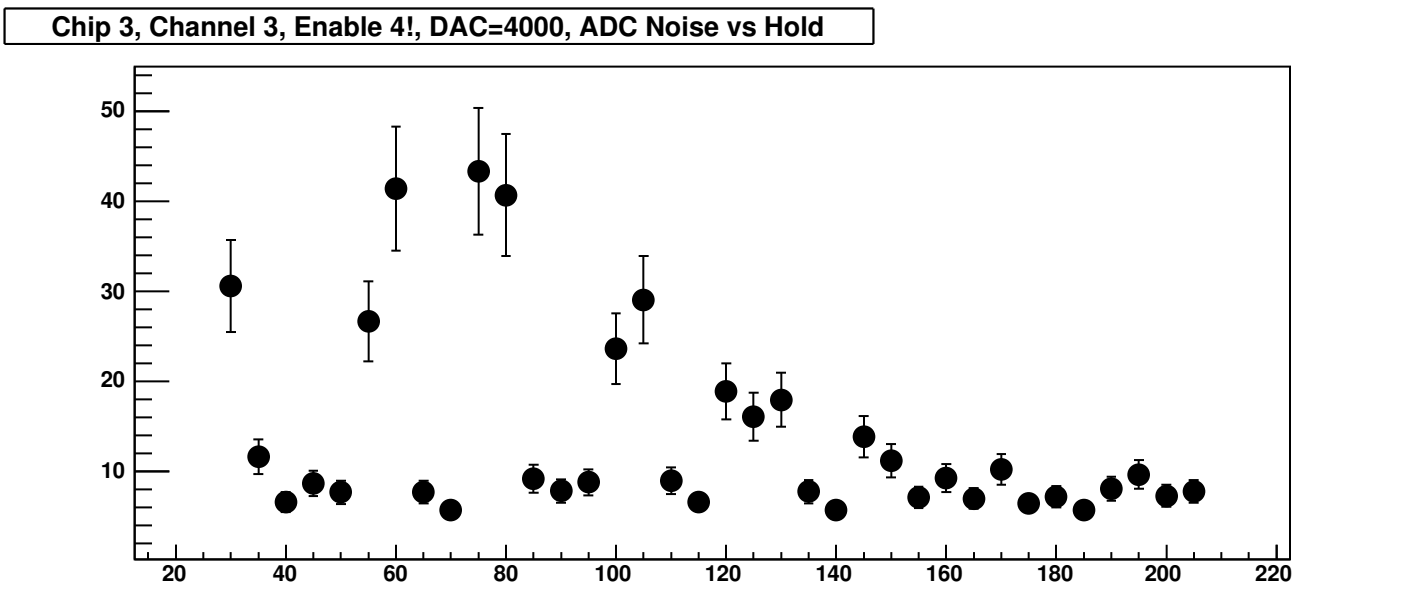

Chip 3, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

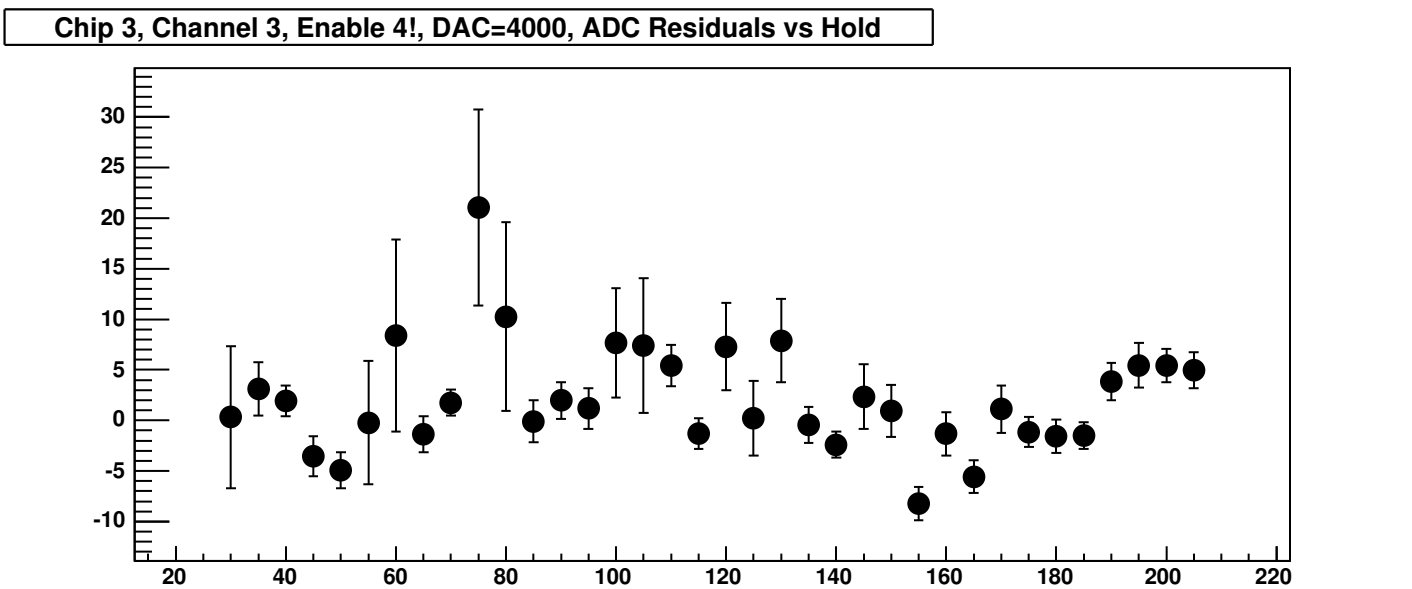

Chip 3, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 134.5 / 38
p0 174.3 ± 0.5007
p1 76.66 ± 0.7185
p2 -3.053e+07 ± 7.938e+05
p3 2.06e+07 ± 2.684e+05
p4 1.393 ± 0.03363
180
170
160
150
140
130
120
110
100
50
100
150
200
250


Chip 3, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250

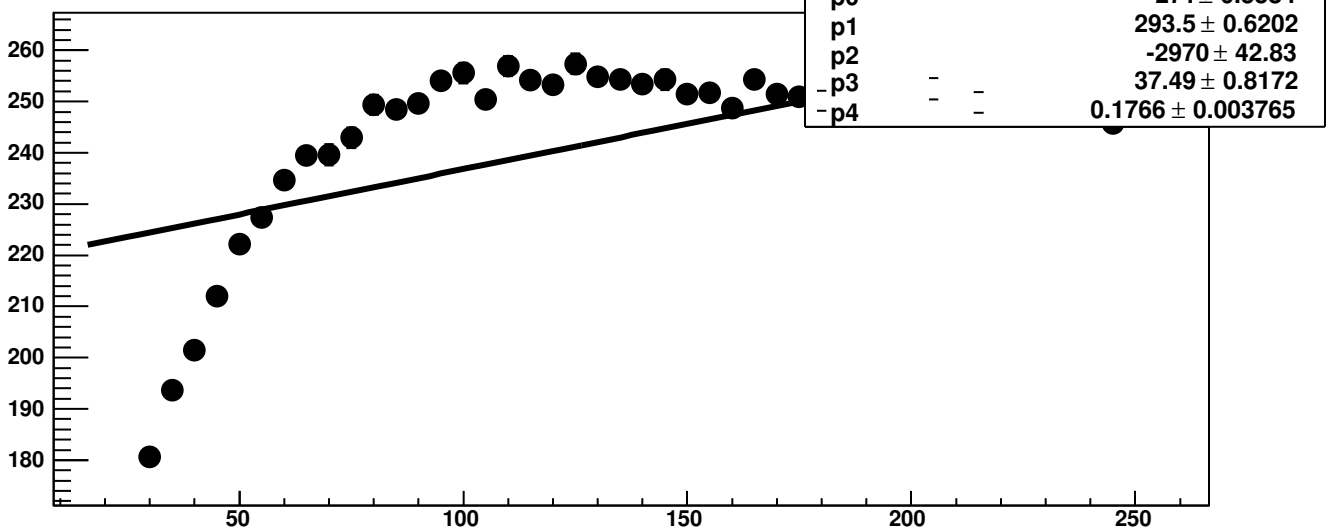

Chip 3, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
-12
50
100
150
200
250


Chip 3, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 116.2 / 32
p0 -767.8 ± 1.07
p1 4.667 ± 0.02774
p2 1.126e+04 ± 1.905
p3 31.99 ± 0.01128
10000
8000
6000
4000
2000
0
20
40
60
80
100
120
140
160
180
200
220

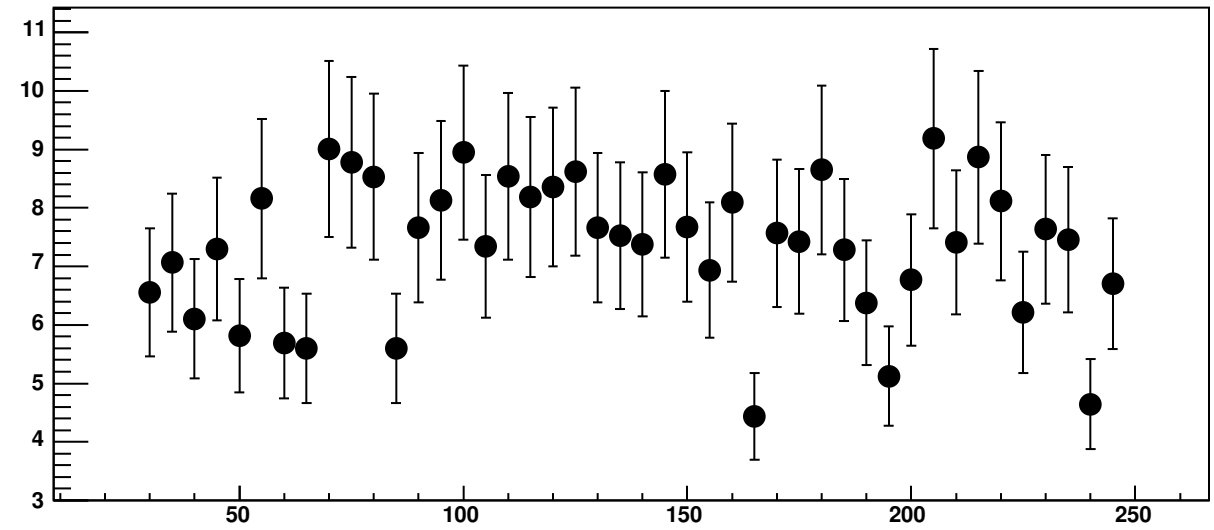

Chip 3, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
20
40
60
80
100
120
140
160
180
200
220


Chip 3, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
20
40
60
80
100
120
140
160
180
200
220

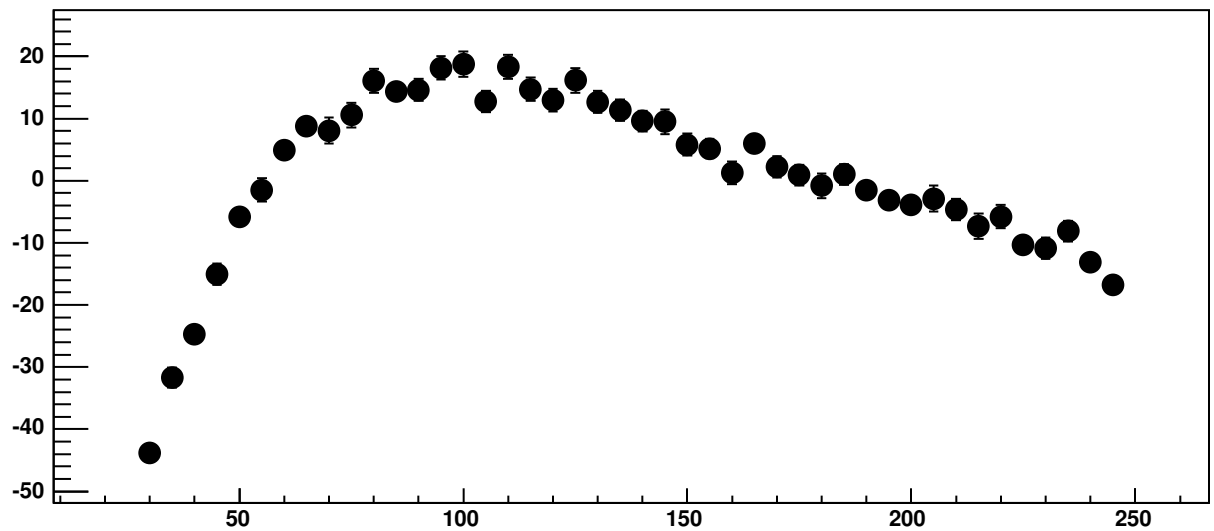
Chip 3, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

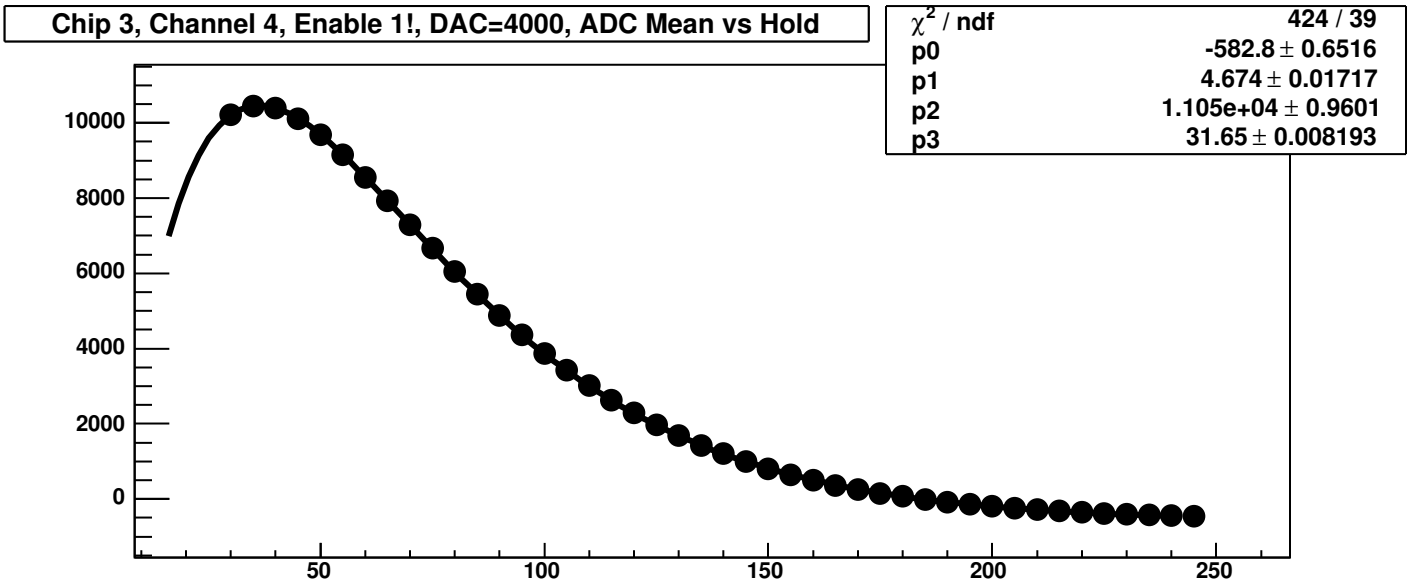


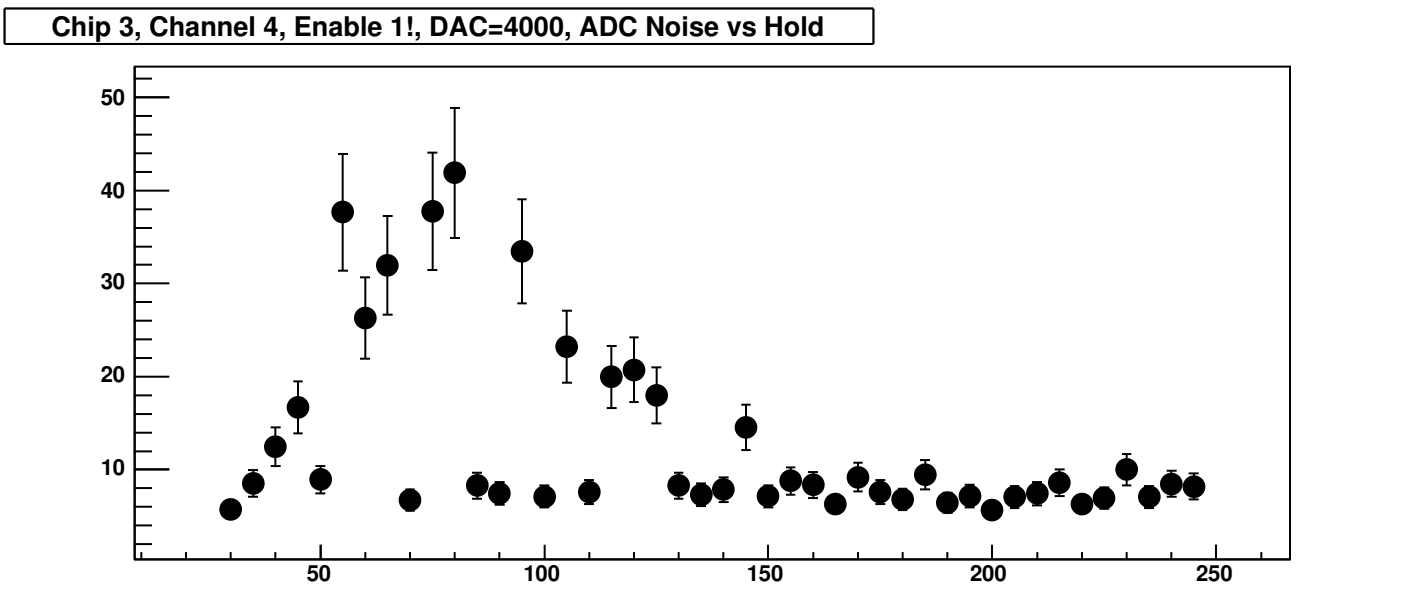
Chip 3, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold

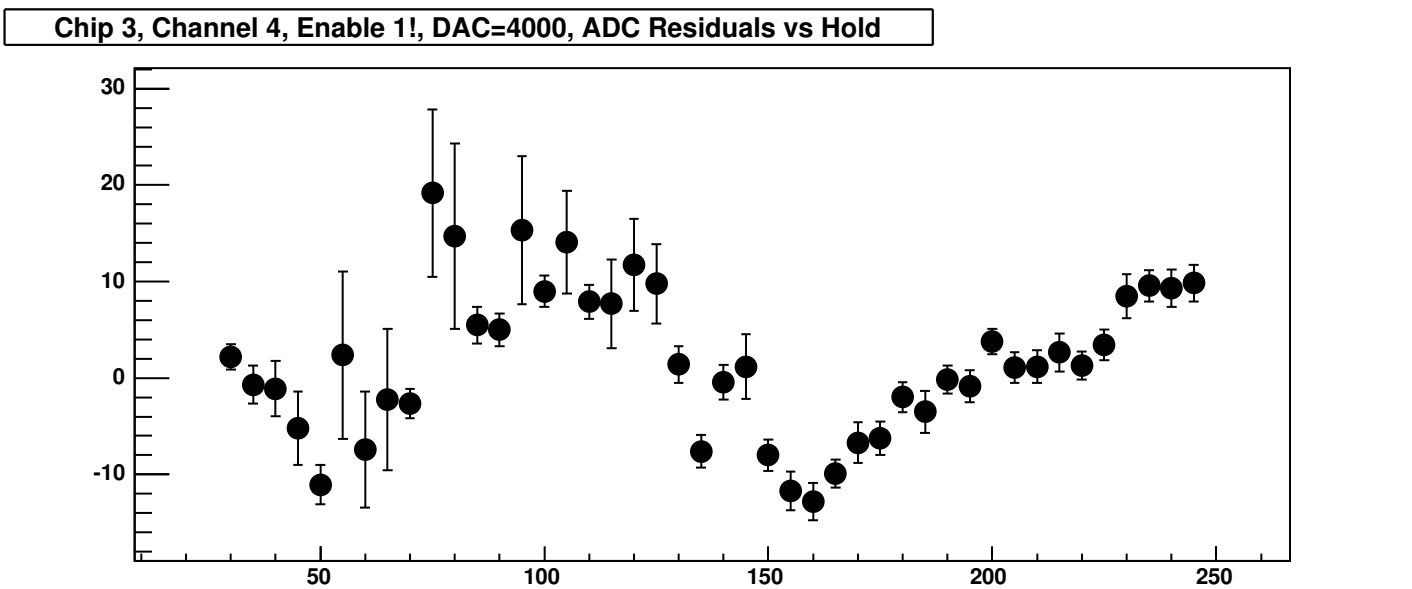


Chip 3, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

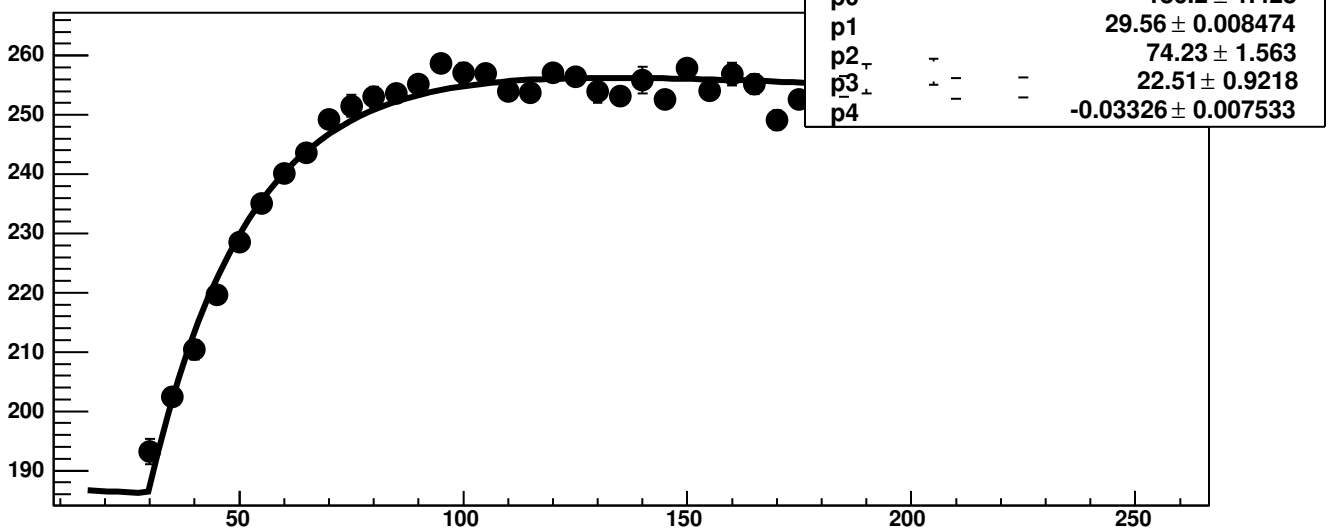



Chip 3, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 424 / 39
p0 -582.8 ± 0.6516
p1 4.674 ± 0.01717
p2 1.105e+04 ± 0.9601
p3 31.65 ± 0.008193
10000
8000
6000
4000
2000
0
50
100
150
200
250

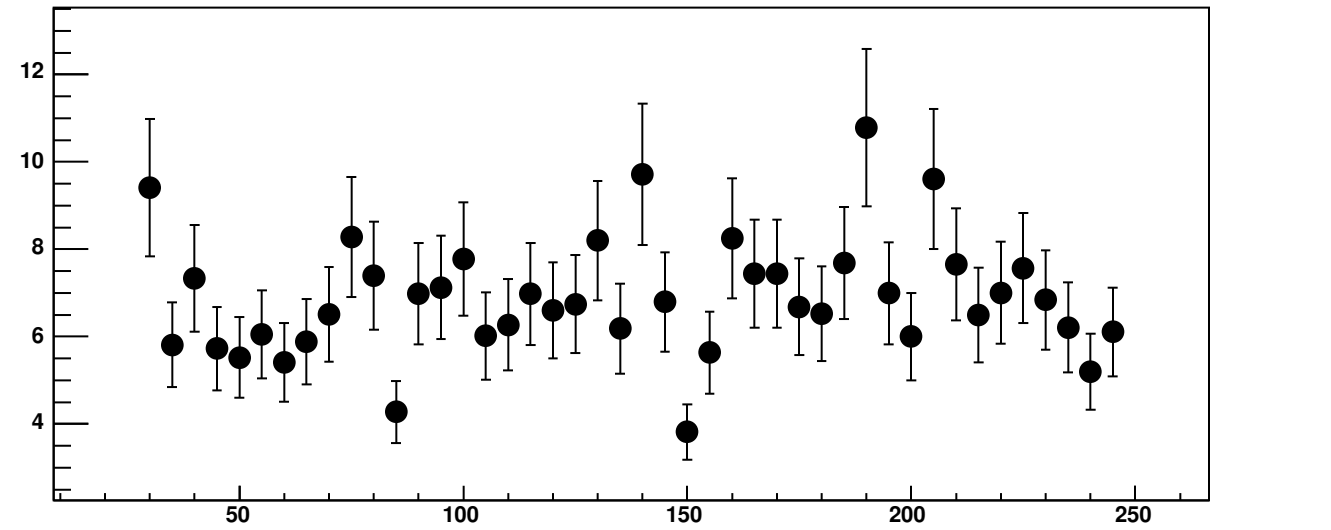

Chip 3, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
50
100
150
200
250


Chip 3, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
50
100
150
200
250

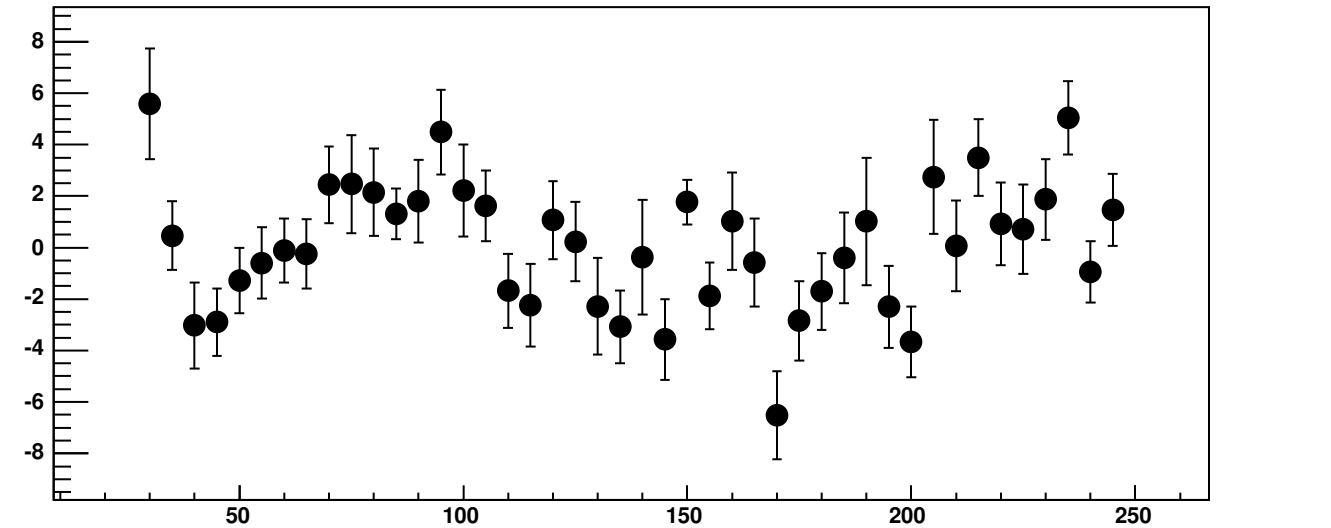
Chip 3, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold



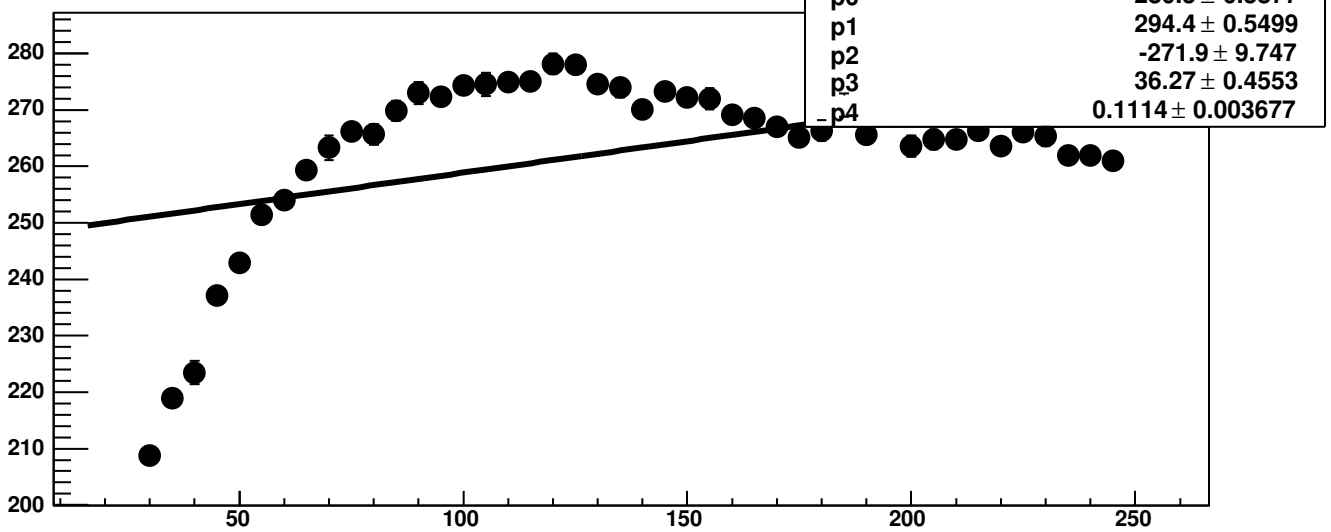
Chip 3, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



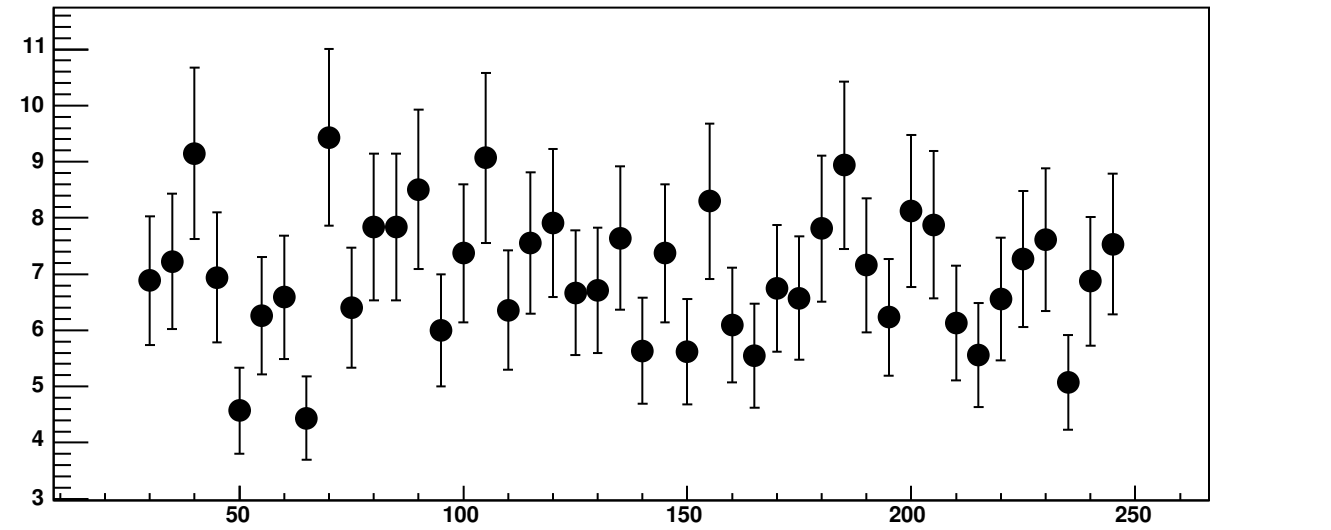
Chip 3, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold



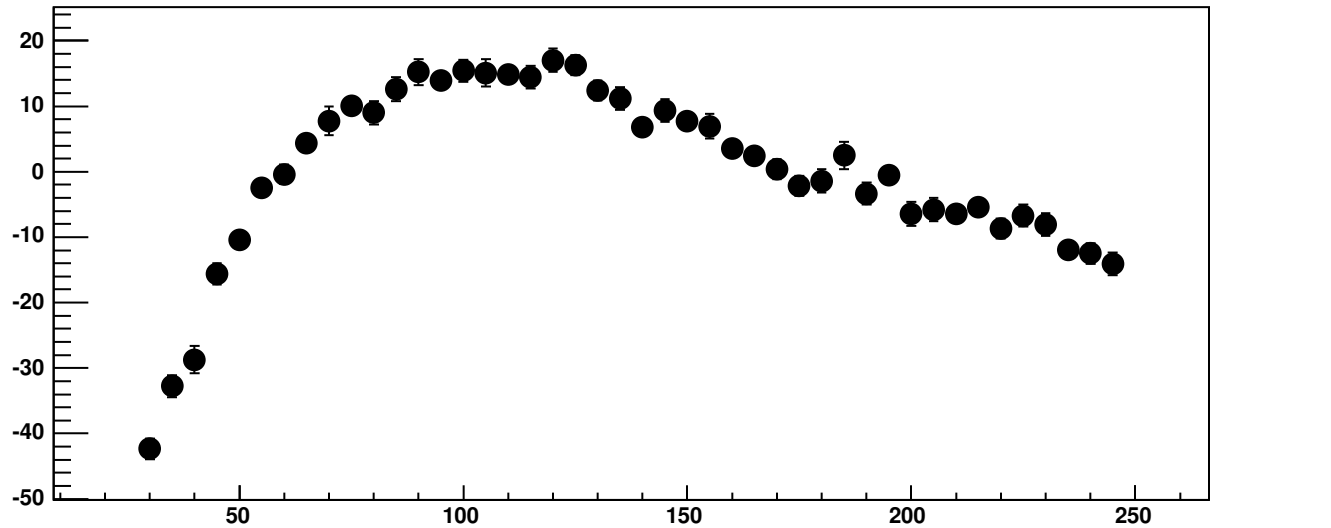
Chip 3, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

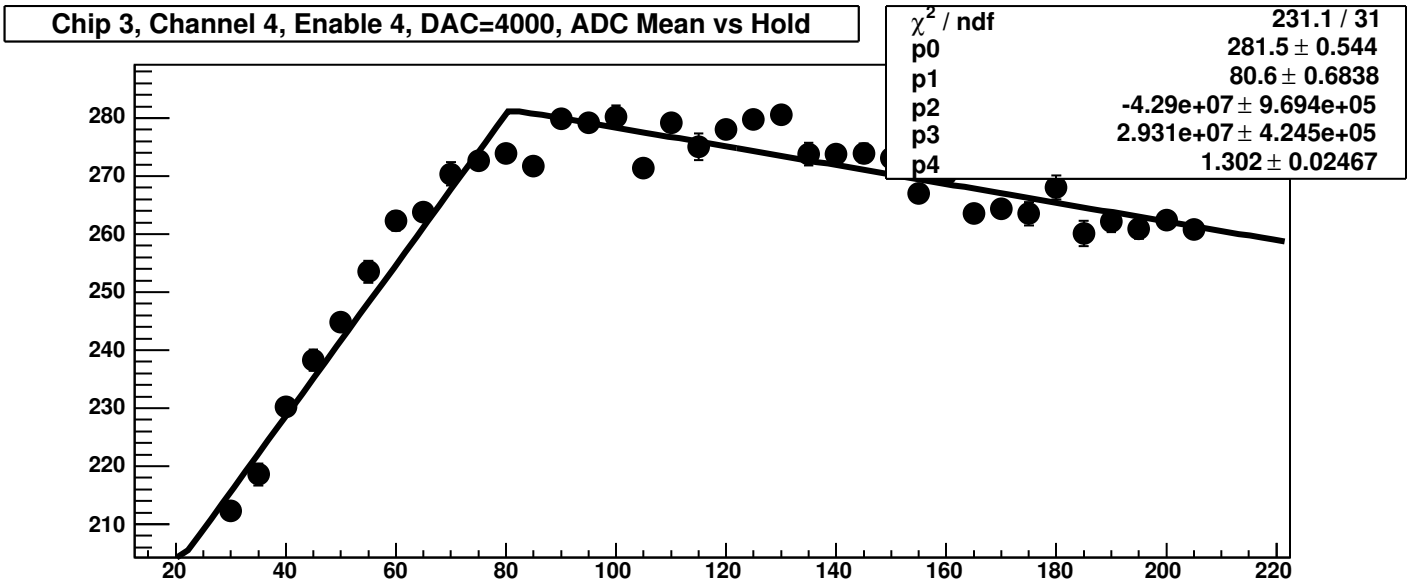


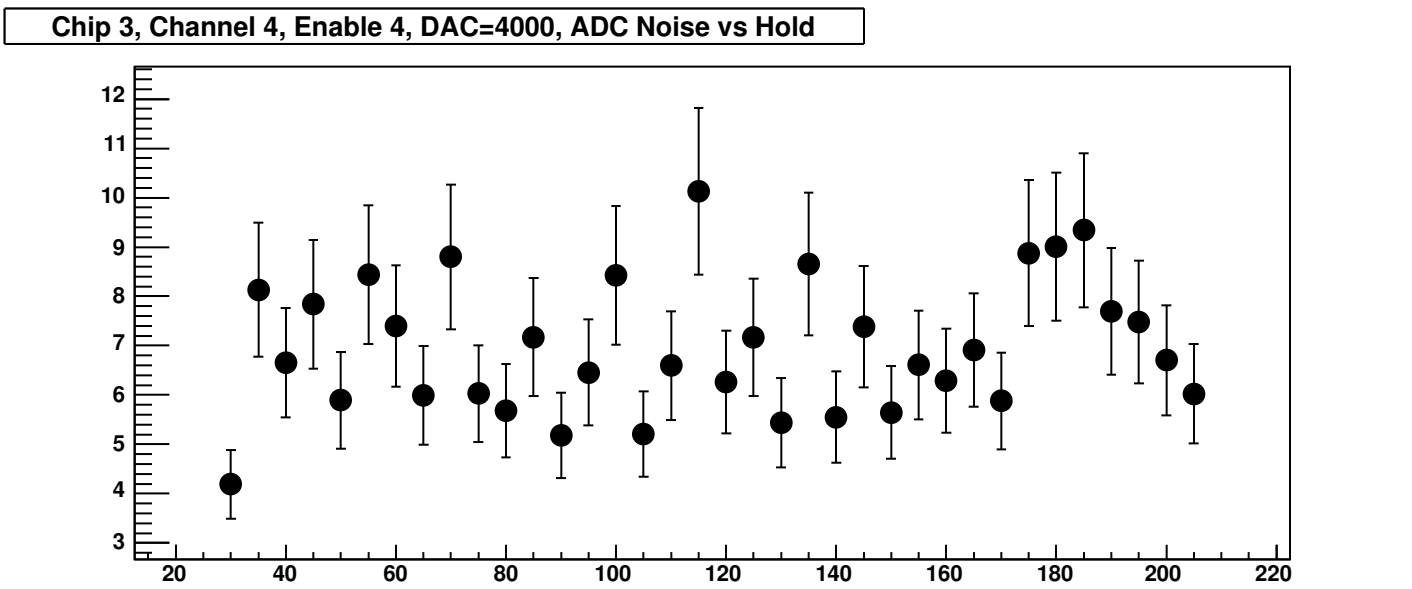
Chip 3, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold

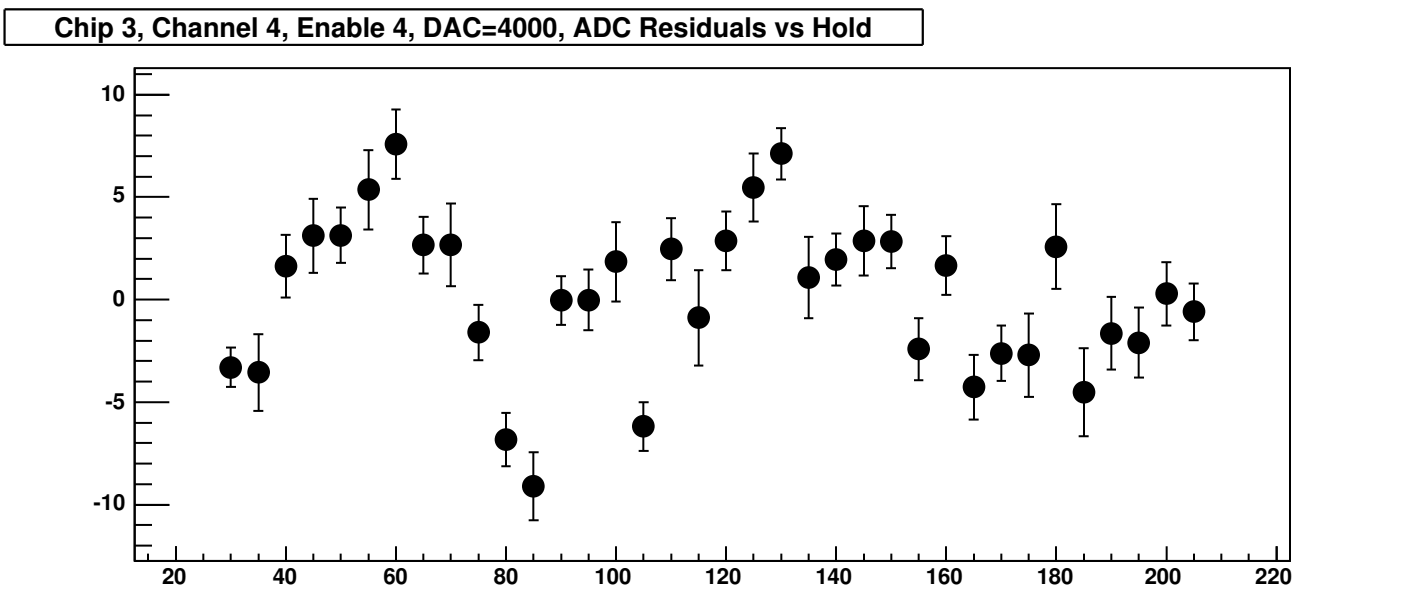


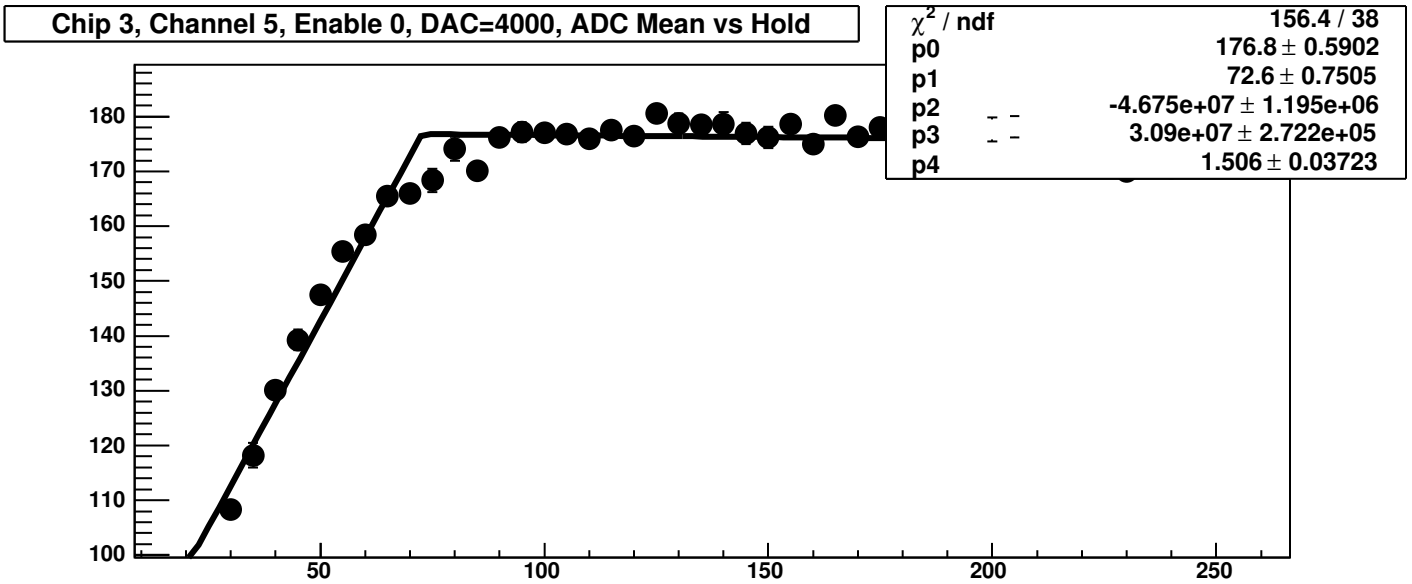
Chip 3, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

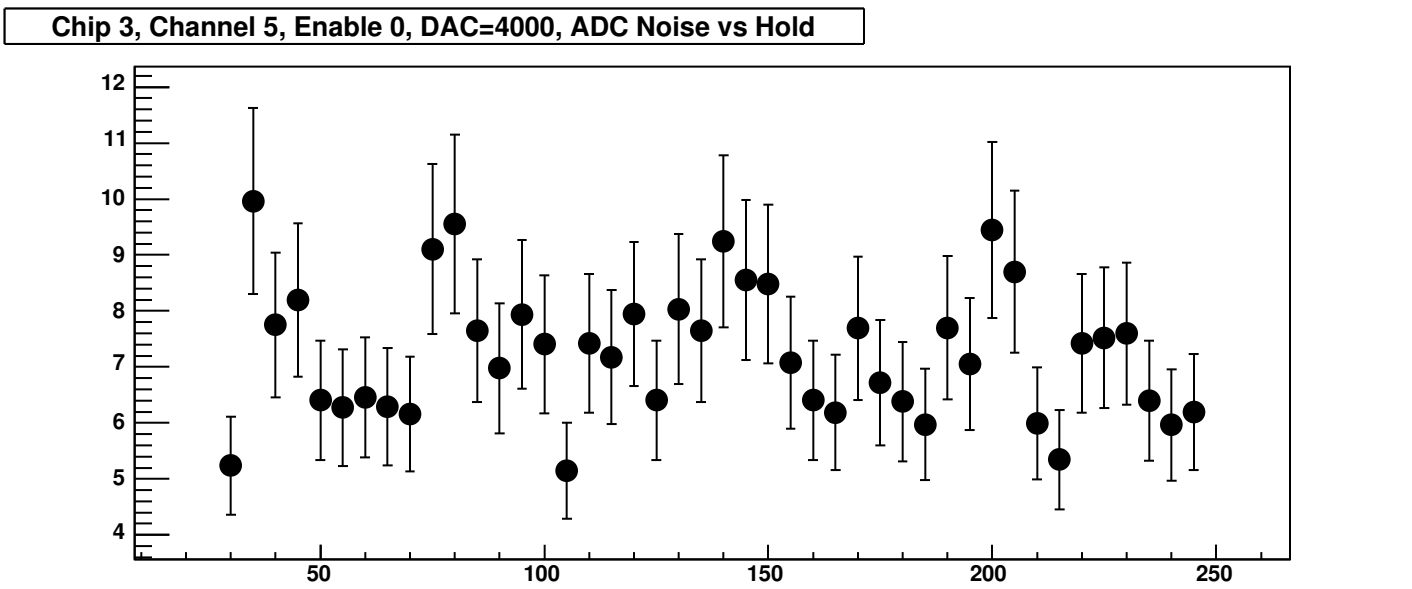


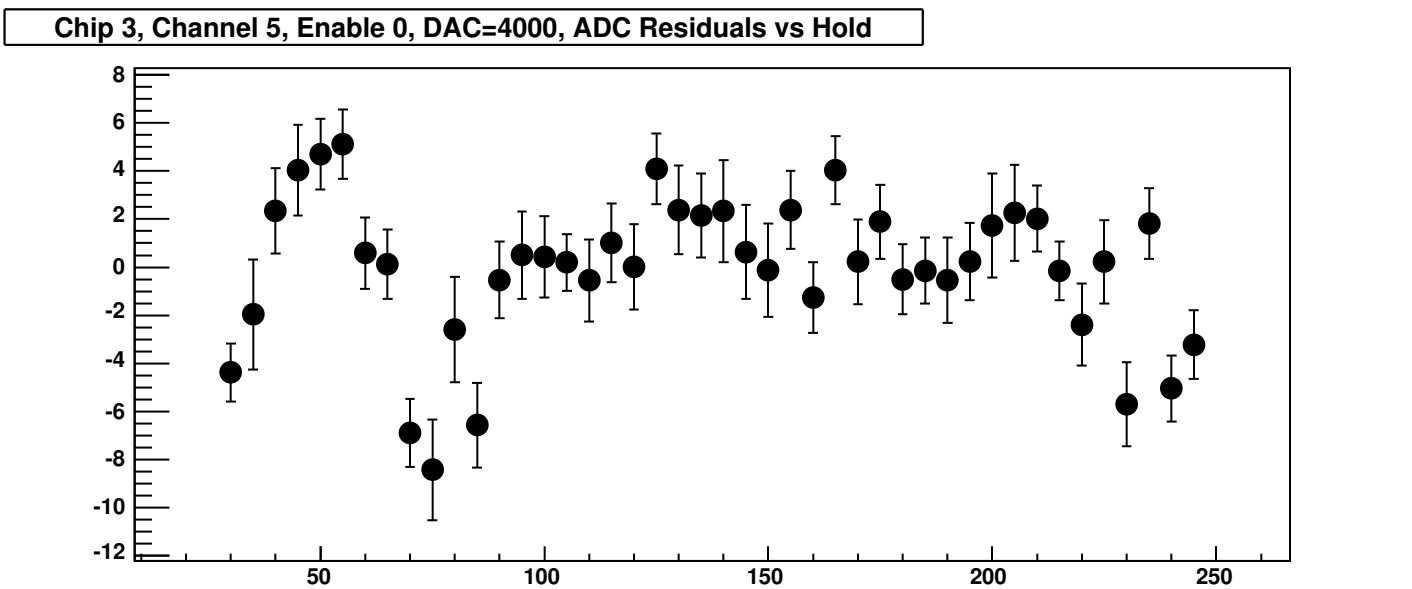

Chip 3, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 231.1 / 31
p0 281.5 ± 0.544
p1 80.6 ± 0.6838
p2 -4.29e+07 ± 9.694e+05
p3 2.931e+07 ± 4.245e+05
p4 1.302 ± 0.02467
280
270
260
250
240
230
220
210
20
40
60
80
100
120
140
160
180
200
220

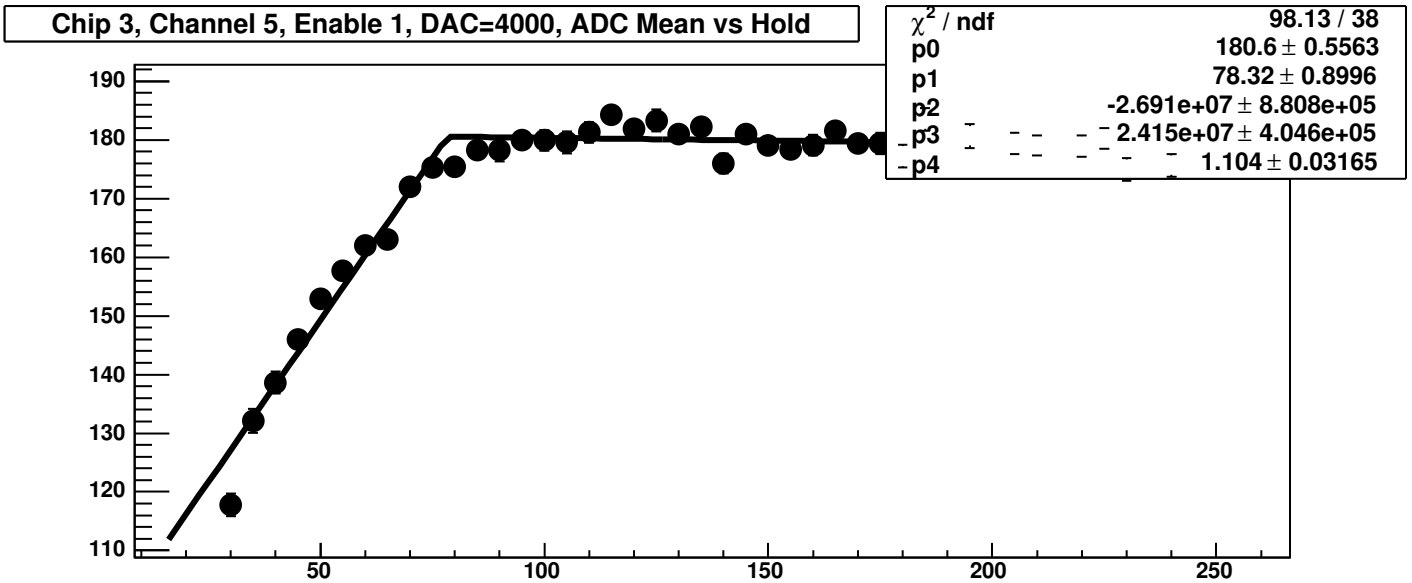

Chip 3, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
20
40
60
80
100
120
140
160
180
200
220

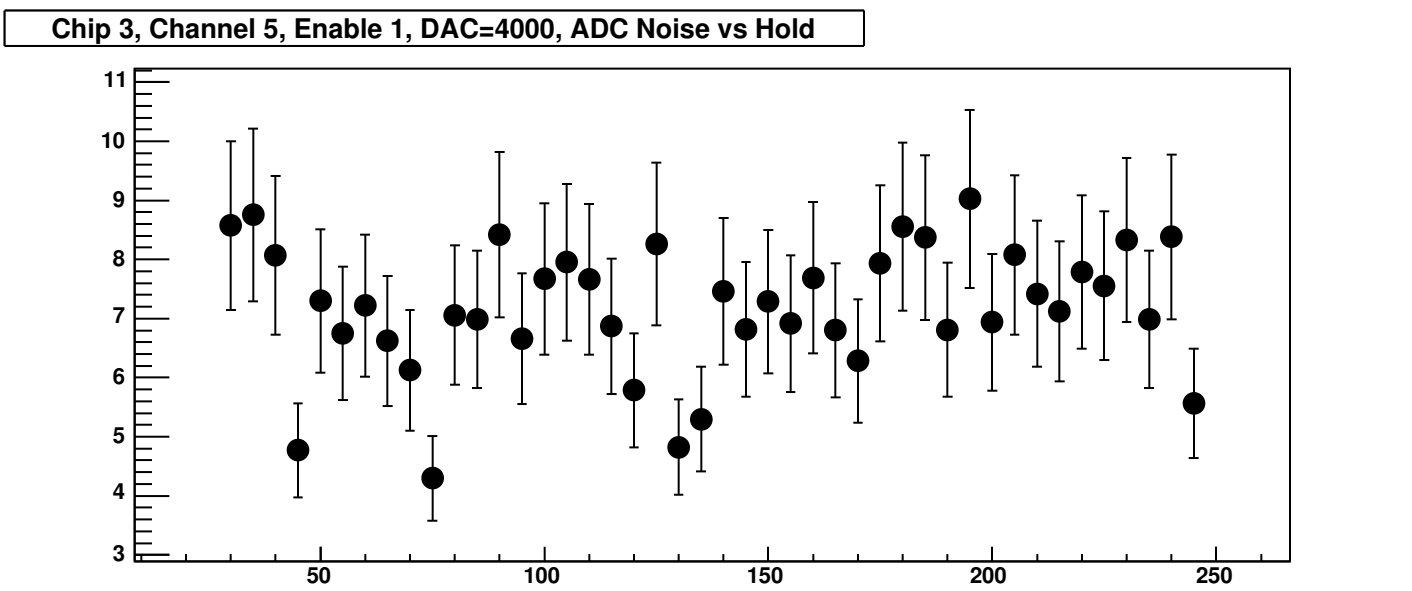

Chip 3, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
20
40
60
80
100
120
140
160
180
200
220

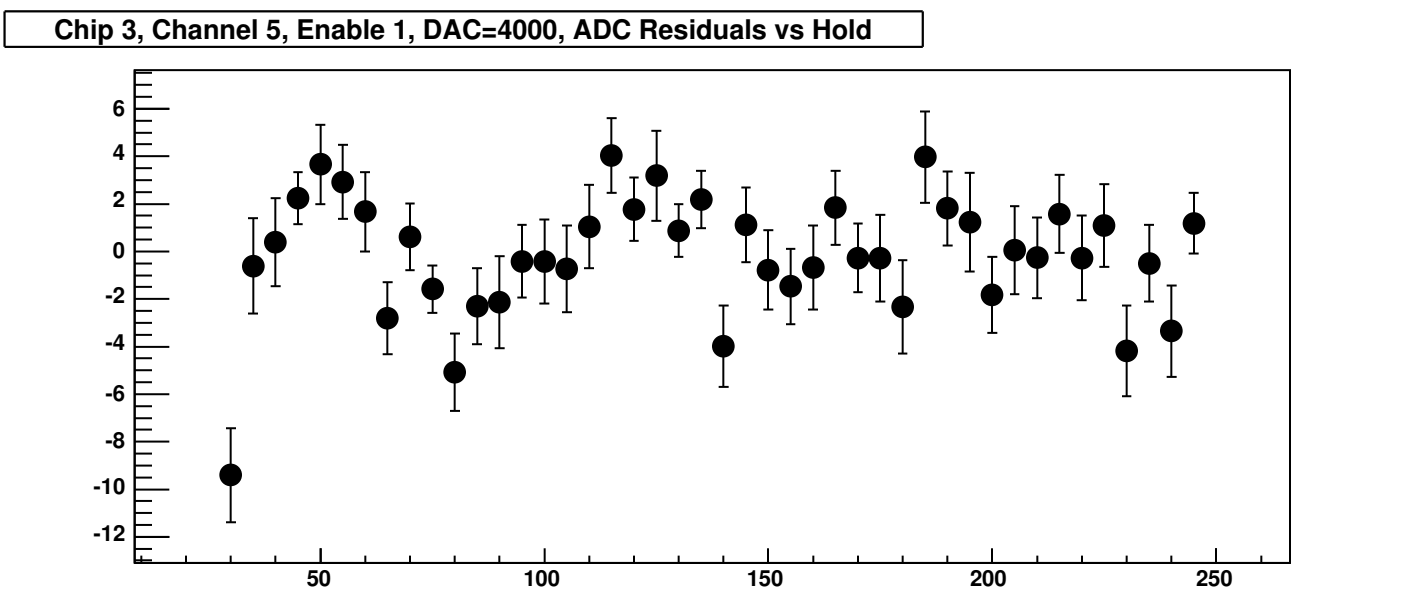

Chip 3, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 156.4 / 38
p0 176.8 ± 0.5902
p1 72.6 ± 0.7505
p2 -4.675e+07 ± 1.195e+06
p3 3.09e+07 ± 2.722e+05
p4 1.506 ± 0.03723
180
170
160
150
140
130
120
110
100
50
100
150
200
250

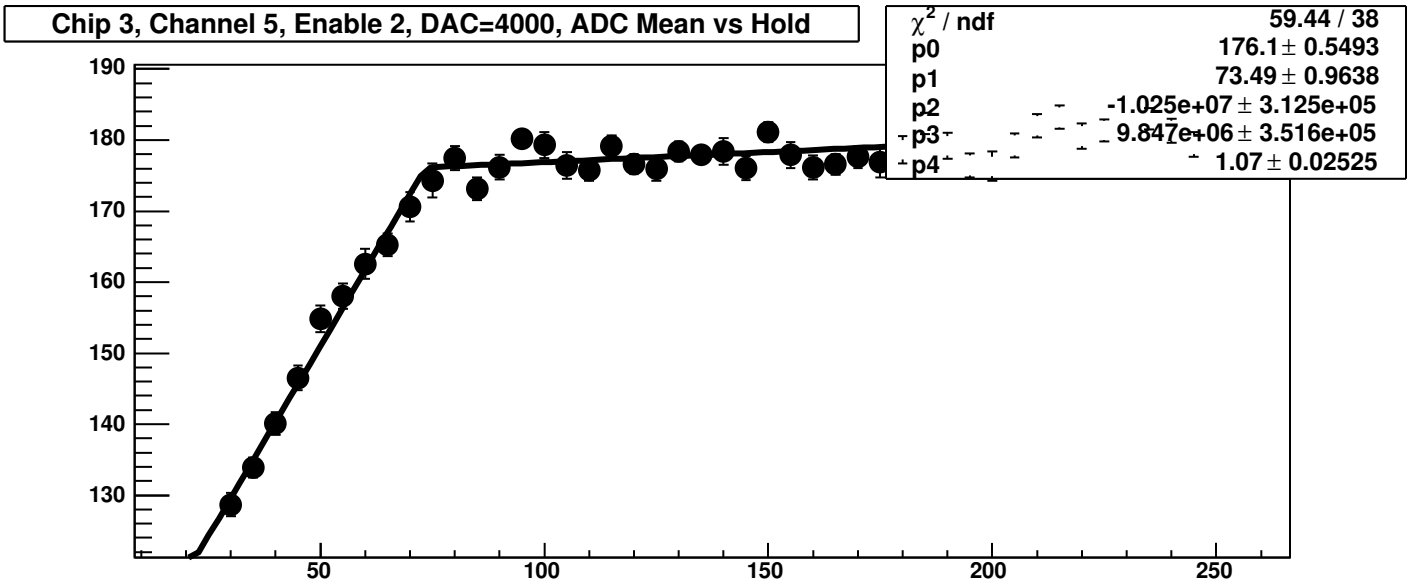

Chip 3, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

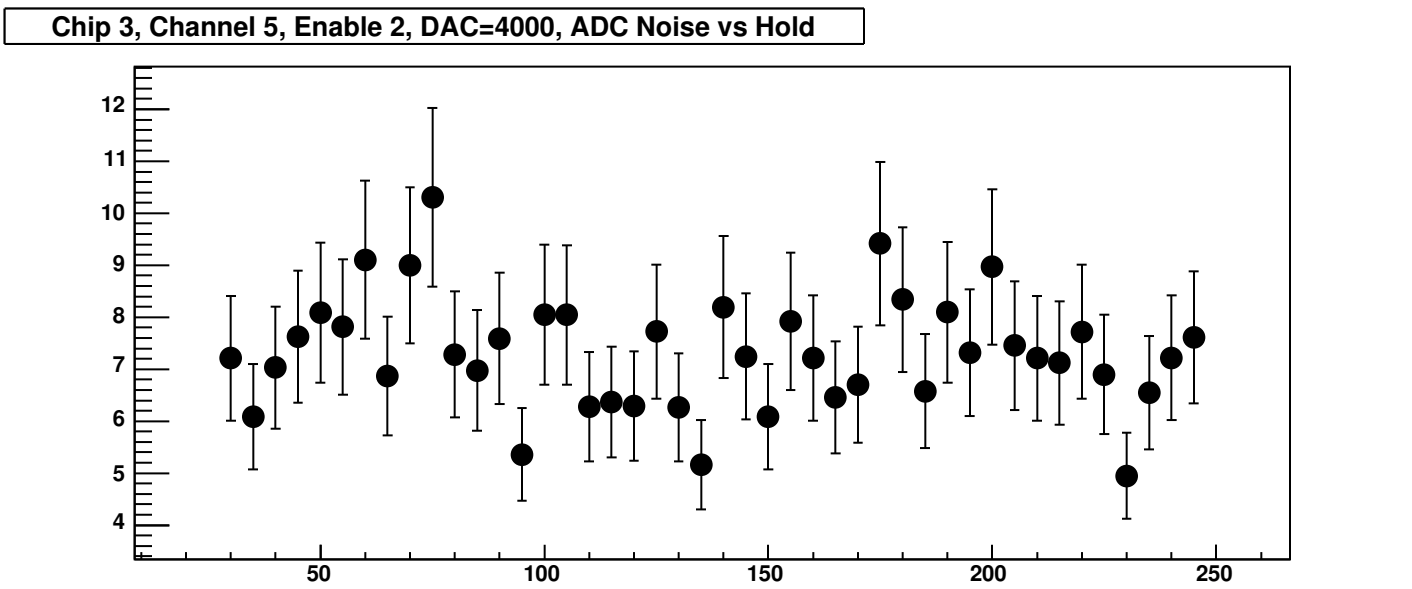

Chip 3, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
-12
50
100
150
200
250

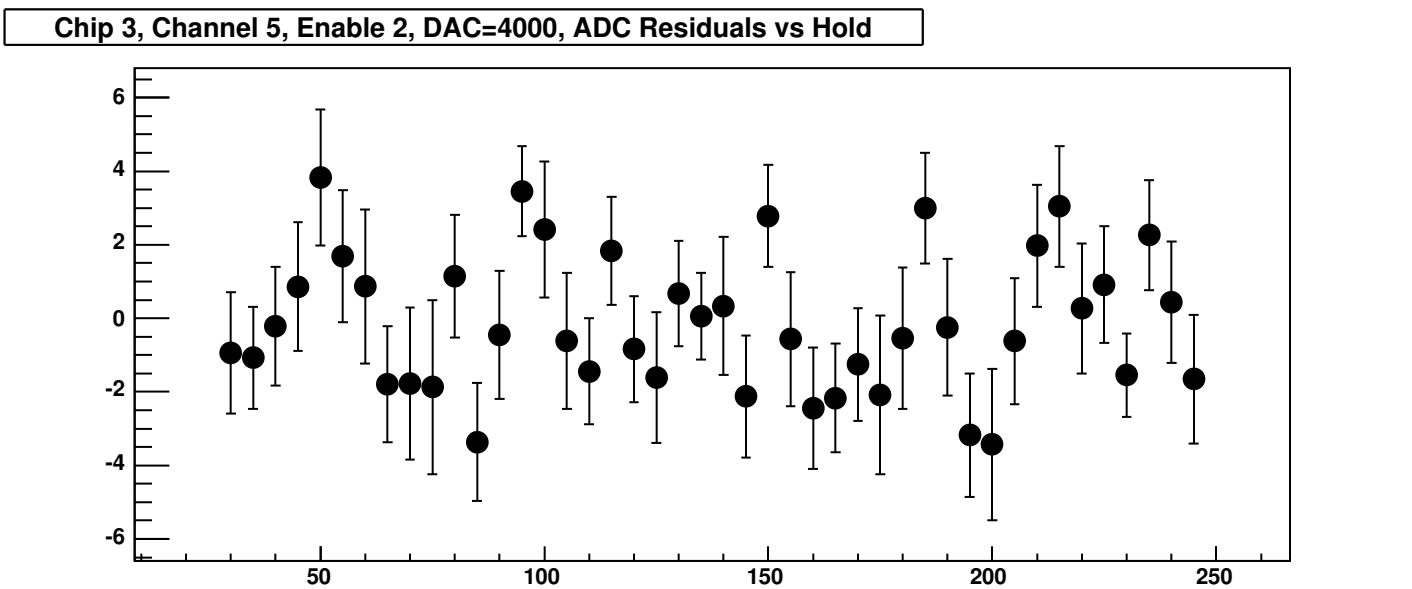

Chip 3, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 98.13 / 38
p0 180.6 ± 0.5563
p1 78.32 ± 0.8996
p2 -2.691e+07 ± 8.808e+05
p3 2.415e+07 ± 4.046e+05
p4 1.104 ± 0.03165
190
180
170
160
150
140
130
120
110
50
100
150
200
250

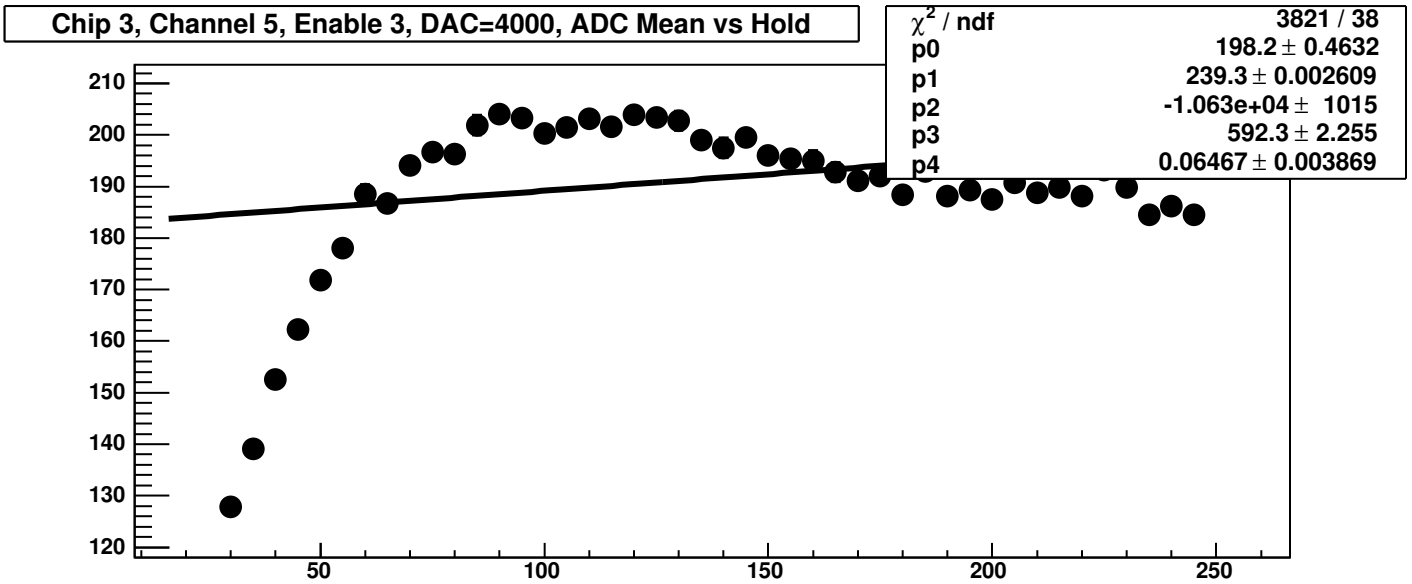

Chip 3, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
50
100
150
200
250

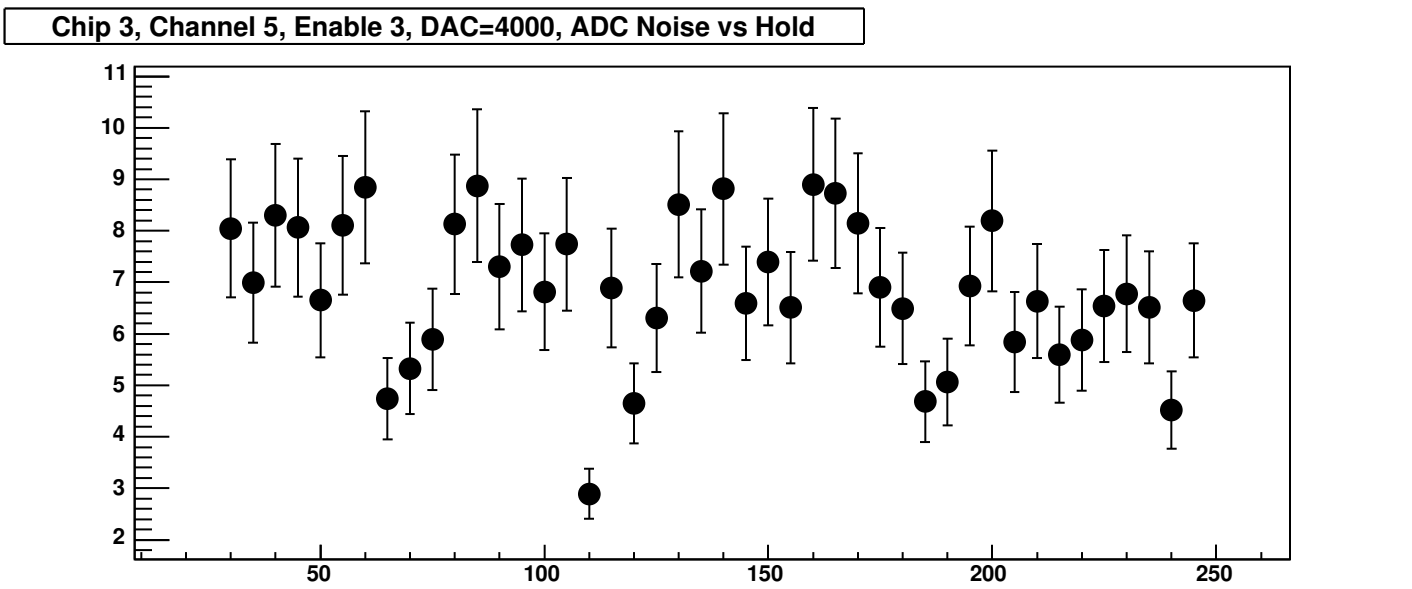

Chip 3, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
-8
-10
-12
50
100
150
200
250

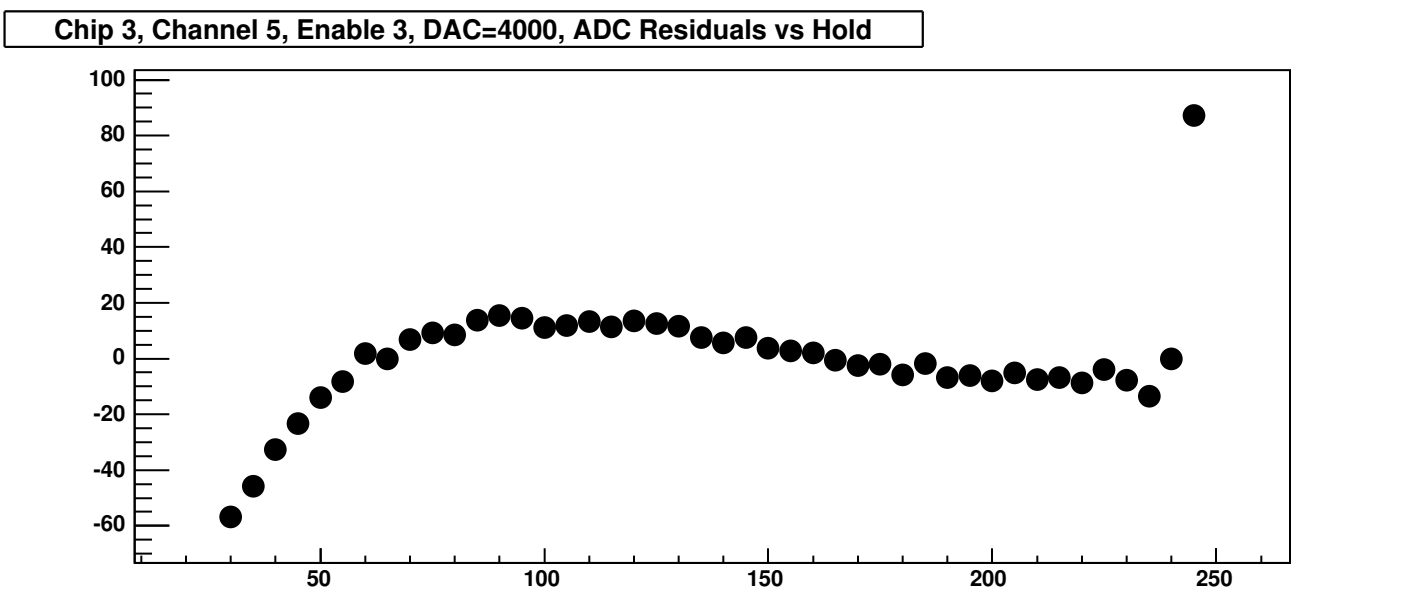

Chip 3, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 59.44 / 38
p0 176.1 ± 0.5493
p1 73.49 ± 0.9638
p2 -1.025e+07 ± 3.125e+05
p3 9.847e+06 ± 3.516e+05
p4 1.07 ± 0.02525
190
180
170
160
150
140
130
50
100
150
200
250

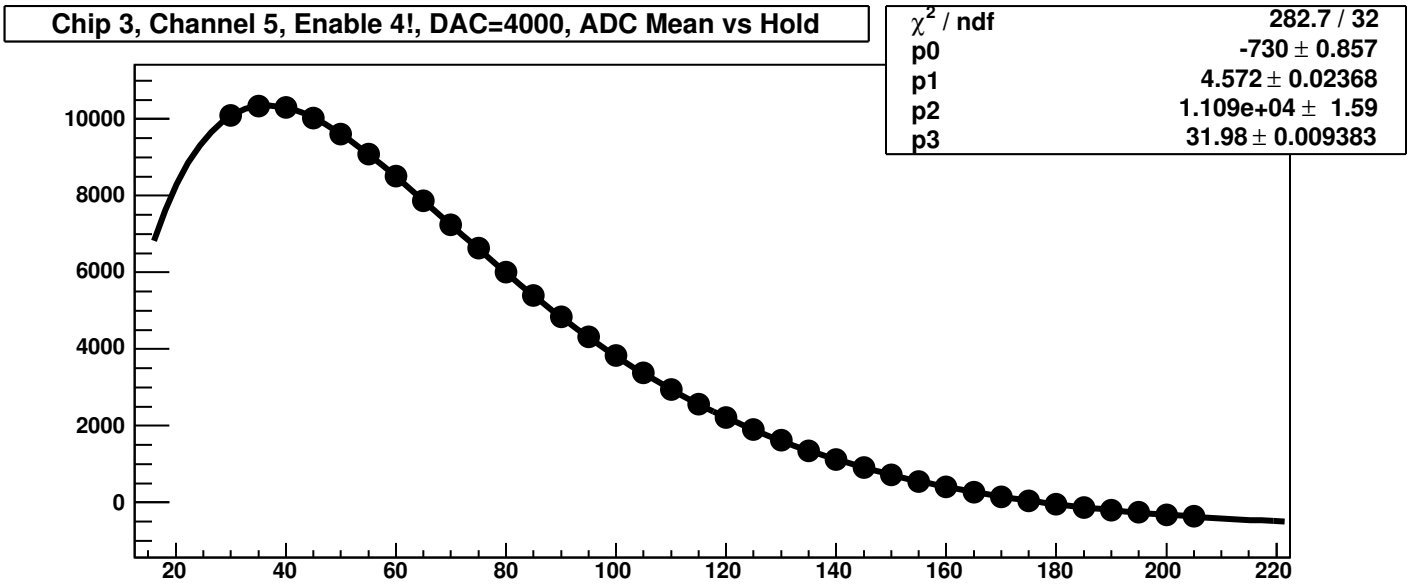

Chip 3, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

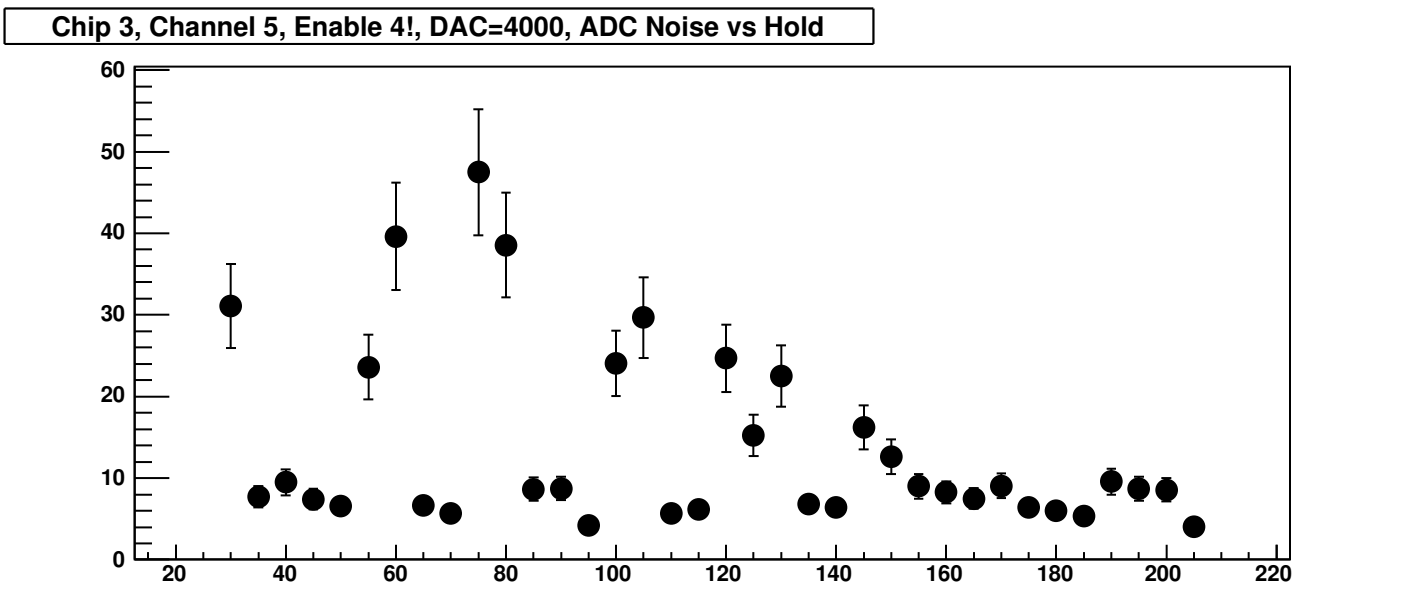

Chip 3, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
50
100
150
200
250

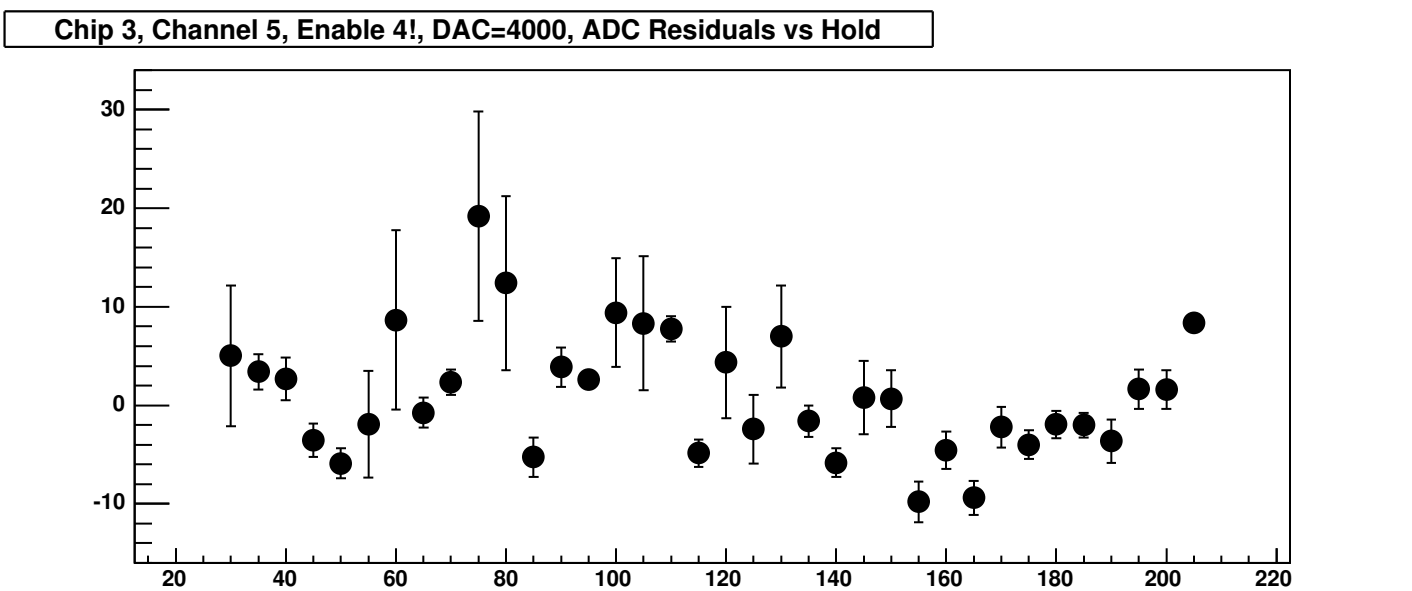

Chip 3, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 3821 / 38
p0 198.2 ± 0.4632
p1 239.3 ± 0.002609
p2 -1.063e+04 ± 1015
p3 592.3 ± 2.255
p4 0.06467 ± 0.003869
210
200
190
180
170
160
150
140
130
120
50
100
150
200
250

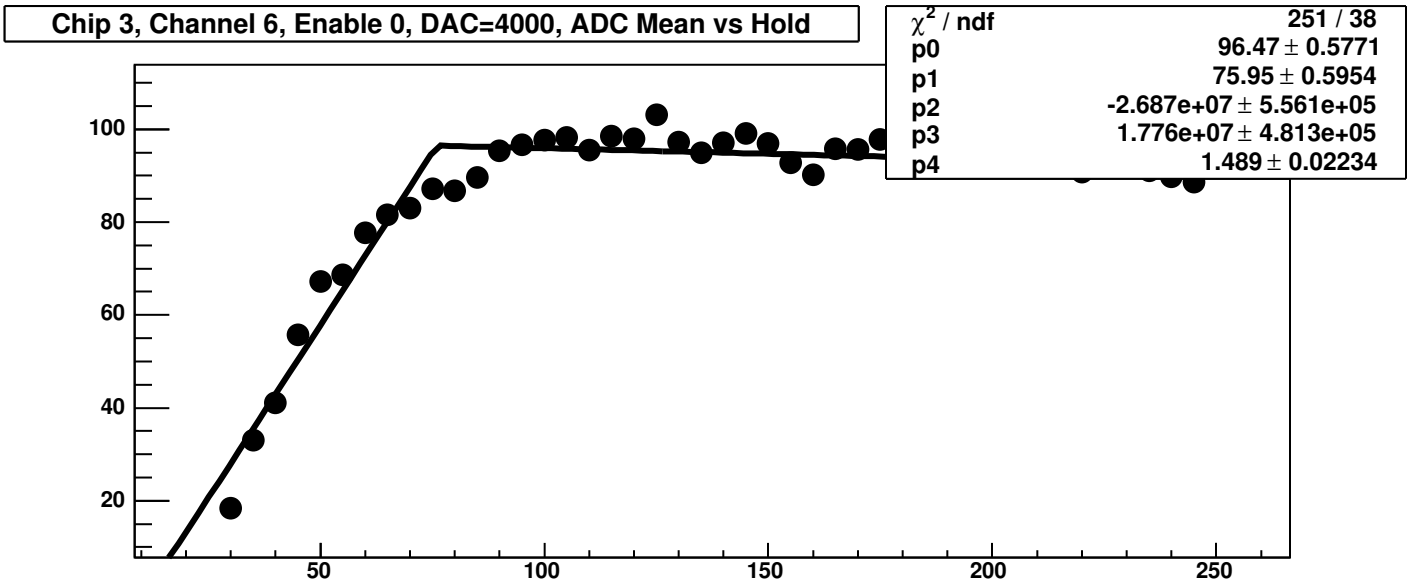

Chip 3, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
2
50
100
150
200
250

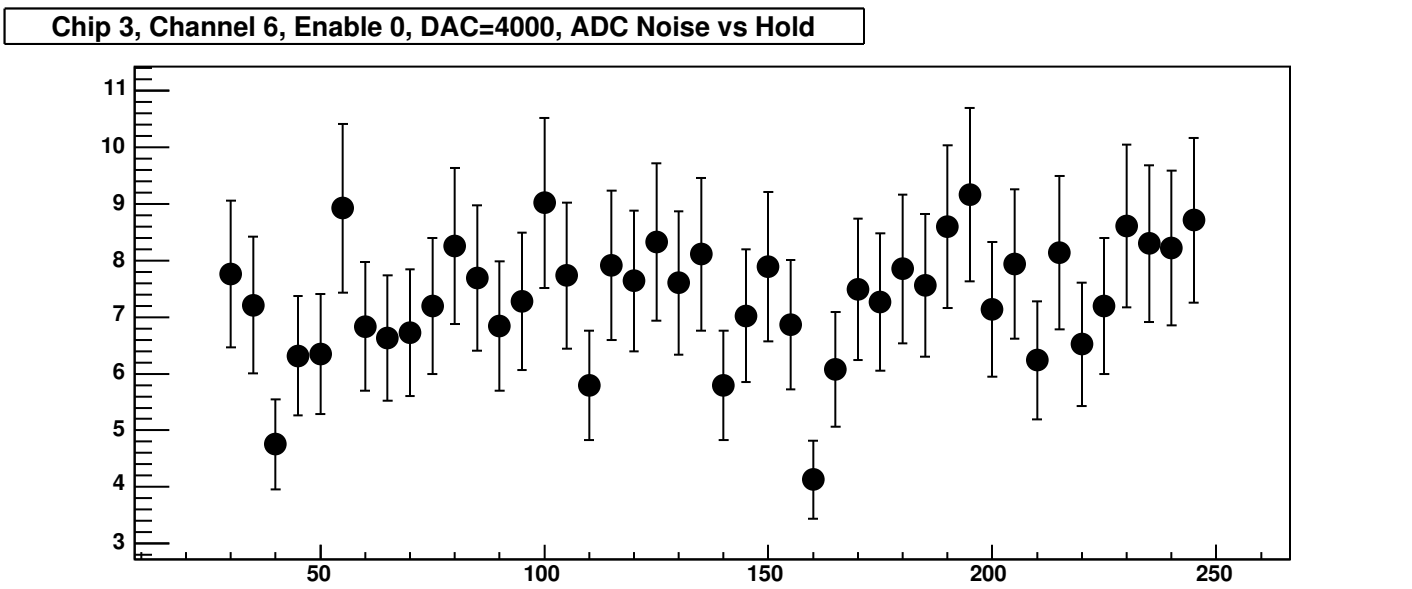

Chip 3, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold
100
80
60
40
20
0
-20
-40
-60
50
100
150
200
250

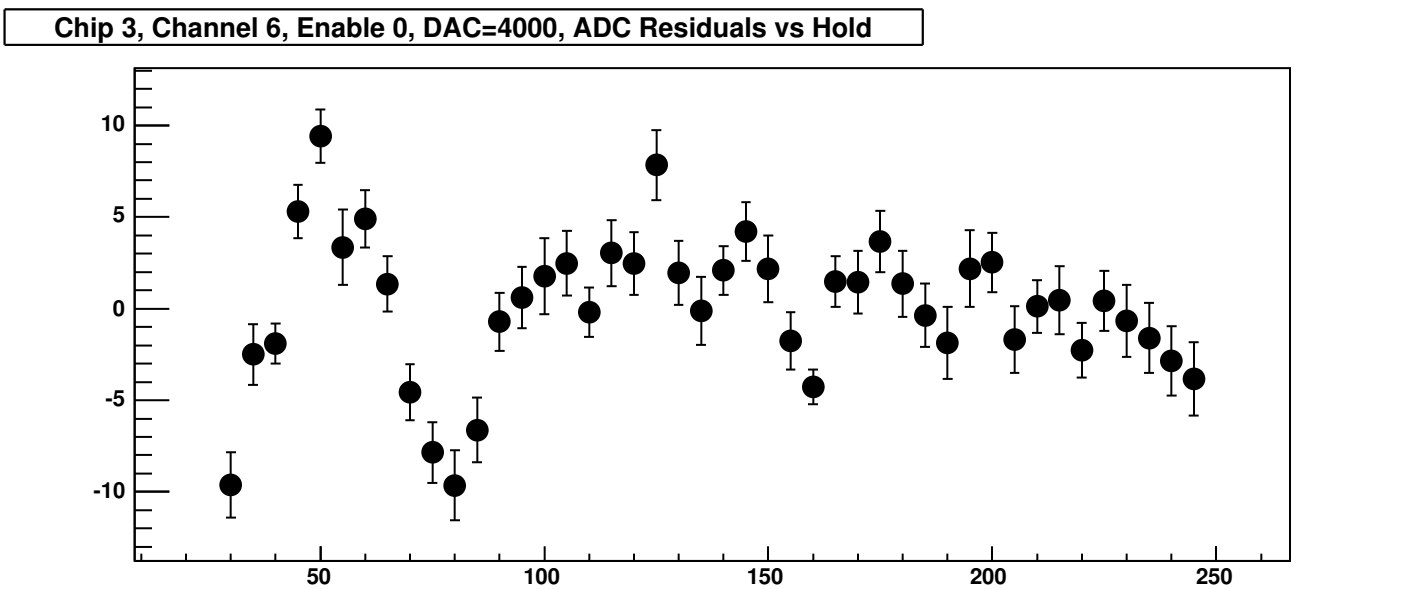

Chip 3, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 282.7 / 32
p0 -730 ± 0.857
p1 4.572 ± 0.02368
p2 1.109e+04 ± 1.59
p3 31.98 ± 0.009383
10000
8000
6000
4000
2000
0
20
40
60
80
100
120
140
160
180
200
220

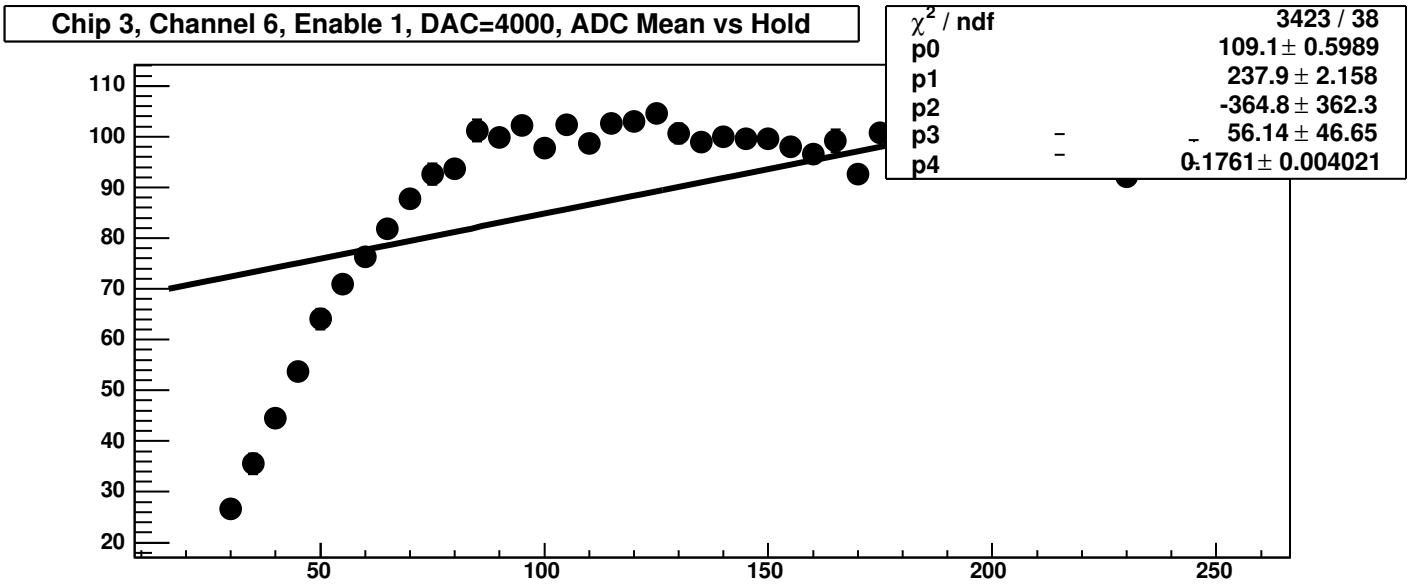

Chip 3, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold
60
50
40
30
20
10
0
20
40
60
80
100
120
140
160
180
200
220

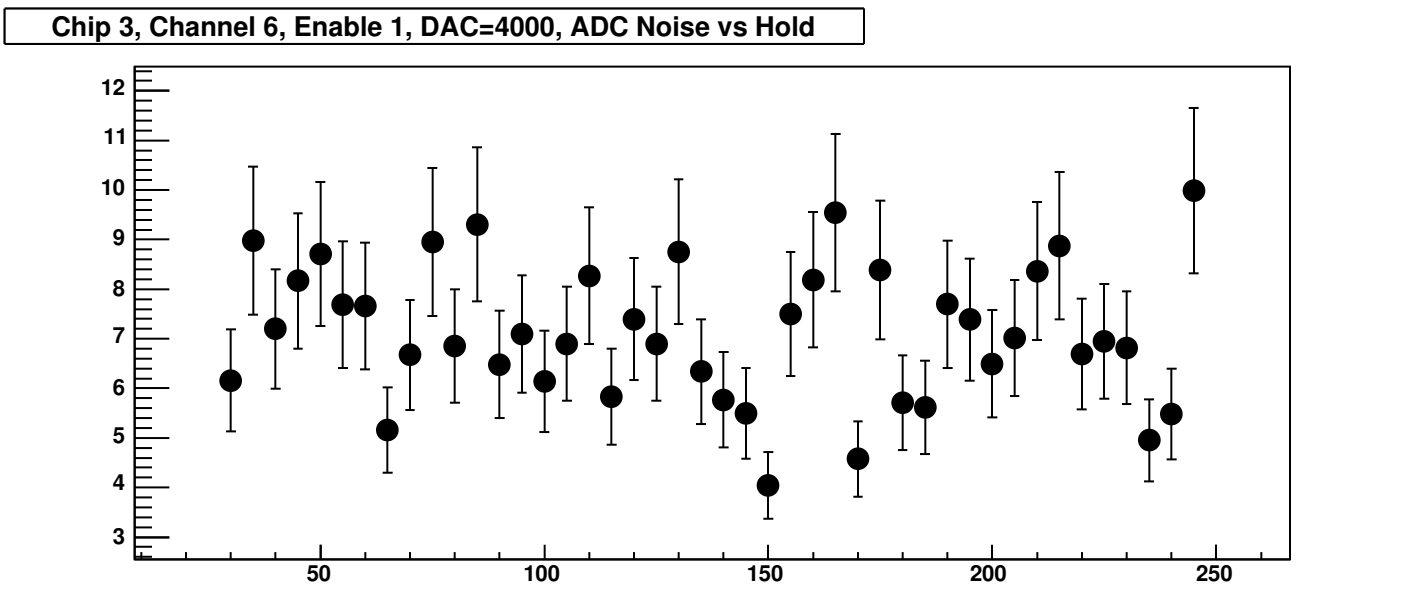

Chip 3, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
20
40
60
80
100
120
140
160
180
200
220

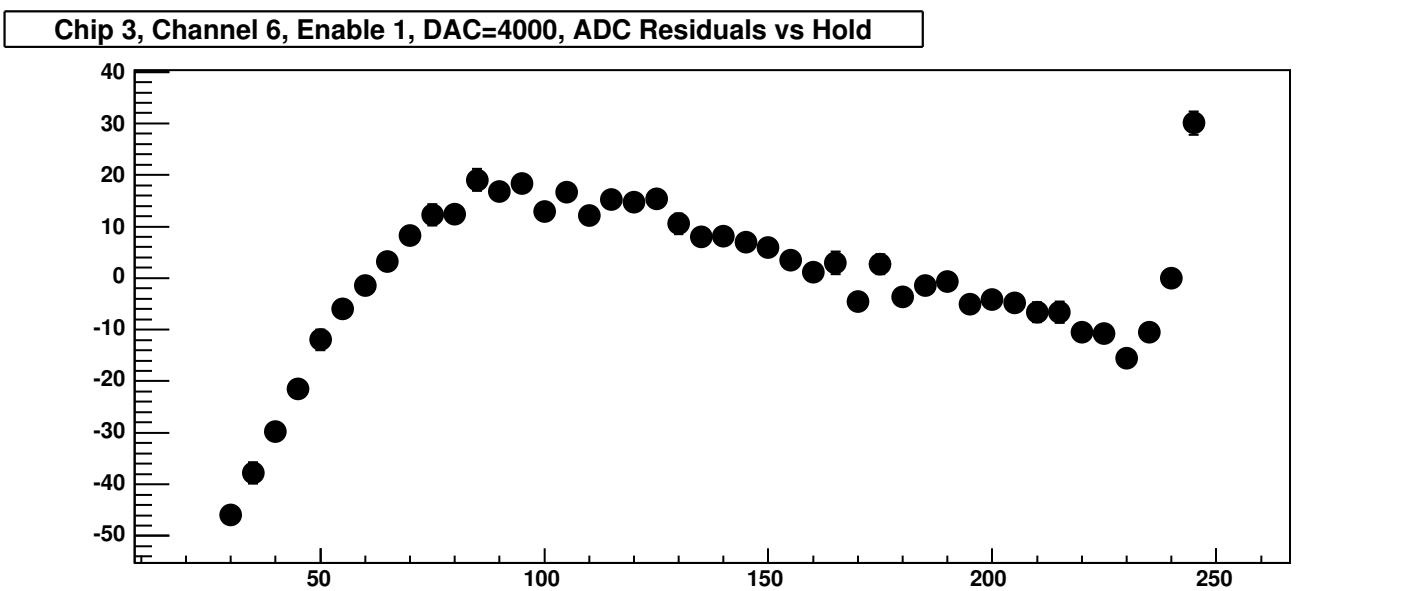

Chip 3, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 251 / 38
p0 96.47 ± 0.5771
p1 75.95 ± 0.5954
p2 -2.687e+07 ± 5.561e+05
p3 1.776e+07 ± 4.813e+05
p4 1.489 ± 0.02234
100
80
60
40
20
50
100
150
200
250

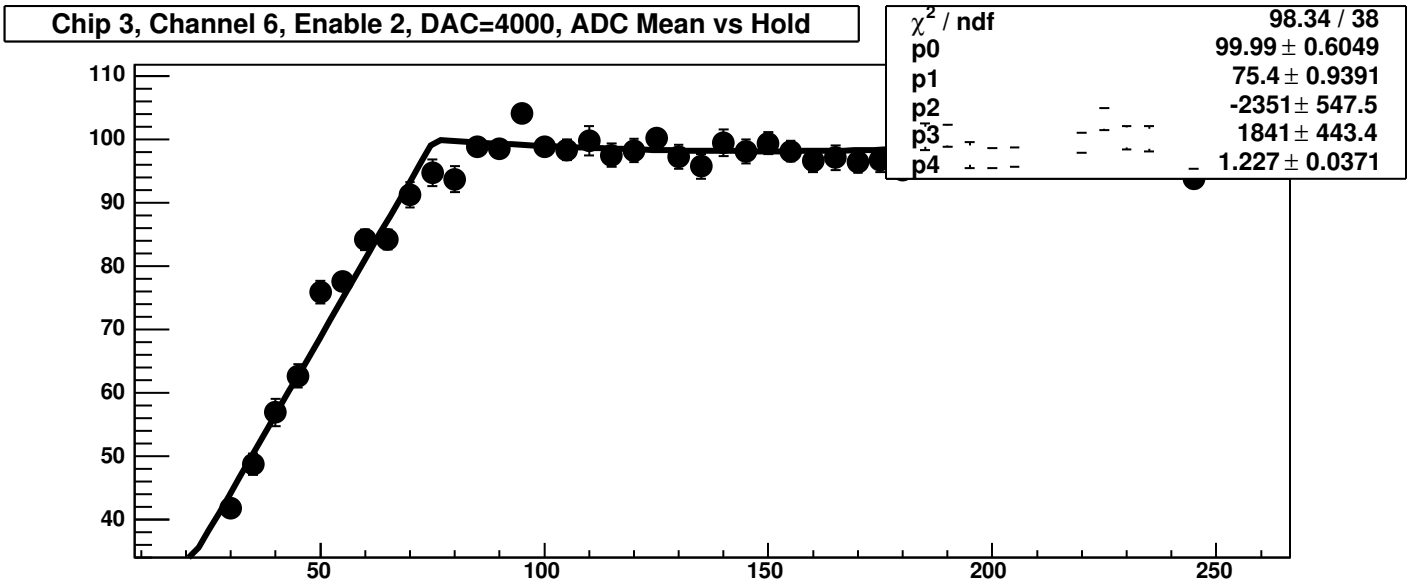

Chip 3, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
50
100
150
200
250

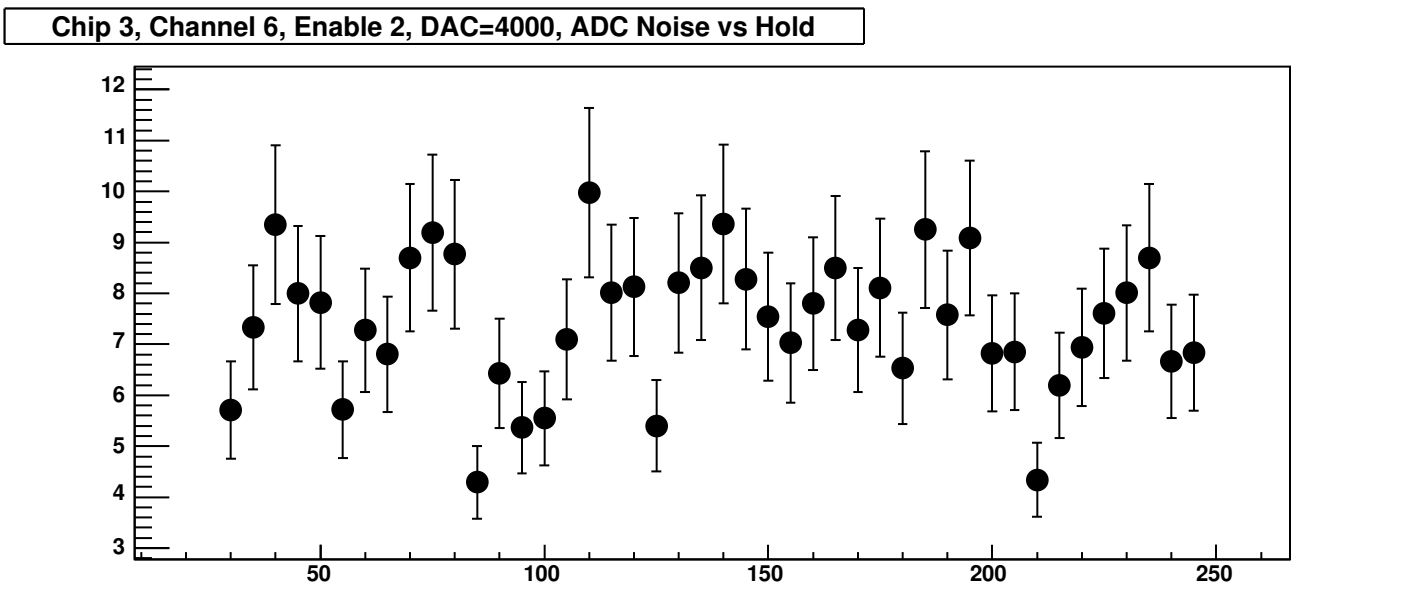

Chip 3, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

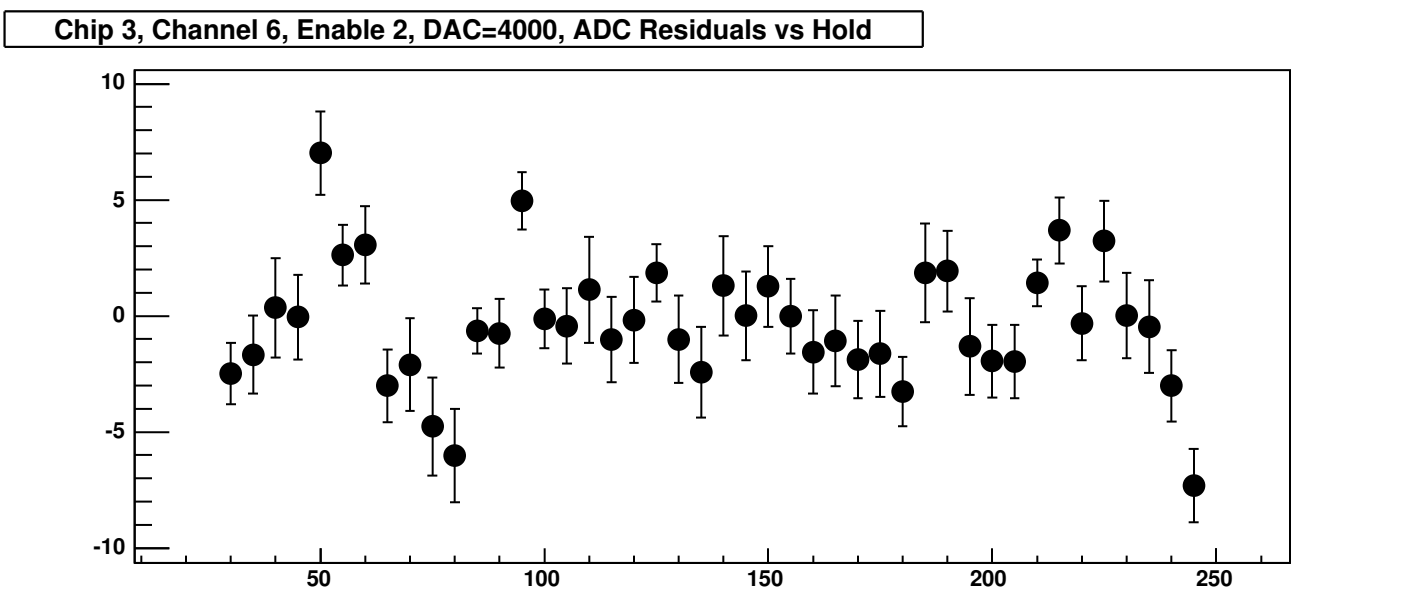

Chip 3, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 3423 / 38
p0 109.1 ± 0.5989
p1 237.9 ± 2.158
p2 -364.8 ± 362.3
p3 56.14 ± 46.65
p4 -0.1761 ± 0.004021
110
100
90
80
70
60
50
40
30
20
50
100
150
200
250

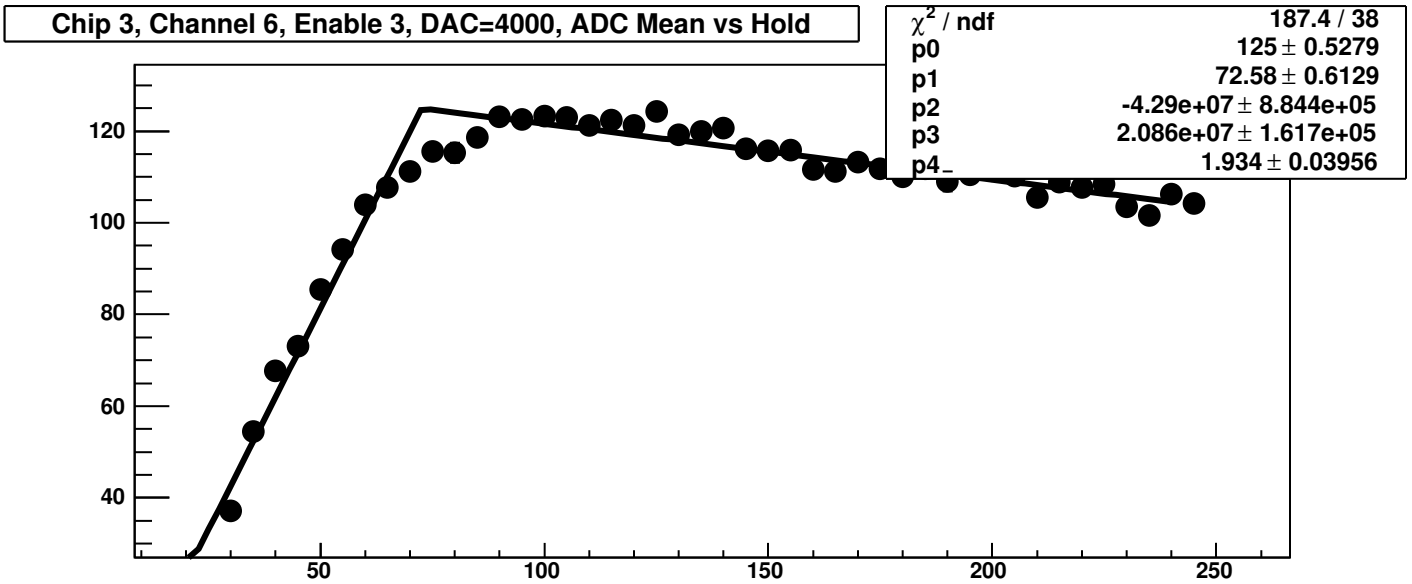

Chip 3, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250

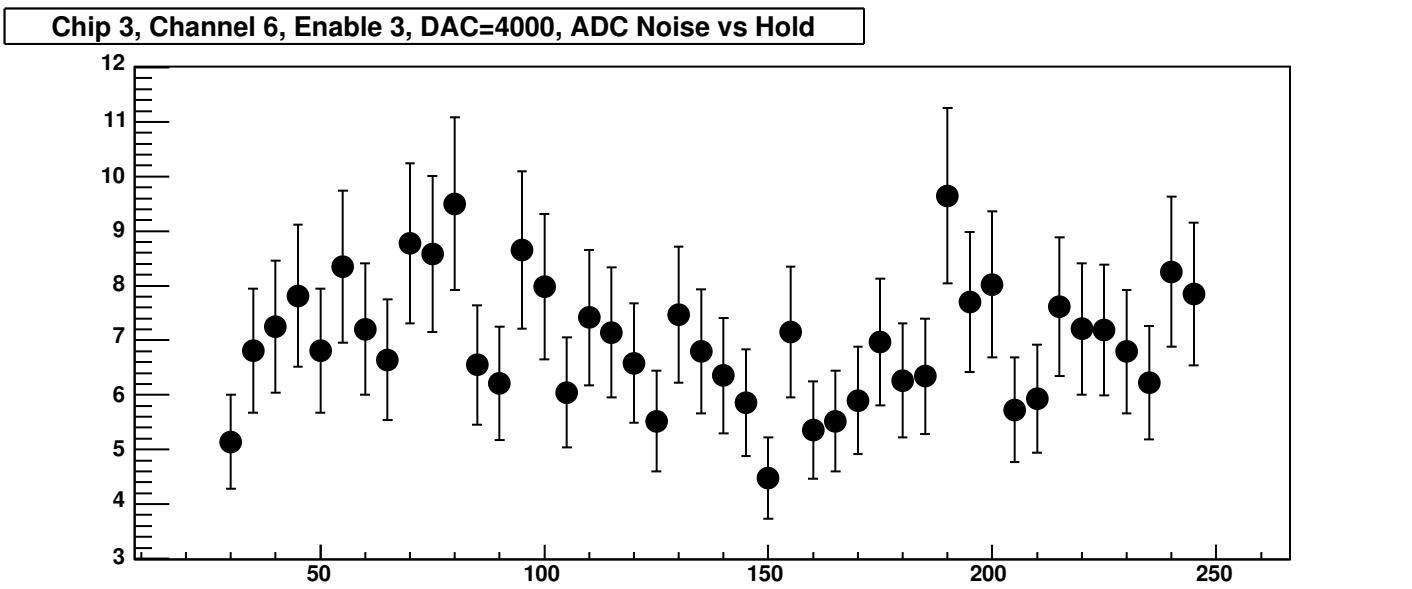

Chip 3, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
-20
-30
-40
-50
50
100
150
200
250

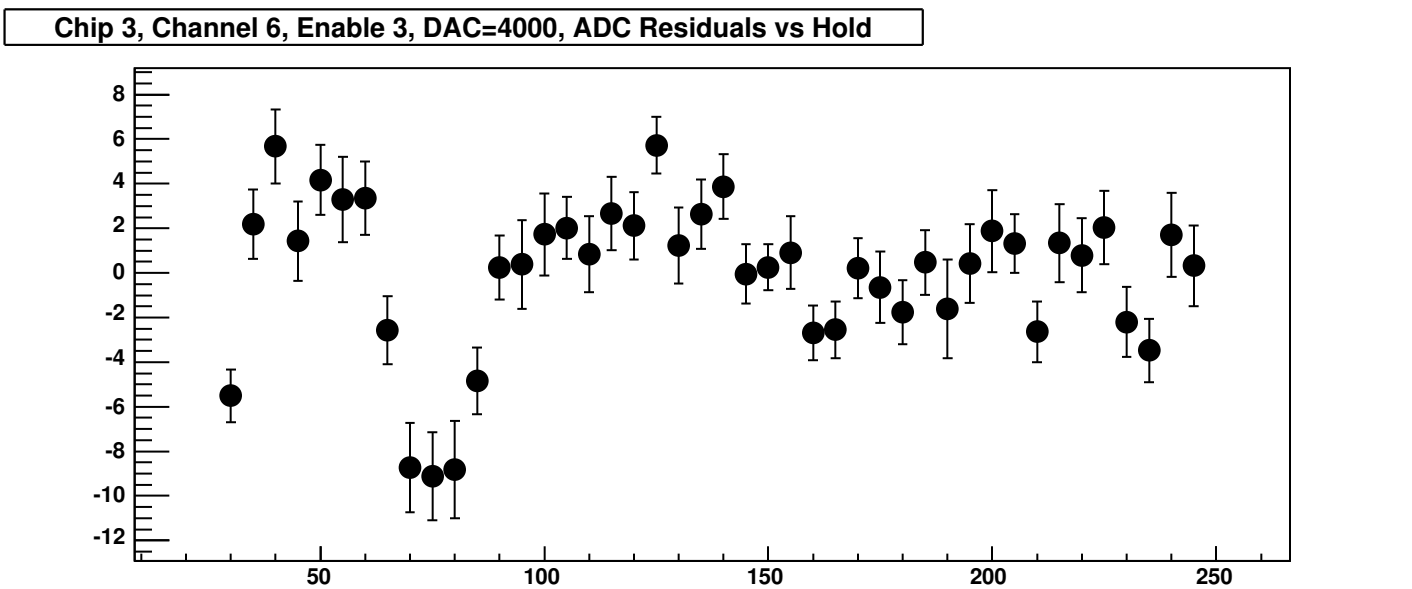

Chip 3, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 98.34 / 38
p0 99.99 ± 0.6049
p1 75.4 ± 0.9391
p2 -2351 ± 547.5
p3 1841 ± 443.4
p4 1.227 ± 0.0371
110
100
90
80
70
60
50
40
50
100
150
200
250


Chip 3, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250

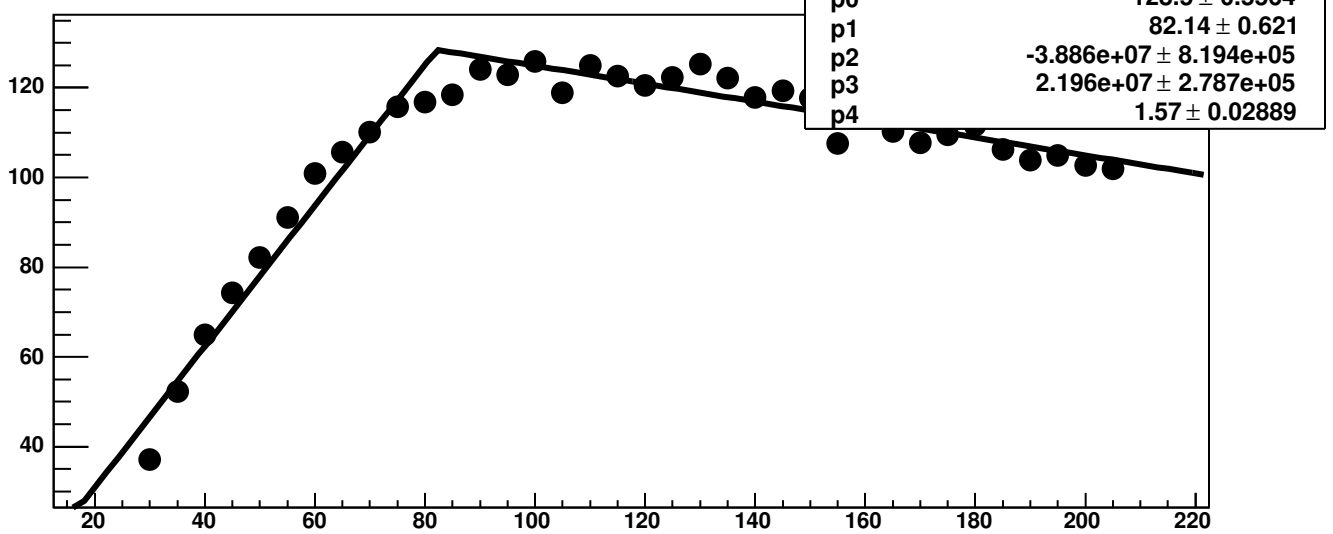

Chip 3, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250


Chip 3, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 187.4 / 38
p0 125 ± 0.5279
p1 72.58 ± 0.6129
p2 -4.29e+07 ± 8.844e+05
p3 2.086e+07 ± 1.617e+05
p4 1.934 ± 0.03956
120
100
80
60
40
50
100
150
200
250

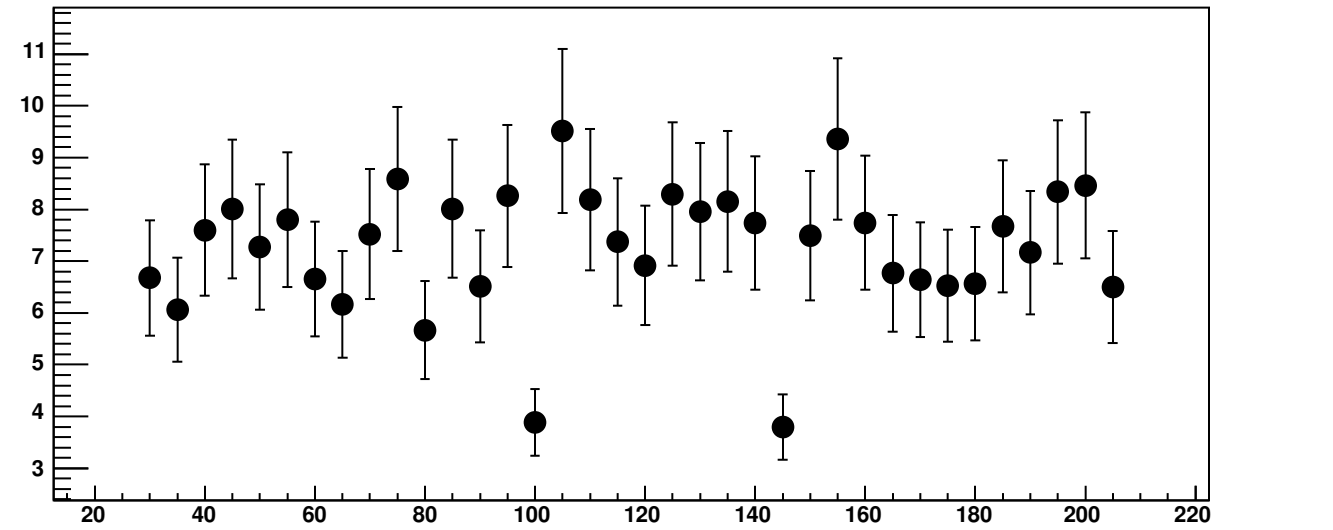

Chip 3, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250


Chip 3, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
-12
50
100
150
200
250

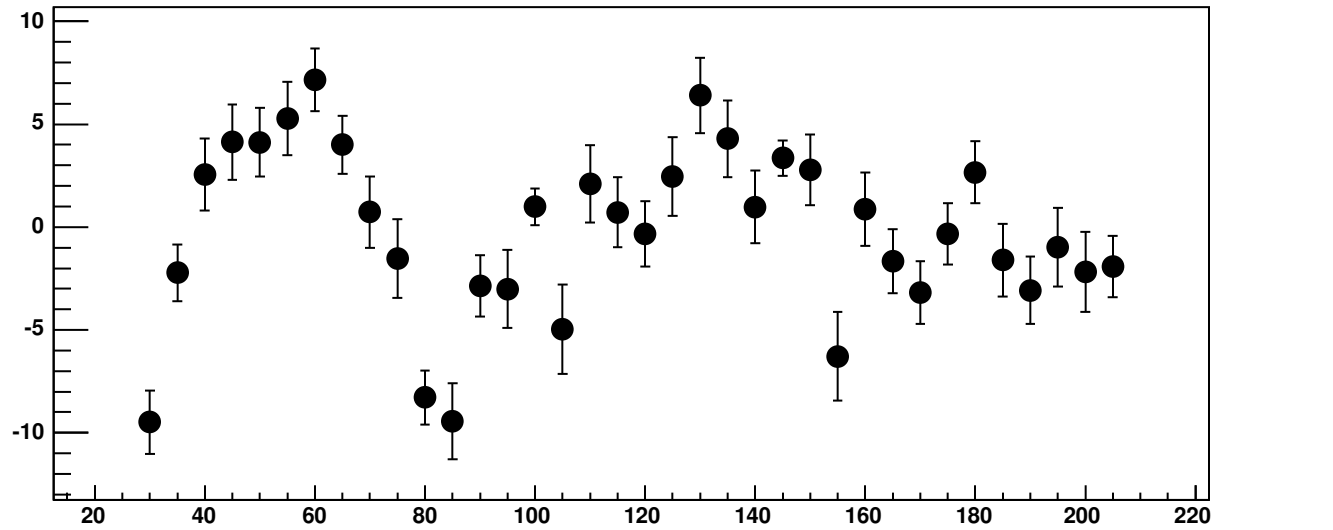
Chip 3, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

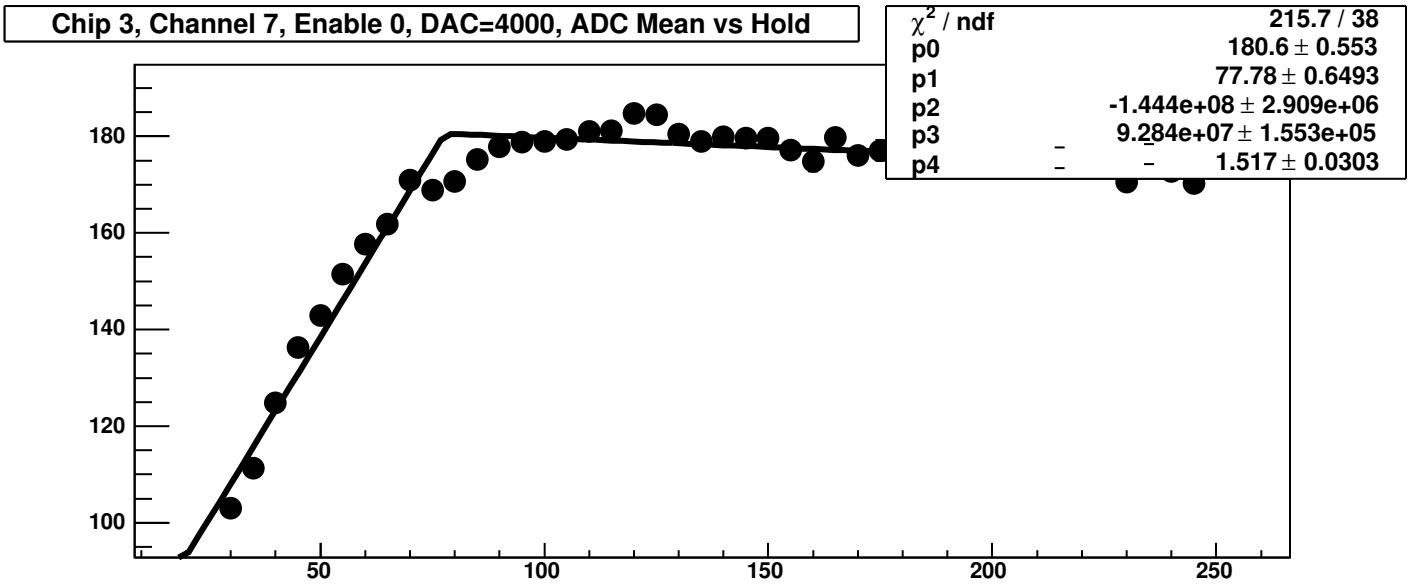


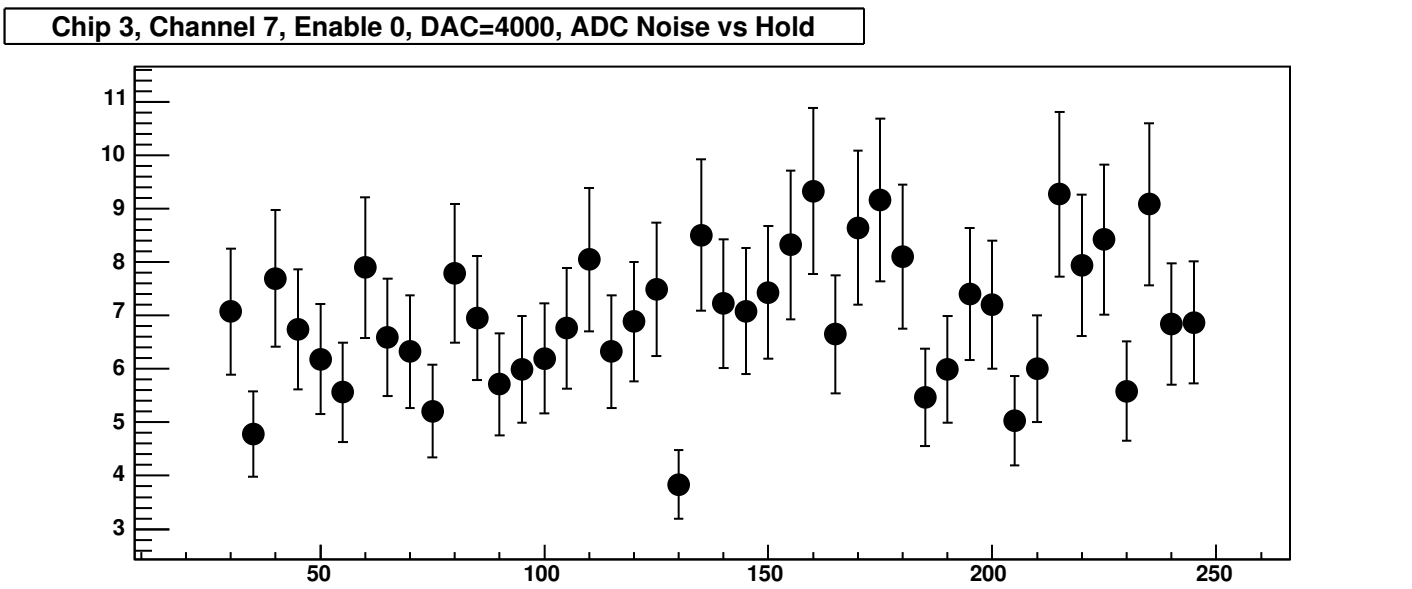
Chip 3, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold

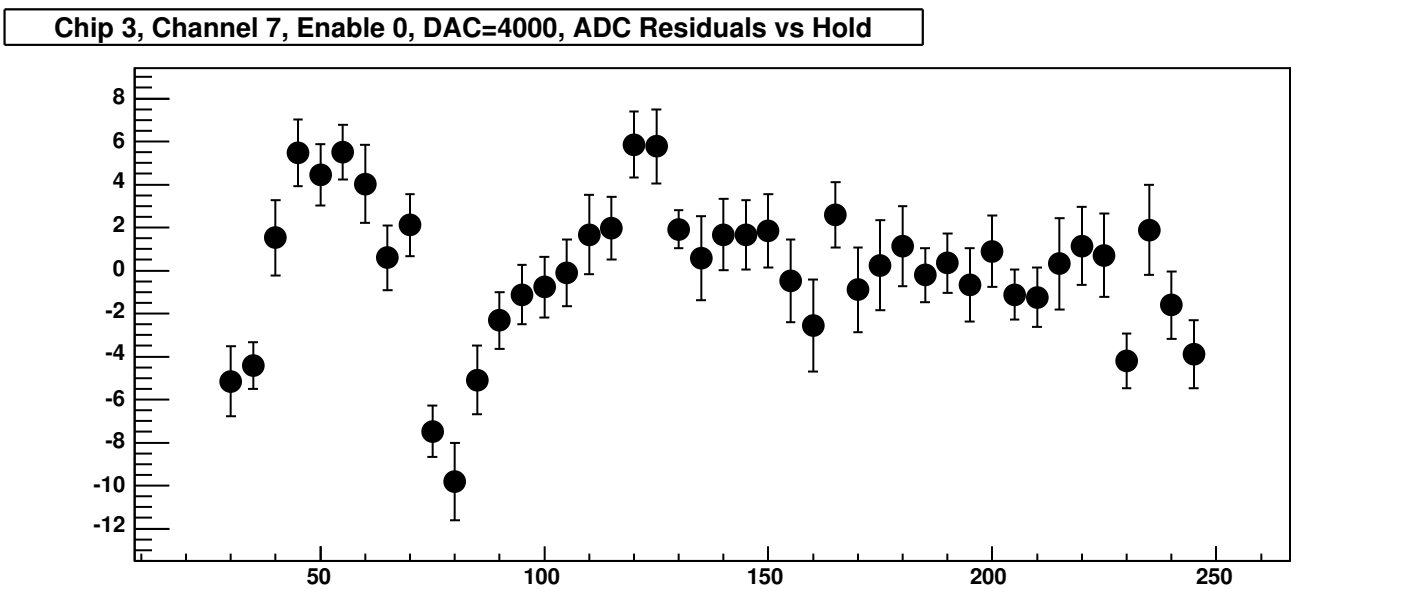


Chip 3, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

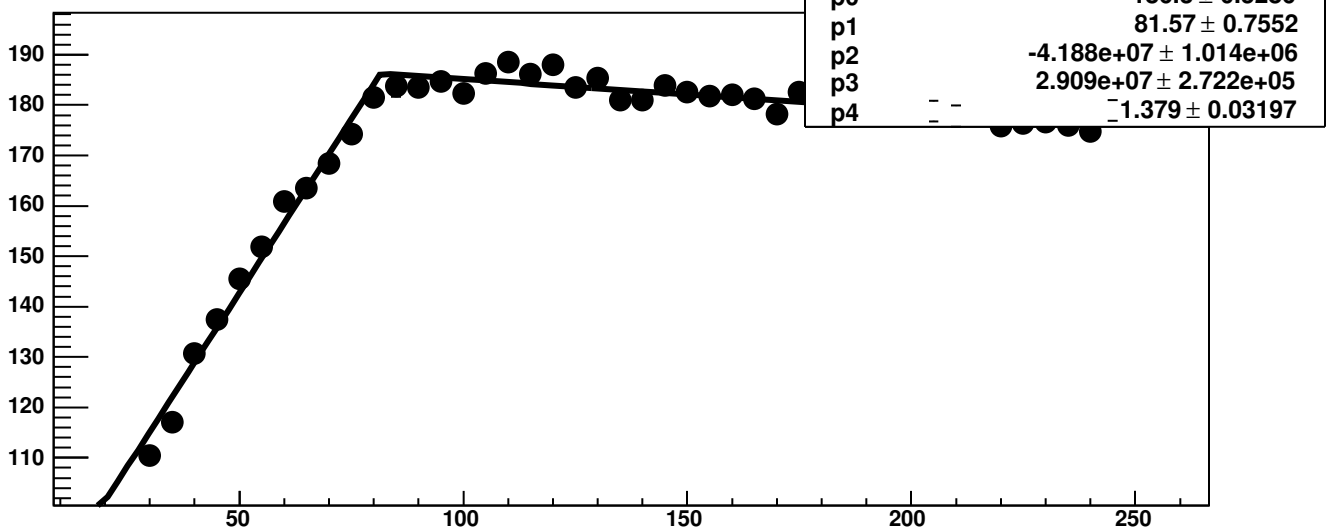



Chip 3, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 215.7 / 38
p0 180.6 ± 0.553
p1 77.78 ± 0.6493
p2 -1.444e+08 ± 2.909e+06
p3 9.284e+07 ± 1.553e+05
p4 1.517 ± 0.0303
180
160
140
120
100
50
100
150
200
250

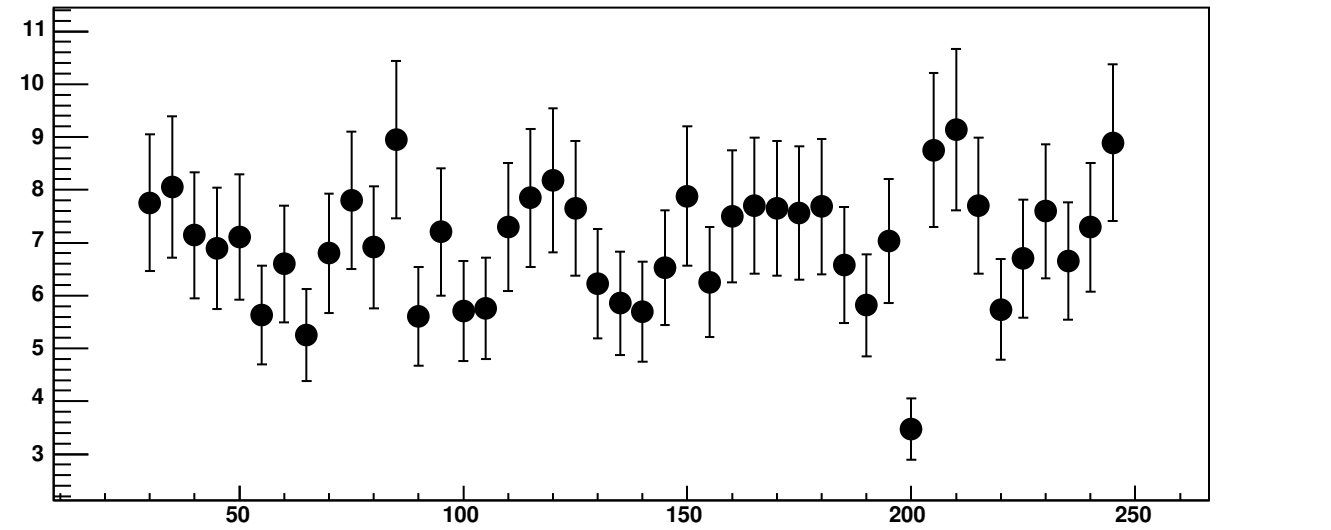

Chip 3, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
50
100
150
200
250


Chip 3, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
-12
50
100
150
200
250

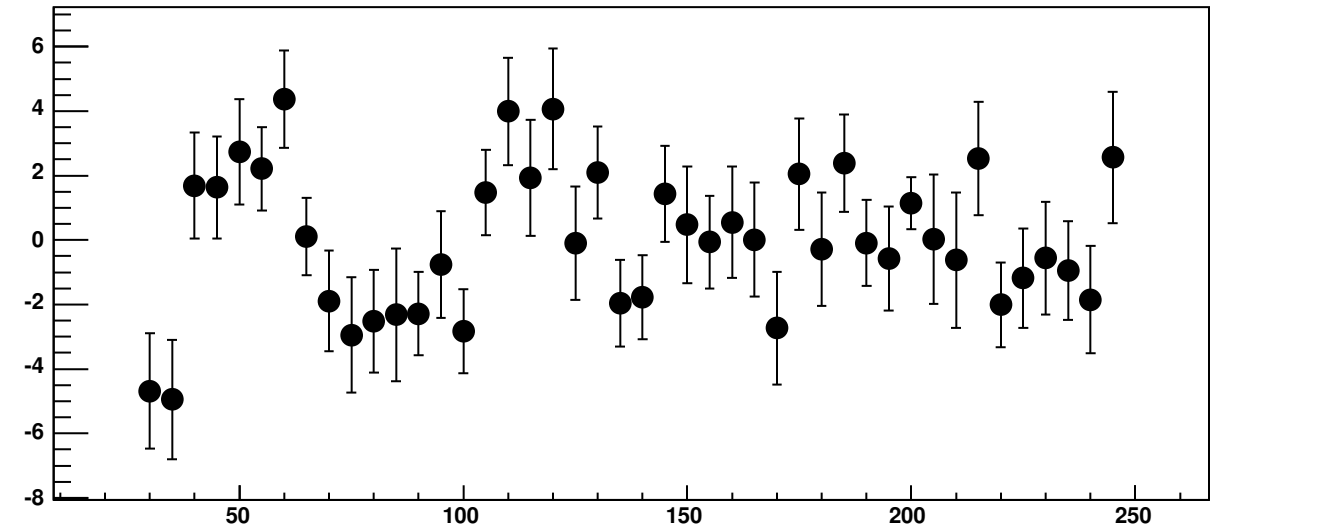
Chip 3, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold



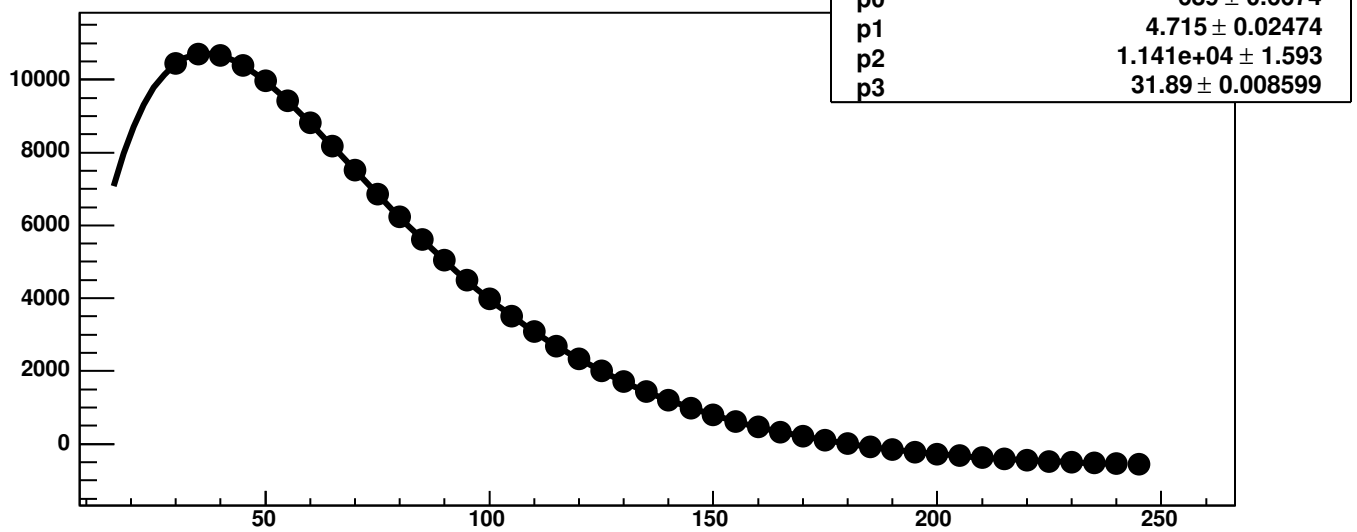
Chip 3, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



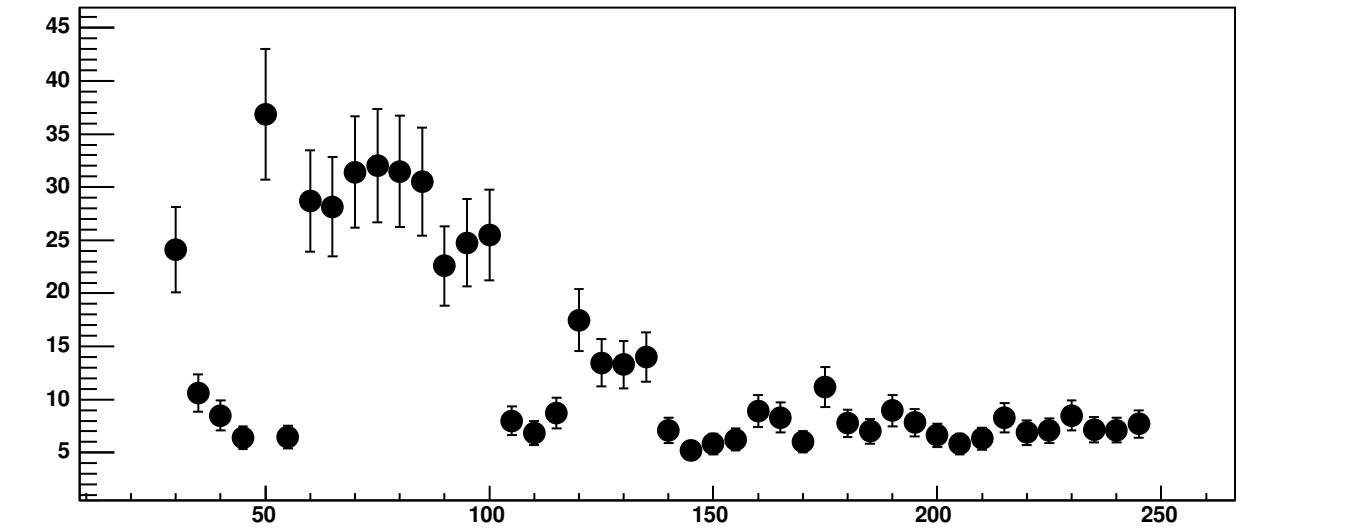
Chip 3, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold



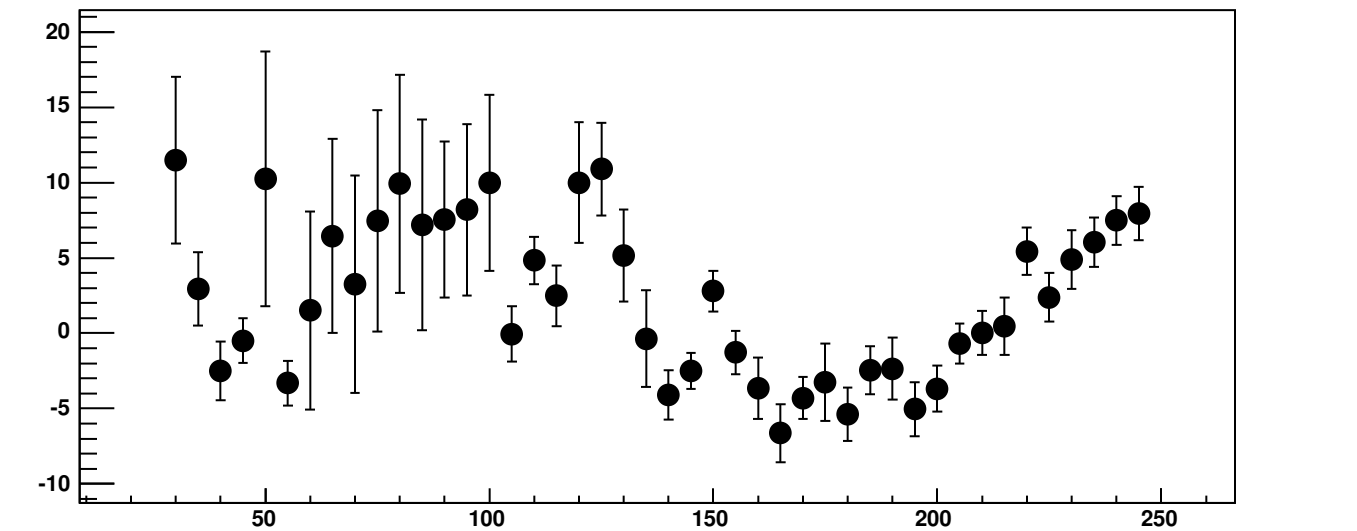
Chip 3, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold

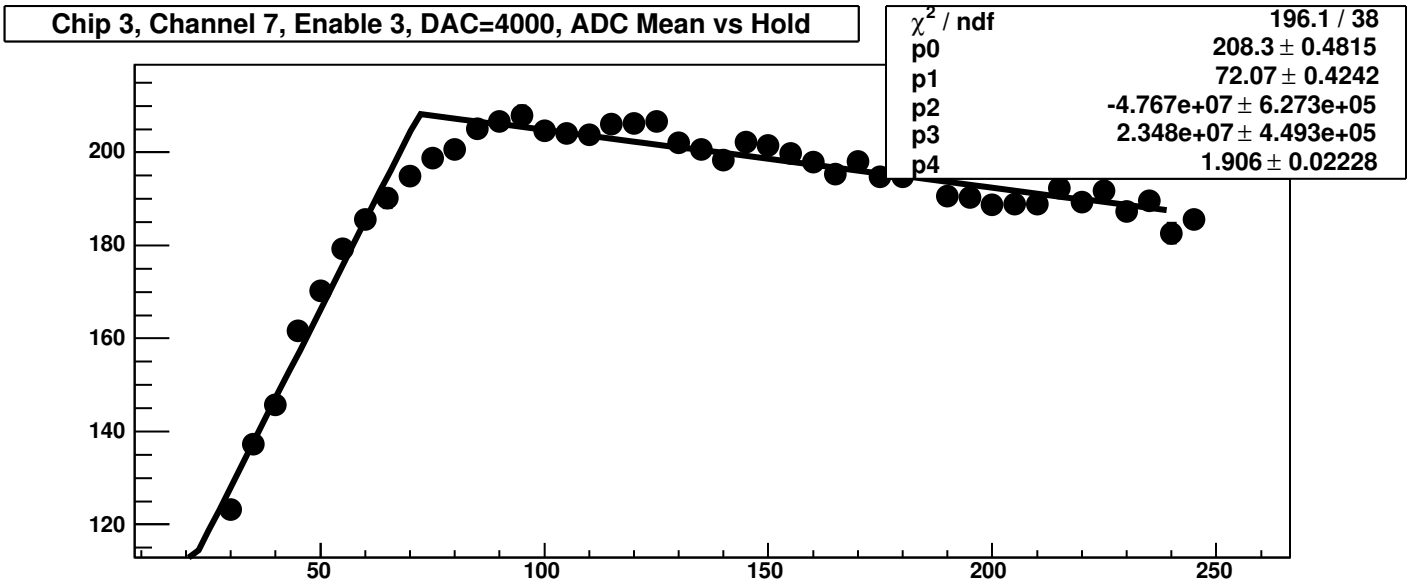


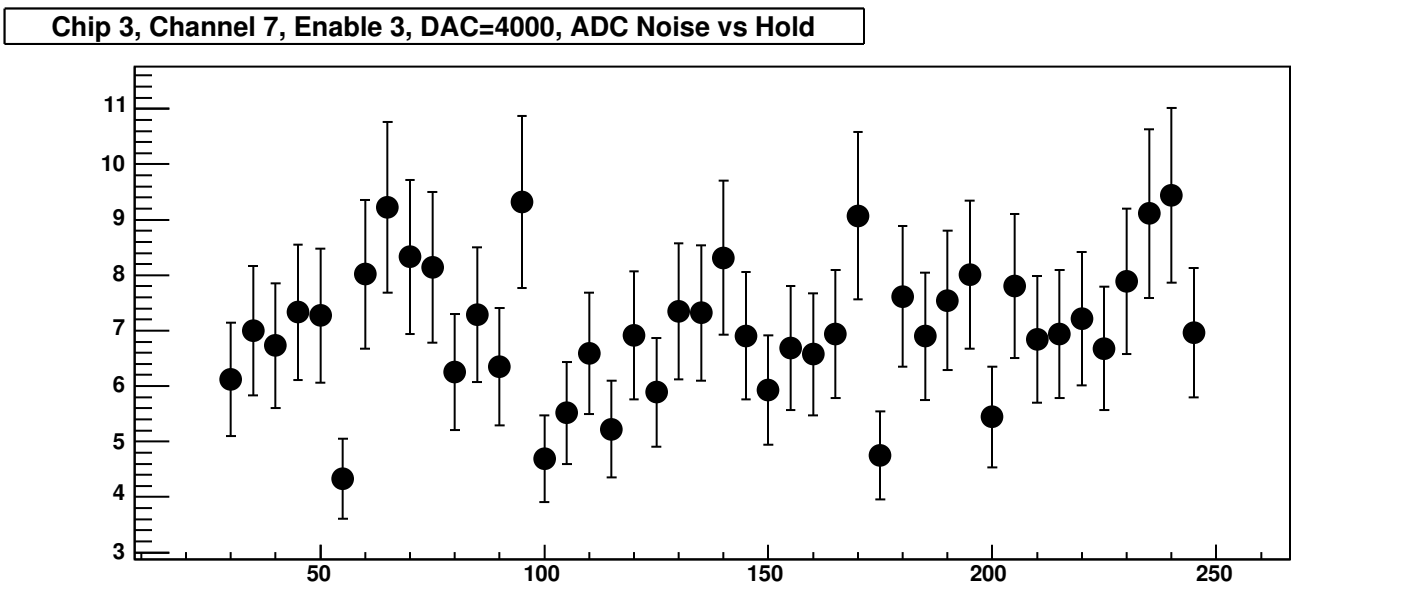
Chip 3, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold

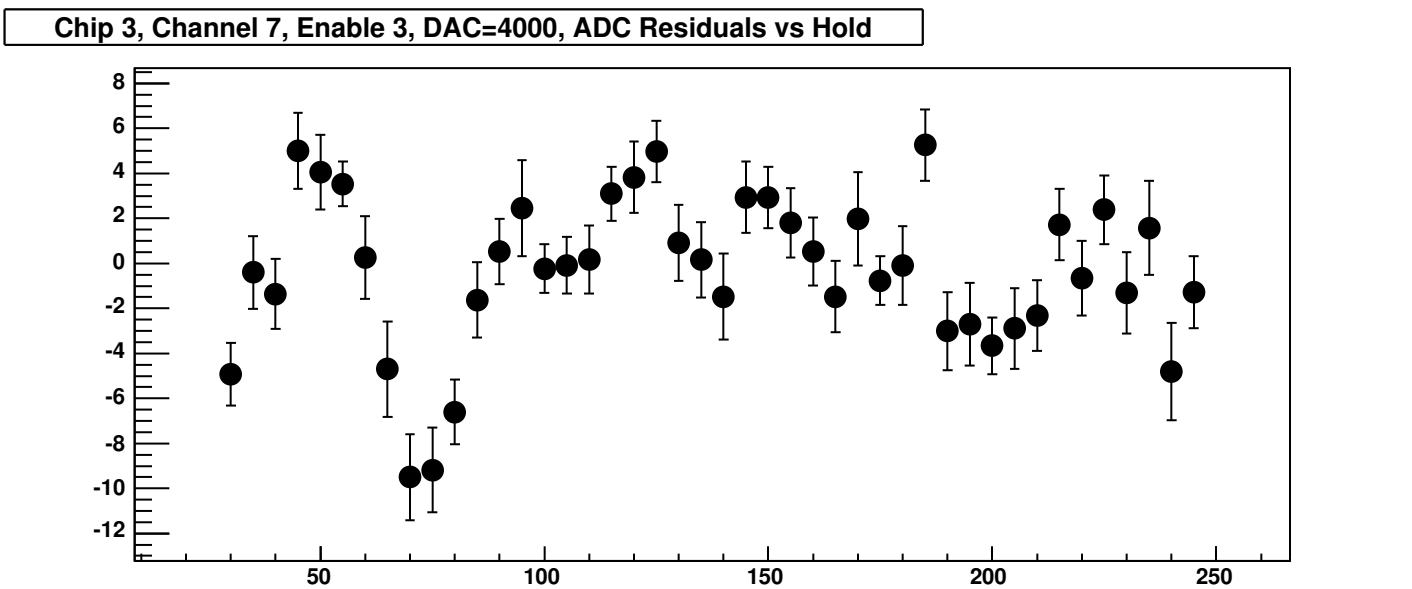


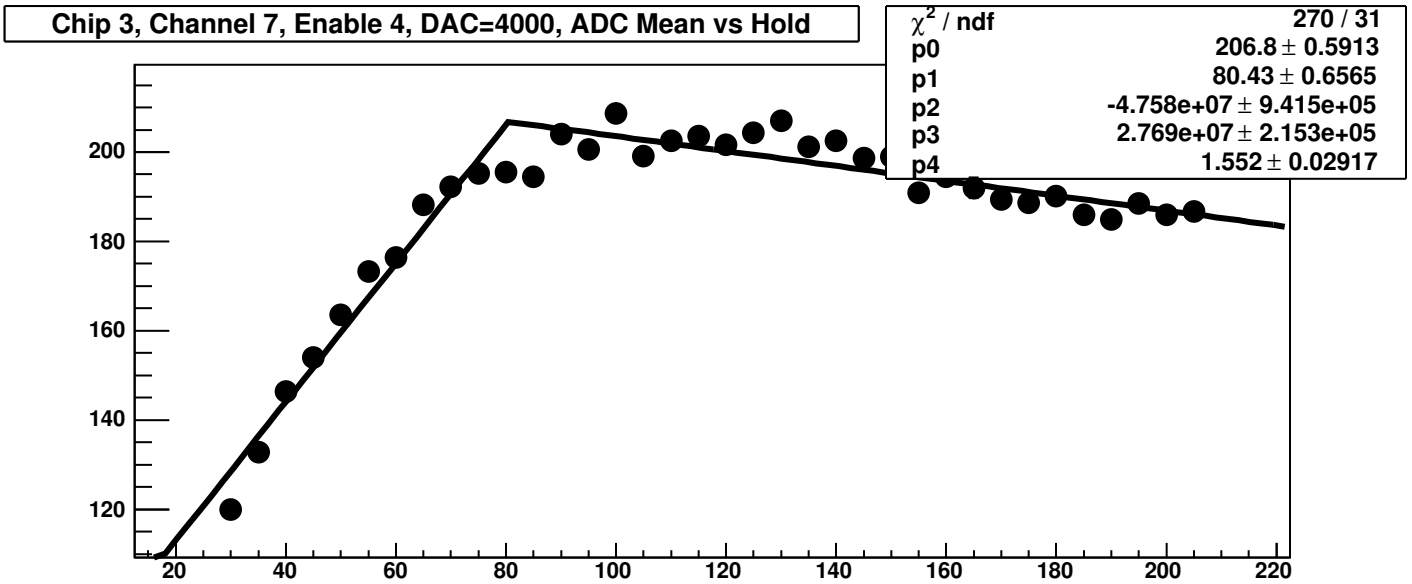
Chip 3, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold

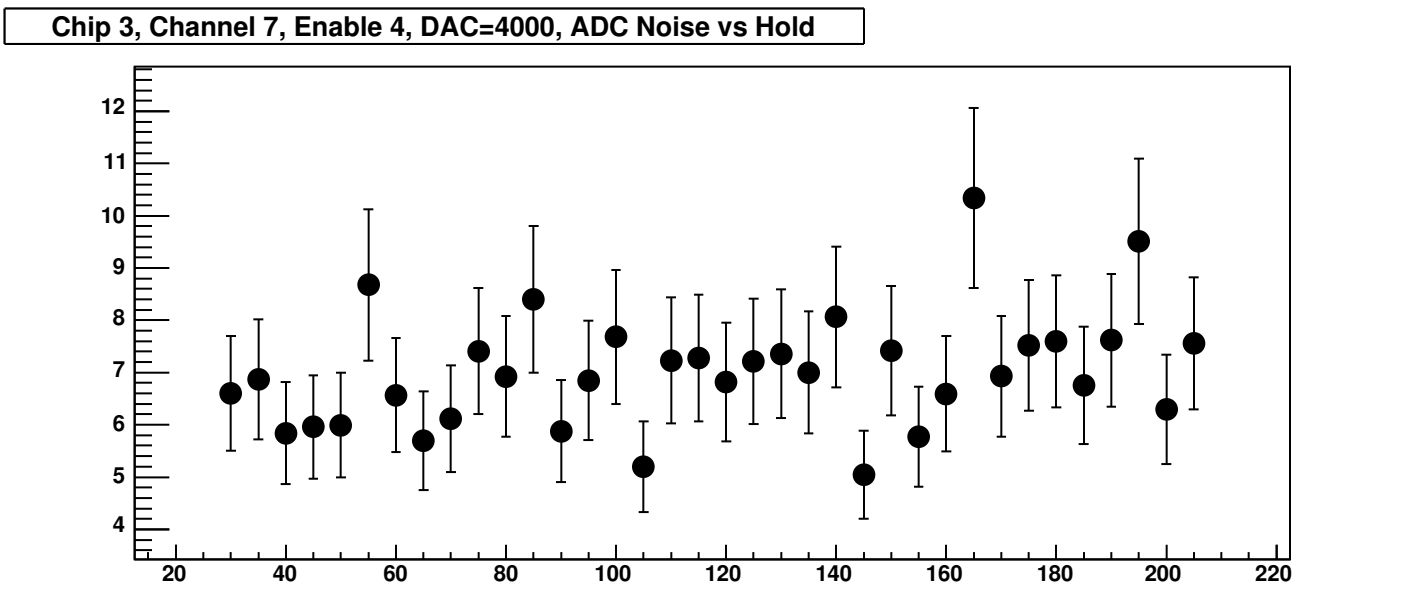


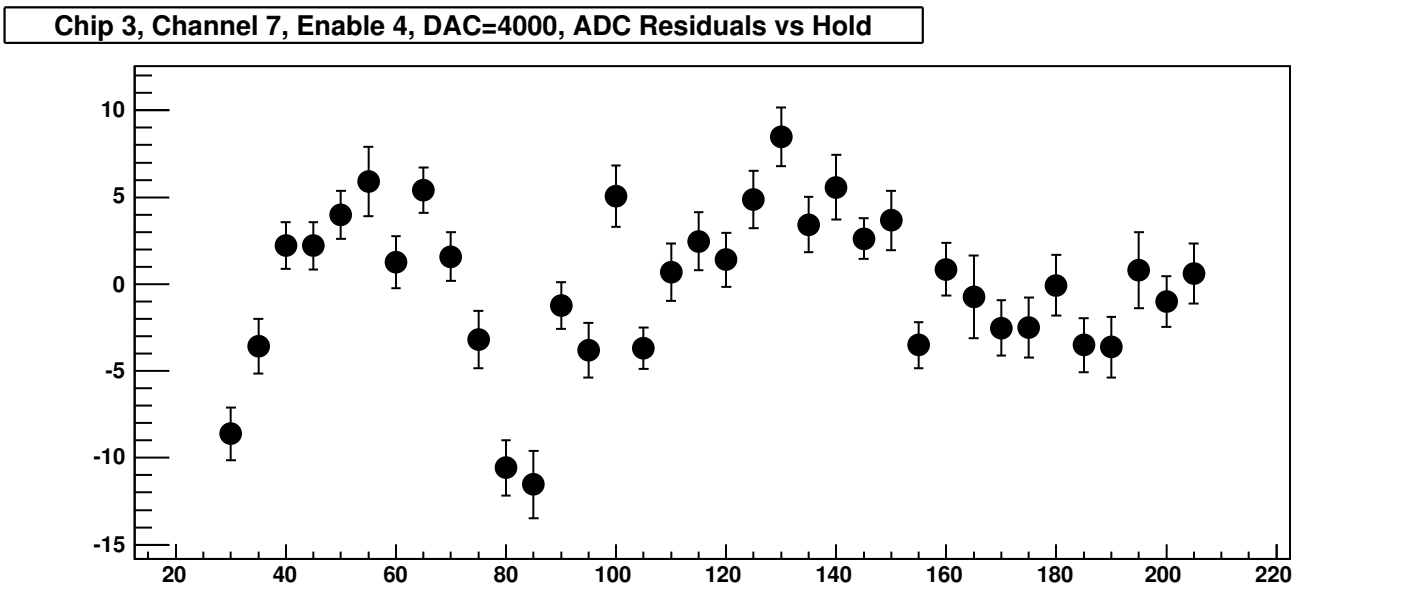

Chip 3, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 196.1 / 38
p0 208.3 ± 0.4815
p1 72.07 ± 0.4242
p2 -4.767e+07 ± 6.273e+05
p3 2.348e+07 ± 4.493e+05
p4 1.906 ± 0.02228
200
180
160
140
120
50
100
150
200
250

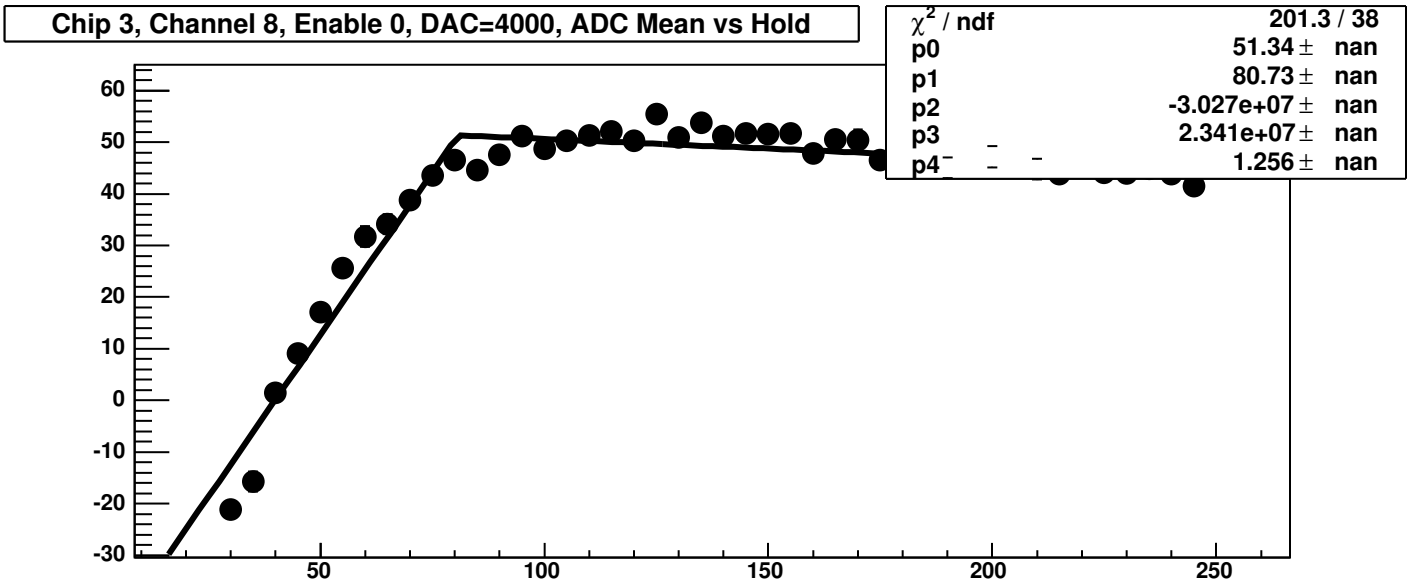

Chip 3, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
50
100
150
200
250

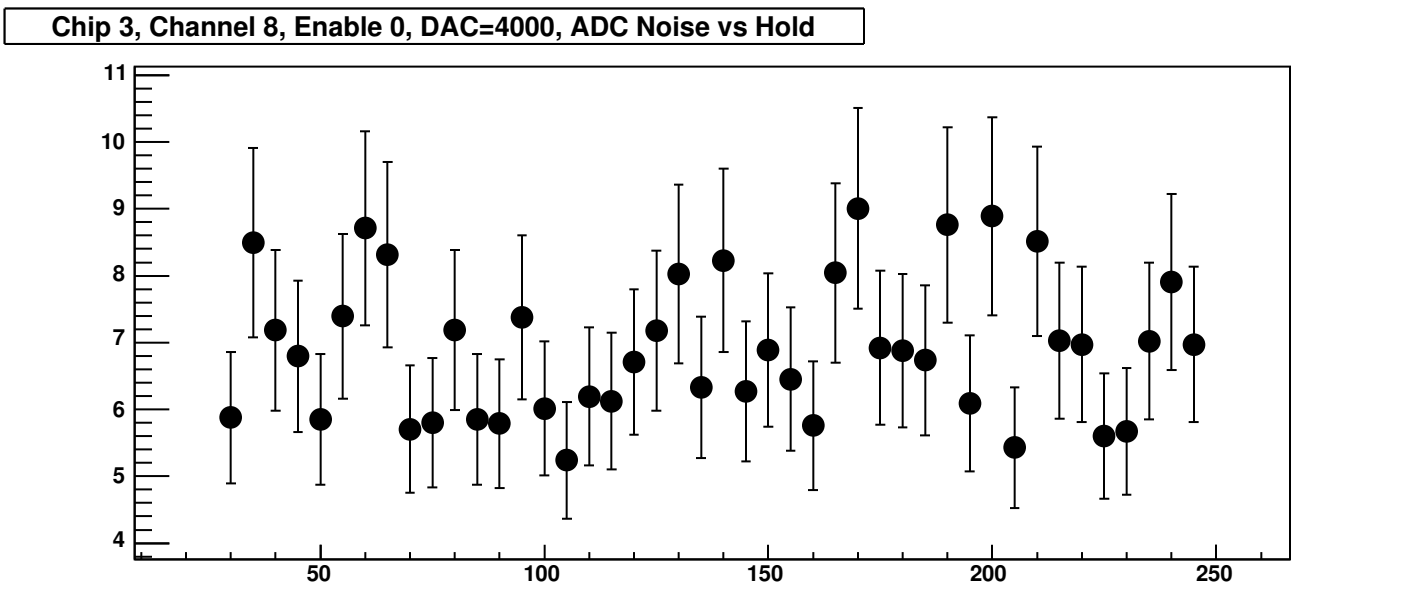

Chip 3, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
-12
50
100
150
200
250

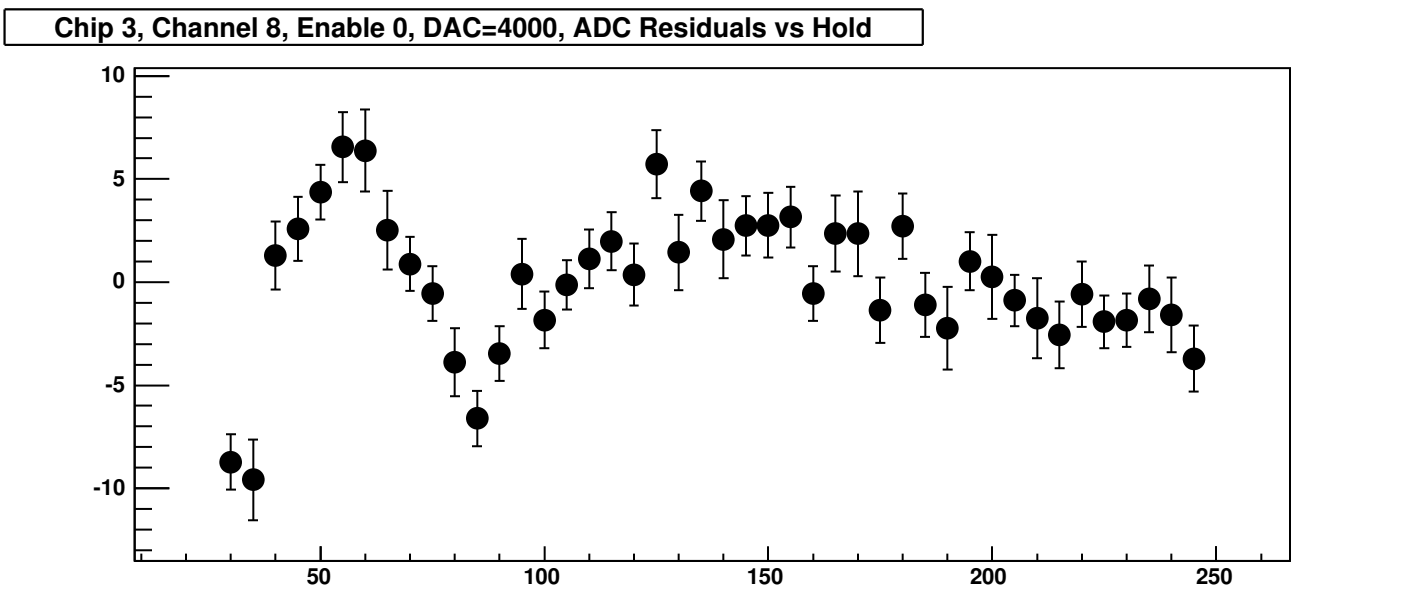

Chip 3, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 270 / 31
p0 206.8 ± 0.5913
p1 80.43 ± 0.6565
p2 -4.758e+07 ± 9.415e+05
p3 2.769e+07 ± 2.153e+05
p4 1.552 ± 0.02917

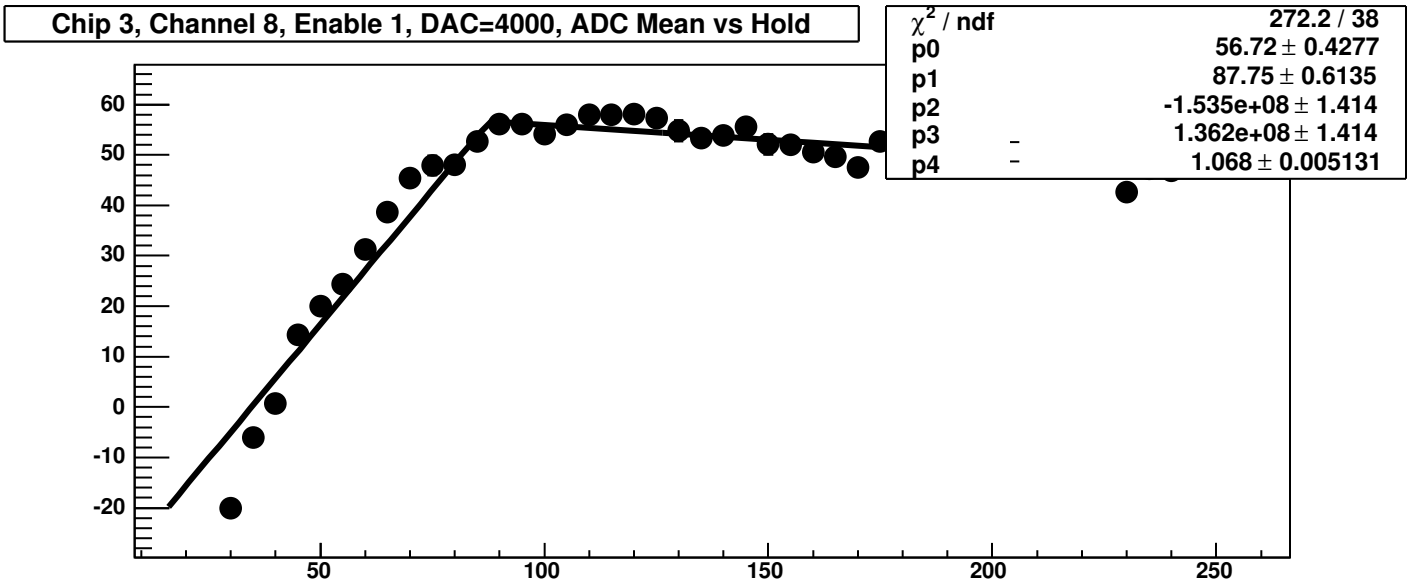

Chip 3, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold

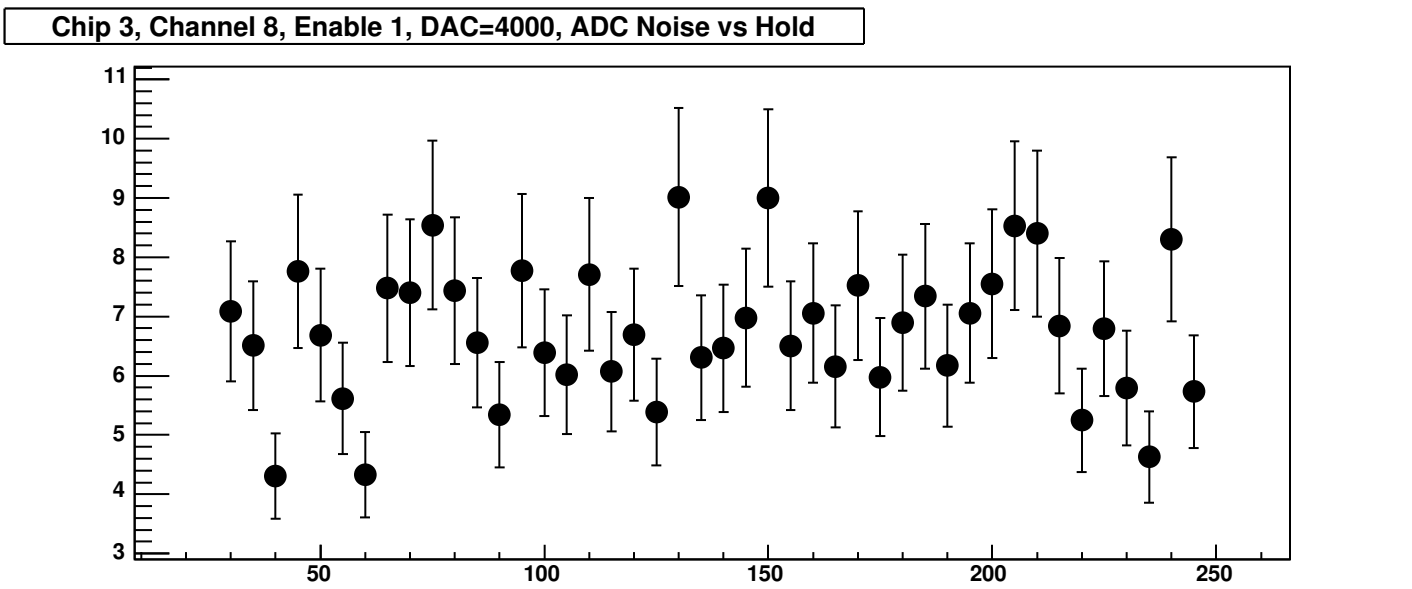

Chip 3, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

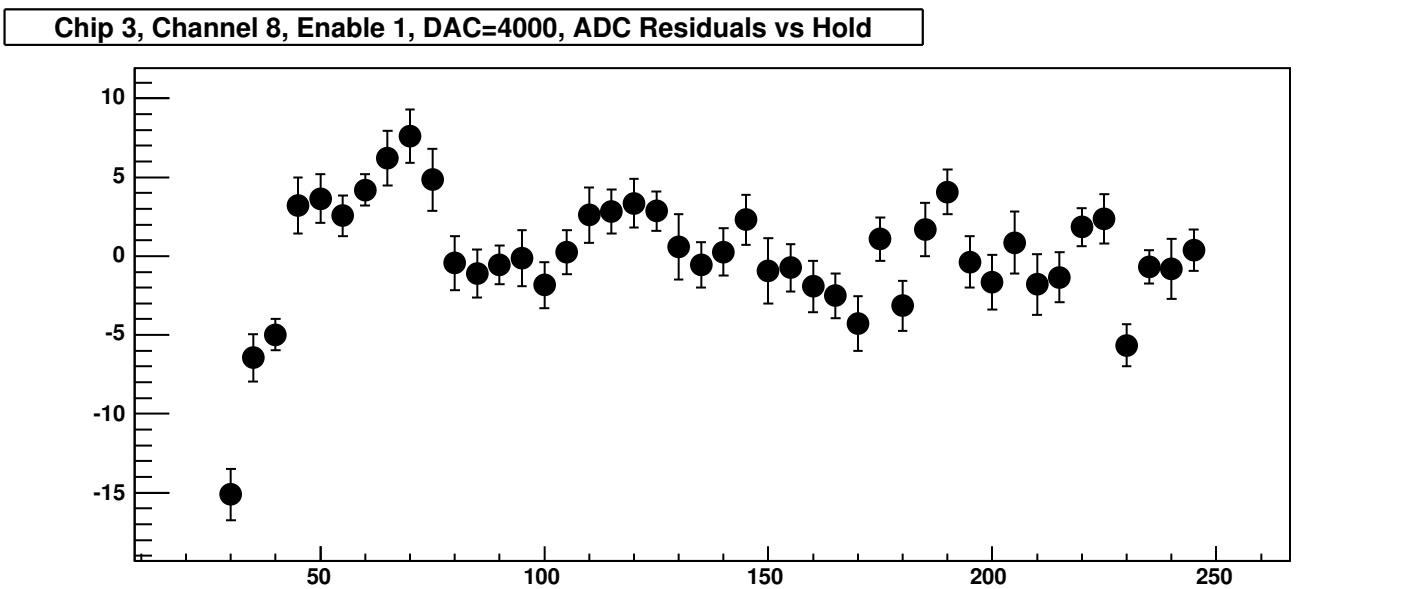

Chip 3, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 201.3 / 38
p0 51.34 ± nan
p1 80.73 ± nan
p2 -3.027e+07 ± nan
p3 2.341e+07 ± nan
p4 1.256 ± nan
60
50
40
30
20
10
0
-10
-20
-30
50
100
150
200
250


Chip 3, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250

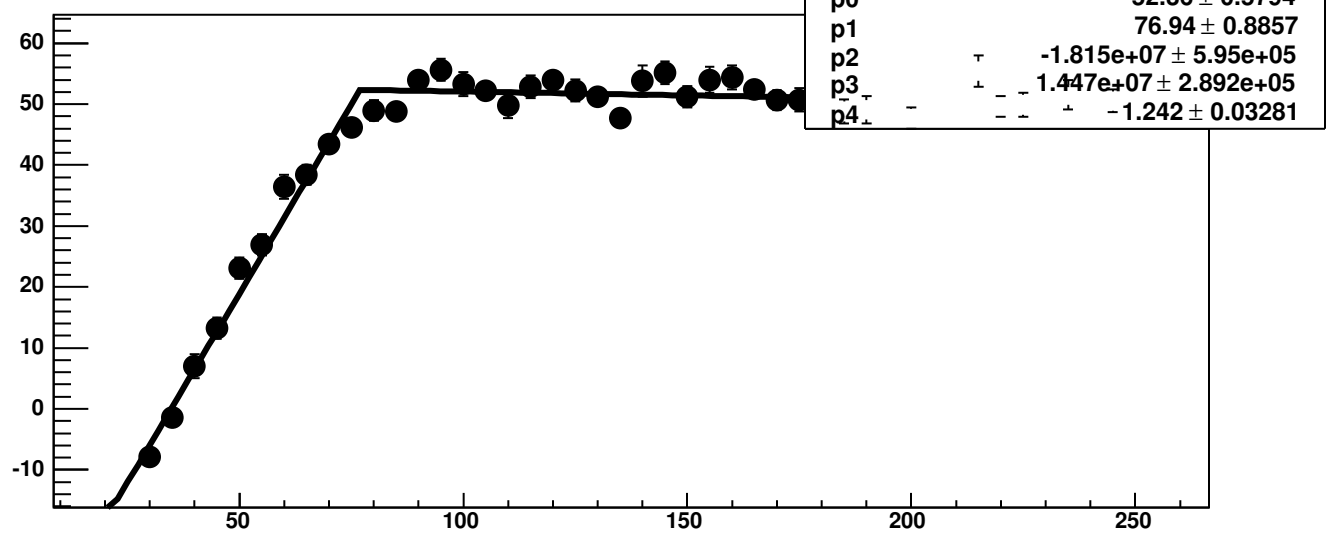

Chip 3, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250


Chip 3, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 272.2 / 38
p0 56.72 ± 0.4277
p1 87.75 ± 0.6135
p2 -1.535e+08 ± 1.414
p3 1.362e+08 ± 1.414
p4 1.068 ± 0.005131

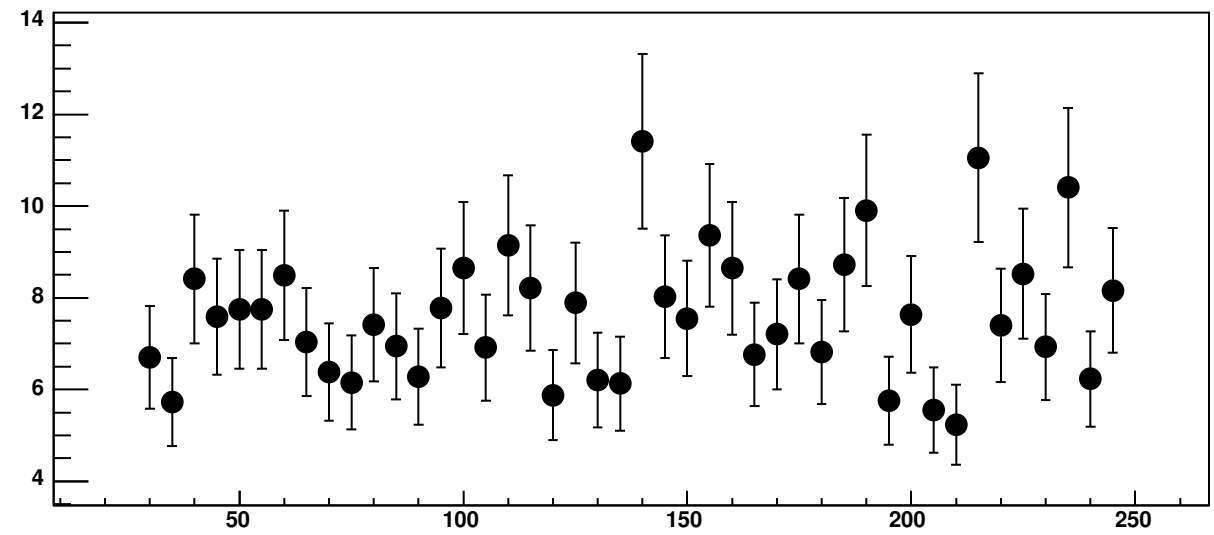

Chip 3, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold


Chip 3, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

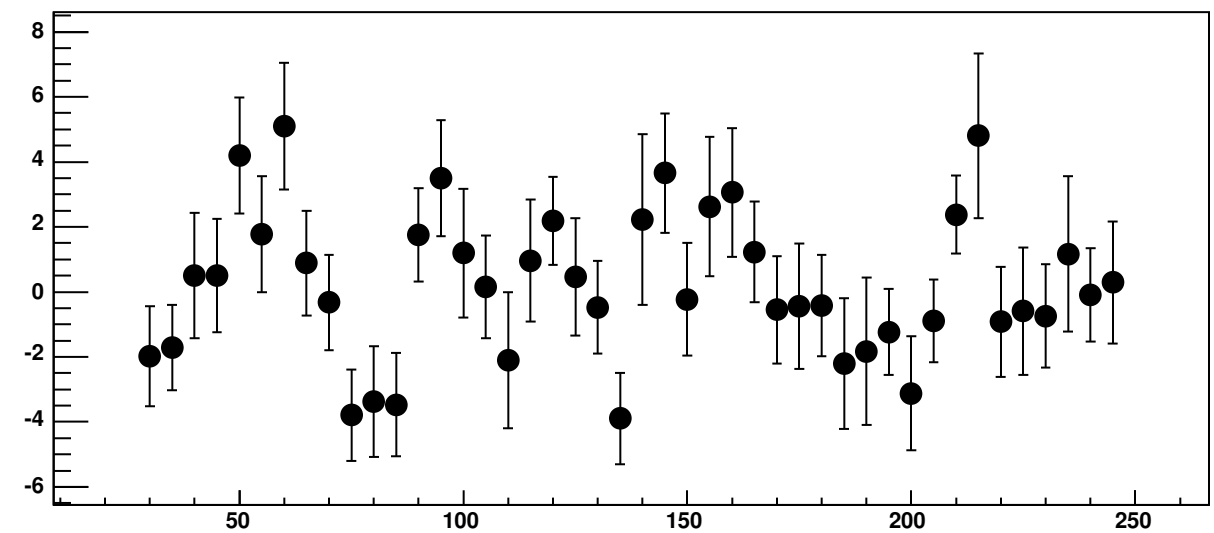
Chip 3, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold



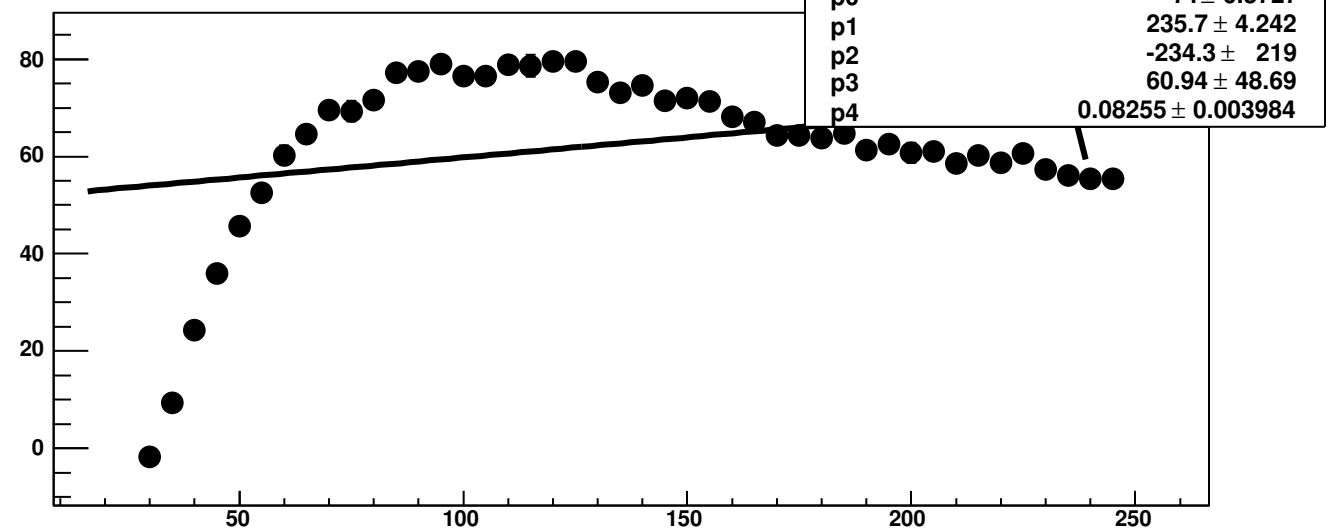
Chip 3, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold



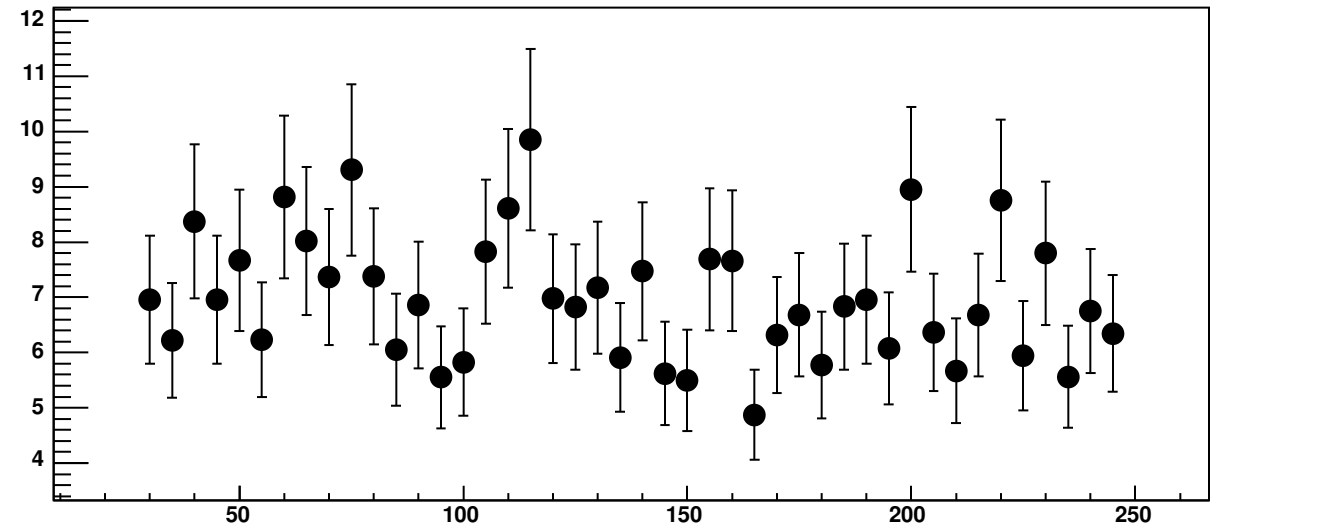
Chip 3, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold



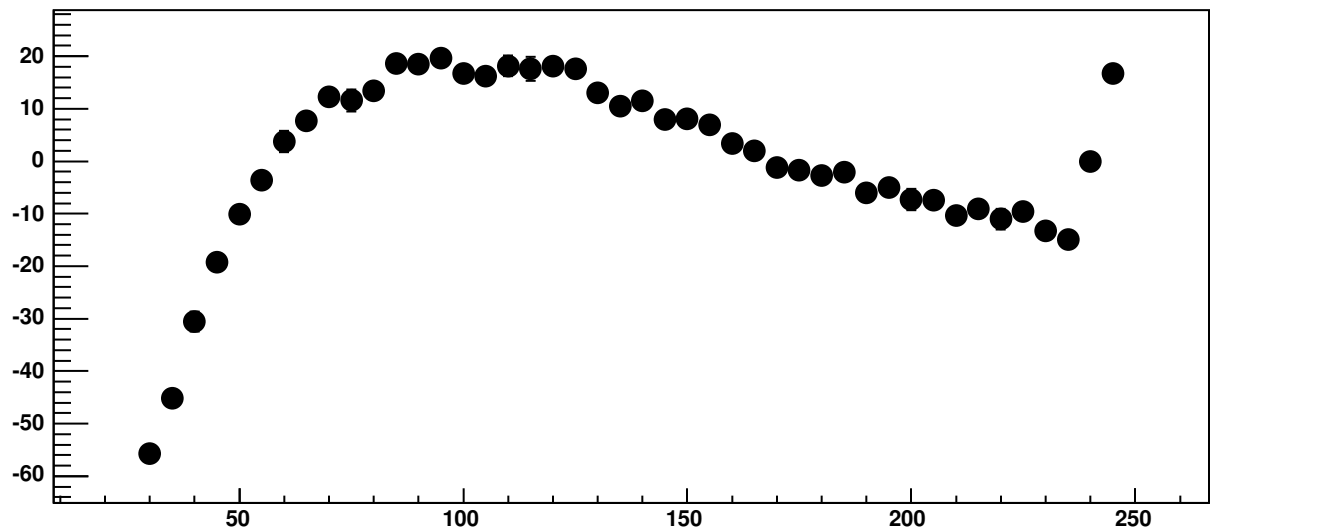
Chip 3, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold



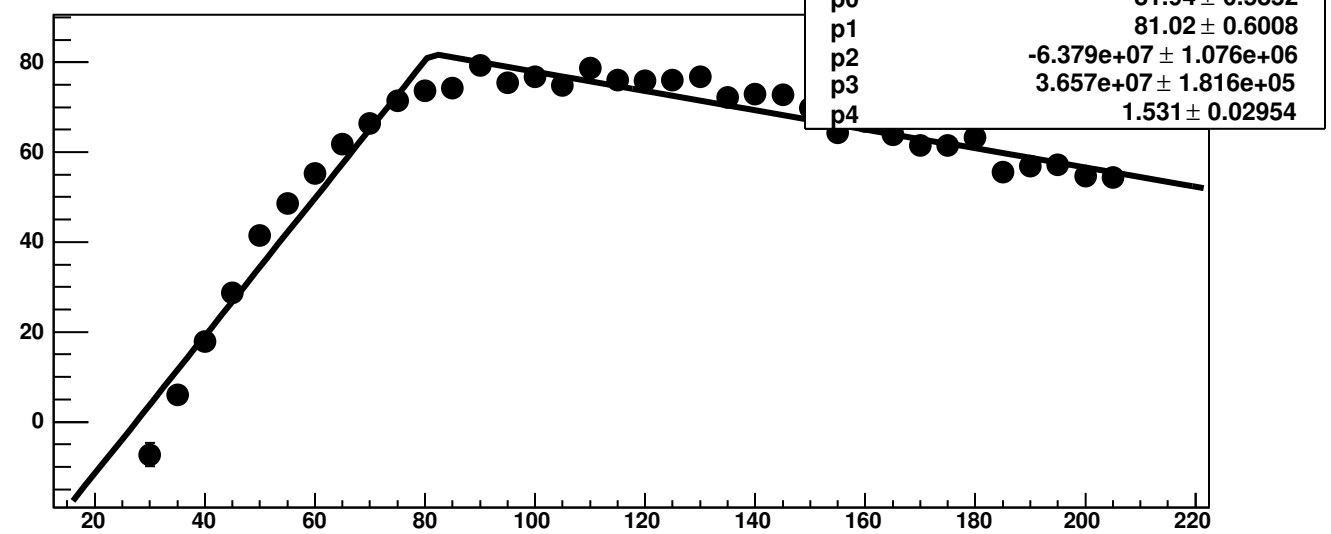
Chip 3, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



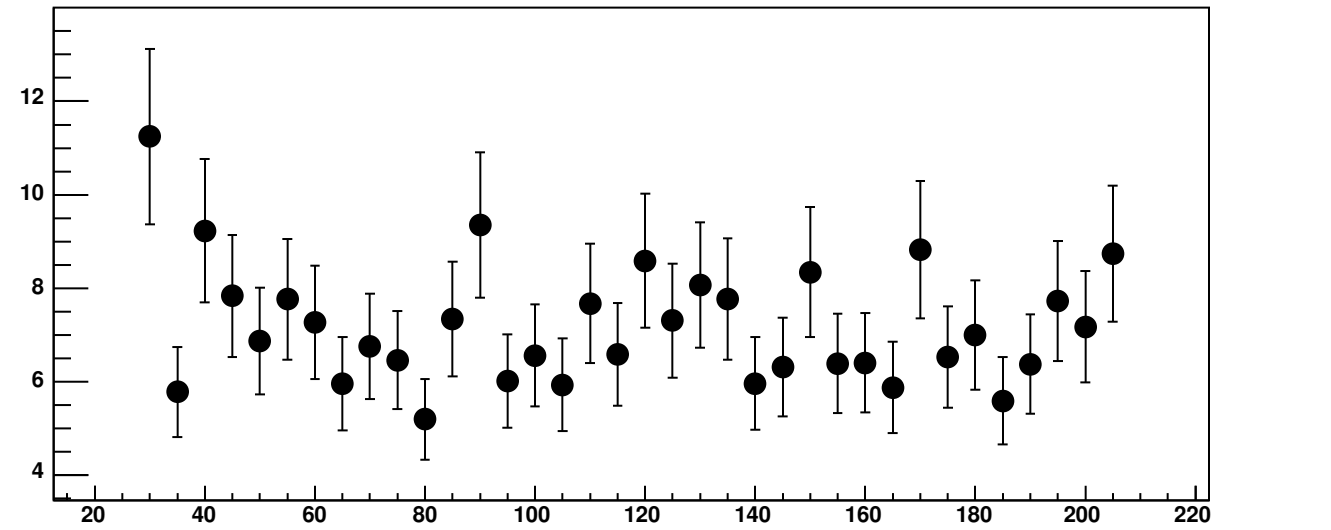
Chip 3, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold



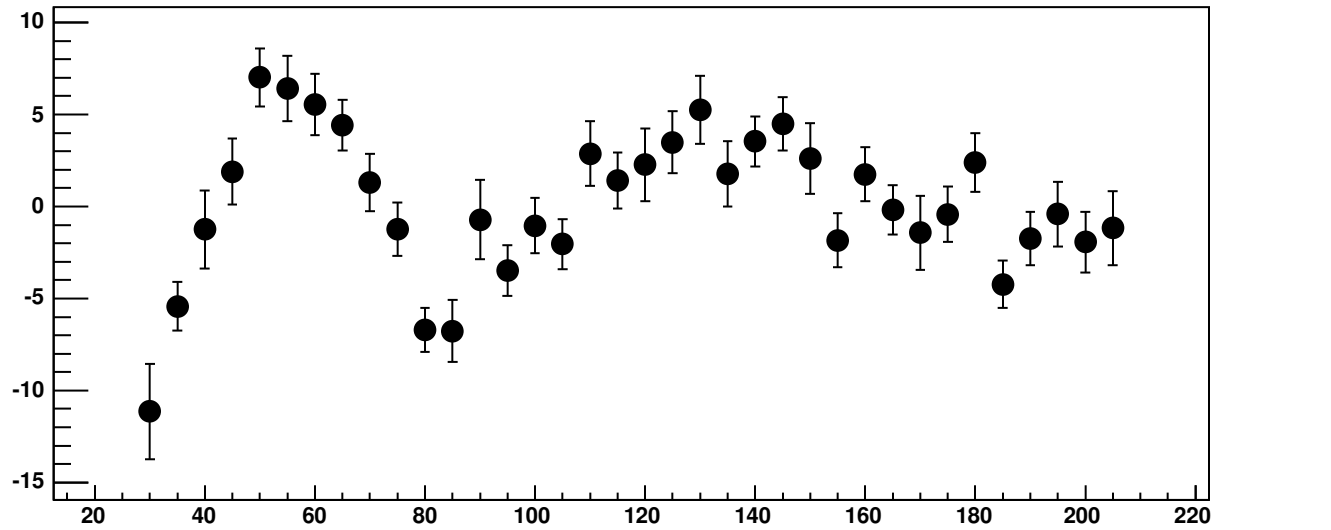
Chip 3, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

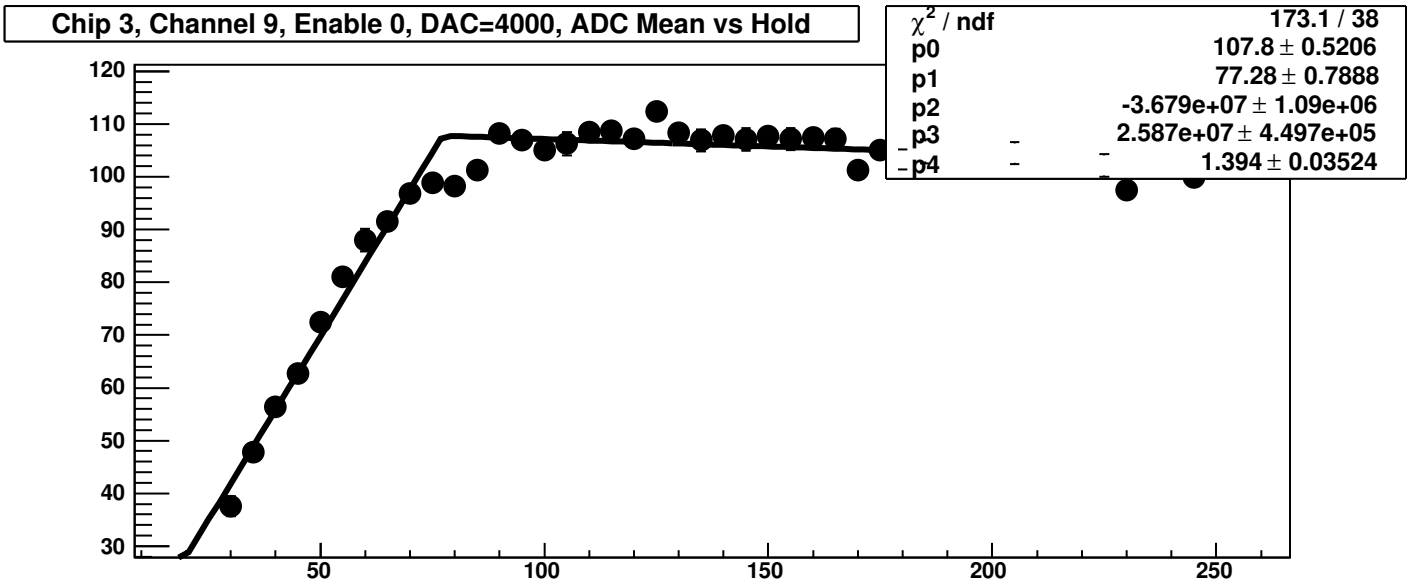


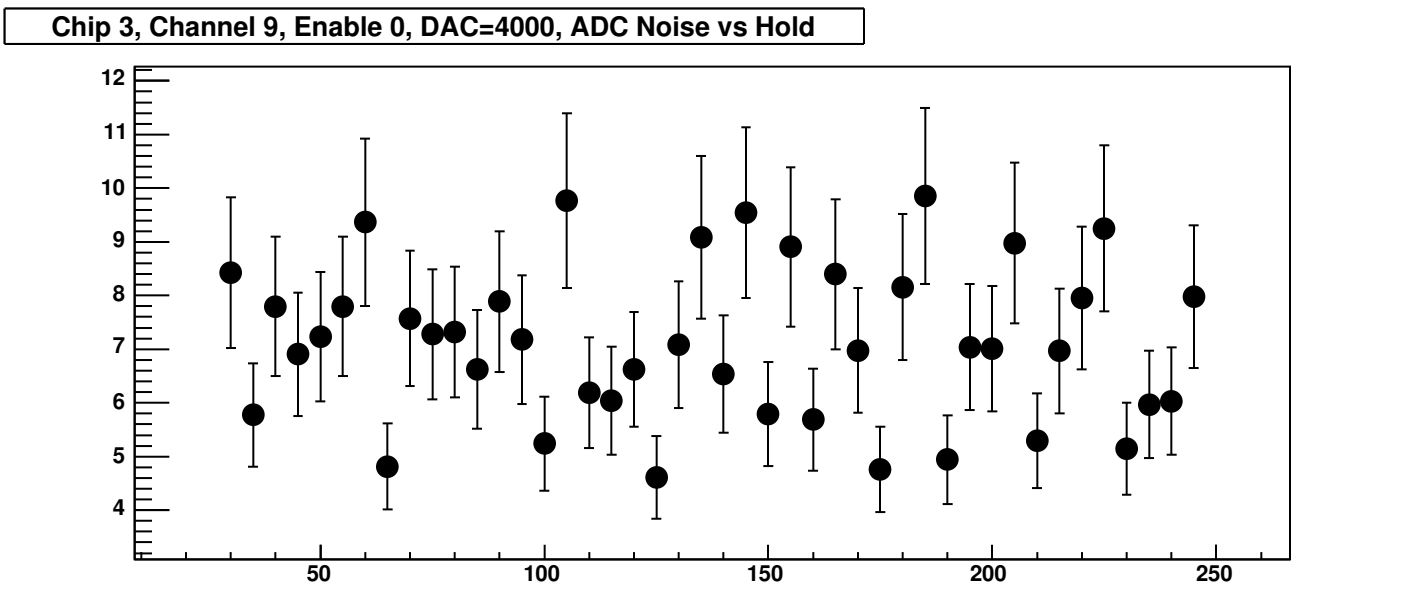
Chip 3, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold

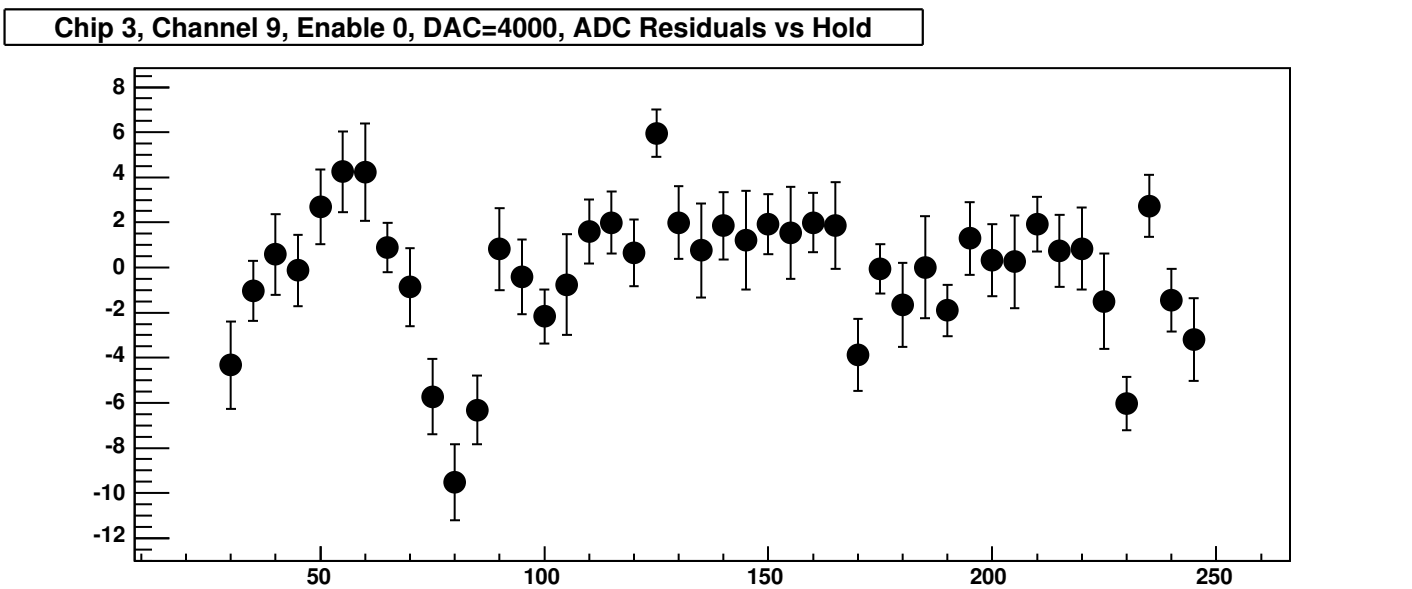


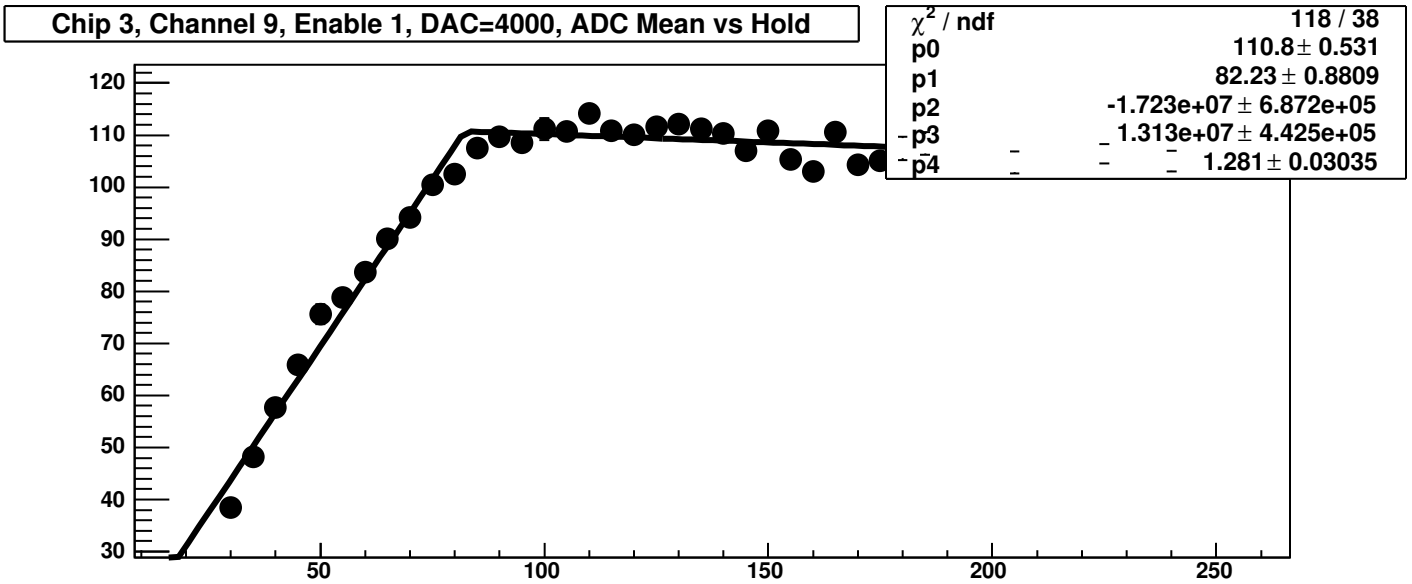
Chip 3, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

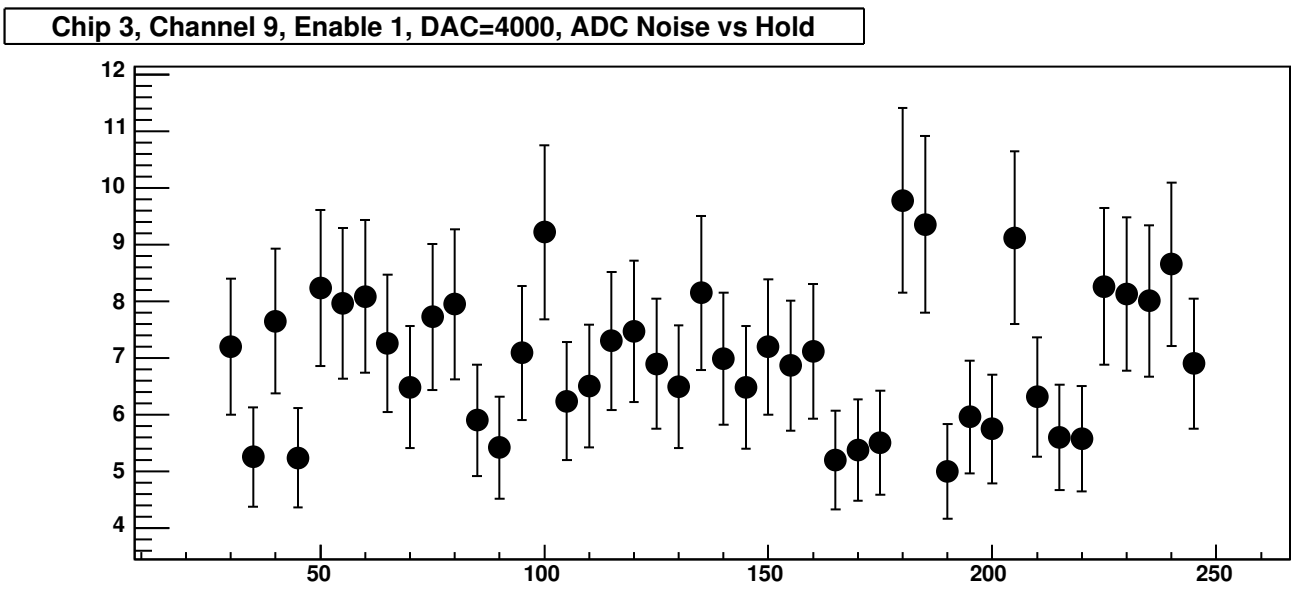


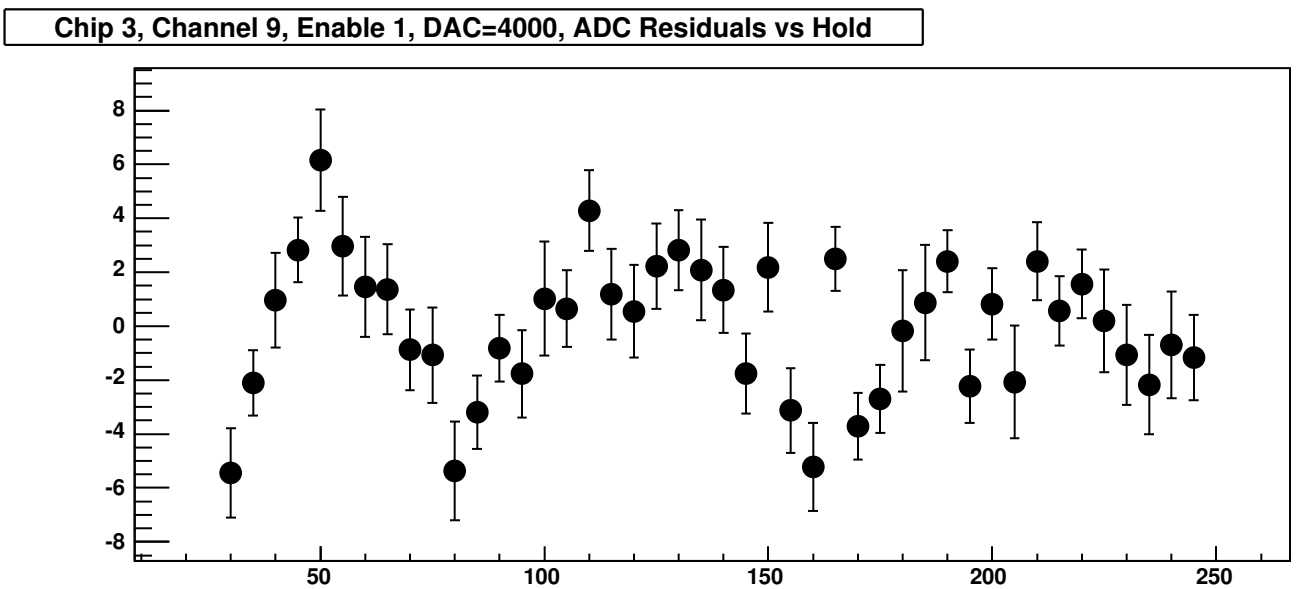

Chip 3, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 173.1 / 38
p0 107.8 ± 0.5206
p1 77.28 ± 0.7888
p2 -3.679e+07 ± 1.09e+06
p3 2.587e+07 ± 4.497e+05
p4 1.394 ± 0.03524
120
110
100
90
80
70
60
50
40
30
50
100
150
200
250


Chip 3, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

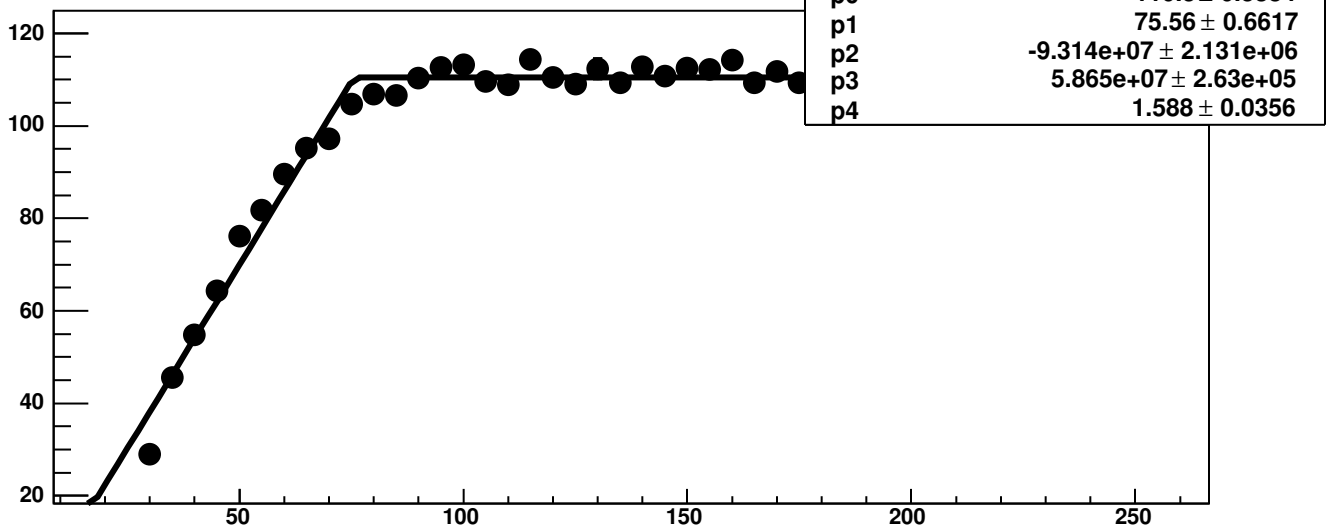

Chip 3, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
-12
50
100
150
200
250


Chip 3, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 118 / 38
p0 110.8 ± 0.531
p1 82.23 ± 0.8809
p2 -1.723e+07 ± 6.872e+05
p3 1.313e+07 ± 4.425e+05
p4 1.281 ± 0.03035
120
110
100
90
80
70
60
50
40
30
50
100
150
200
250

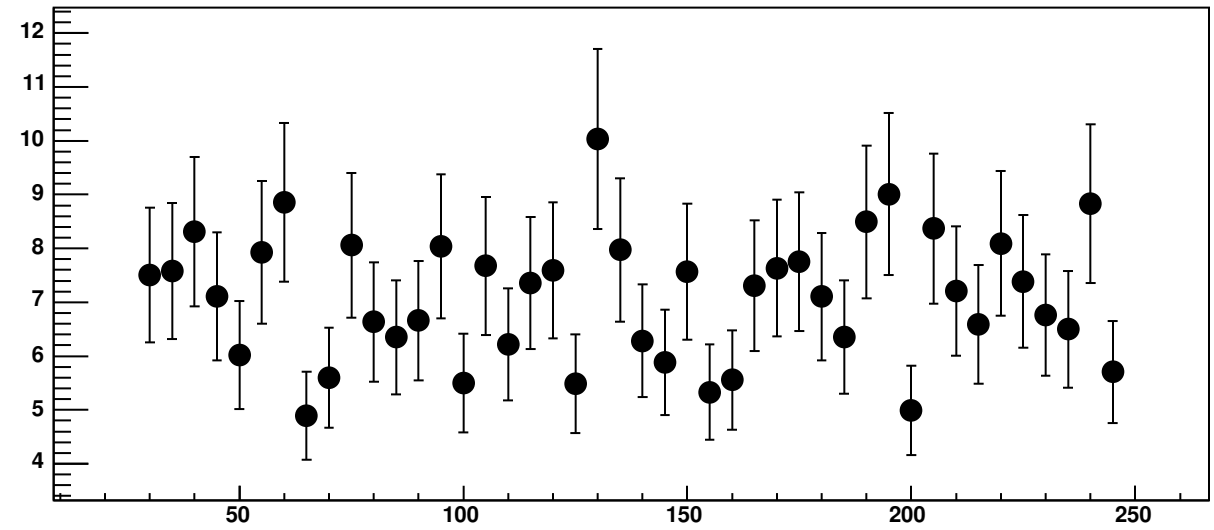

Chip 3, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 3, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

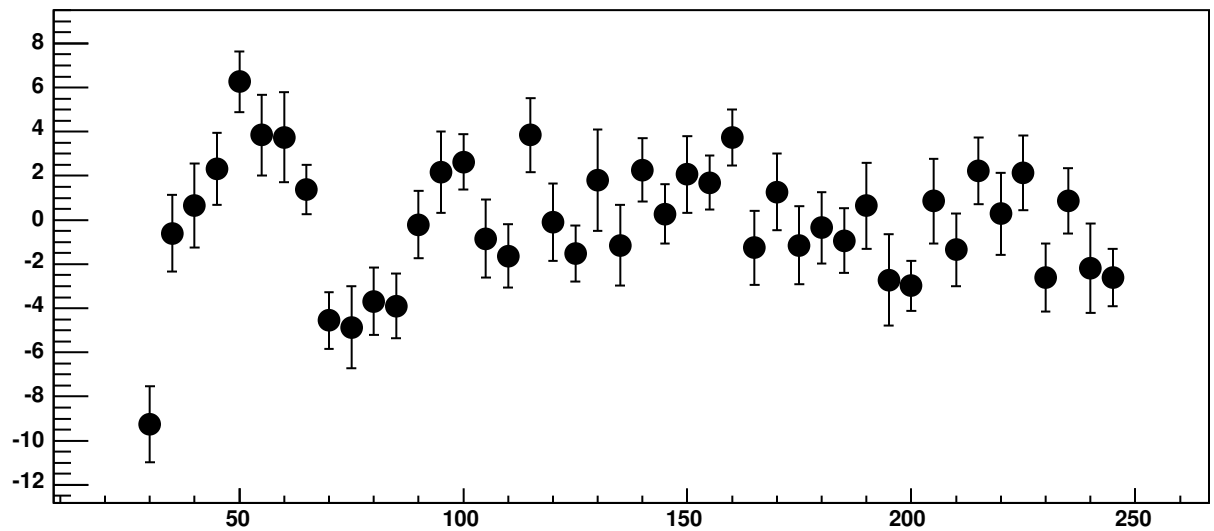
Chip 3, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold



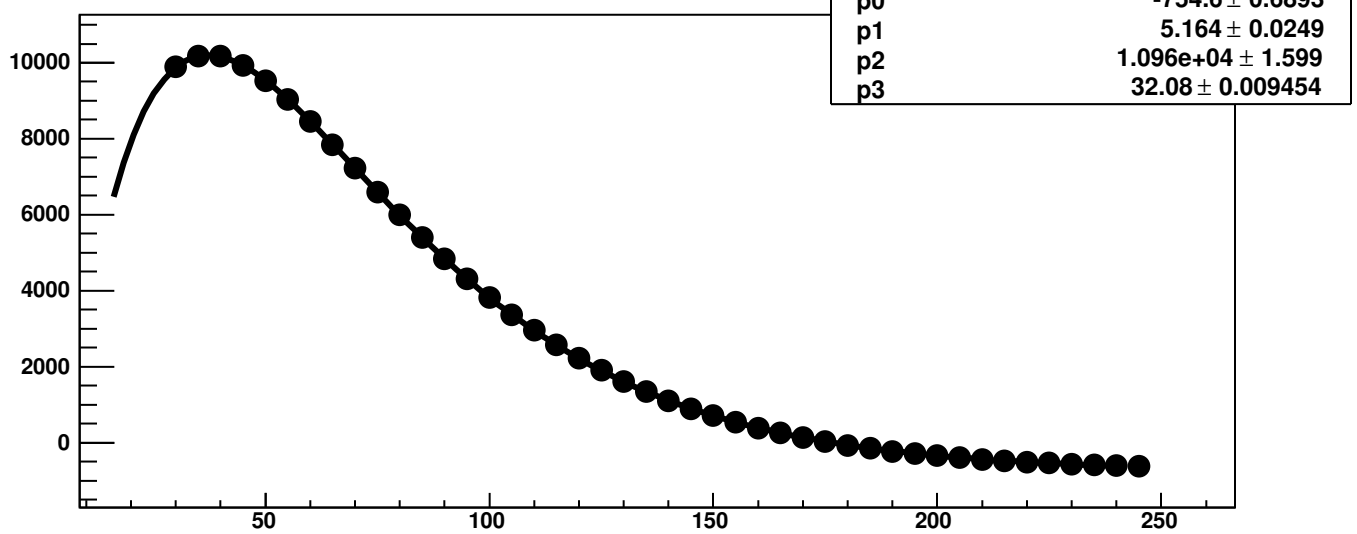
Chip 3, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



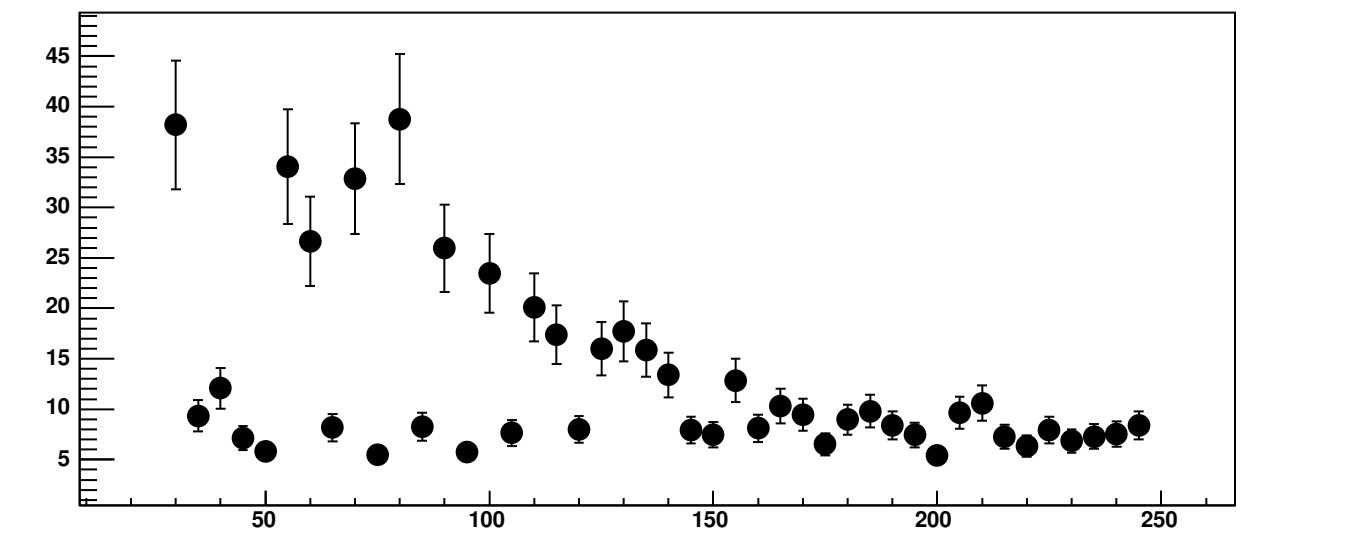
Chip 3, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold



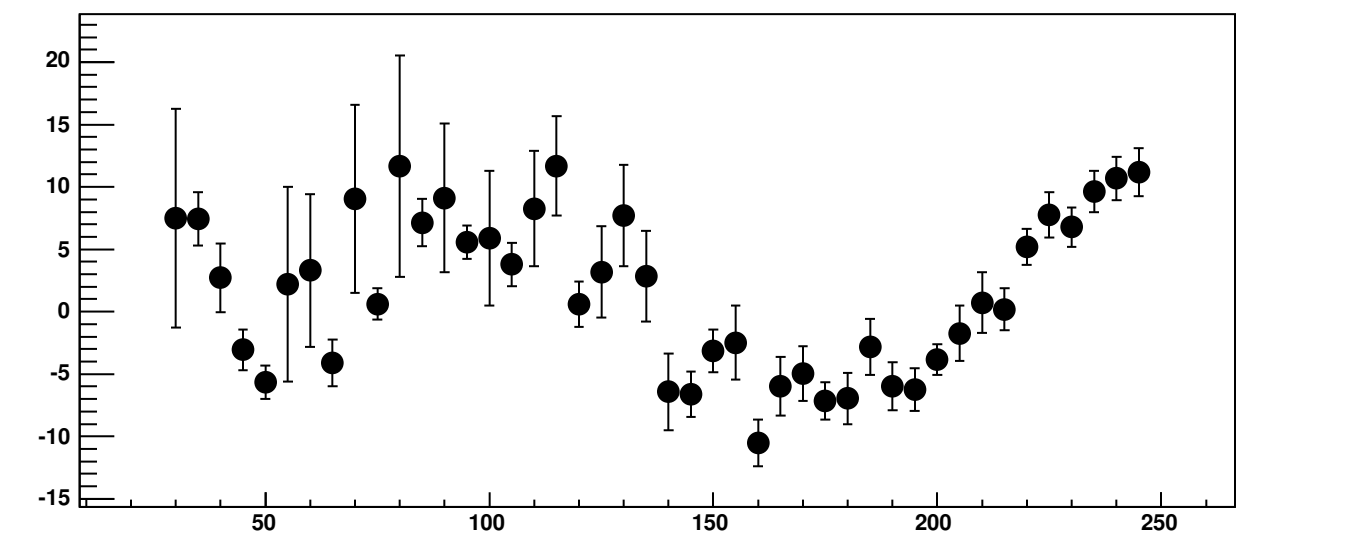
Chip 3, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold

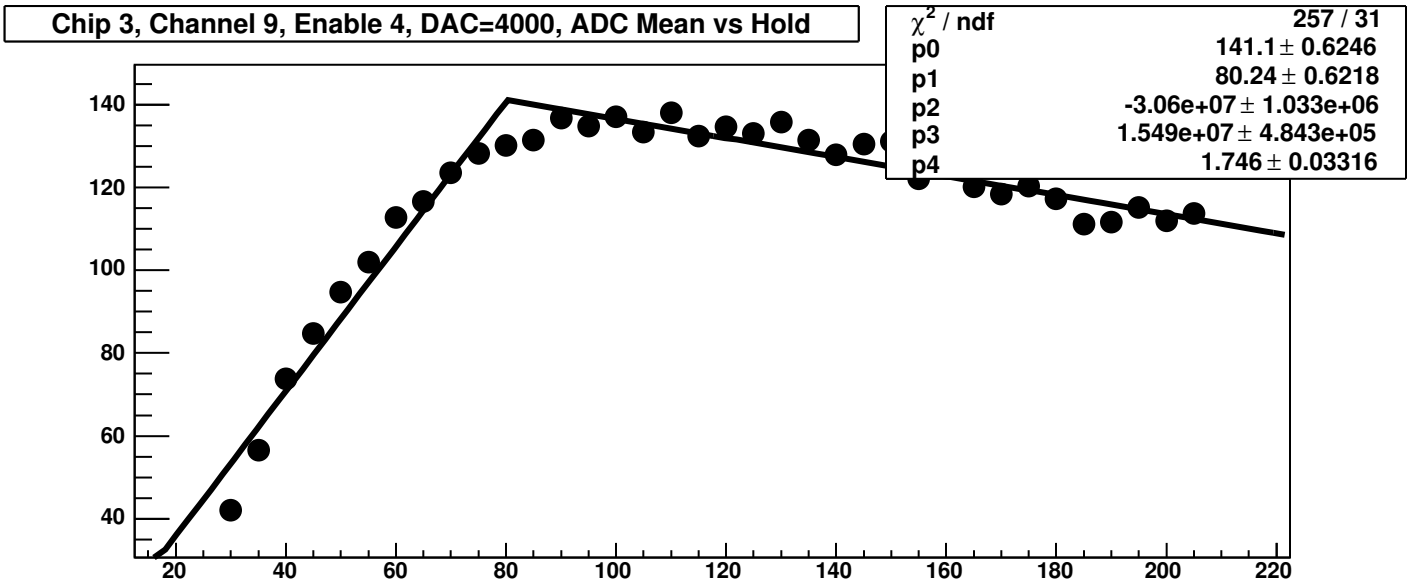


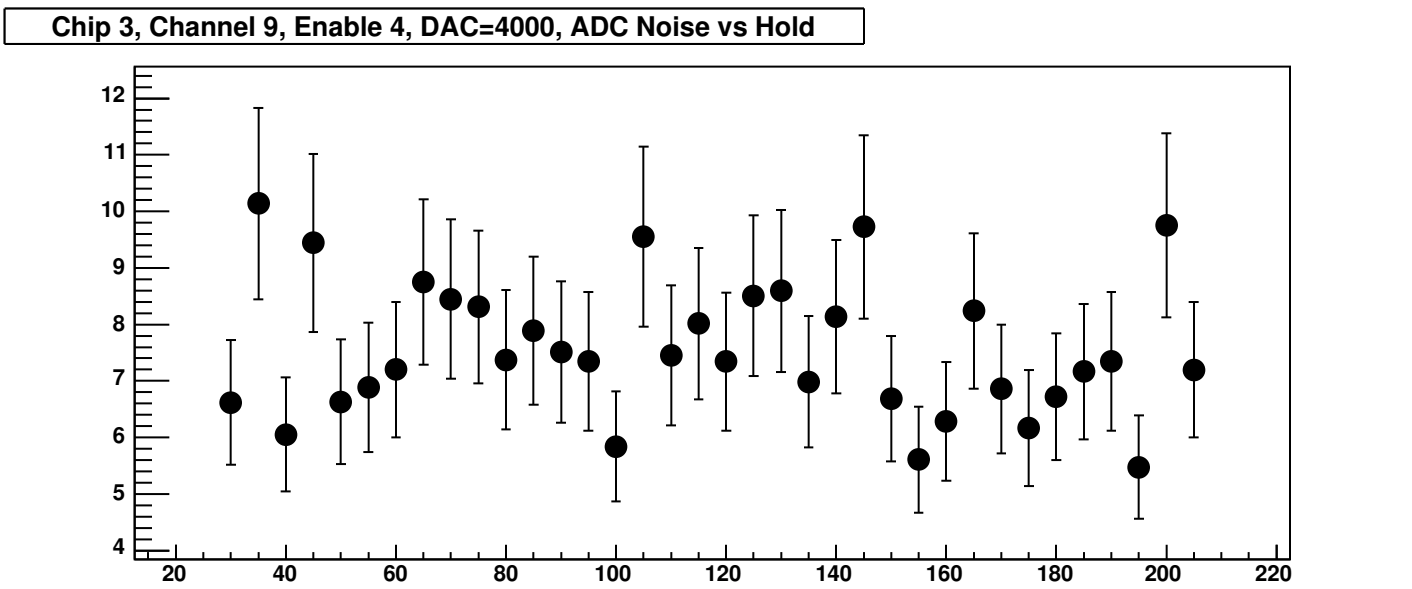
Chip 3, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold

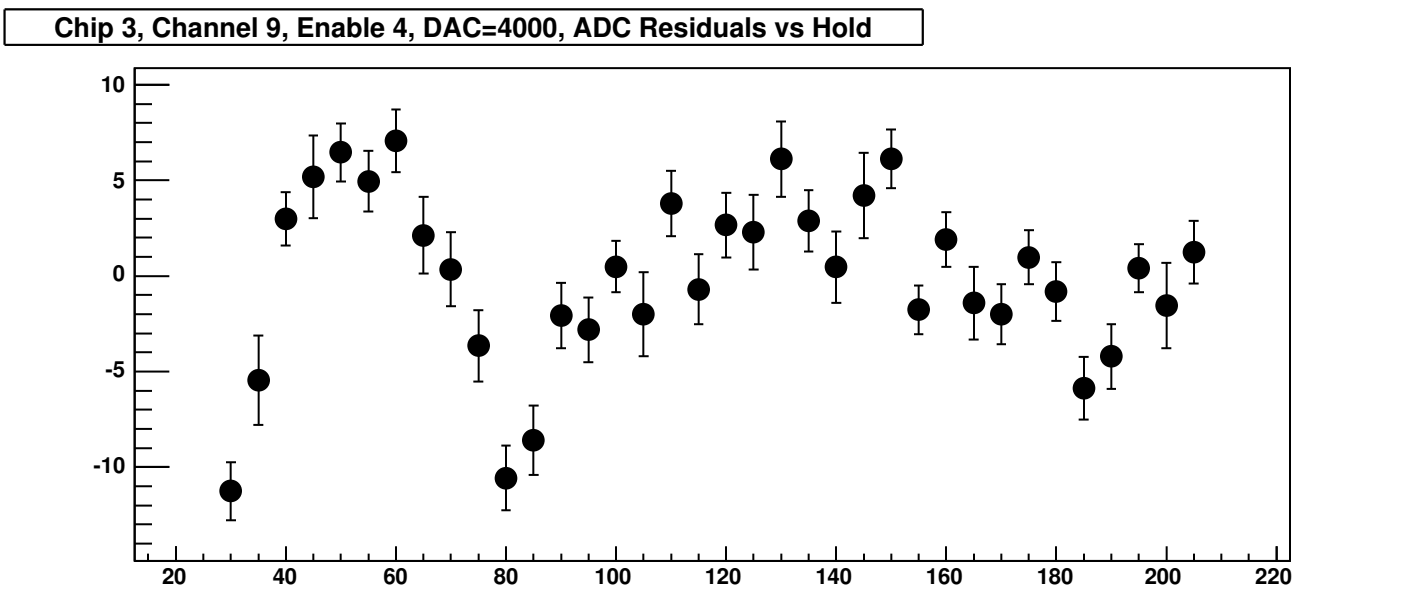


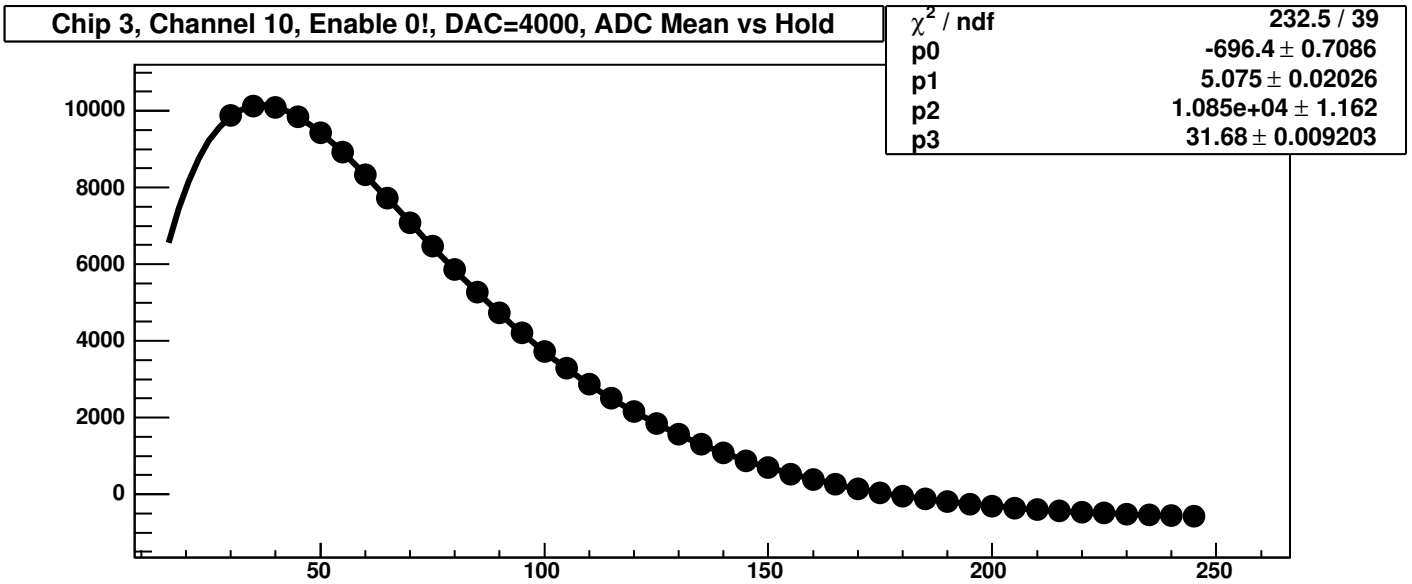
Chip 3, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold

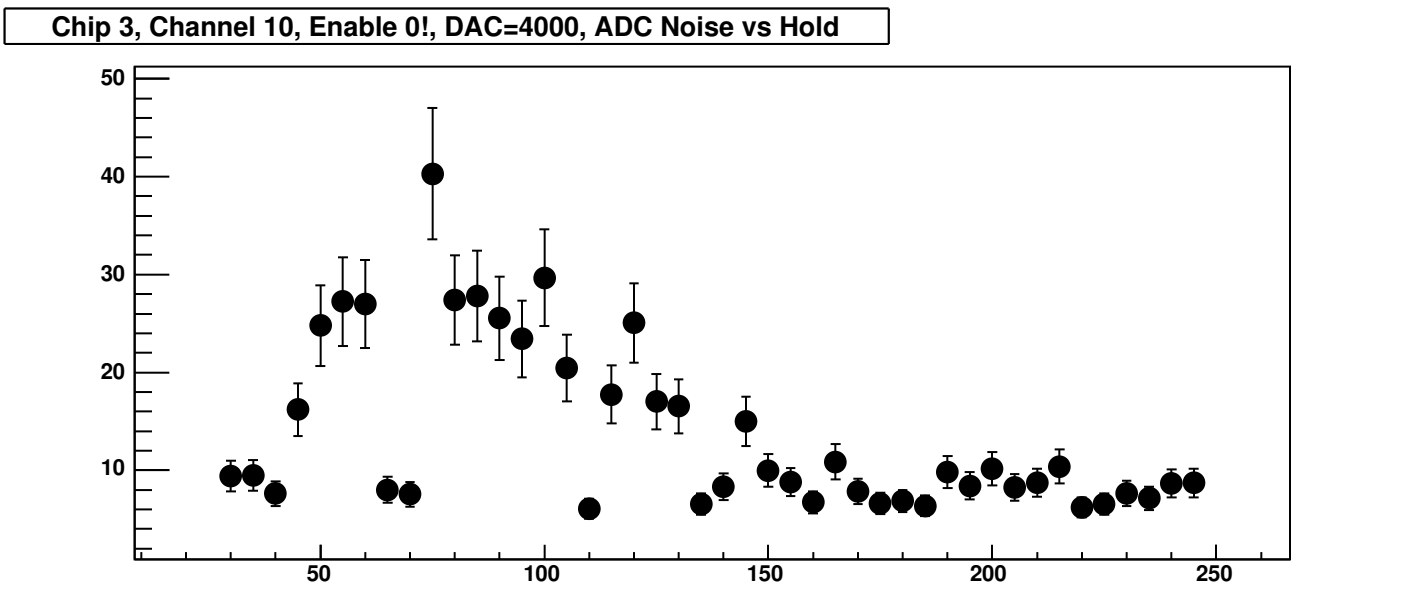


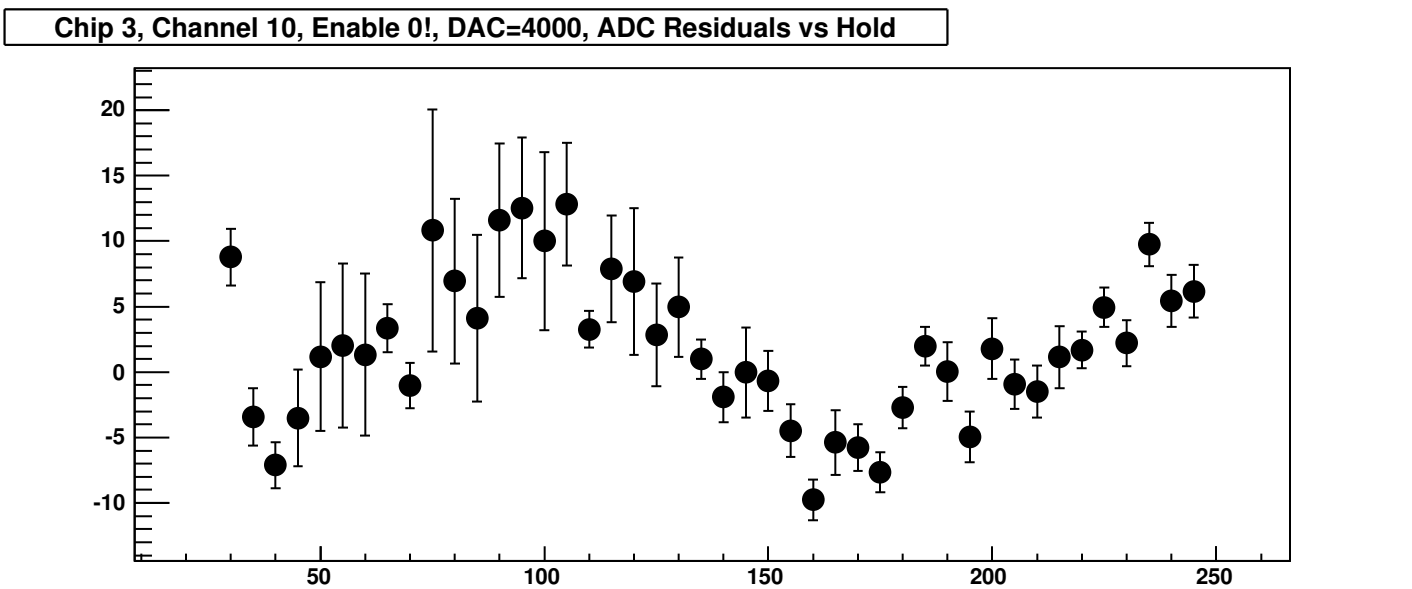

Chip 3, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 257 / 31
p0 141.1 ± 0.6246
p1 80.24 ± 0.6218
p2 -3.06e+07 ± 1.033e+06
p3 1.549e+07 ± 4.843e+05
p4 1.746 ± 0.03316


Chip 3, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold

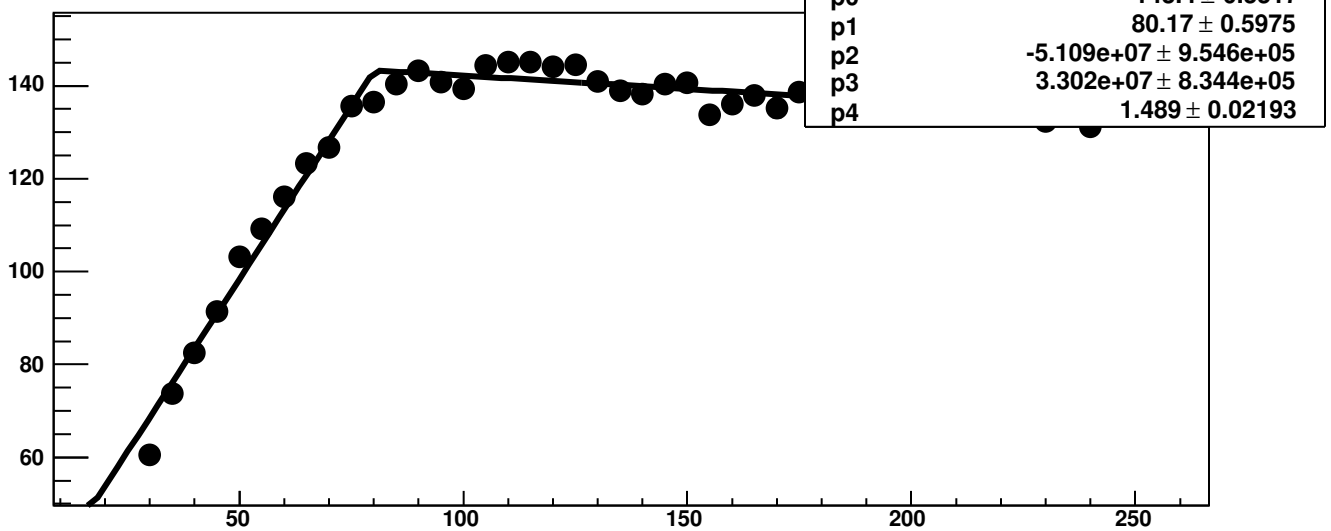

Chip 3, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold


Chip 3, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 232.5 / 39
p0 -696.4 ± 0.7086
p1 5.075 ± 0.02026
p2 1.085e+04 ± 1.162
p3 31.68 ± 0.009203
10000
8000
6000
4000
2000
0
50
100
150
200
250

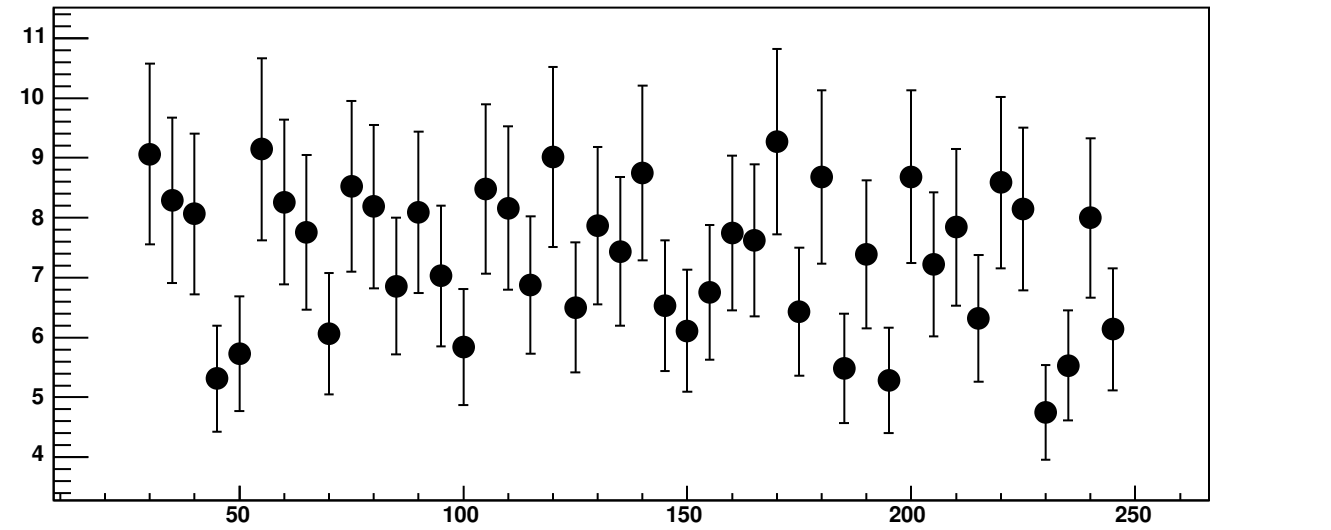

Chip 3, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
50
100
150
200
250


Chip 3, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
50
100
150
200
250

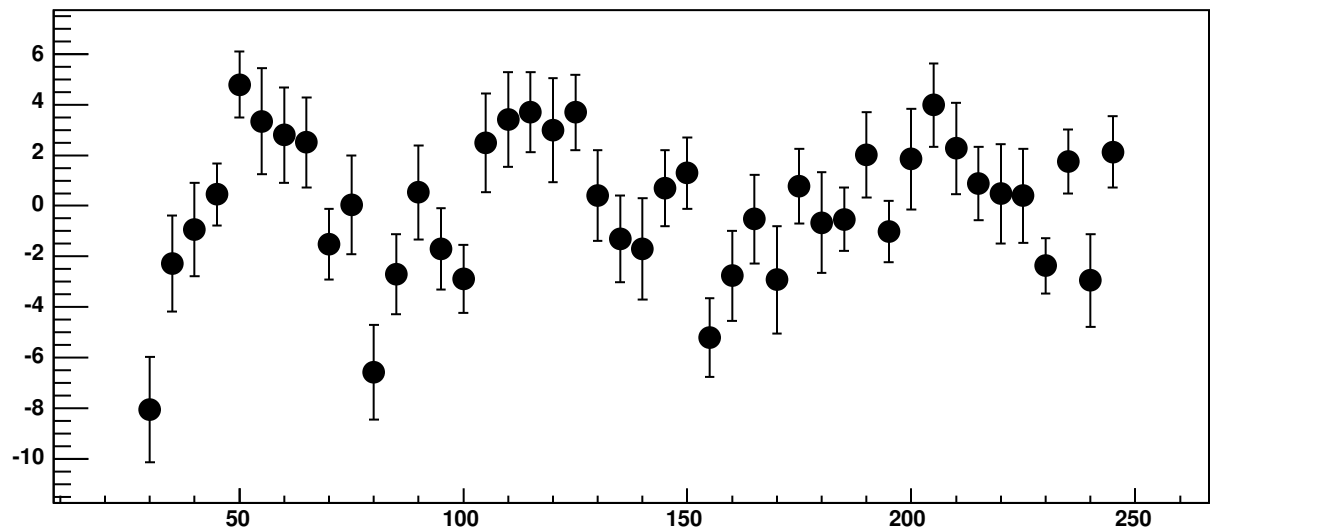
Chip 3, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold



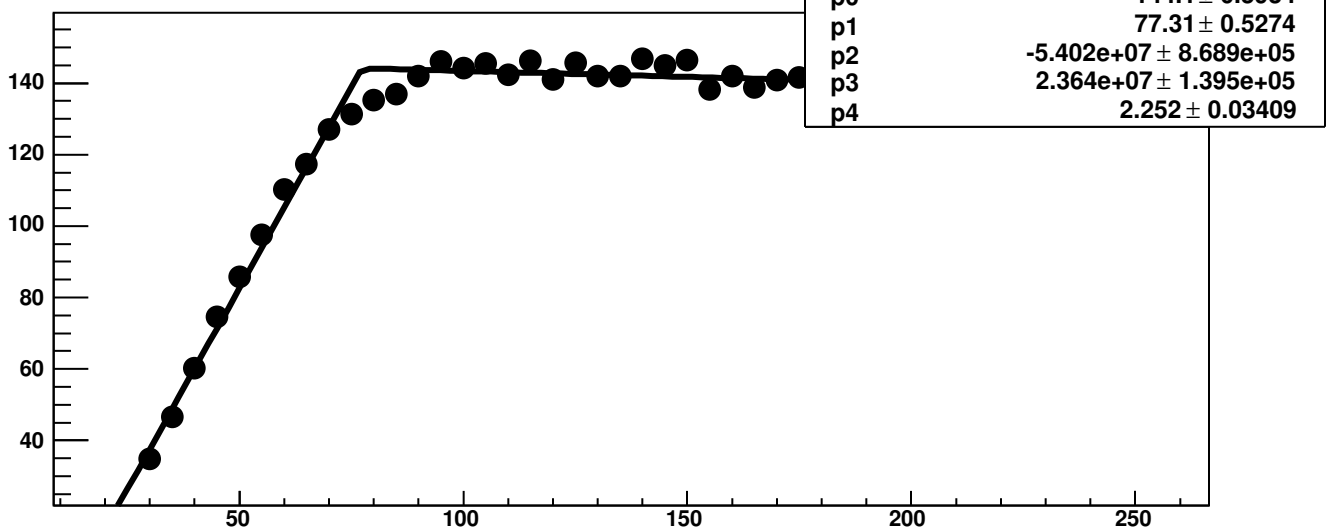
Chip 3, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



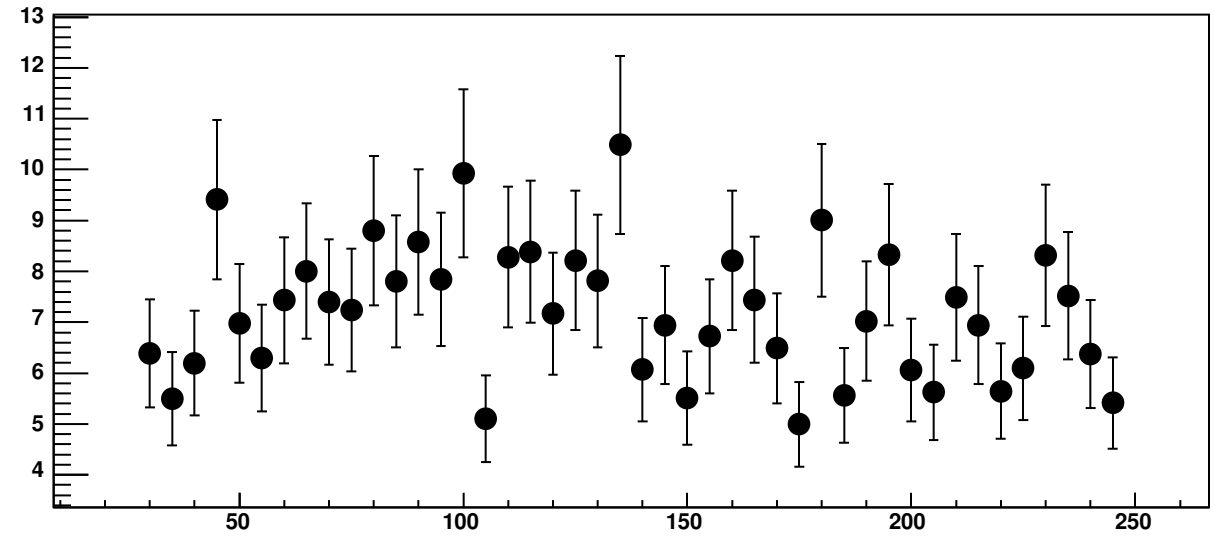
Chip 3, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold



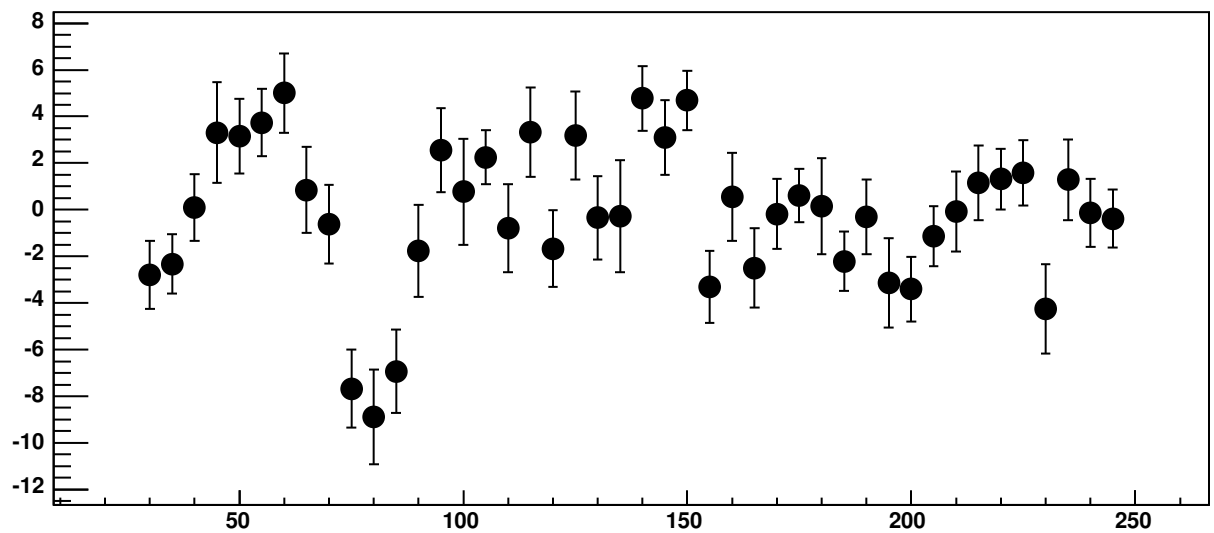
Chip 3, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold



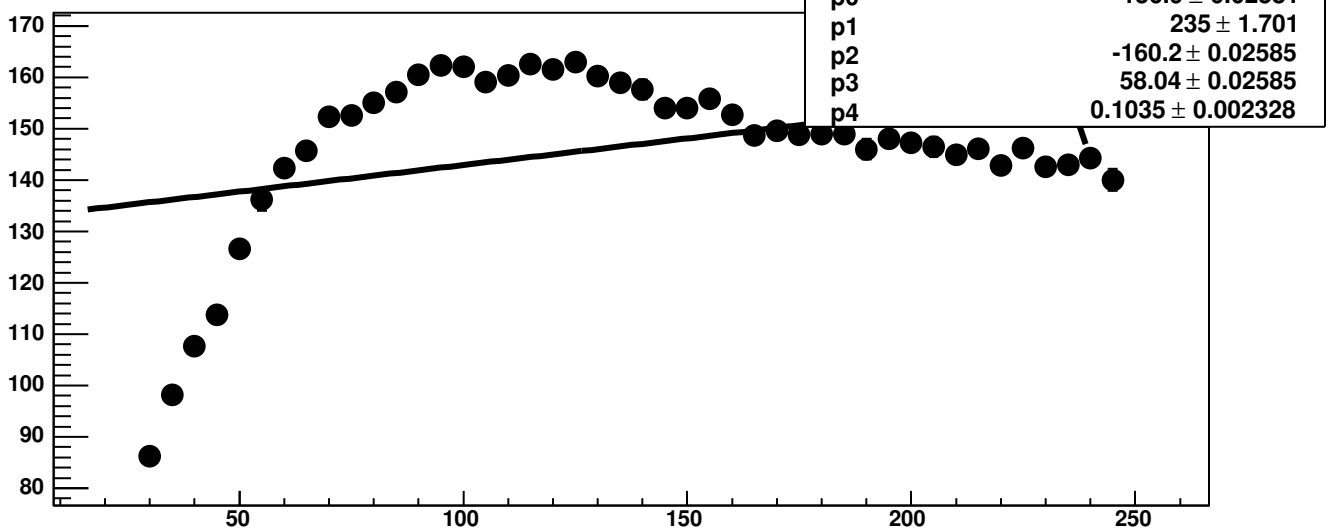
Chip 3, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



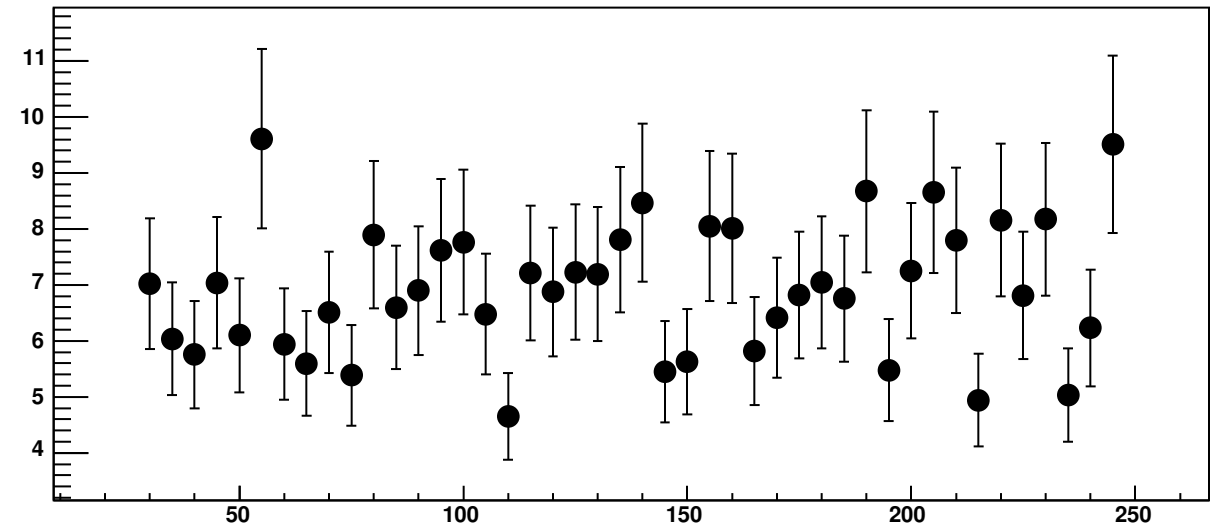
Chip 3, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold



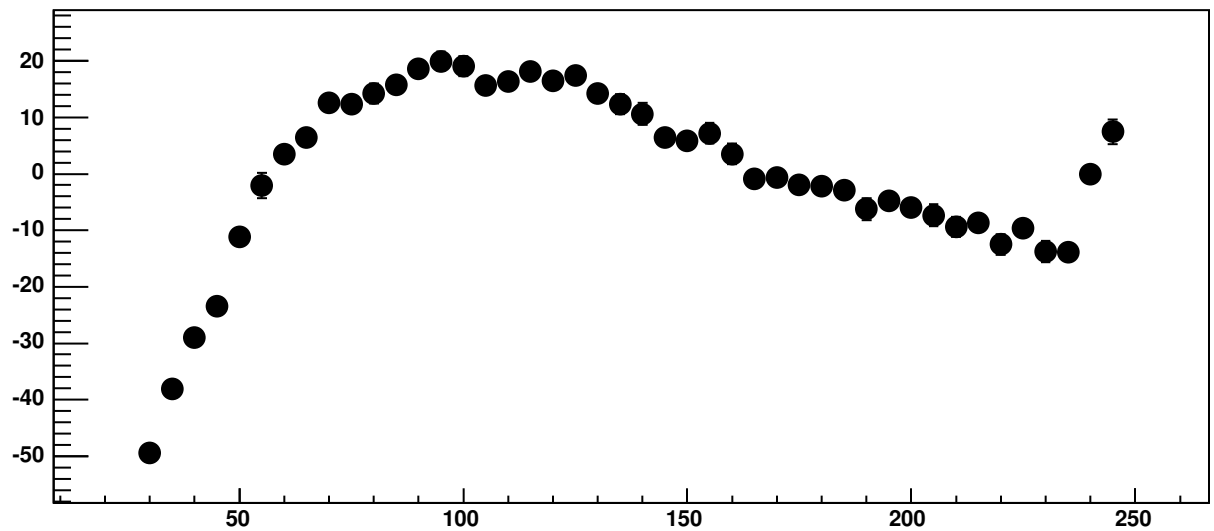
Chip 3, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold

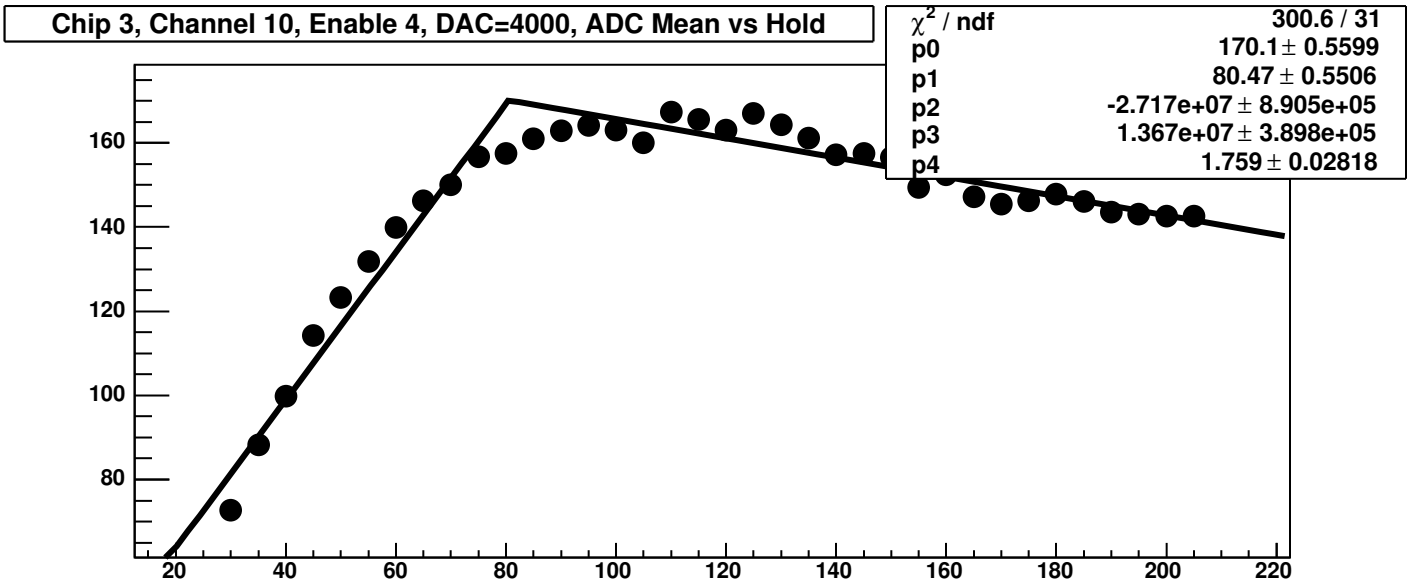


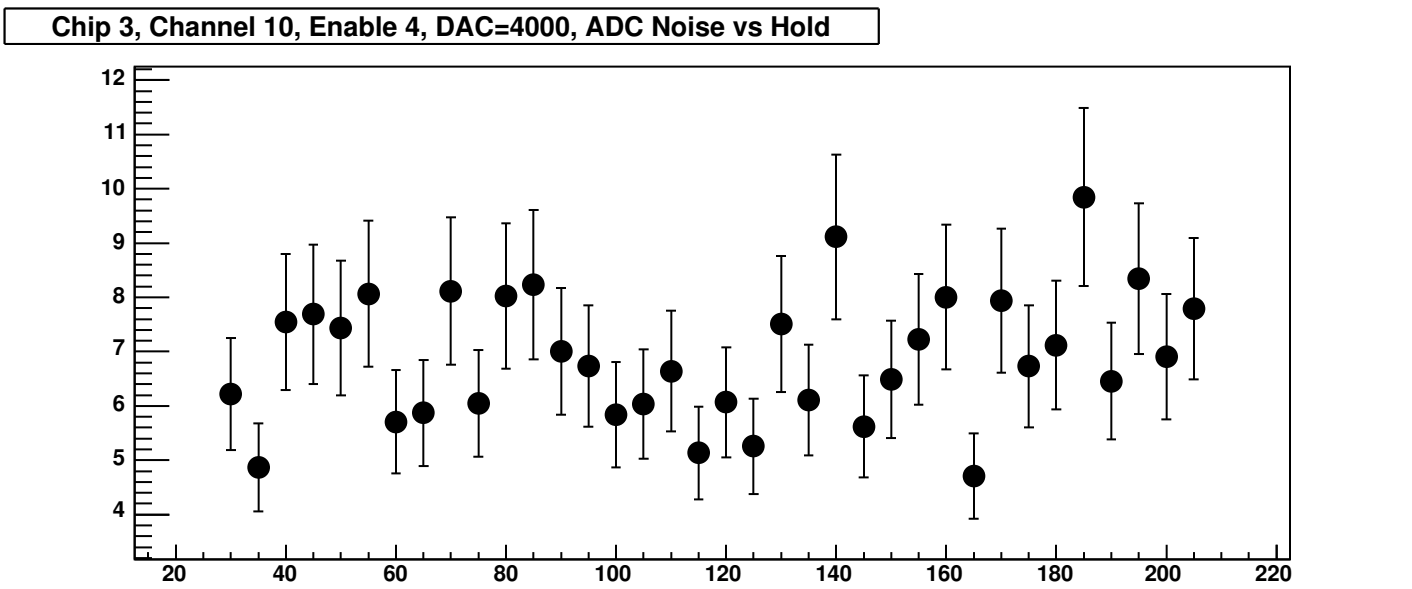
Chip 3, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold

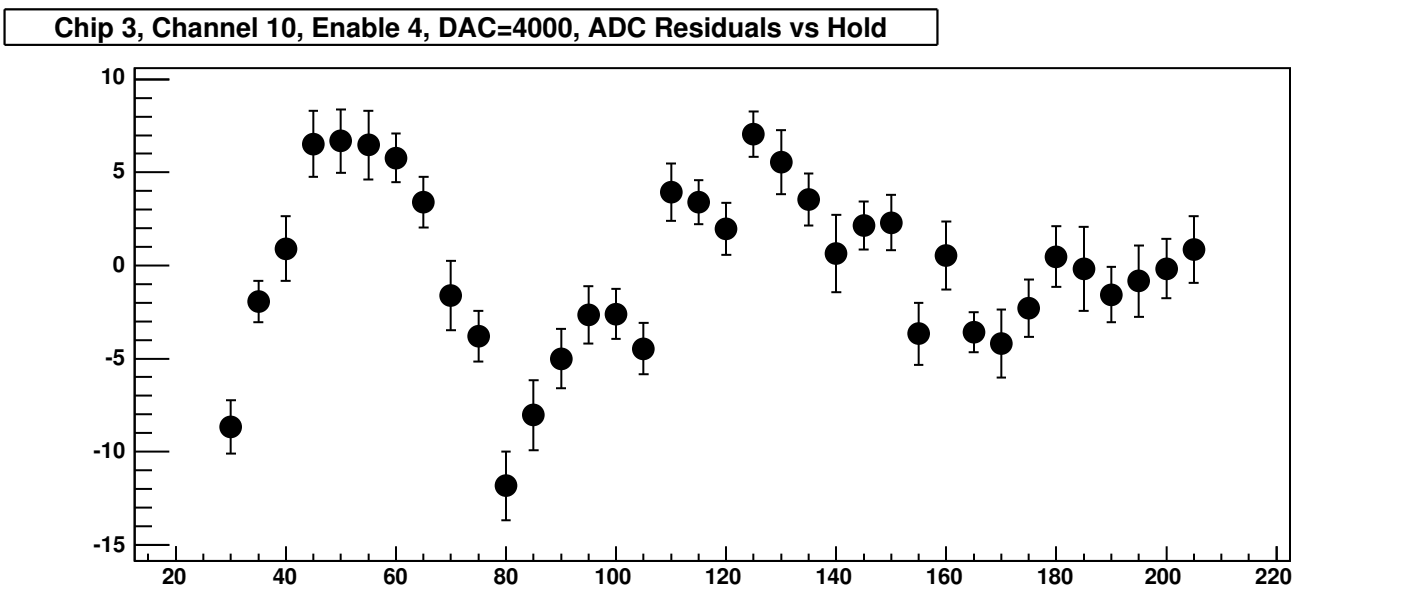


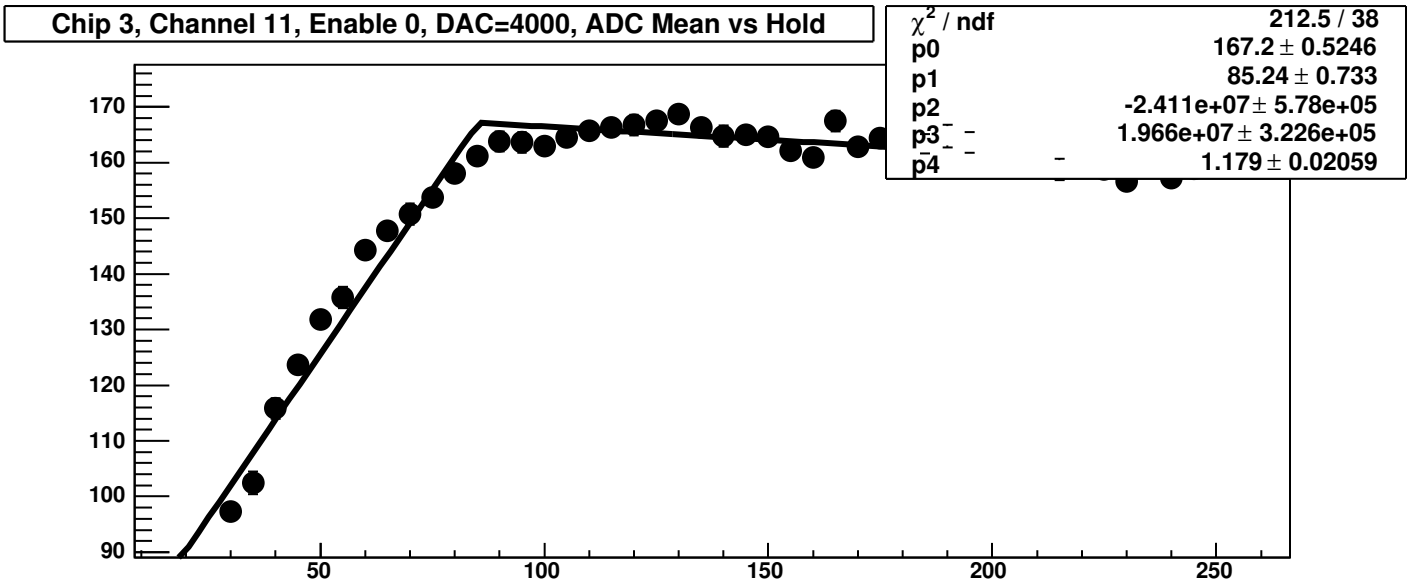
Chip 3, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold

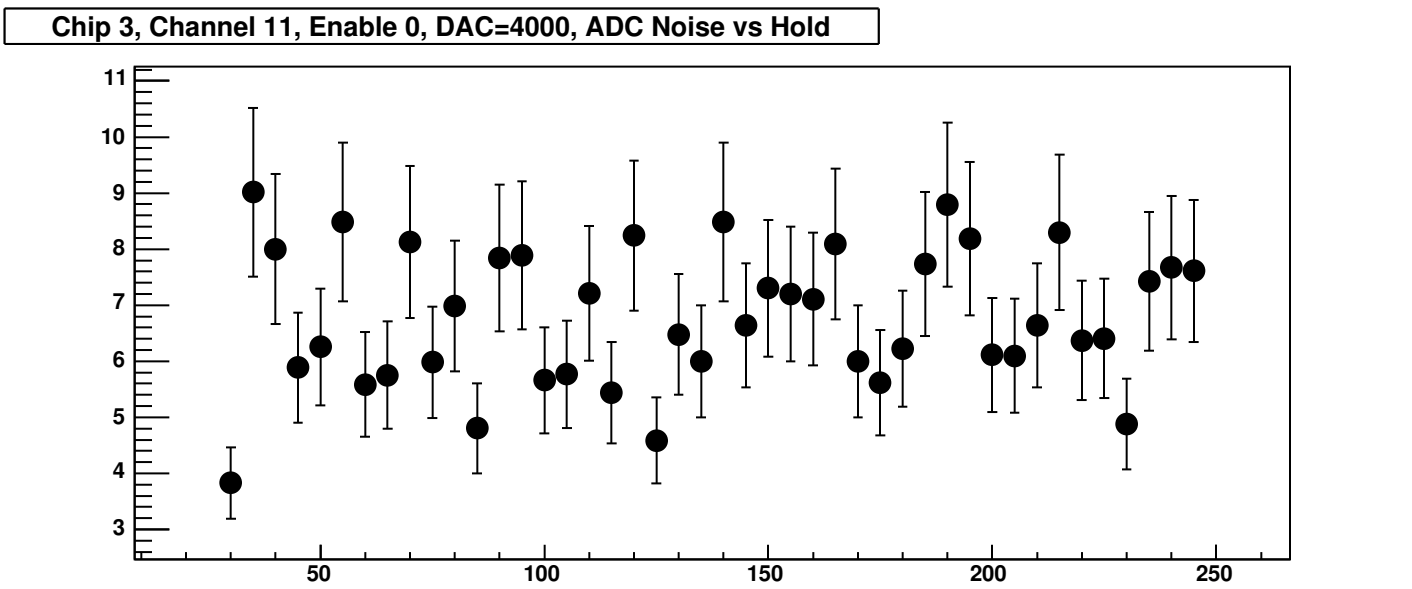


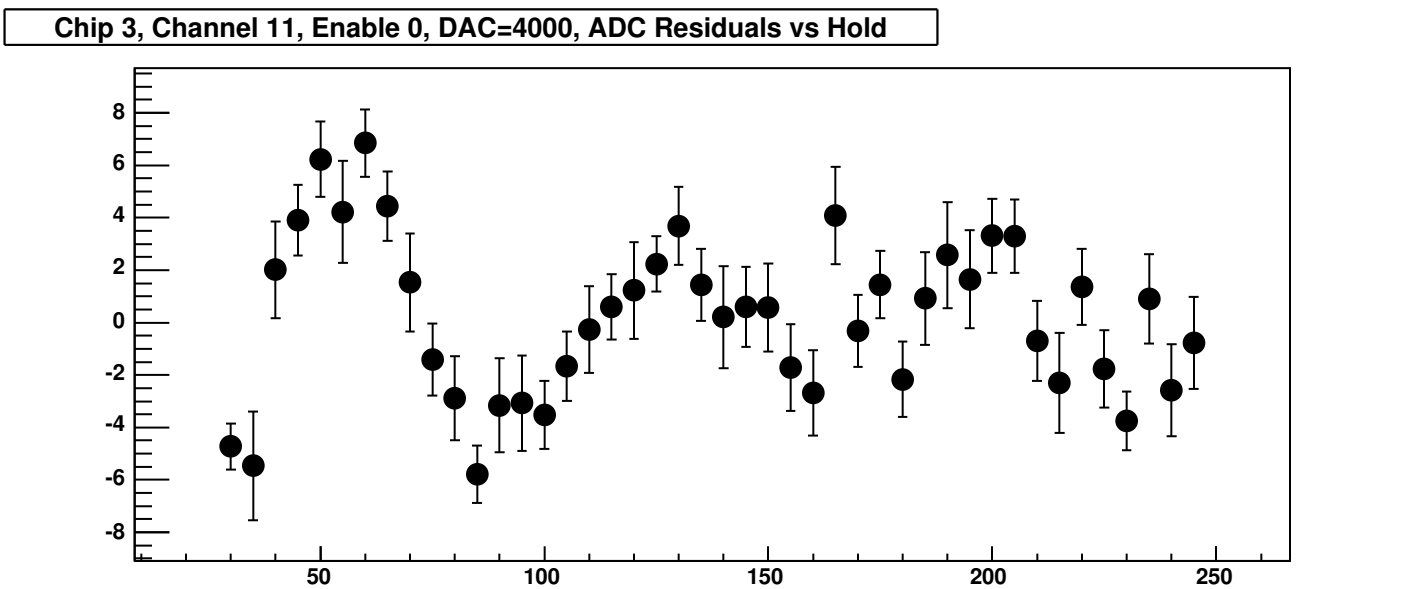

Chip 3, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 300.6 / 31
p0 170.1 ± 0.5599
p1 80.47 ± 0.5506
p2 -2.717e+07 ± 8.905e+05
p3 1.367e+07 ± 3.898e+05
p4 1.759 ± 0.02818

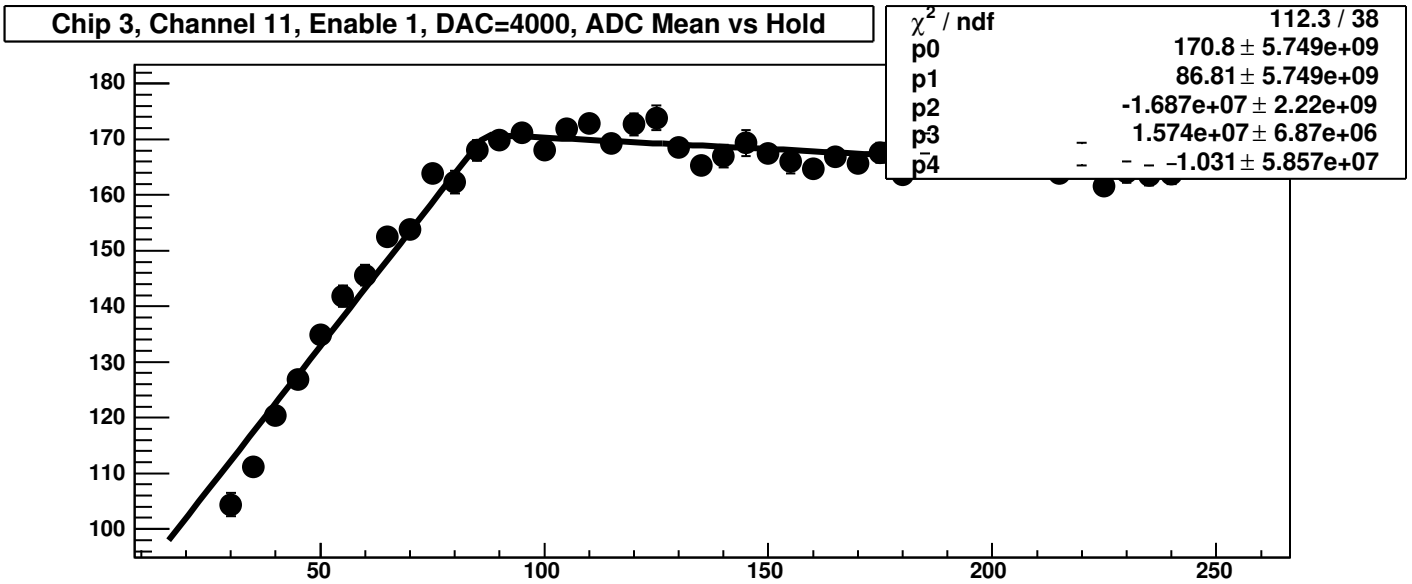

Chip 3, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold

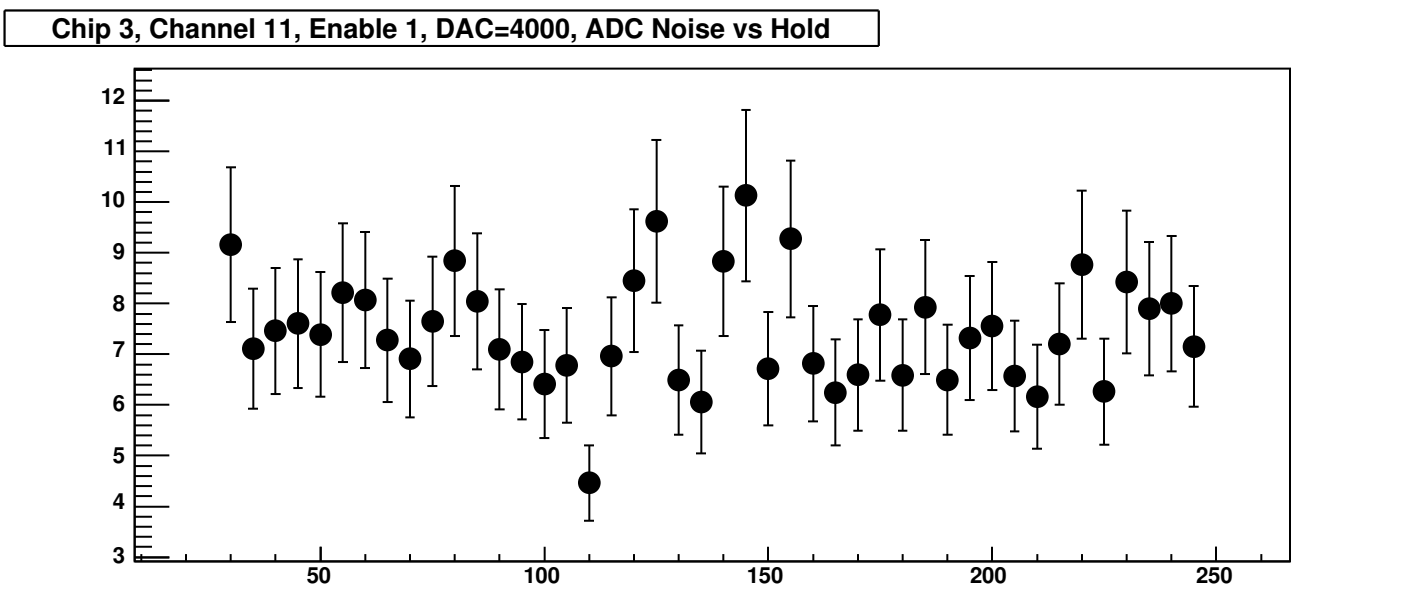

Chip 3, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

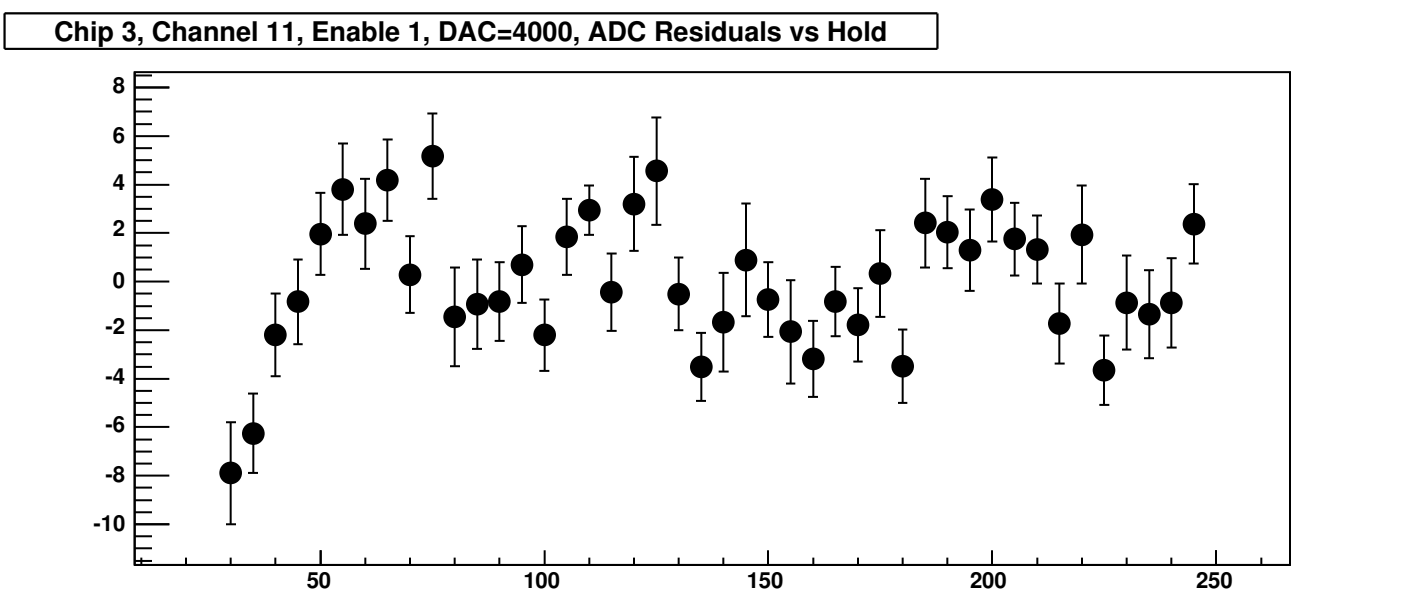

Chip 3, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 212.5 / 38
p0 167.2 ± 0.5246
p1 85.24 ± 0.733
p2 -2.411e+07 ± 5.78e+05
p3 1.966e+07 ± 3.226e+05
p4 1.179 ± 0.02059
170
160
150
140
130
120
110
100
90
50
100
150
200
250

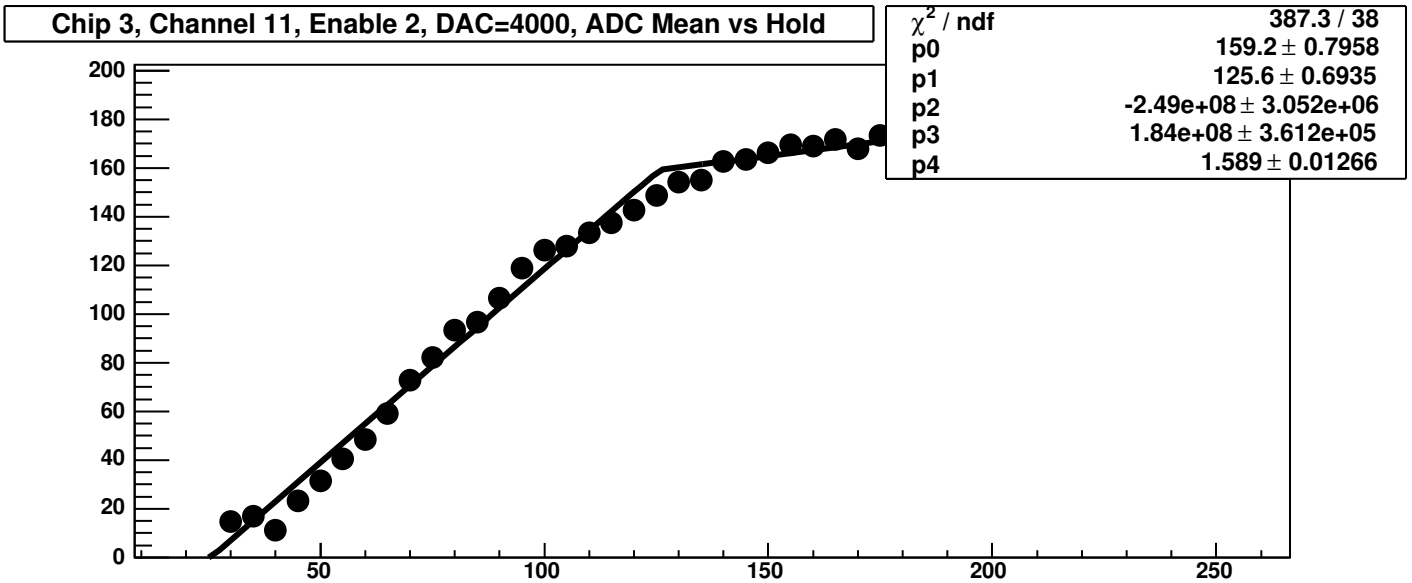

Chip 3, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
50
100
150
200
250

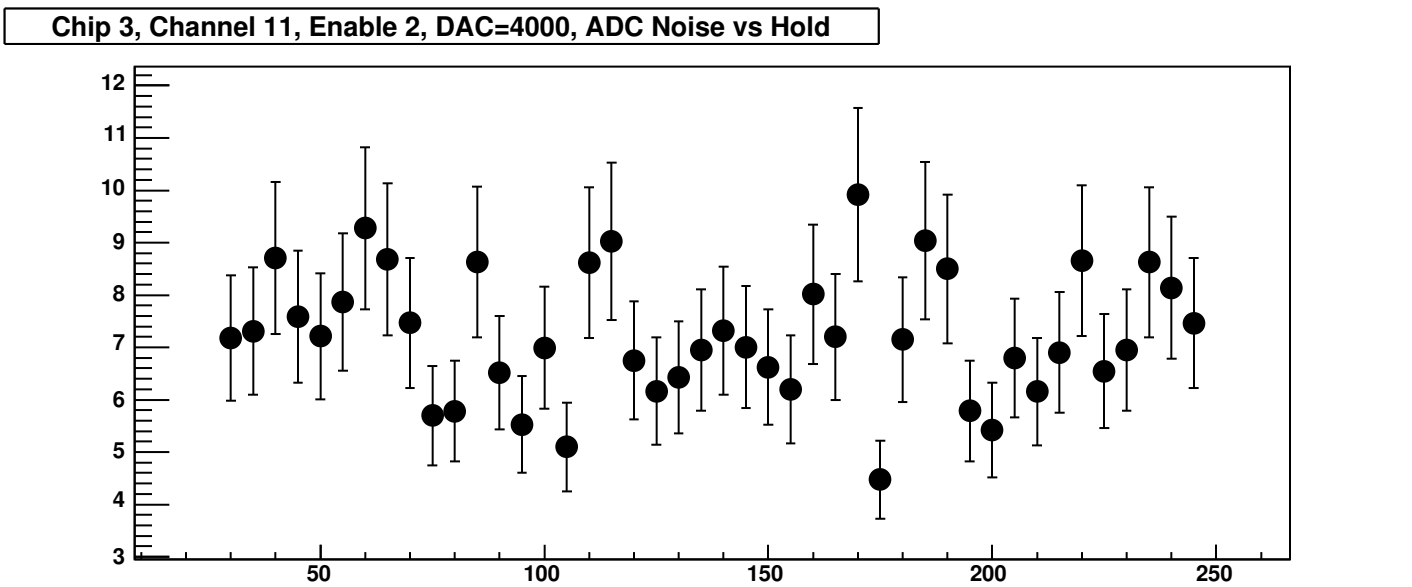

Chip 3, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

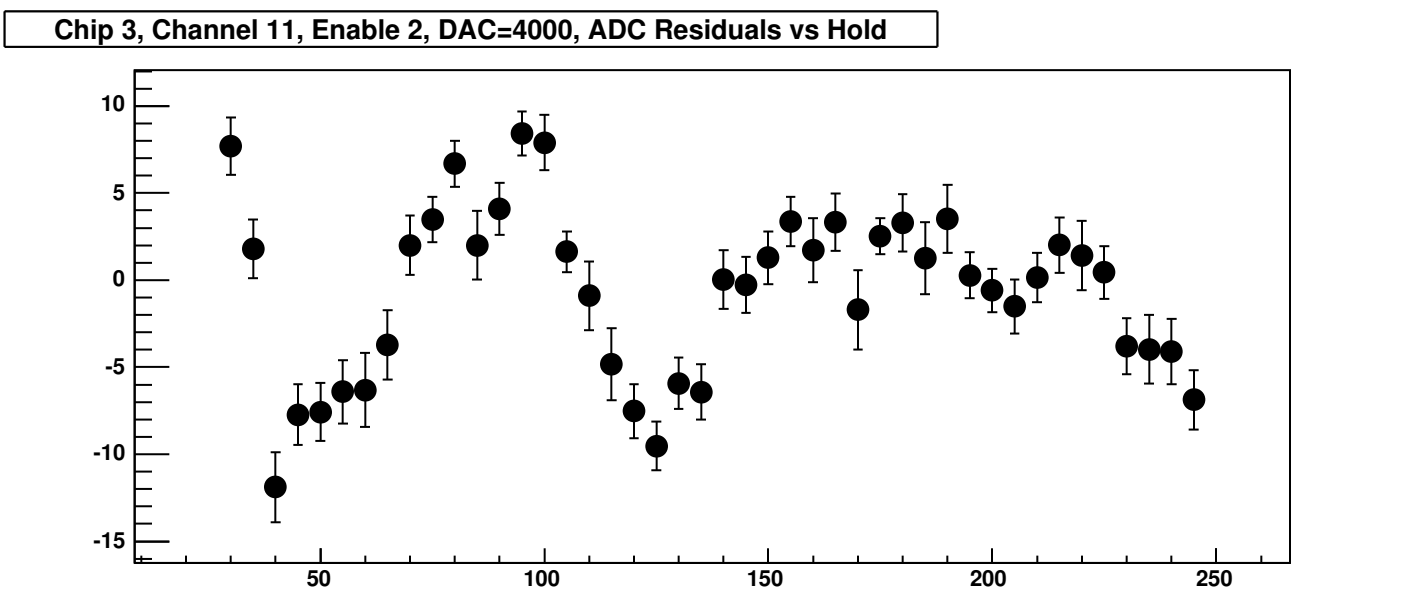

Chip 3, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 112.3 / 38
p0 170.8 ± 5.749e+09
p1 86.81 ± 5.749e+09
p2 -1.687e+07 ± 2.22e+09
p3 1.574e+07 ± 6.87e+06
p4 -1.031 ± 5.857e+07
180
170
160
150
140
130
120
110
100
50
100
150
200
250

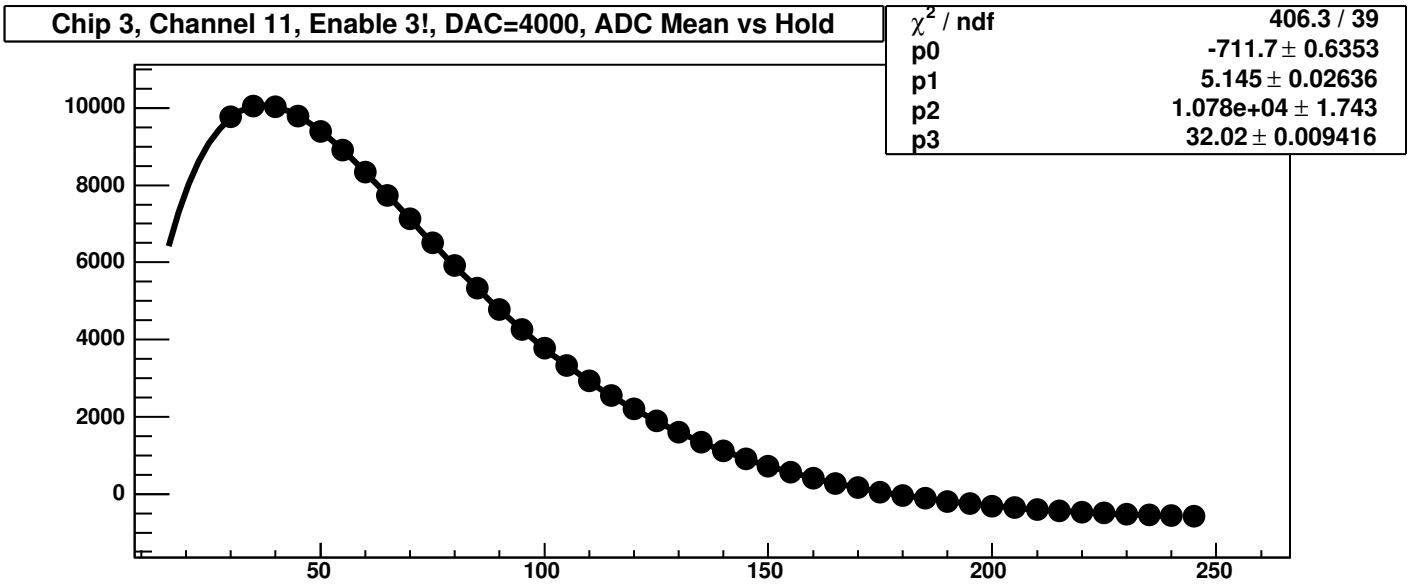

Chip 3, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250

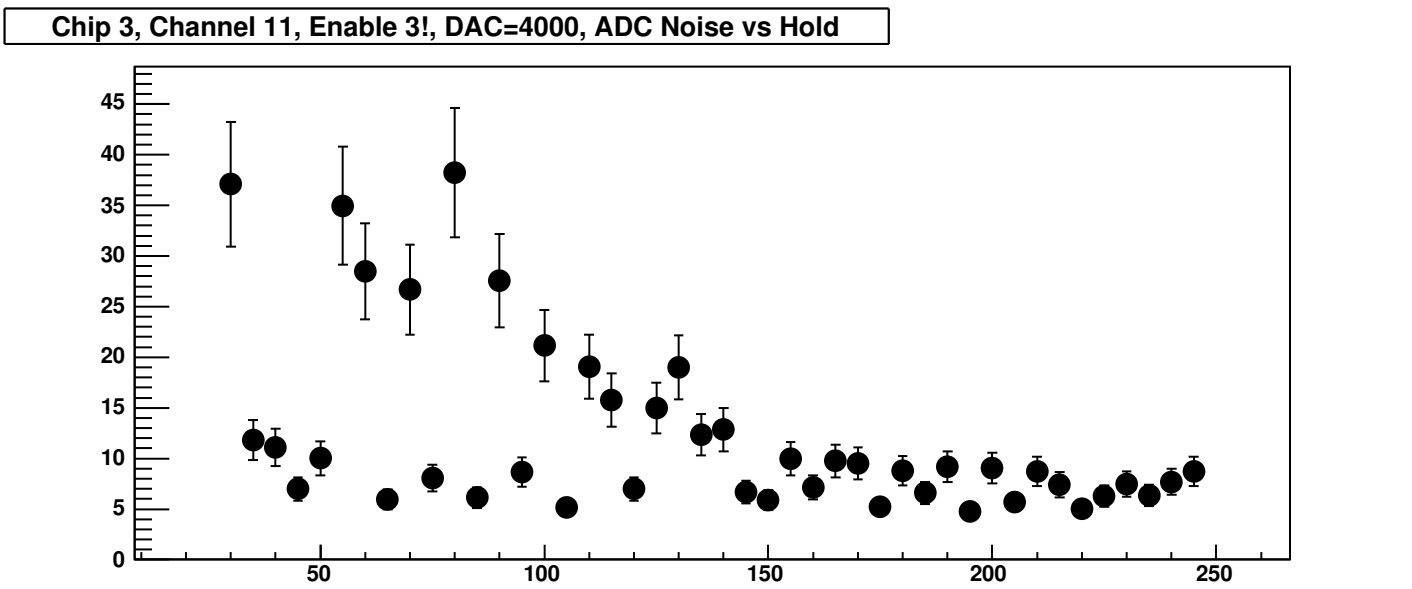

Chip 3, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
50
100
150
200
250

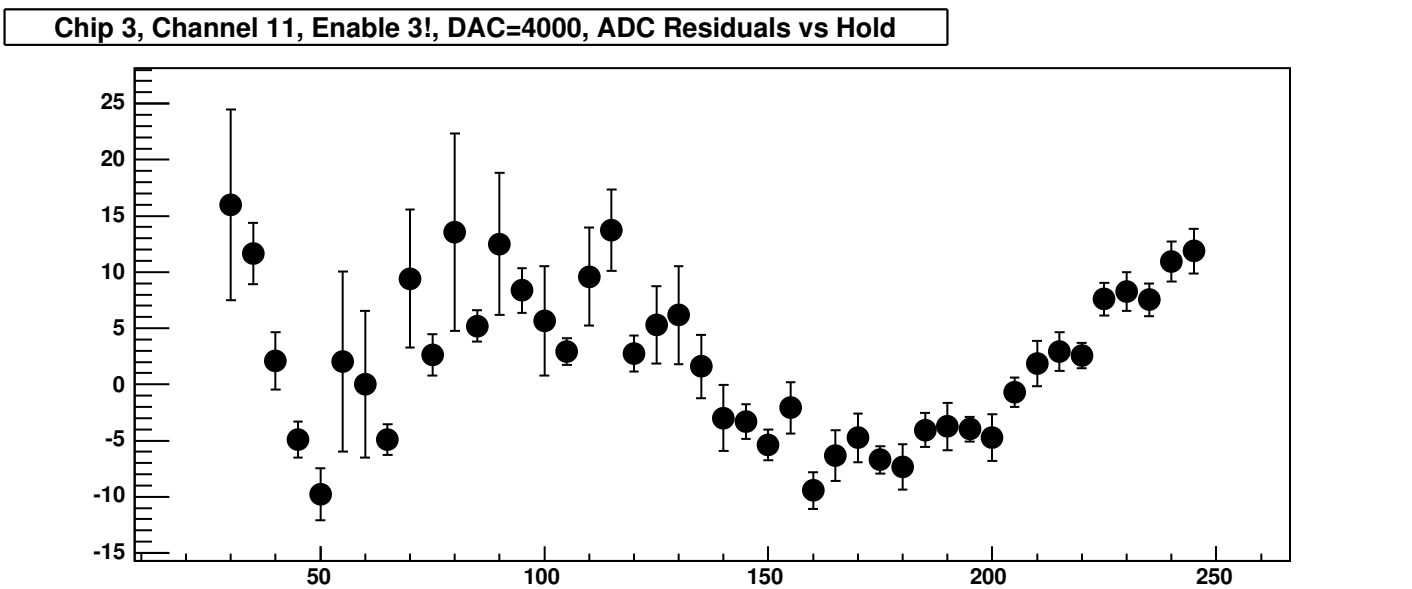

Chip 3, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 387.3 / 38
p0 159.2 ± 0.7958
p1 125.6 ± 0.6935
p2 -2.49e+08 ± 3.052e+06
p3 1.84e+08 ± 3.612e+05
p4 1.589 ± 0.01266
200
180
160
140
120
100
80
60
40
20
0
50
100
150
200
250

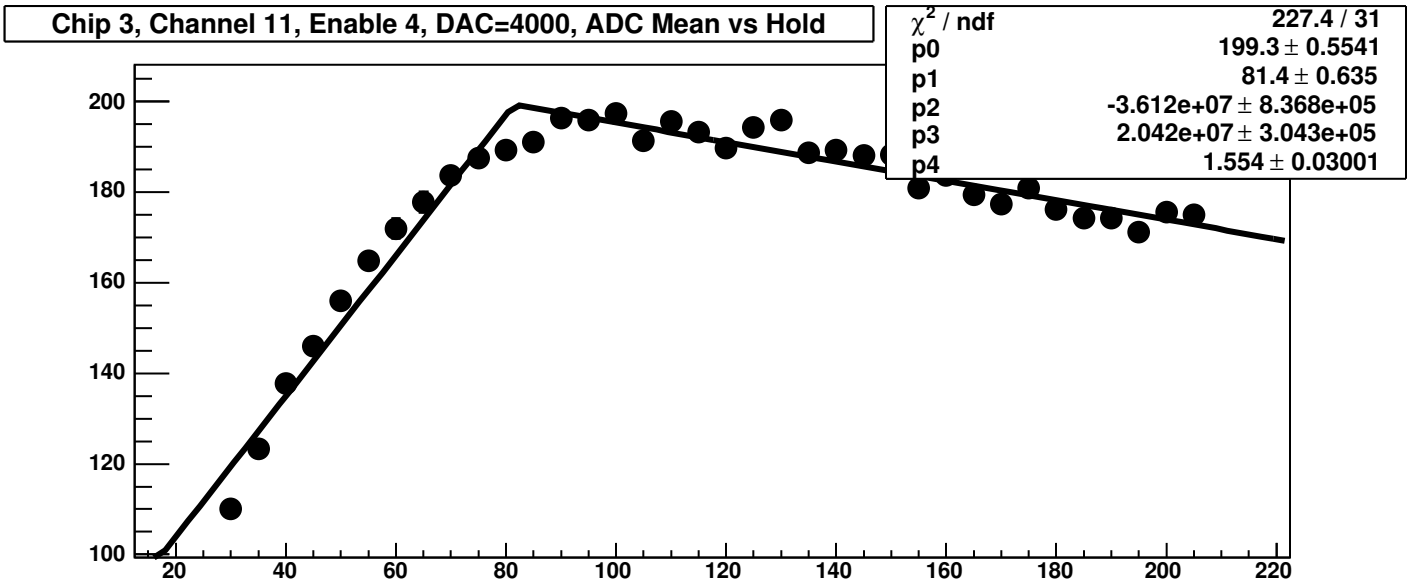

Chip 3, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250

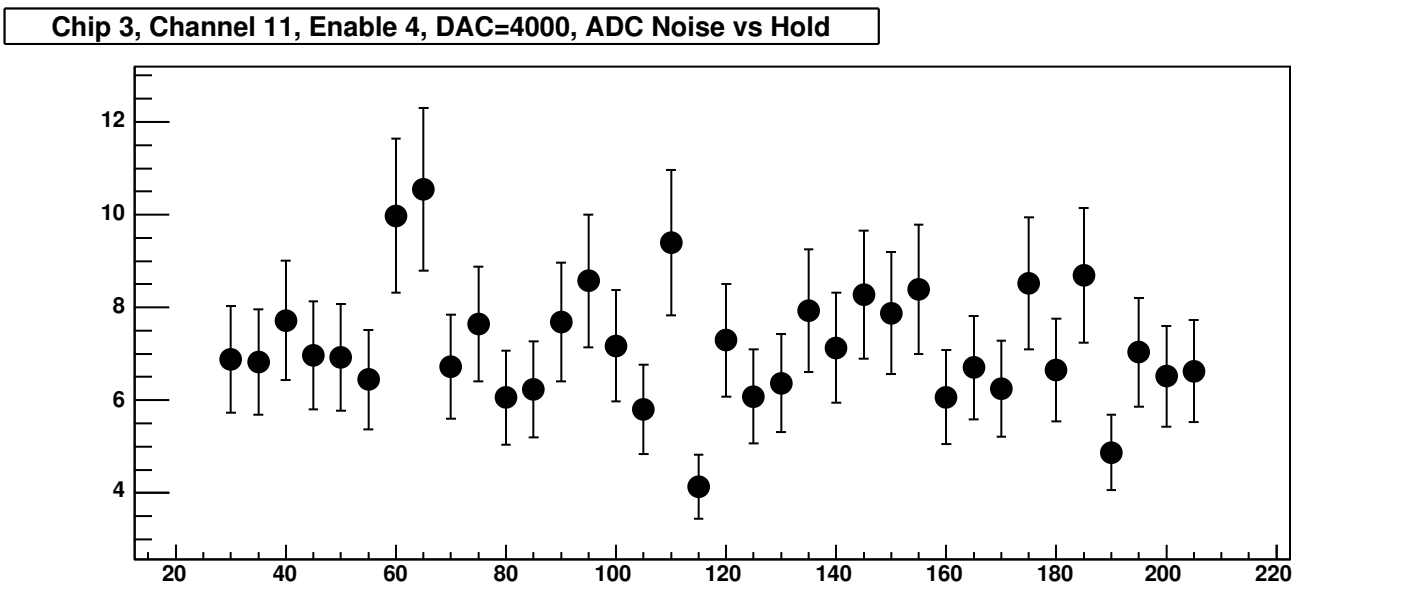

Chip 3, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
50
100
150
200
250

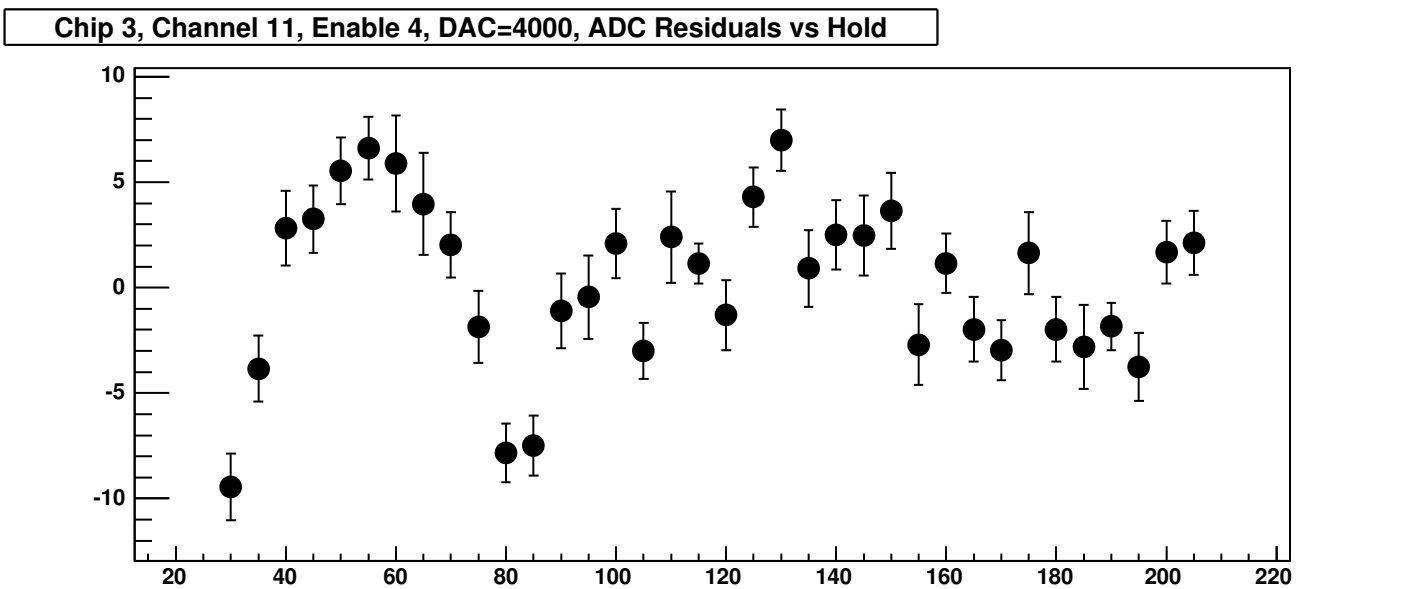

Chip 3, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 406.3 / 39
p0 -711.7 ± 0.6353
p1 5.145 ± 0.02636
p2 1.078e+04 ± 1.743
p3 32.02 ± 0.009416
10000
8000
6000
4000
2000
0
50
100
150
200
250


Chip 3, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold
45
40
35
30
25
20
15
10
5
0
50
100
150
200
250

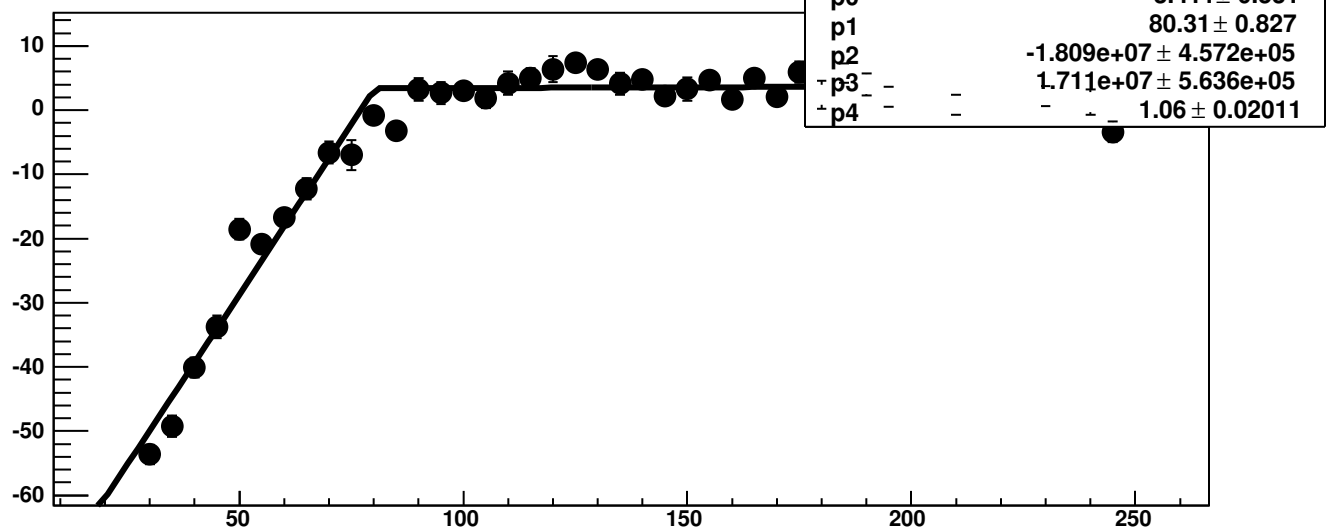

Chip 3, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
-15
50
100
150
200
250


Chip 3, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 227.4 / 31
p0 199.3 ± 0.5541
p1 81.4 ± 0.635
p2 -3.612e+07 ± 8.368e+05
p3 2.042e+07 ± 3.043e+05
p4 1.554 ± 0.03001

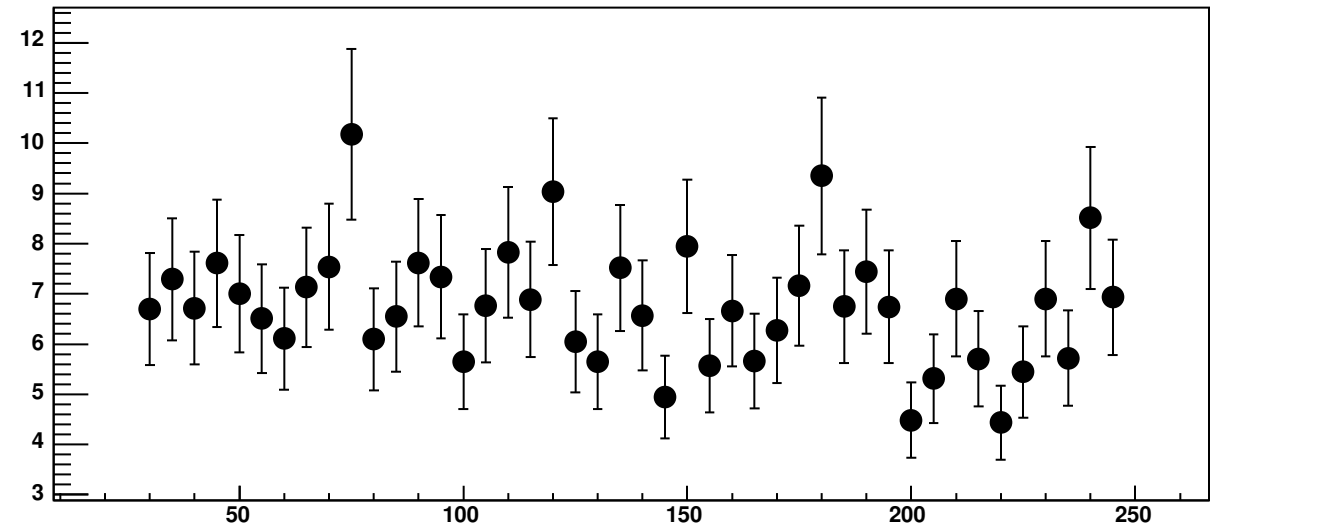

Chip 3, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold


Chip 3, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

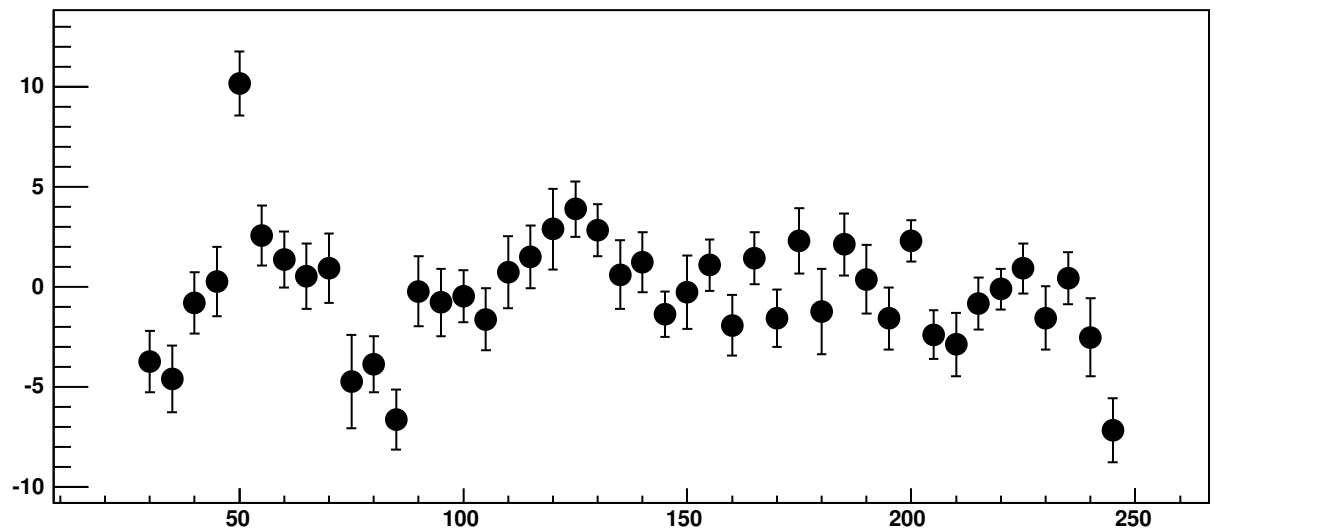
Chip 3, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

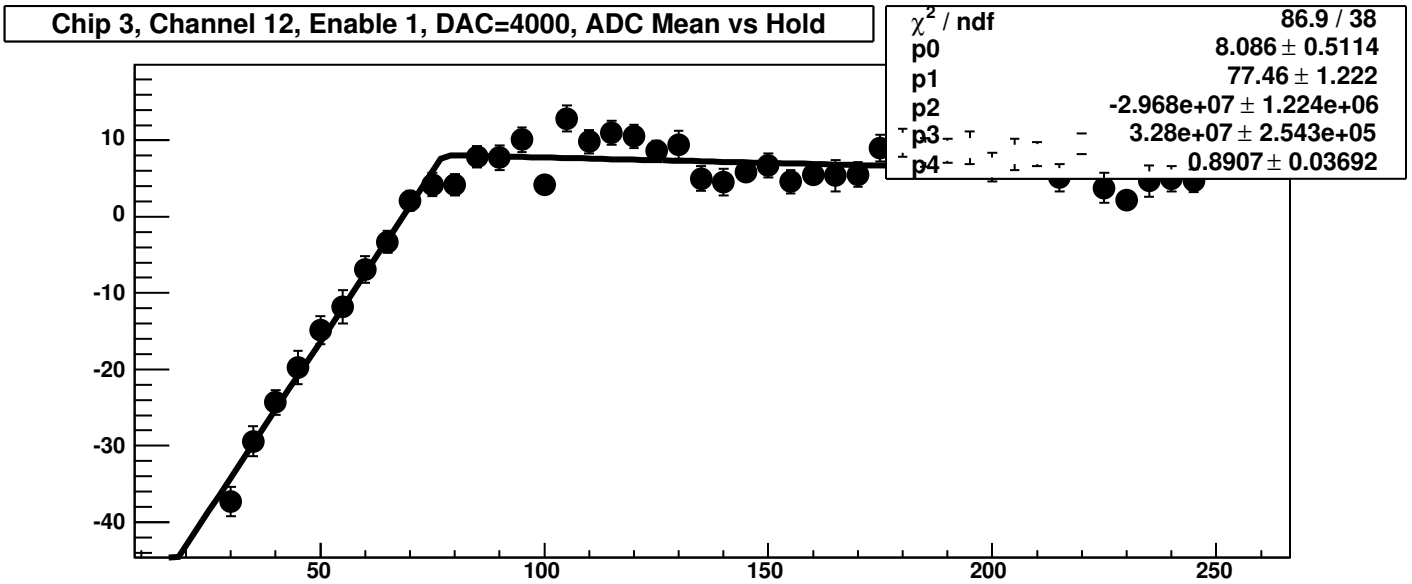


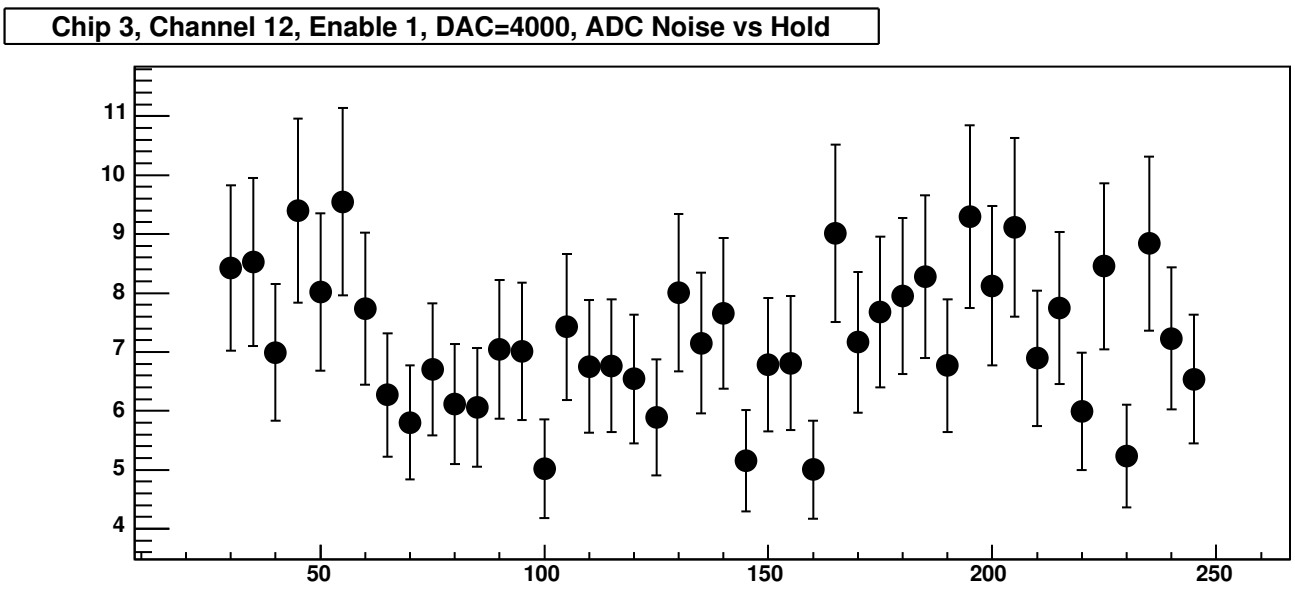
Chip 3, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold

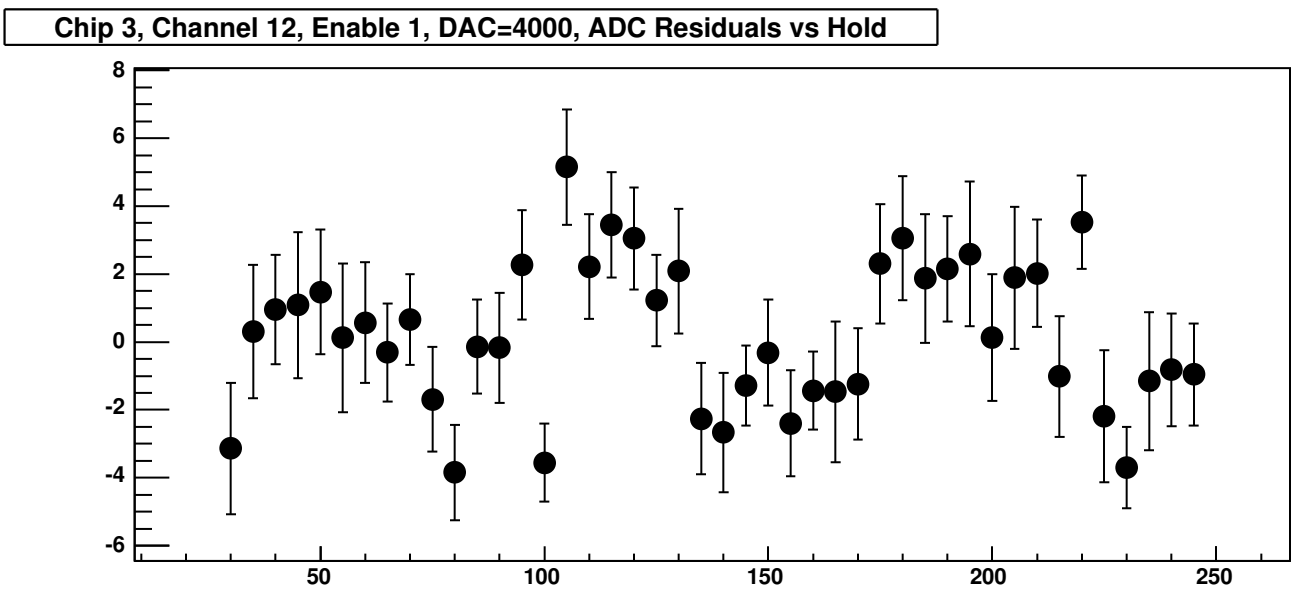


Chip 3, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

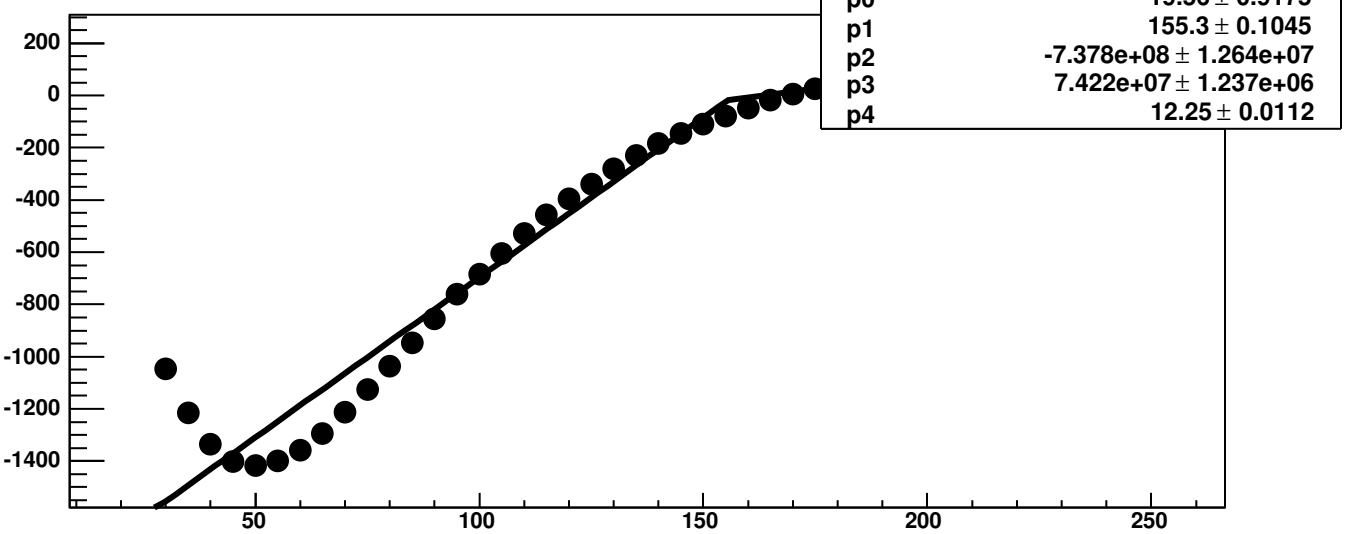



Chip 3, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 86.9 / 38
p0 8.086 ± 0.5114
p1 77.46 ± 1.222
p2 -2.968e+07 ± 1.224e+06
p3 3.28e+07 ± 2.543e+05
p4 0.8907 ± 0.03692
10
0
-10
-20
-30
-40
50
100
150
200
250

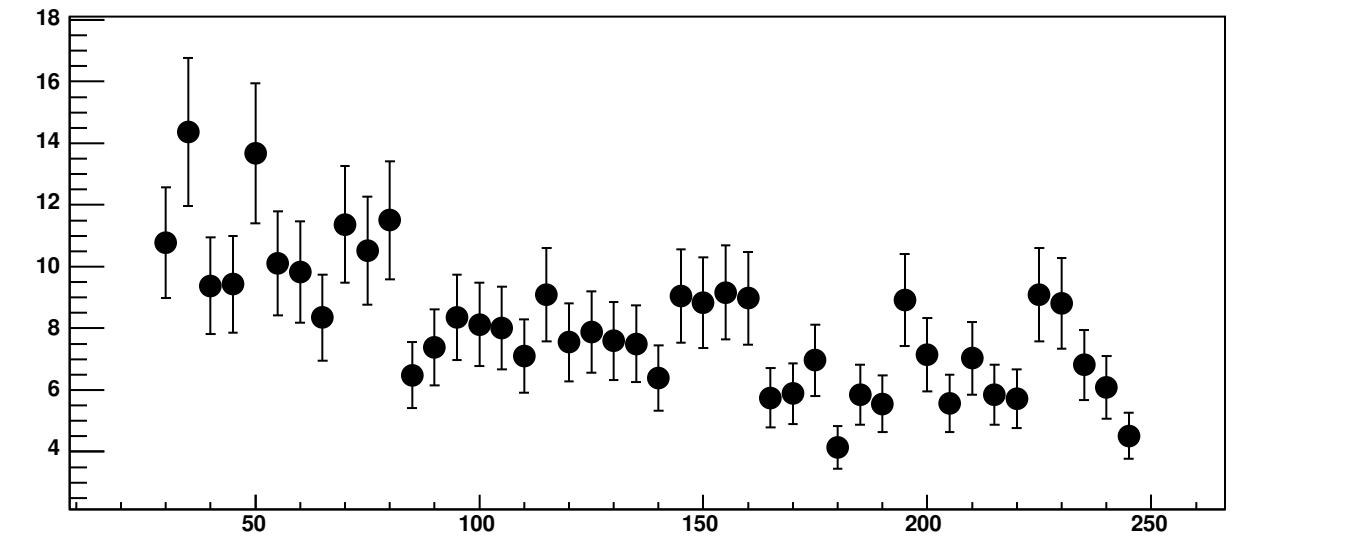

Chip 3, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 3, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
50
100
150
200
250

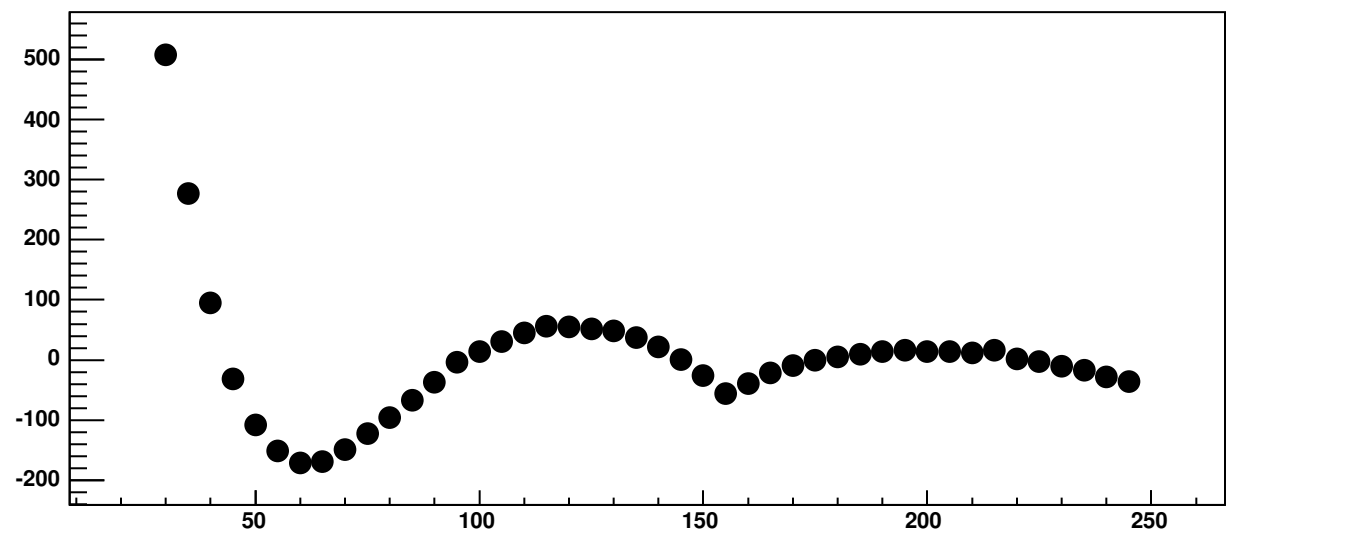
Chip 3, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold



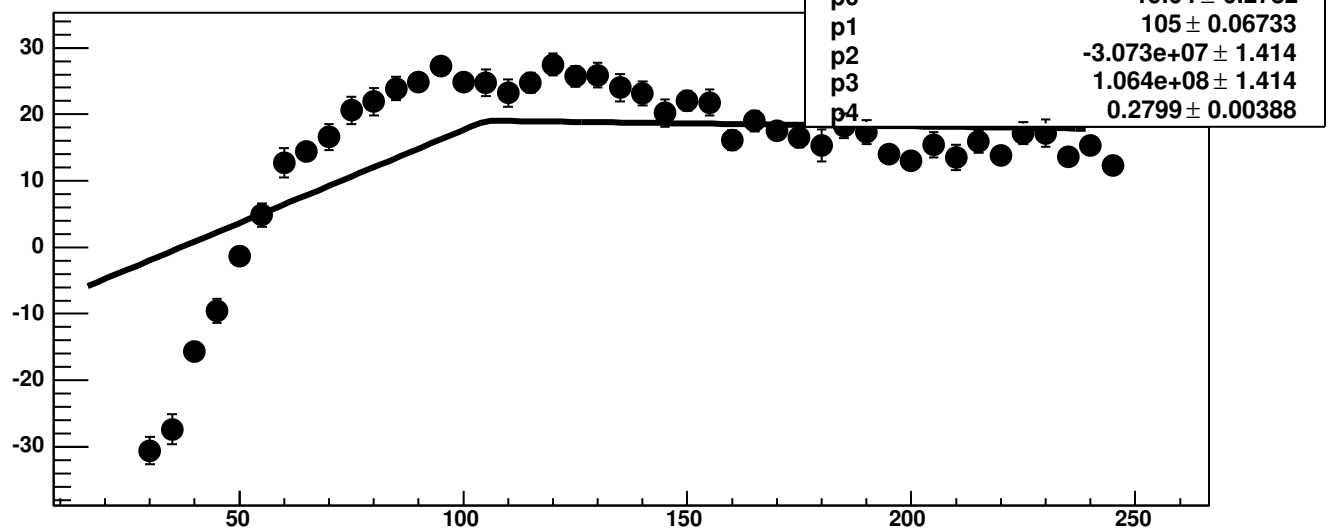
Chip 3, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



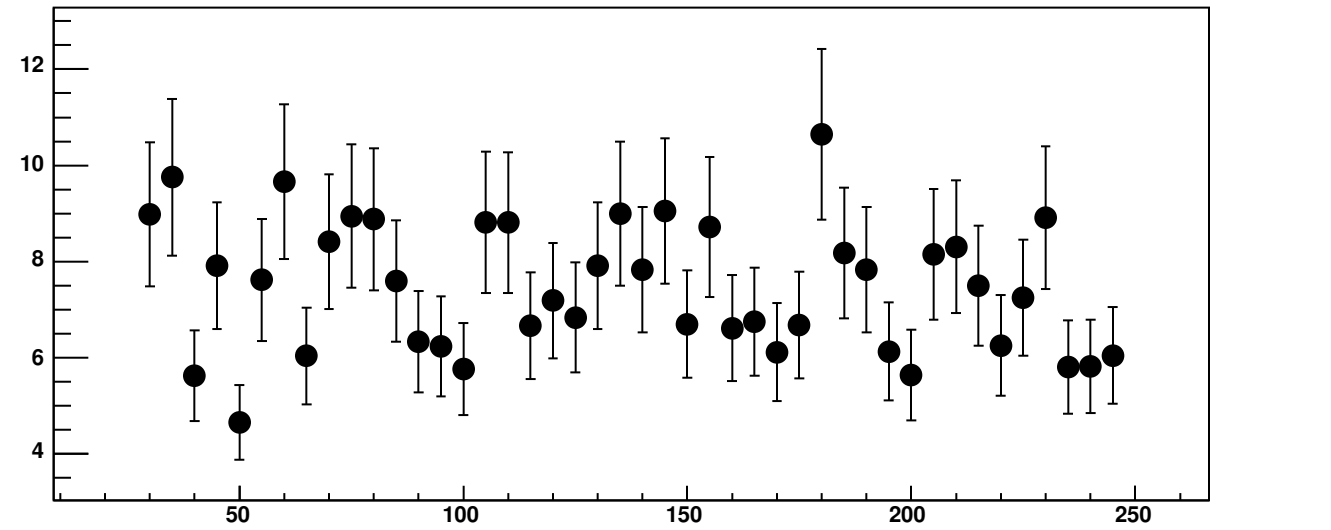
Chip 3, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold



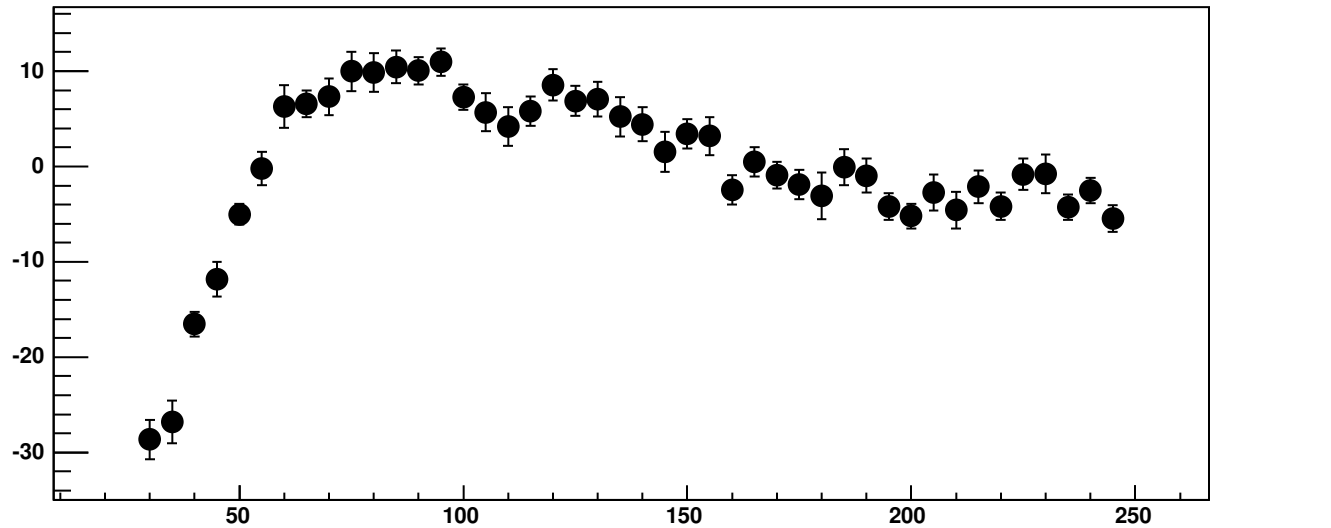
Chip 3, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

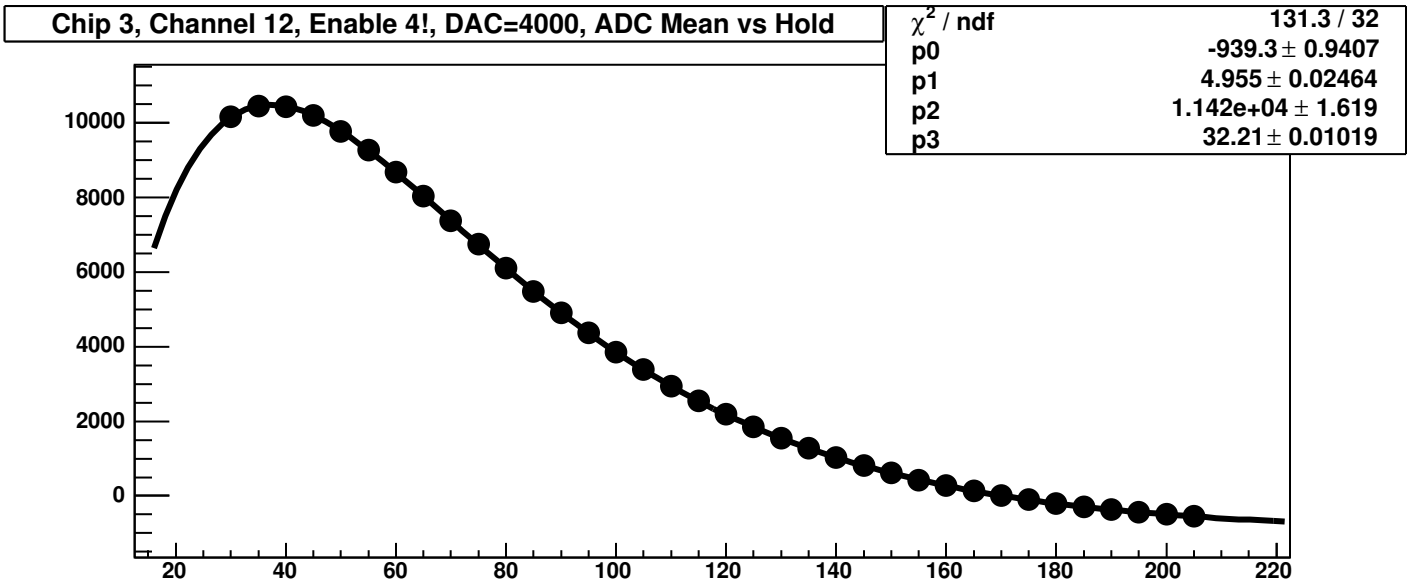


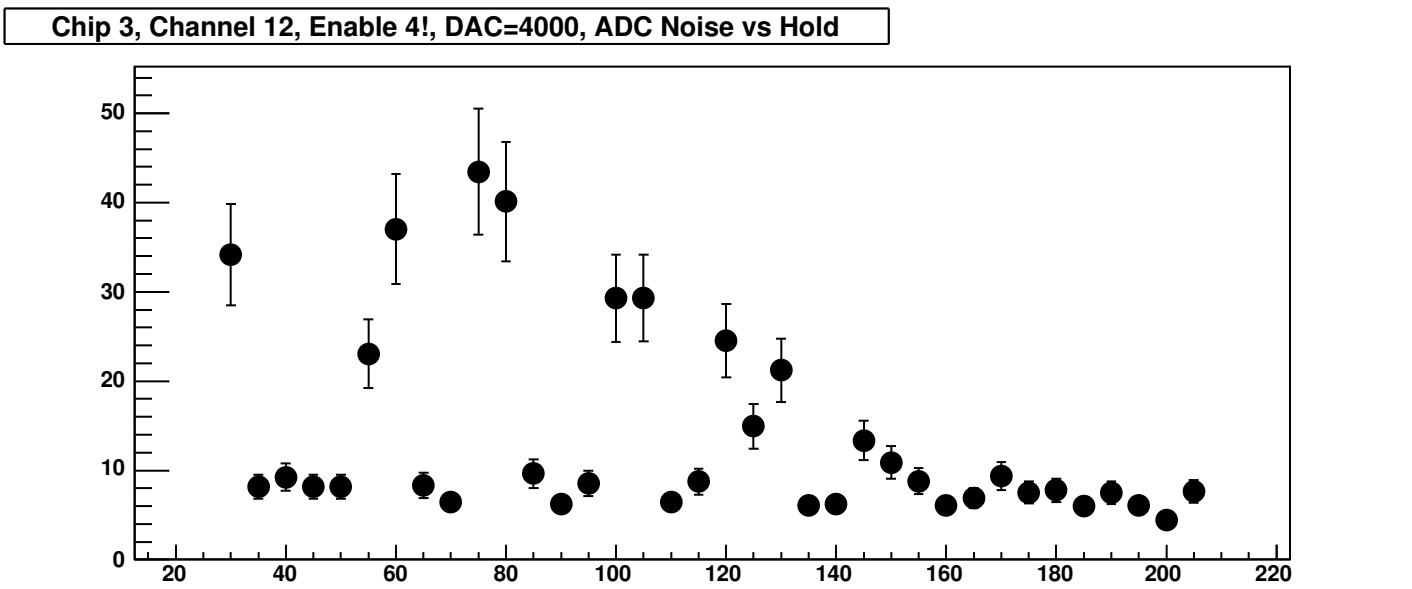
Chip 3, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold

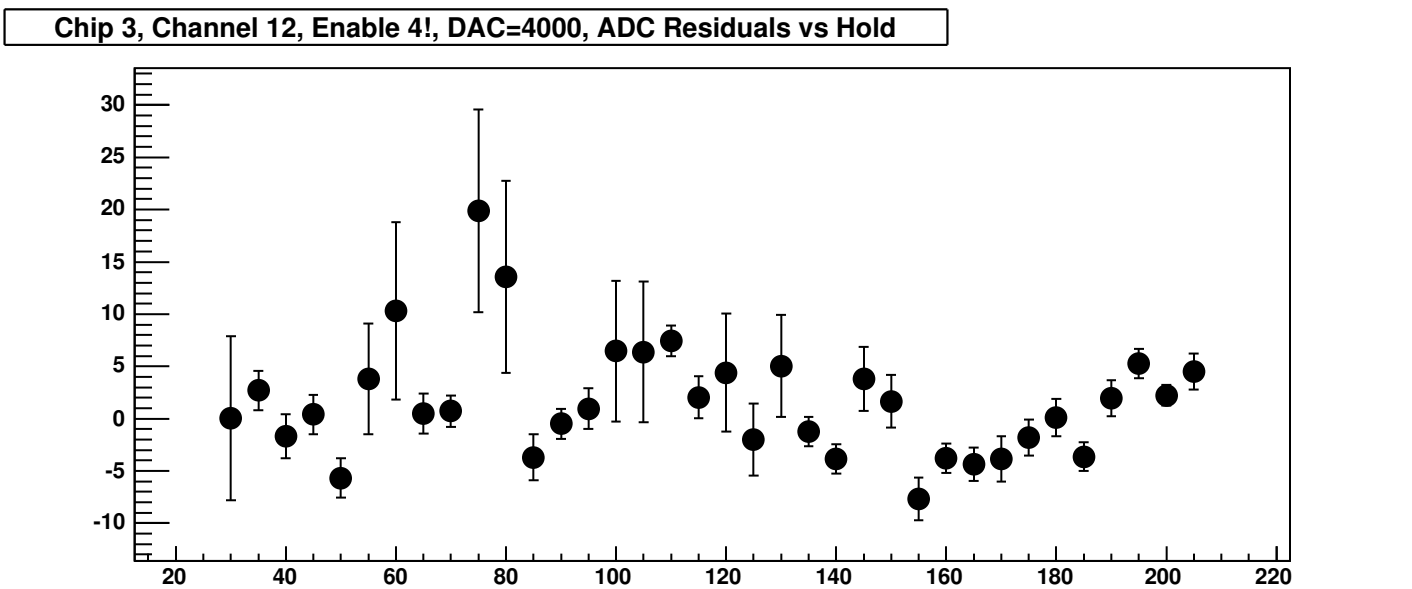


Chip 3, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

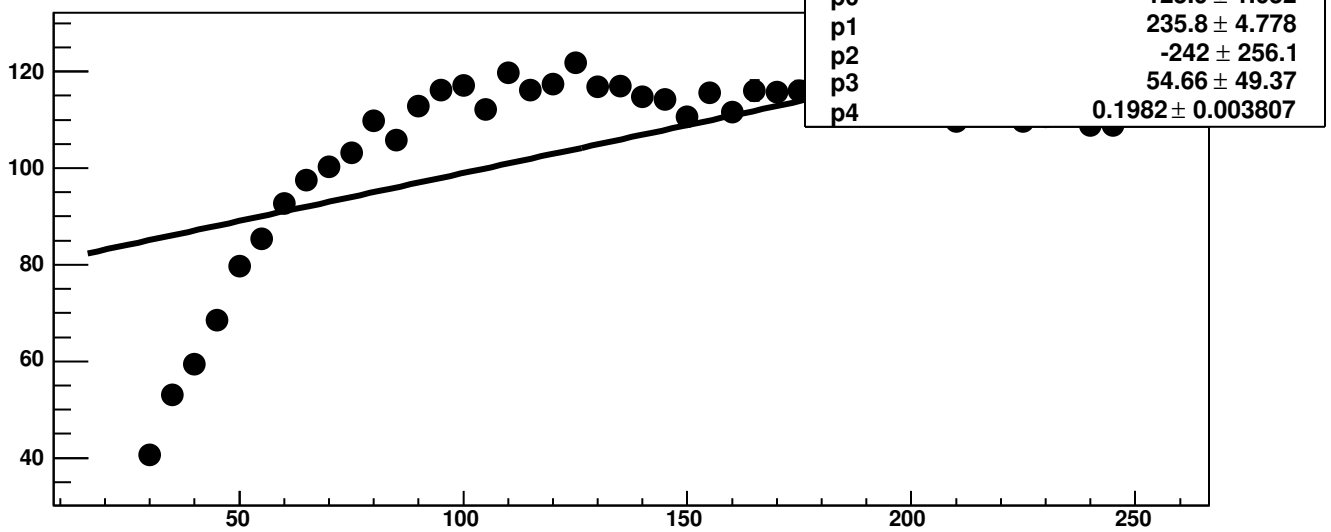



Chip 3, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 131.3 / 32
p0 -939.3 ± 0.9407
p1 4.955 ± 0.02464
p2 1.142e+04 ± 1.619
p3 32.21 ± 0.01019
10000
8000
6000
4000
2000
0
20
40
60
80
100
120
140
160
180
200
220

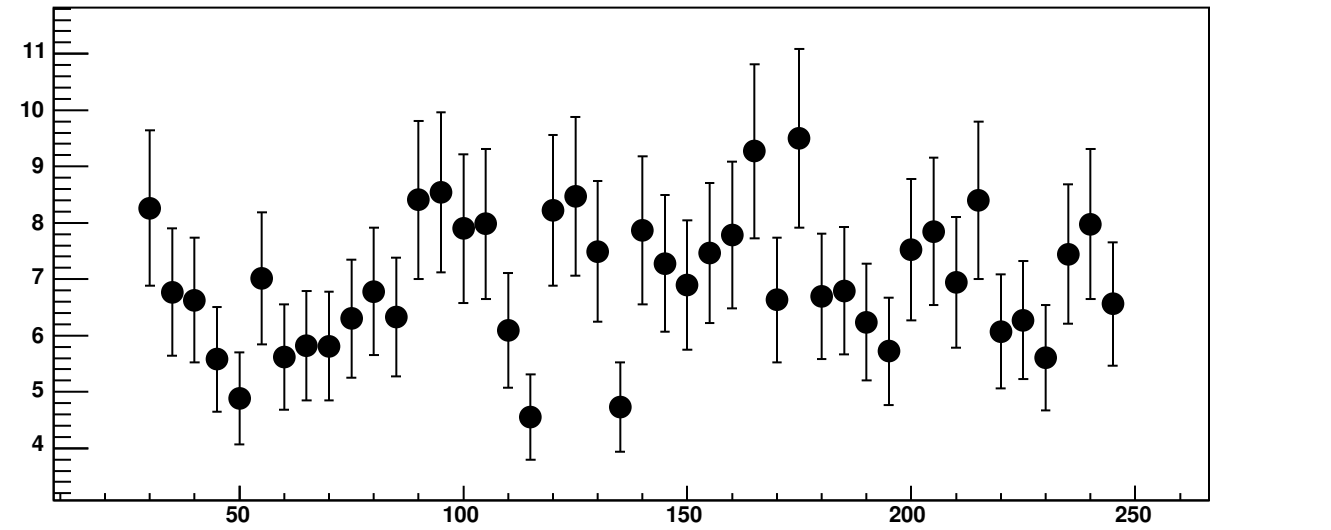

Chip 3, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
0
20
40
60
80
100
120
140
160
180
200
220


Chip 3, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
20
40
60
80
100
120
140
160
180
200
220

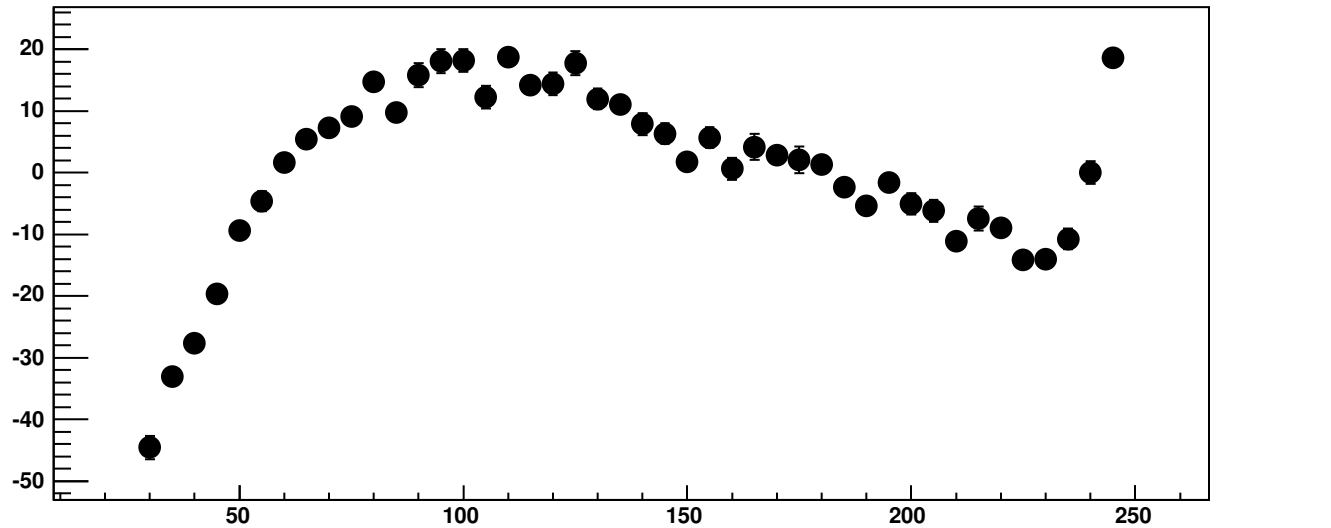
Chip 3, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold

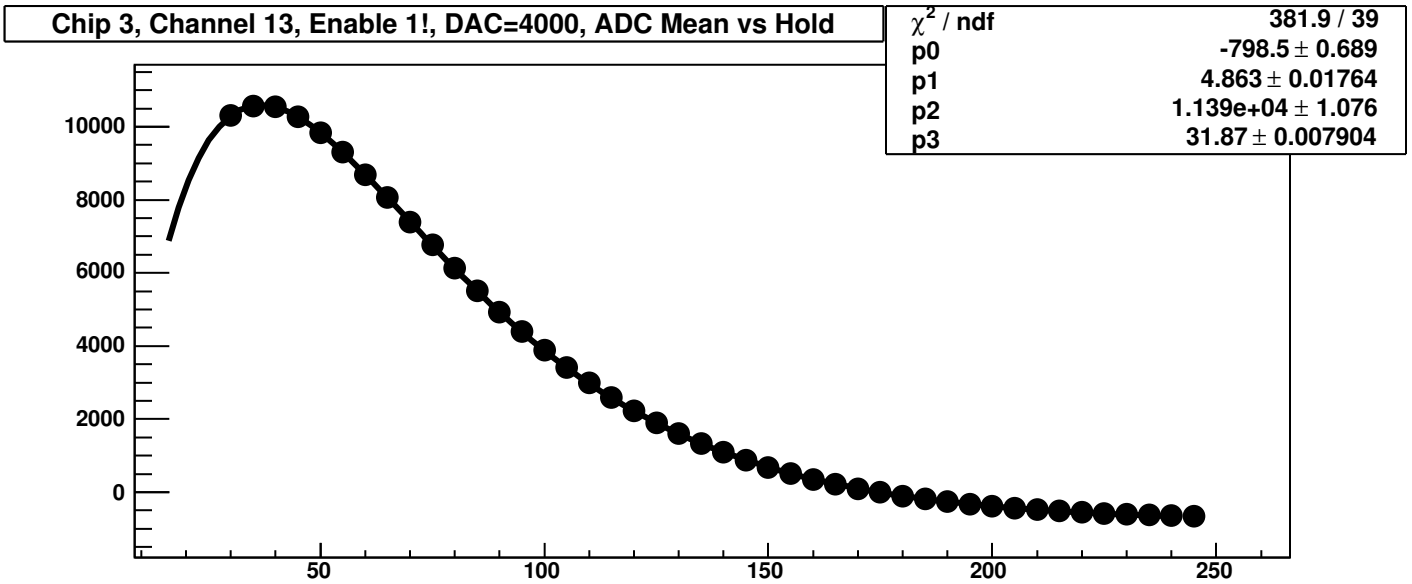


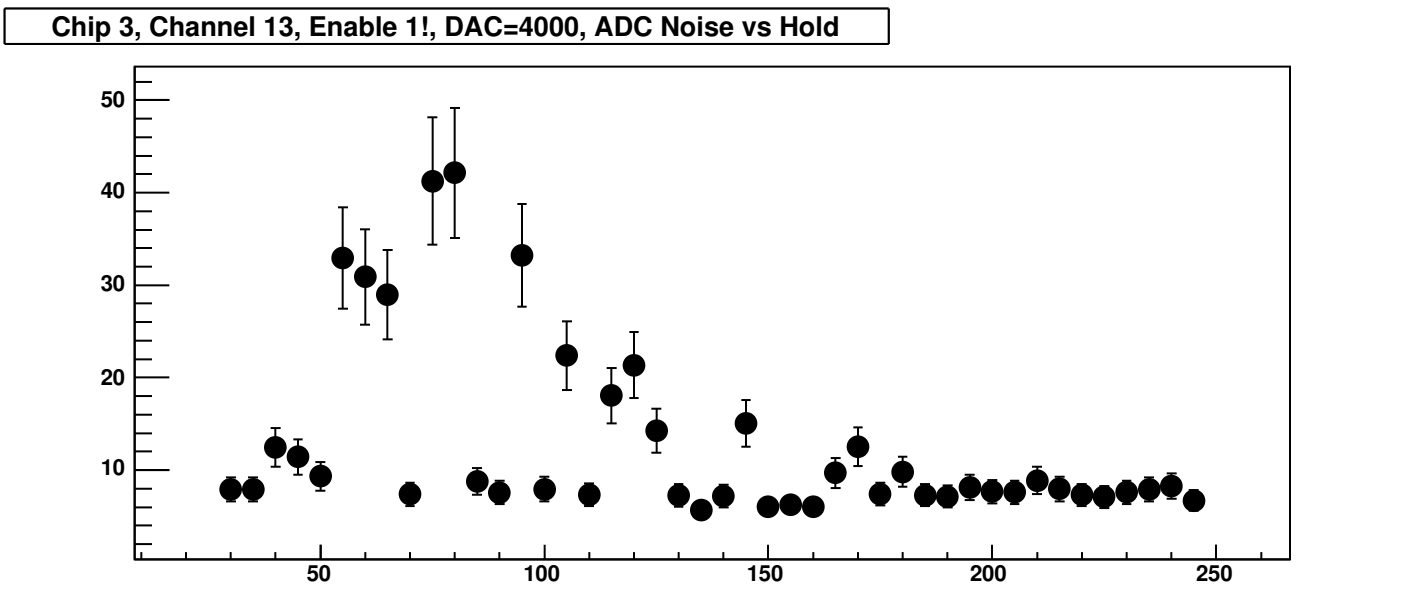
Chip 3, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold

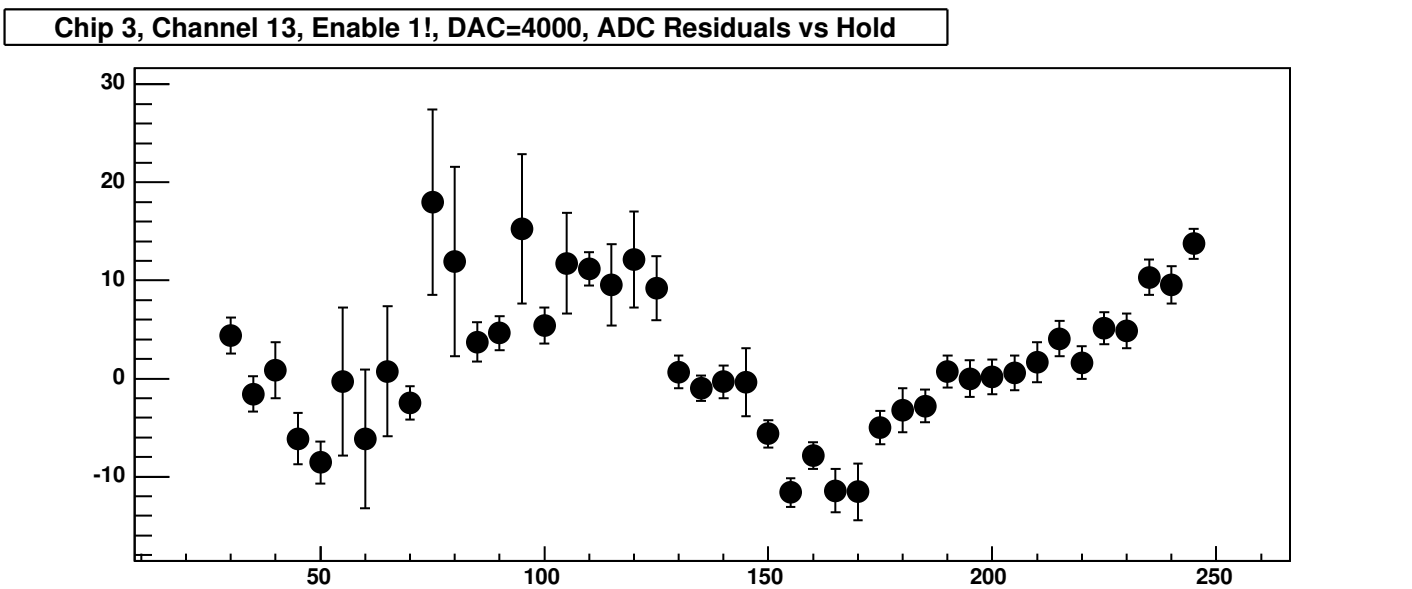


Chip 3, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold

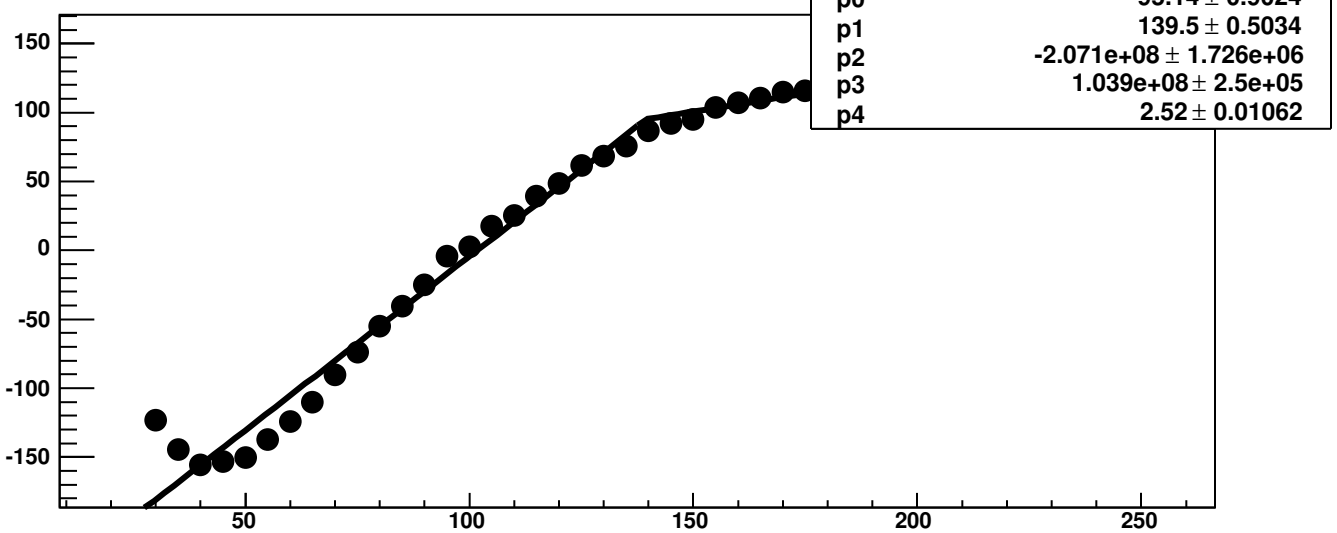



Chip 3, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 381.9 / 39
p0 -798.5 ± 0.689
p1 4.863 ± 0.01764
p2 1.139e+04 ± 1.076
p3 31.87 ± 0.007904
10000
8000
6000
4000
2000
0
50
100
150
200
250

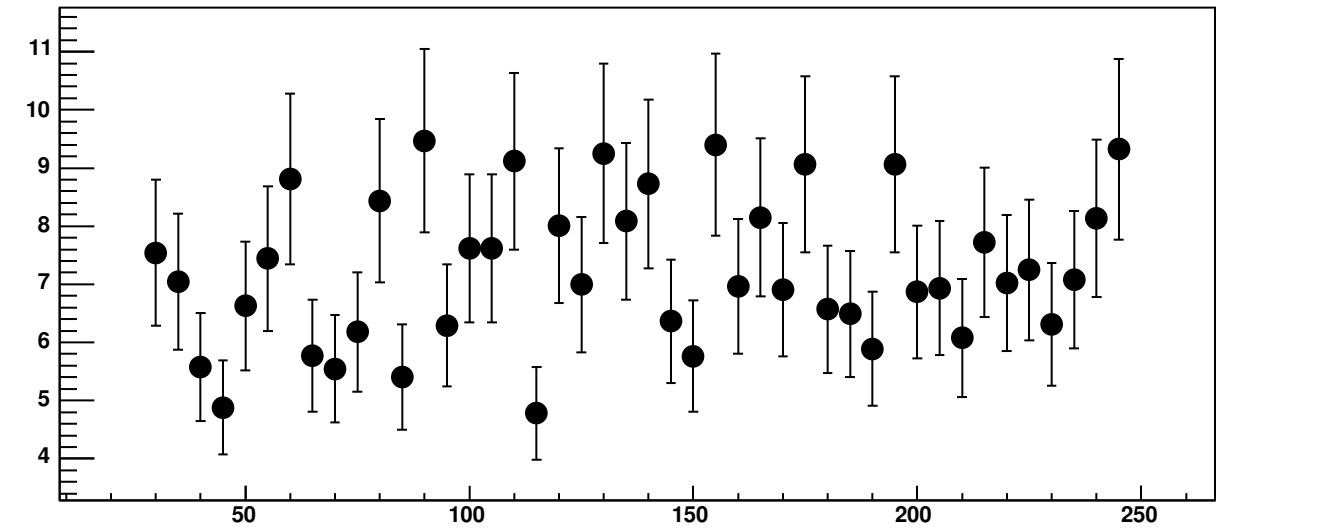

Chip 3, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
50
100
150
200
250


Chip 3, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
50
100
150
200
250

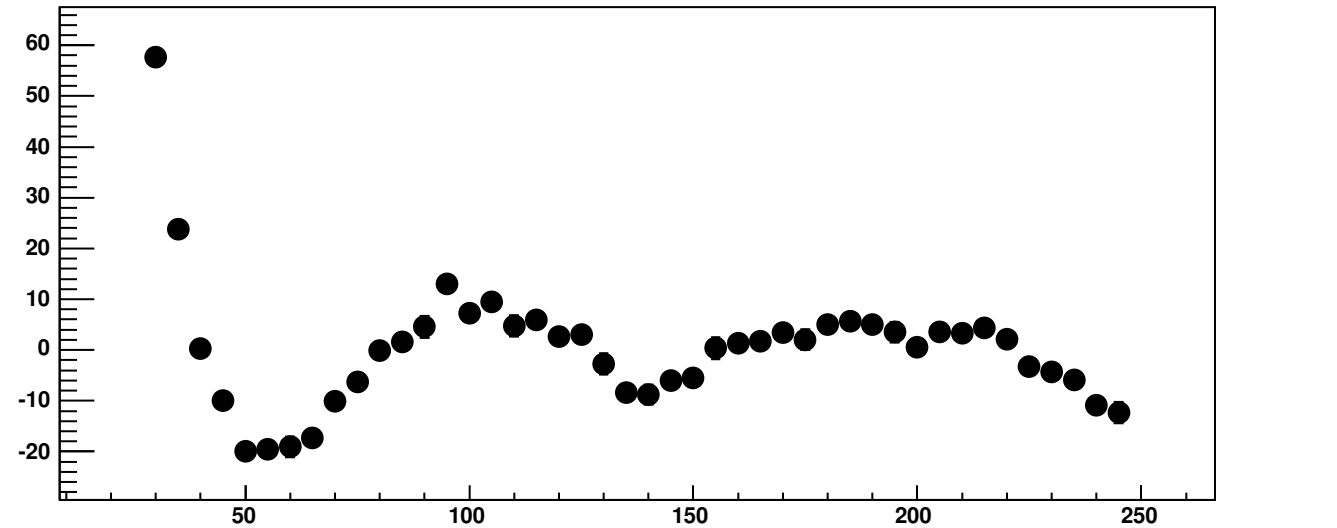
Chip 3, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold



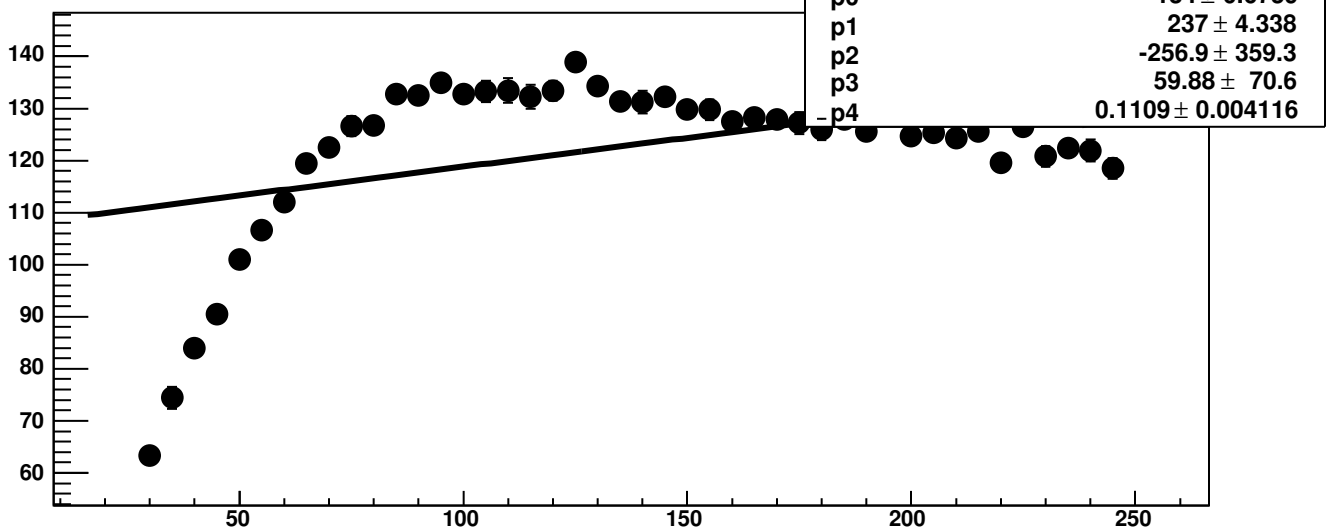
Chip 3, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



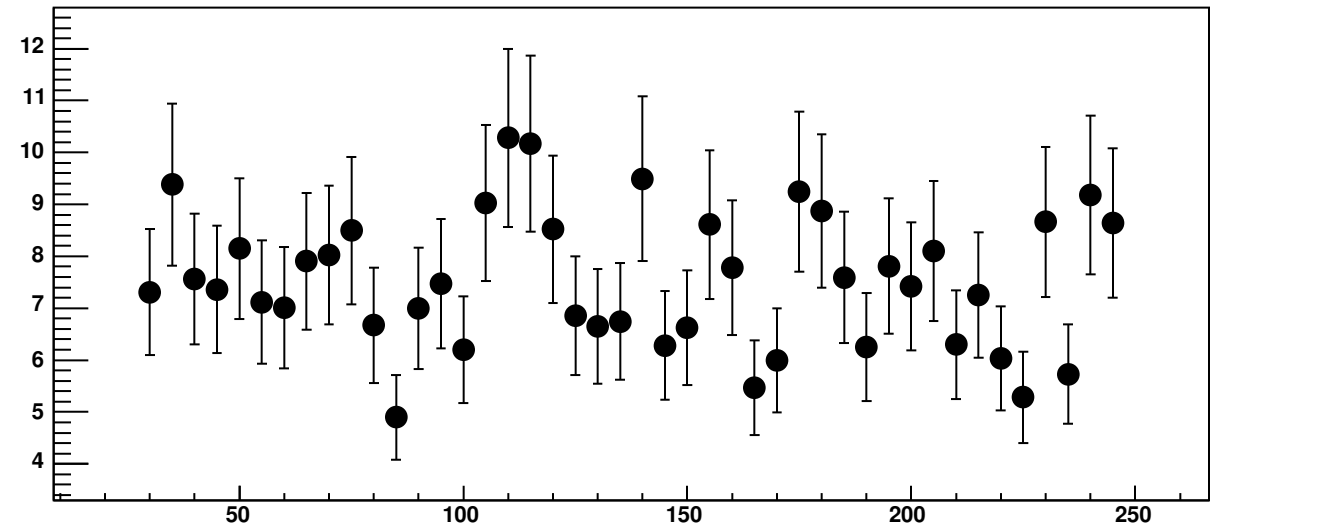
Chip 3, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold



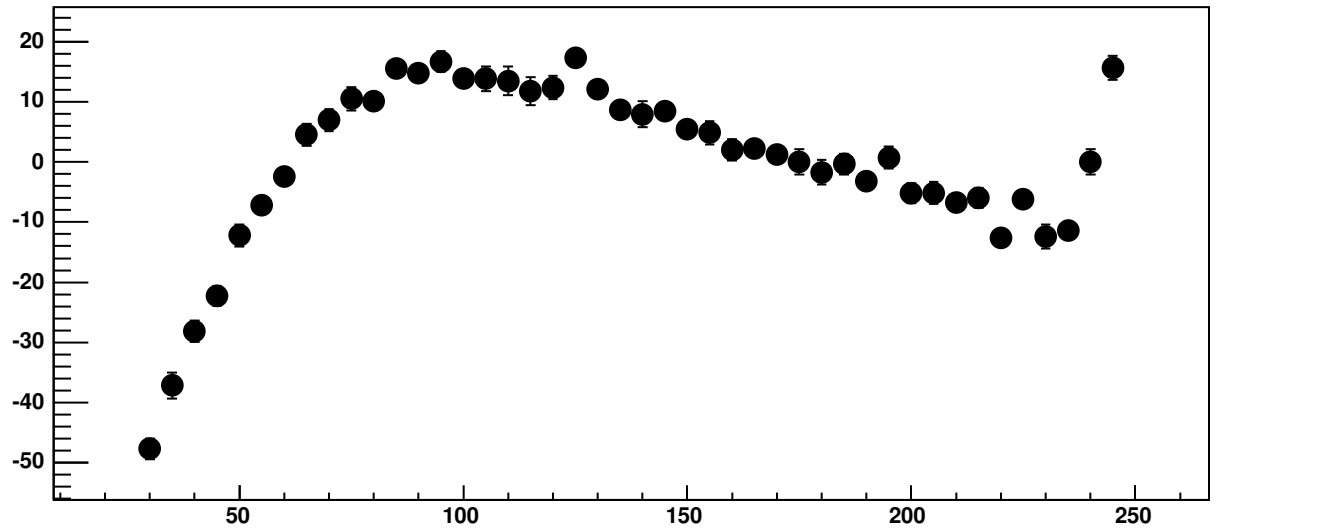
Chip 3, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

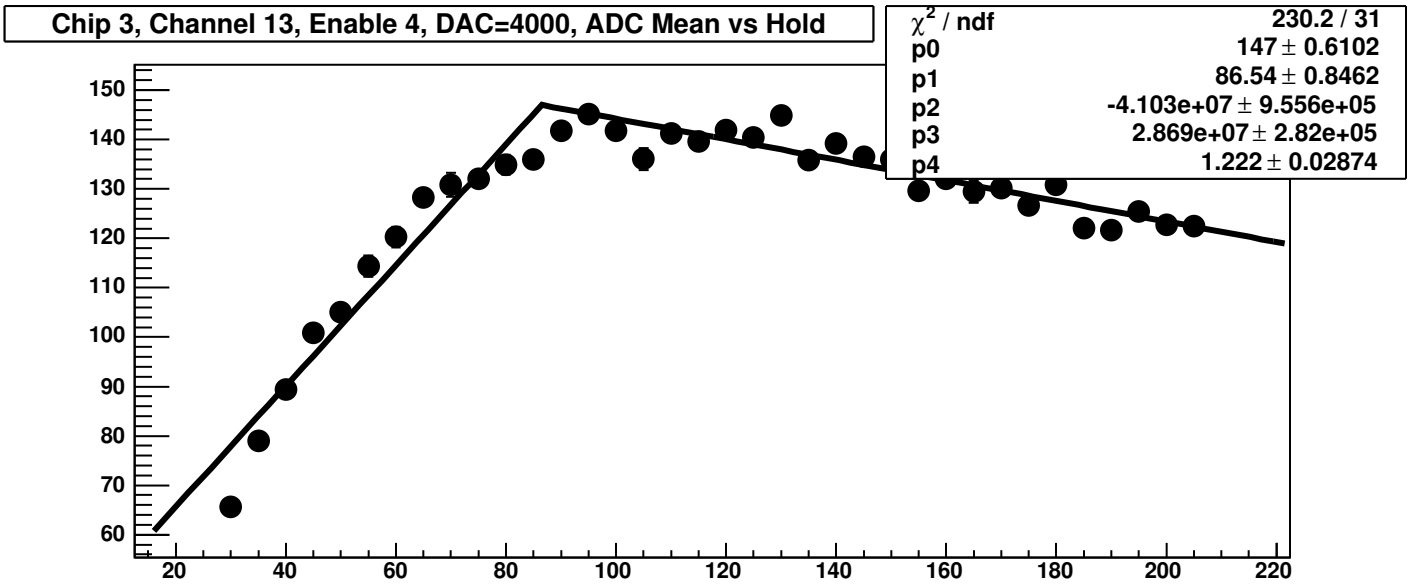


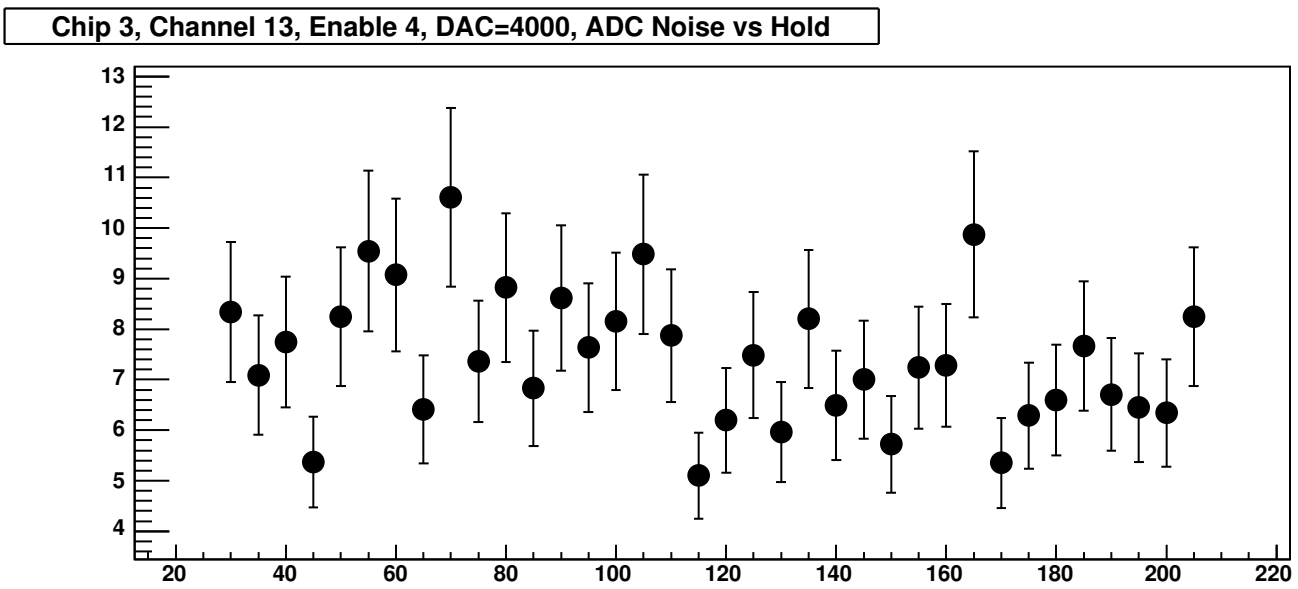
Chip 3, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold

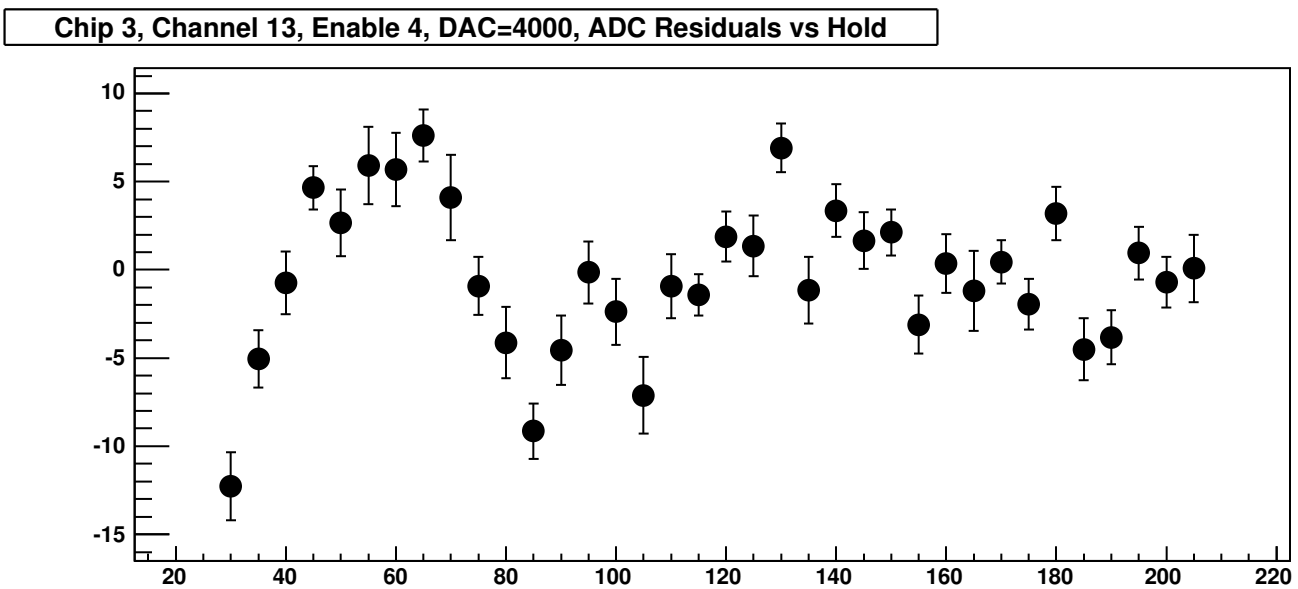


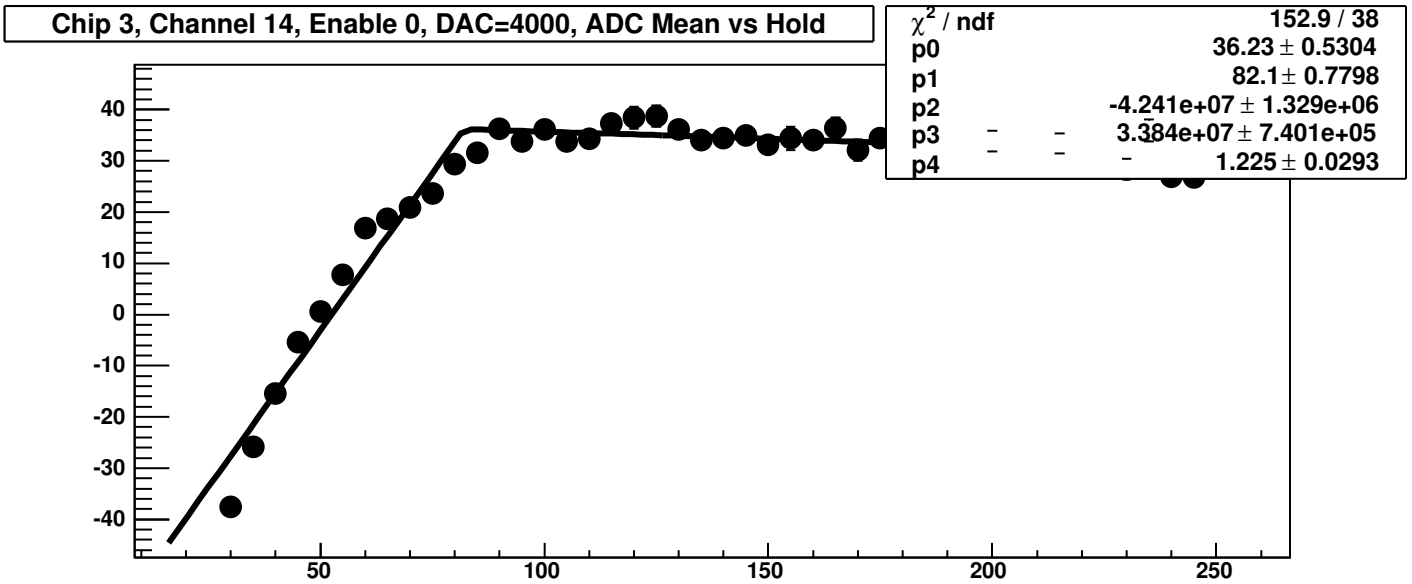
Chip 3, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

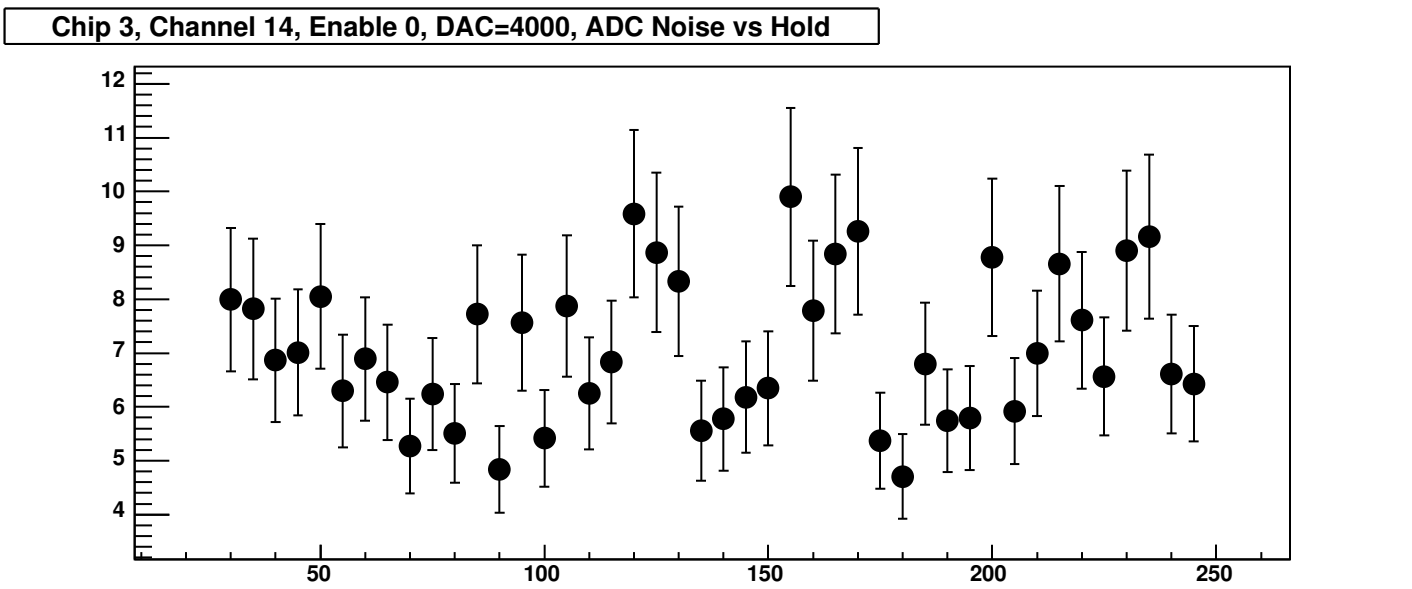


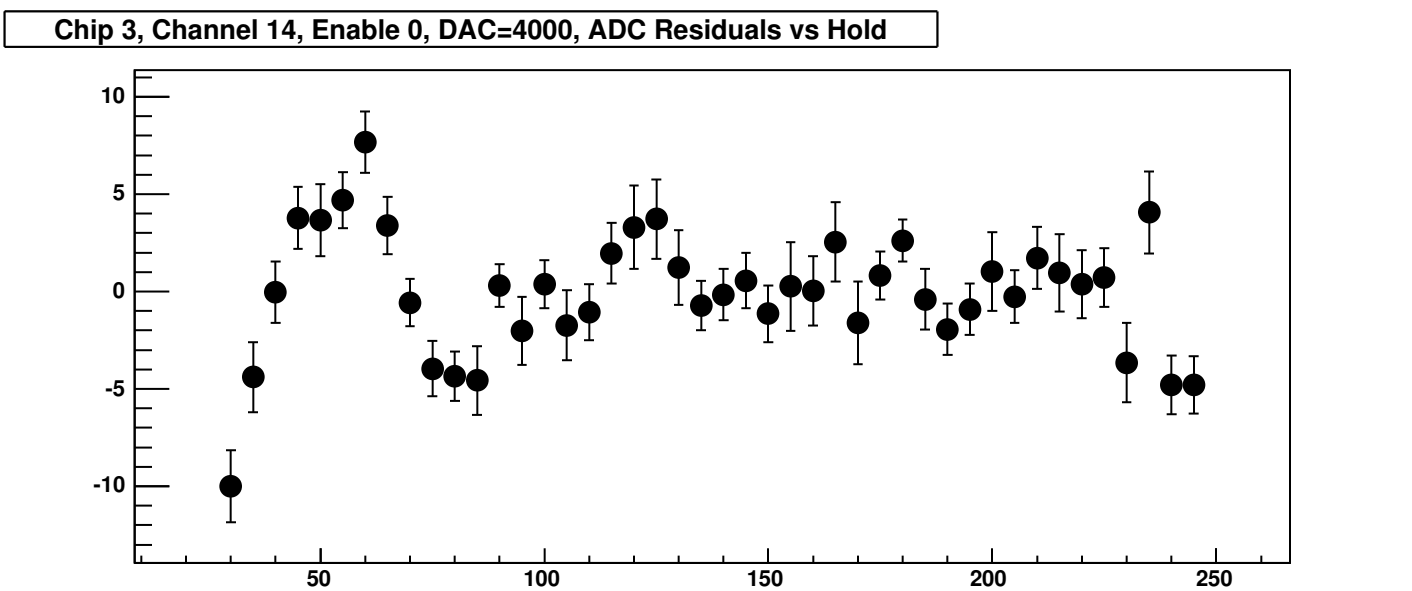

Chip 3, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 230.2 / 31
p0 147 ± 0.6102
p1 86.54 ± 0.8462
p2 -4.103e+07 ± 9.556e+05
p3 2.869e+07 ± 2.82e+05
p4 1.222 ± 0.02874


Chip 3, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold

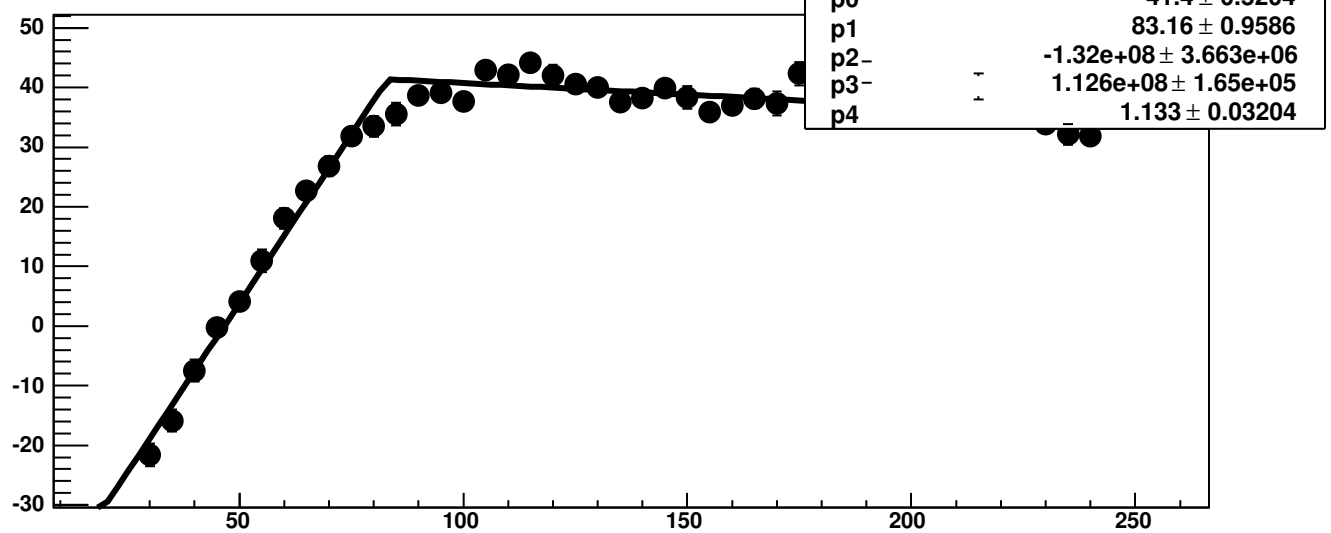

Chip 3, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold


Chip 3, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 152.9 / 38
p0 36.23 ± 0.5304
p1 82.1 ± 0.7798
p2 -4.241e+07 ± 1.329e+06
p3 3.384e+07 ± 7.401e+05
p4 1.225 ± 0.0293
40
30
20
10
0
-10
-20
-30
-40
50
100
150
200
250

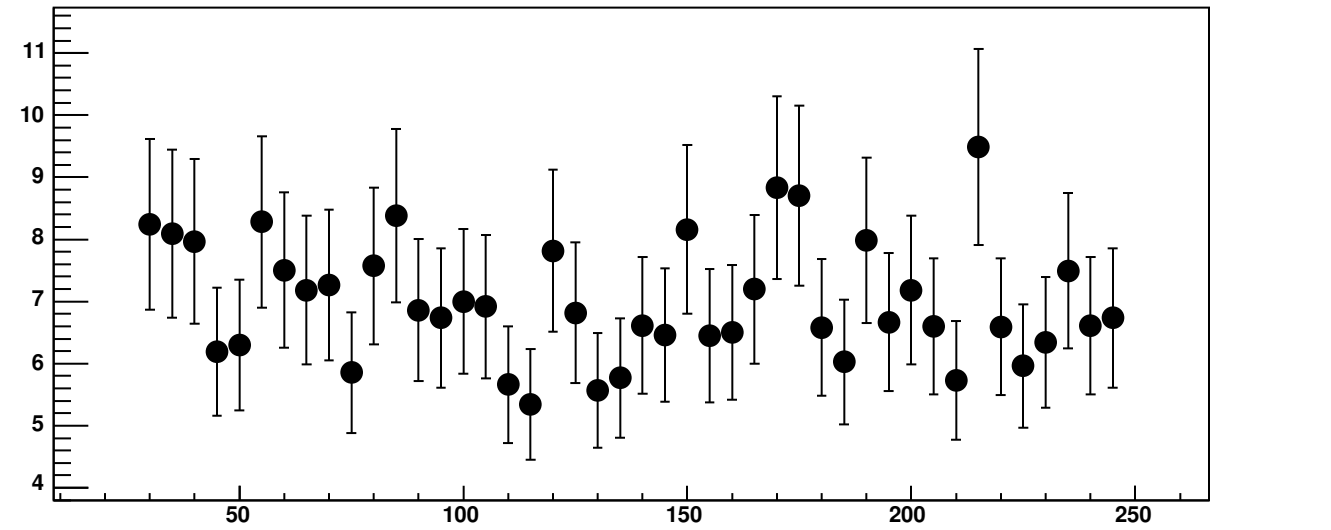

Chip 3, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 3, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

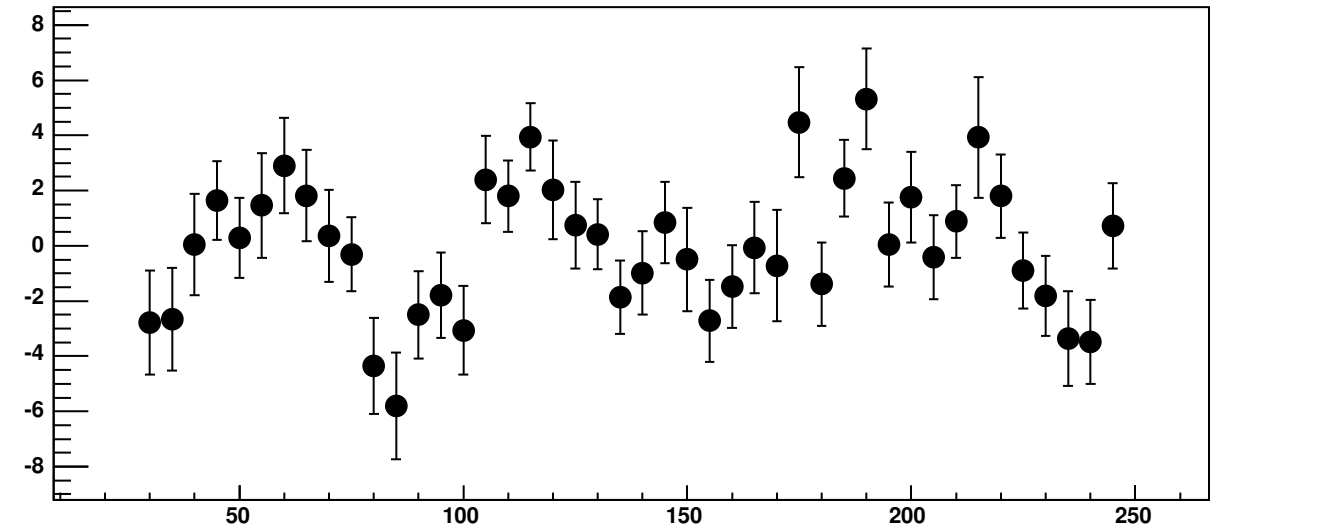
Chip 3, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold



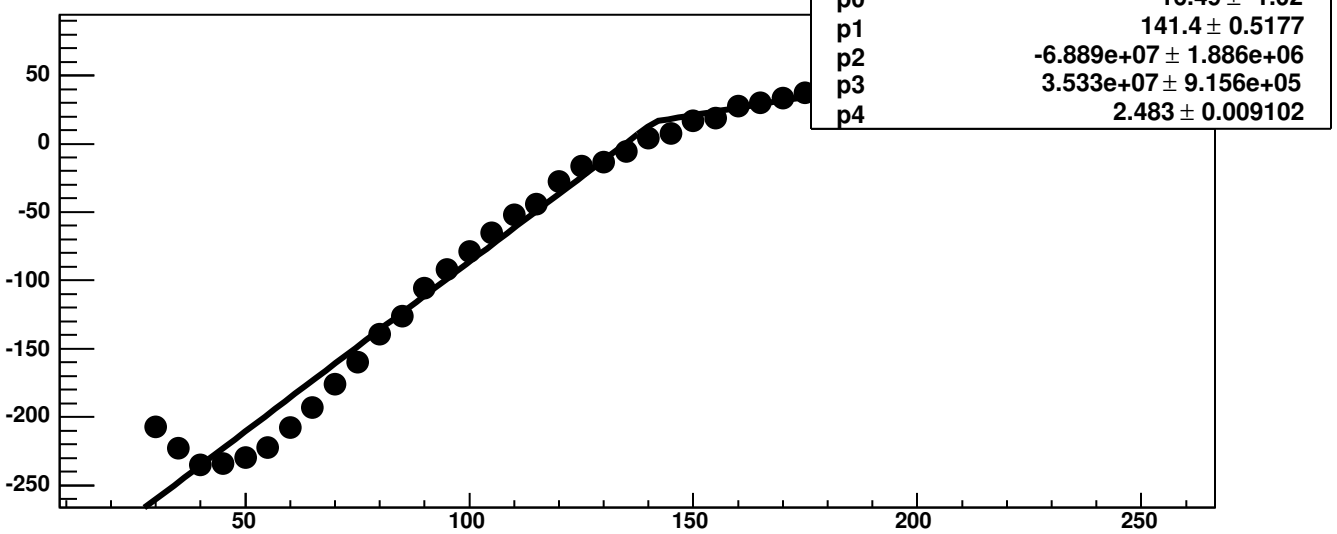
Chip 3, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold



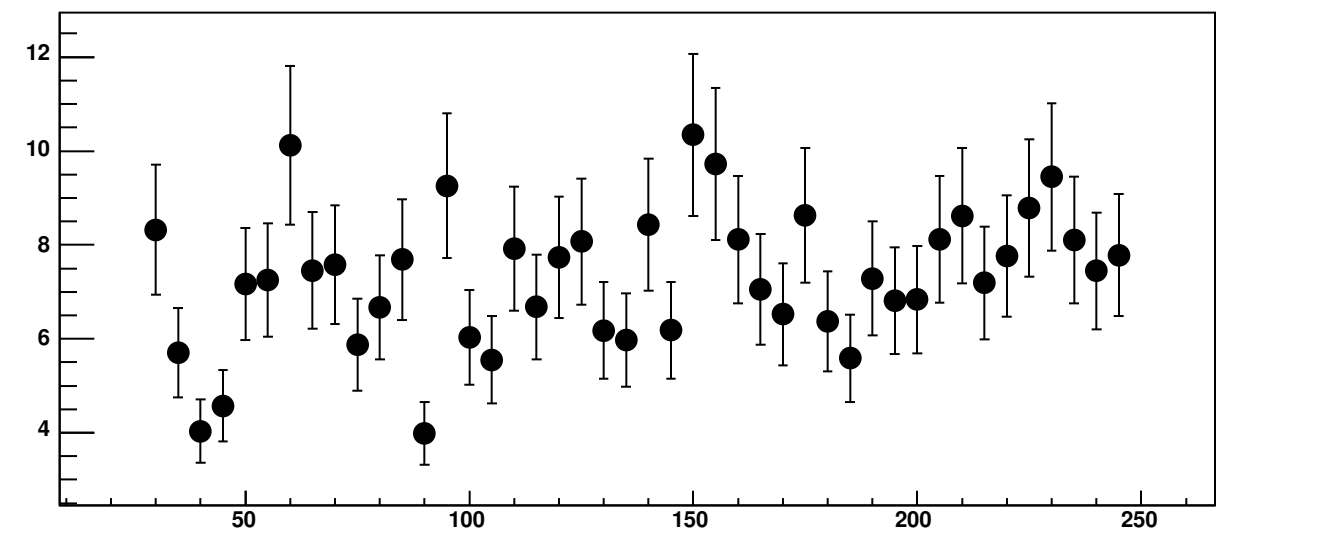
Chip 3, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold



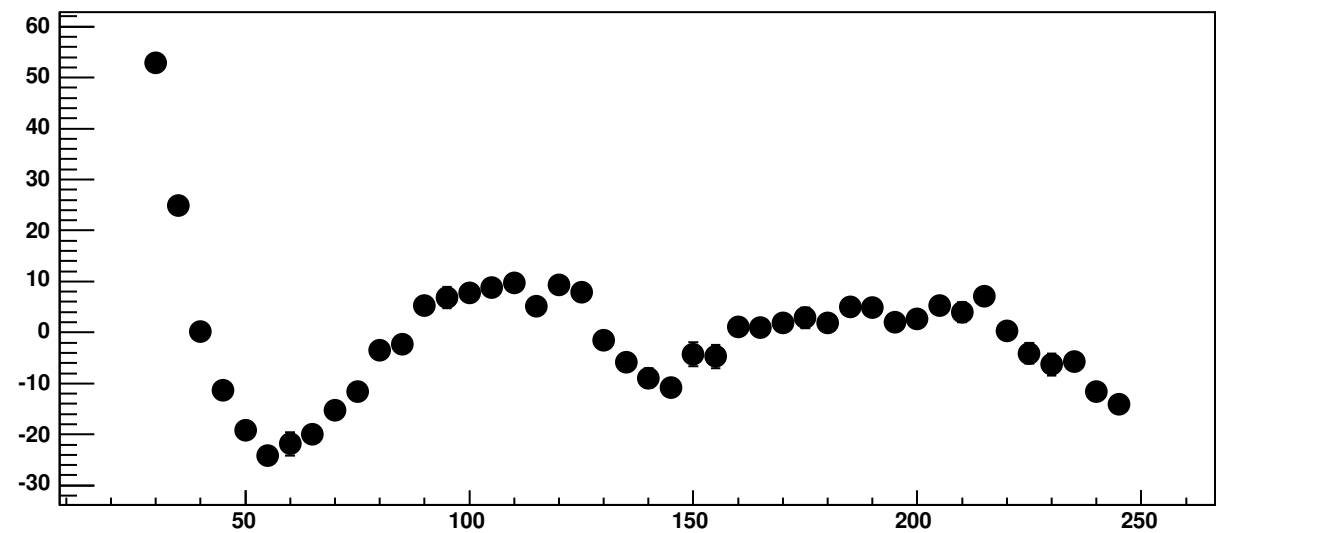
Chip 3, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold



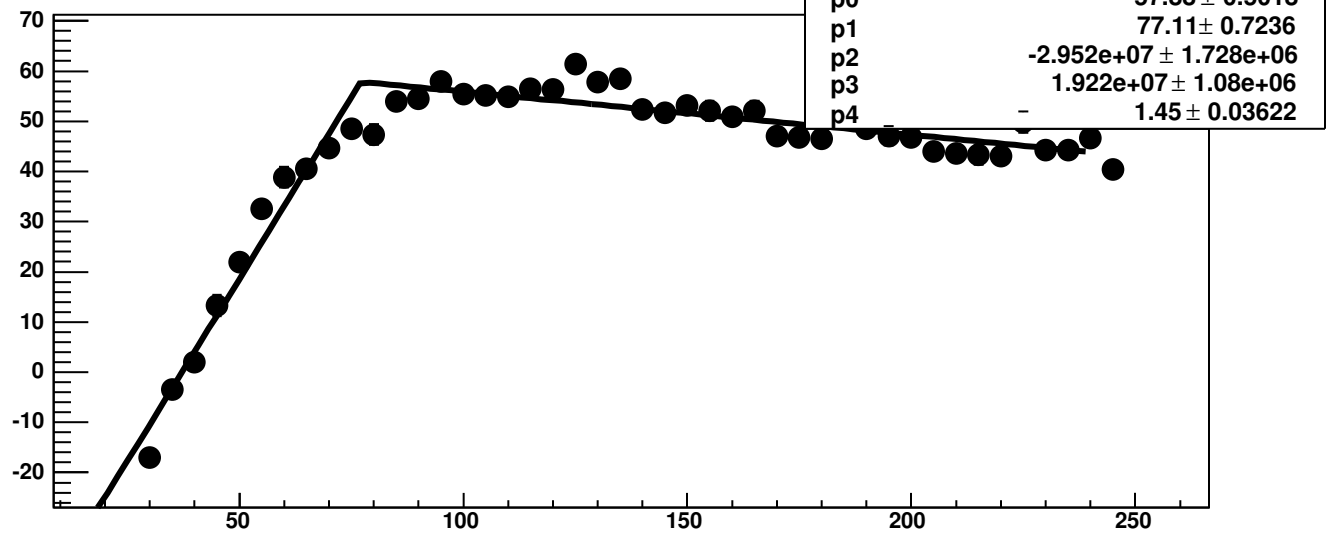
Chip 3, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



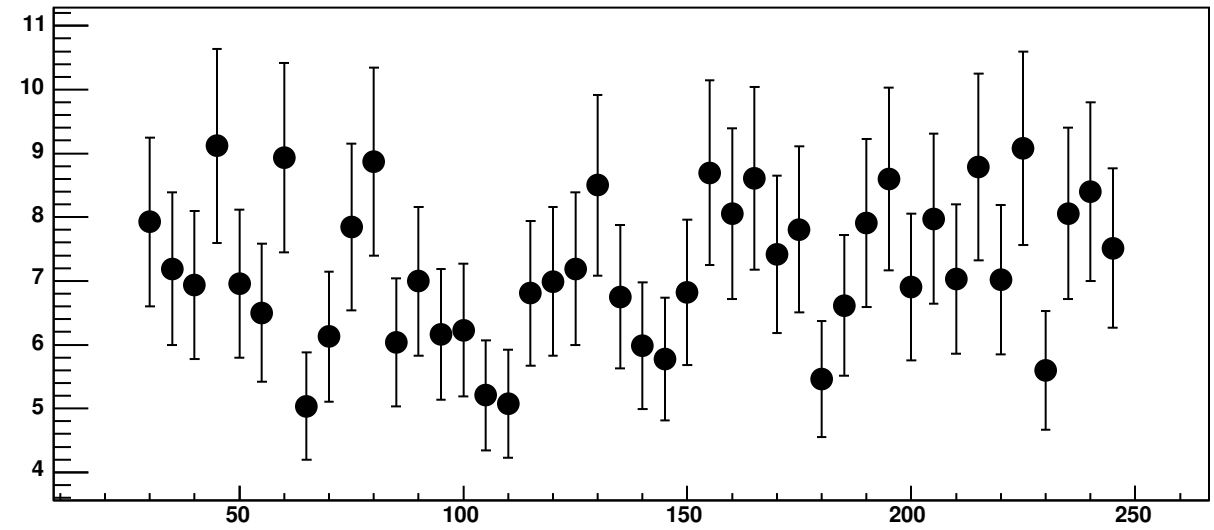
Chip 3, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold



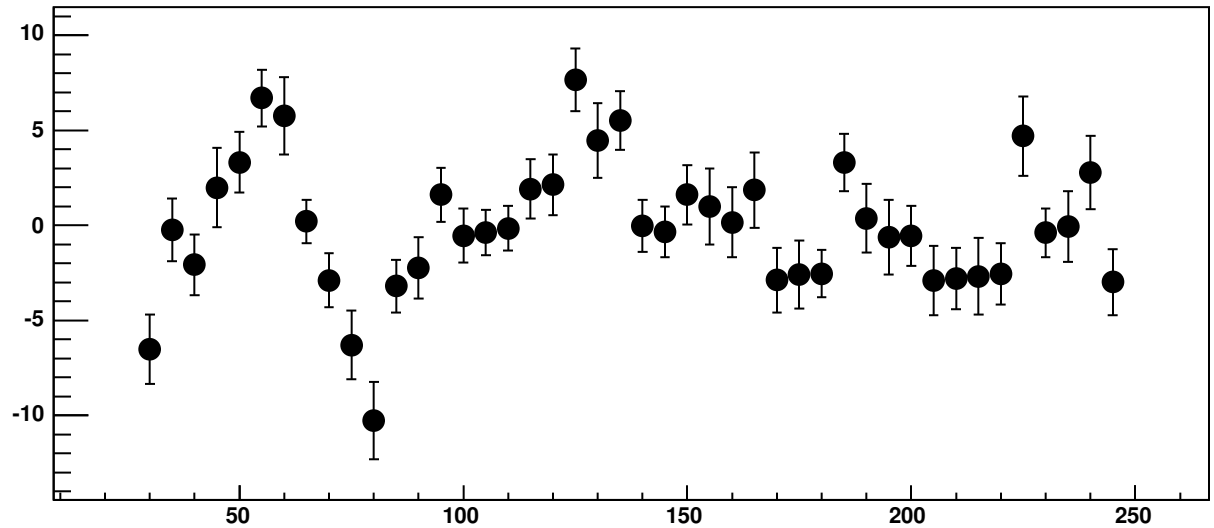
Chip 3, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

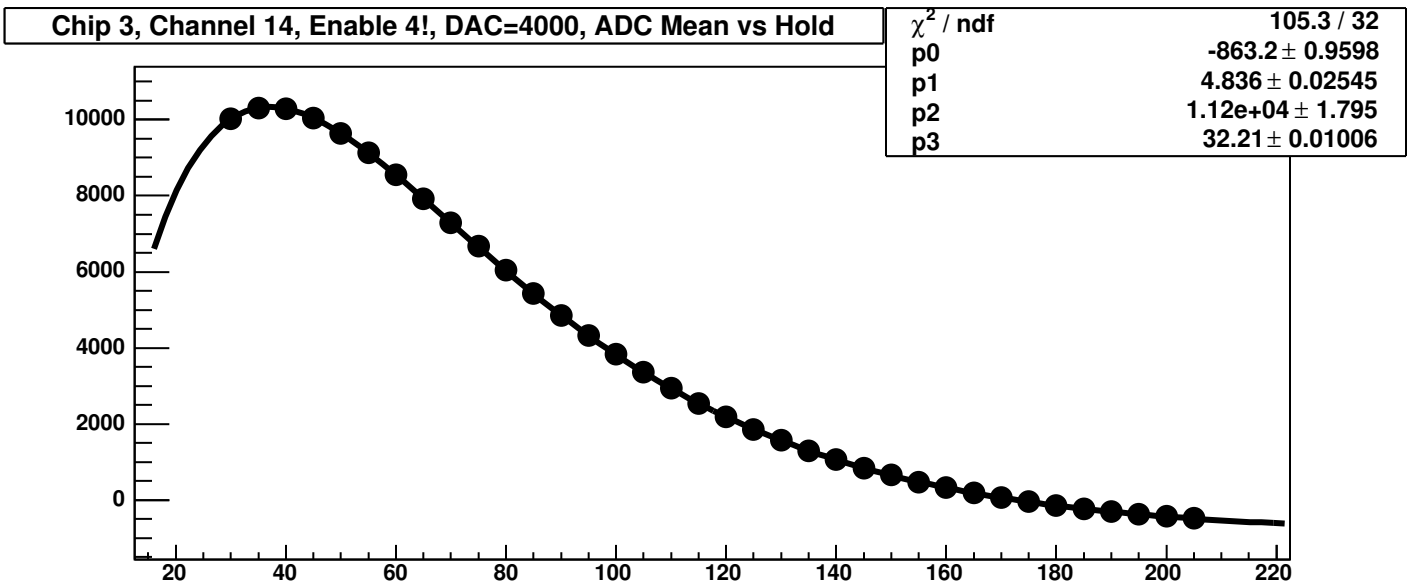


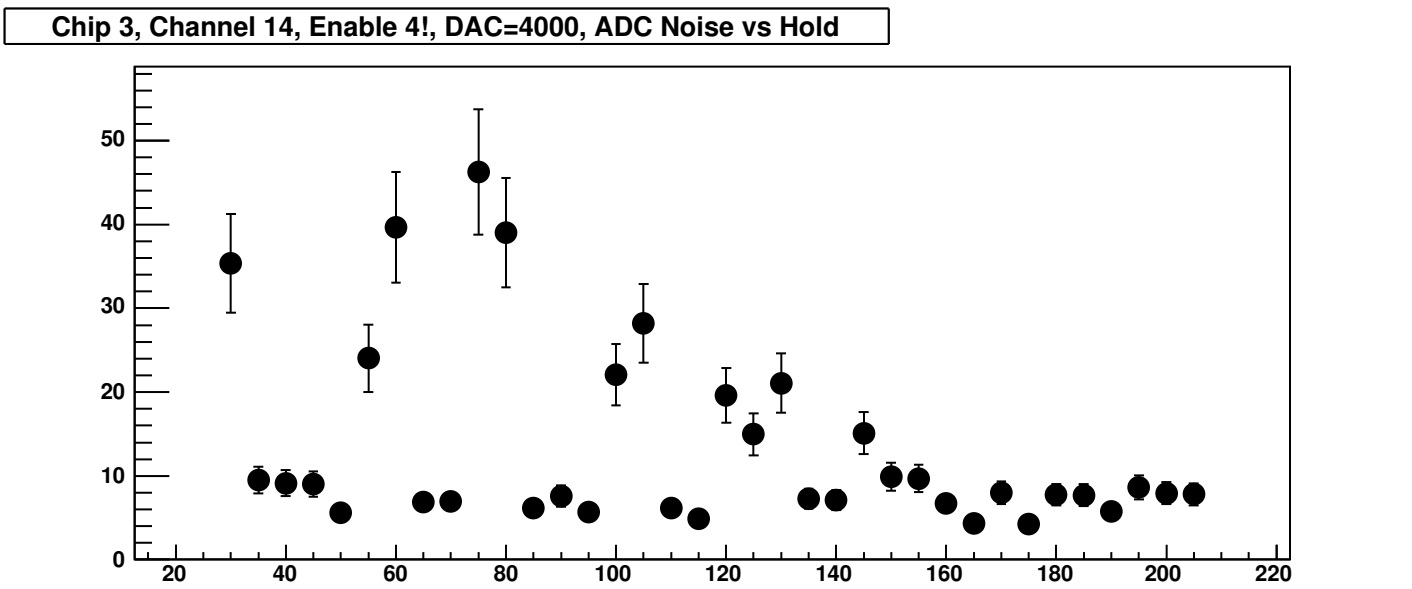
Chip 3, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold

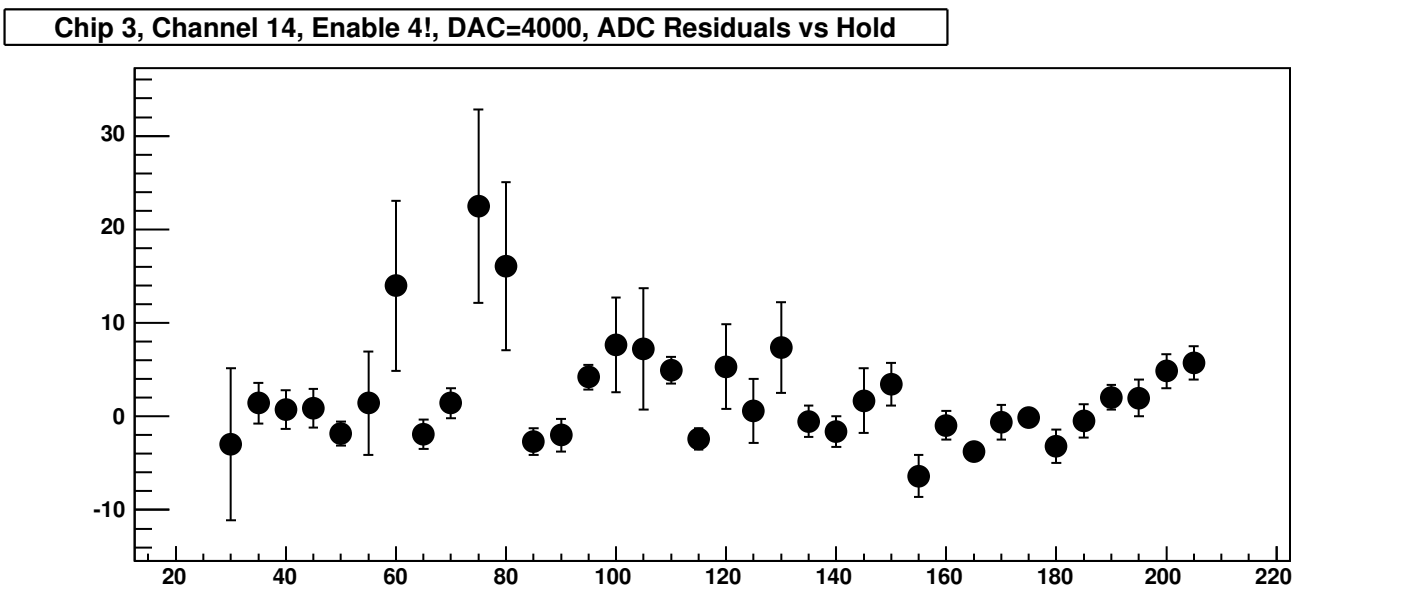


Chip 3, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

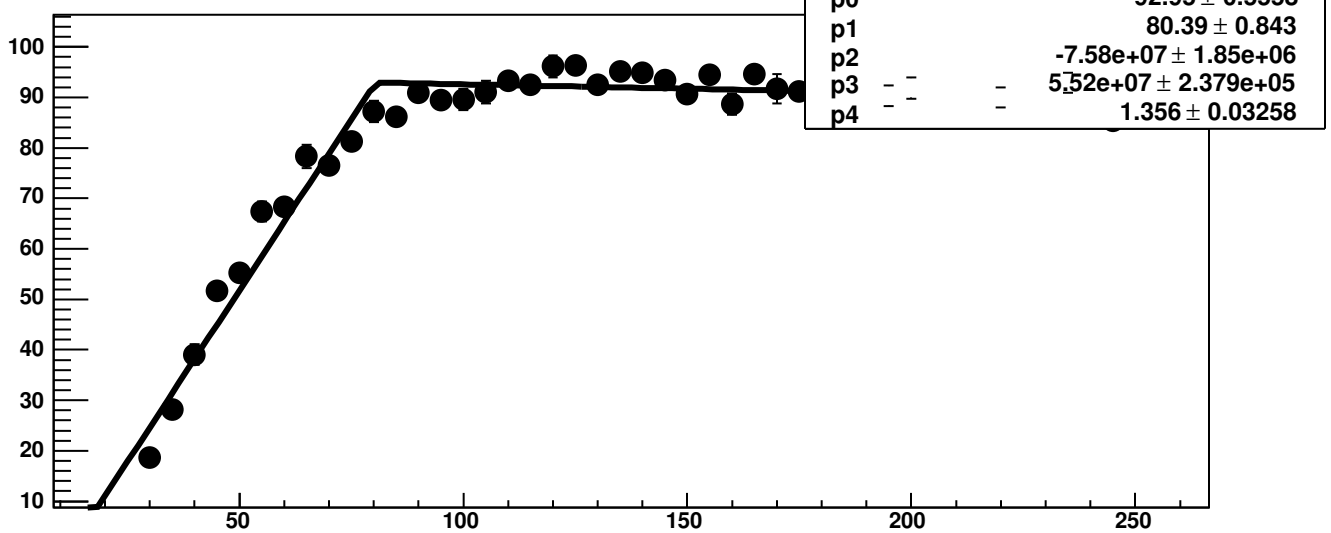



Chip 3, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 105.3 / 32
p0 -863.2 ± 0.9598
p1 4.836 ± 0.02545
p2 1.12e+04 ± 1.795
p3 32.21 ± 0.01006
10000
8000
6000
4000
2000
0
20
40
60
80
100
120
140
160
180
200
220

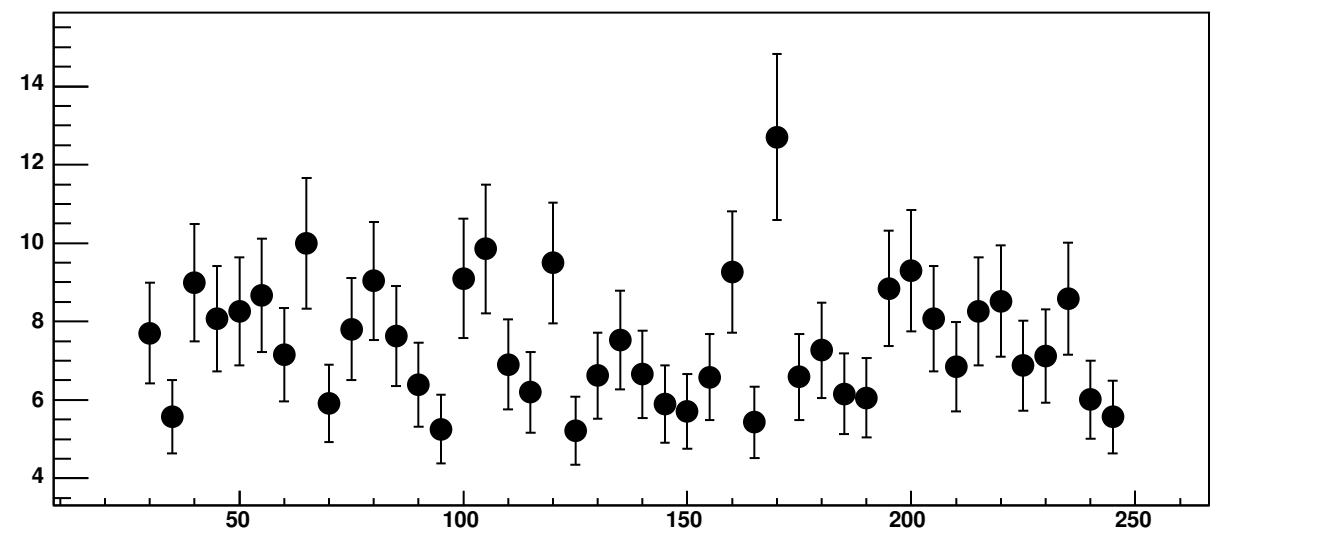

Chip 3, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
0
20
40
60
80
100
120
140
160
180
200
220


Chip 3, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
20
40
60
80
100
120
140
160
180
200
220

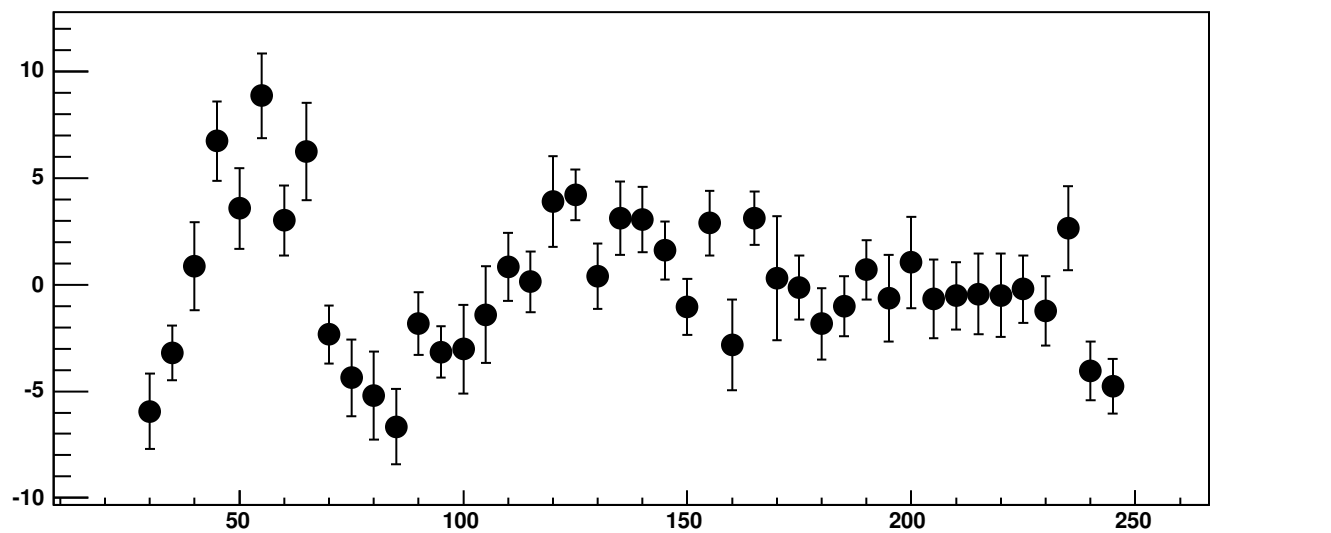
Chip 3, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold



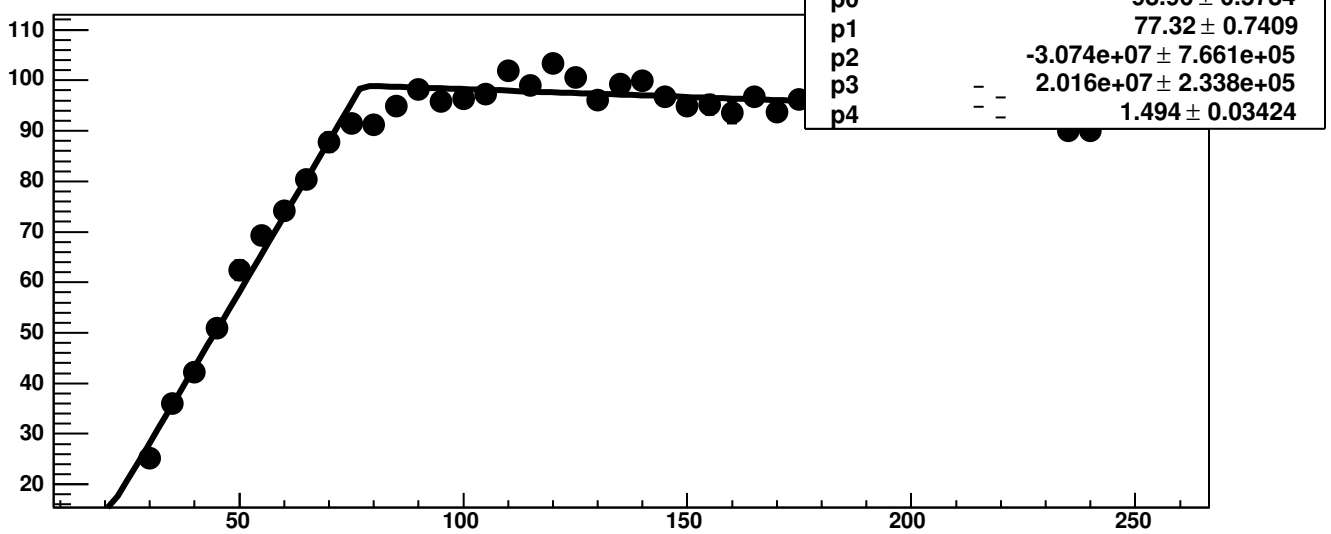
Chip 3, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



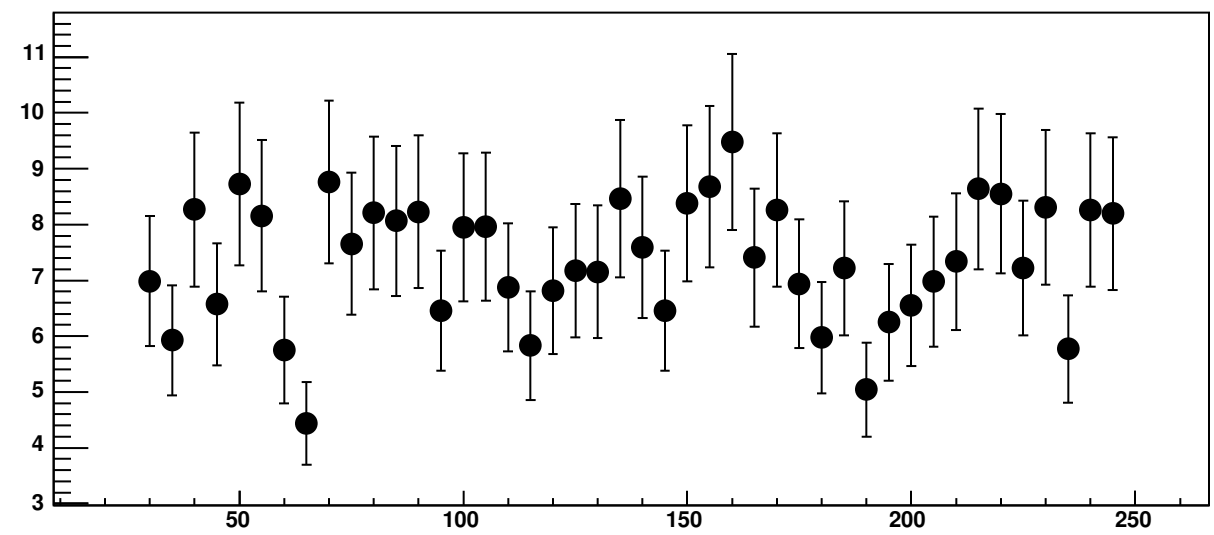
Chip 3, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold



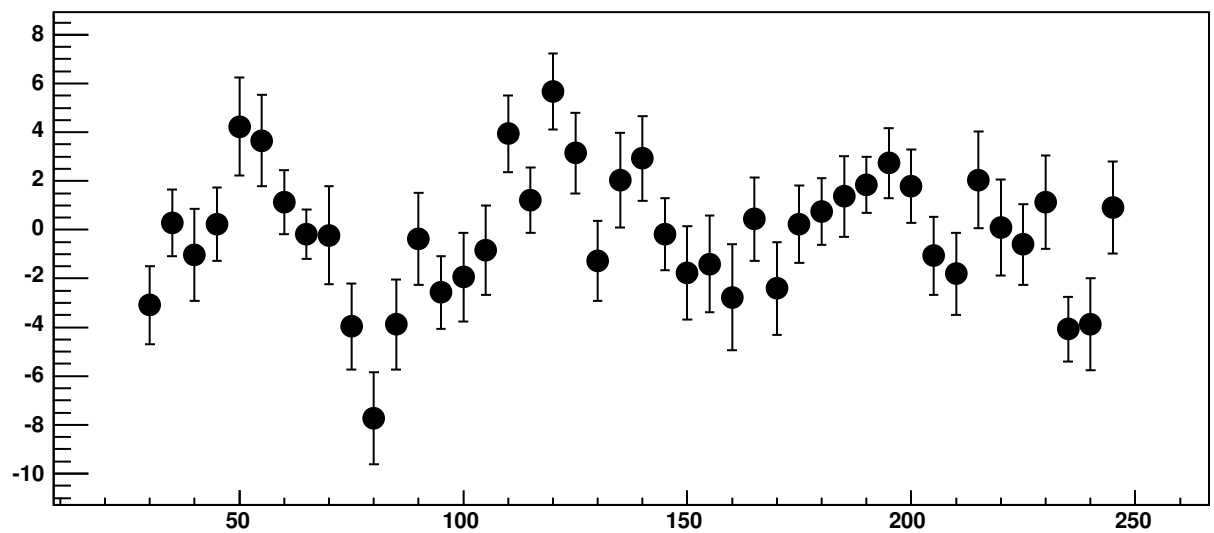
Chip 3, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

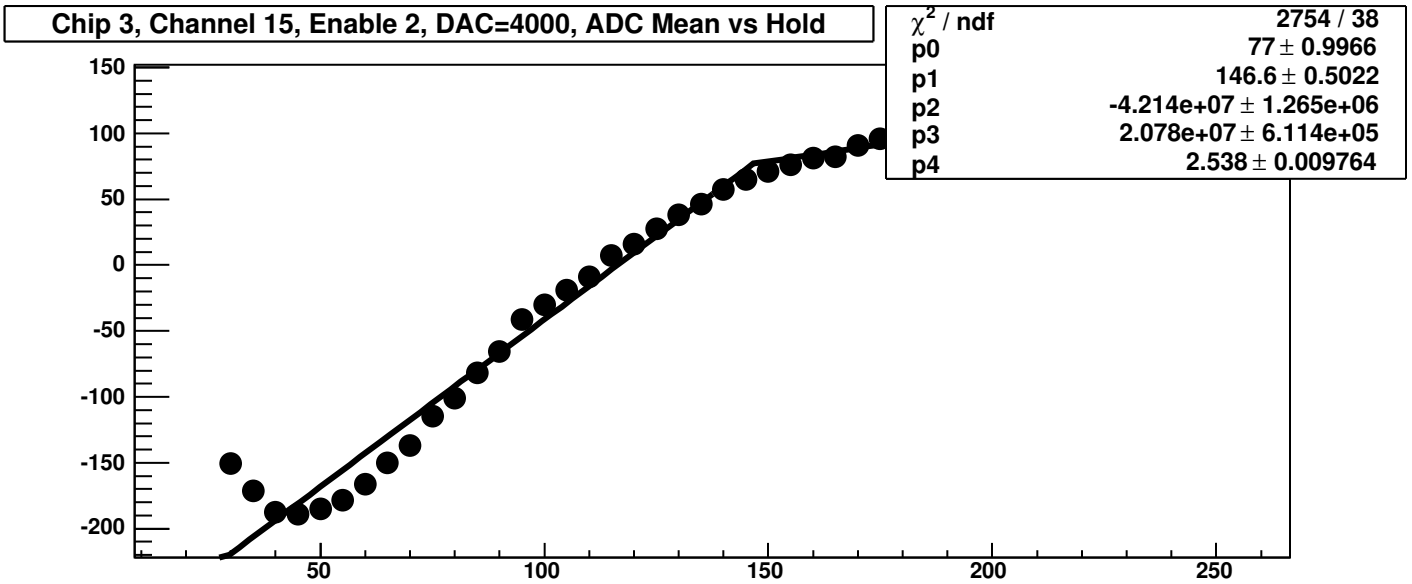


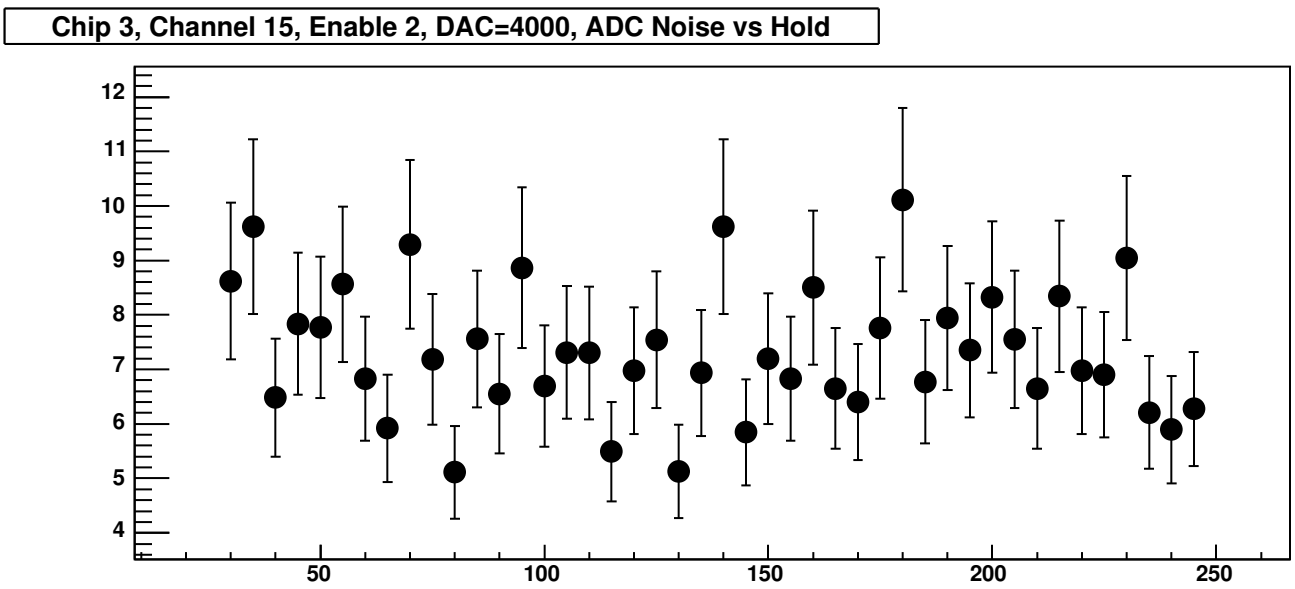
Chip 3, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold

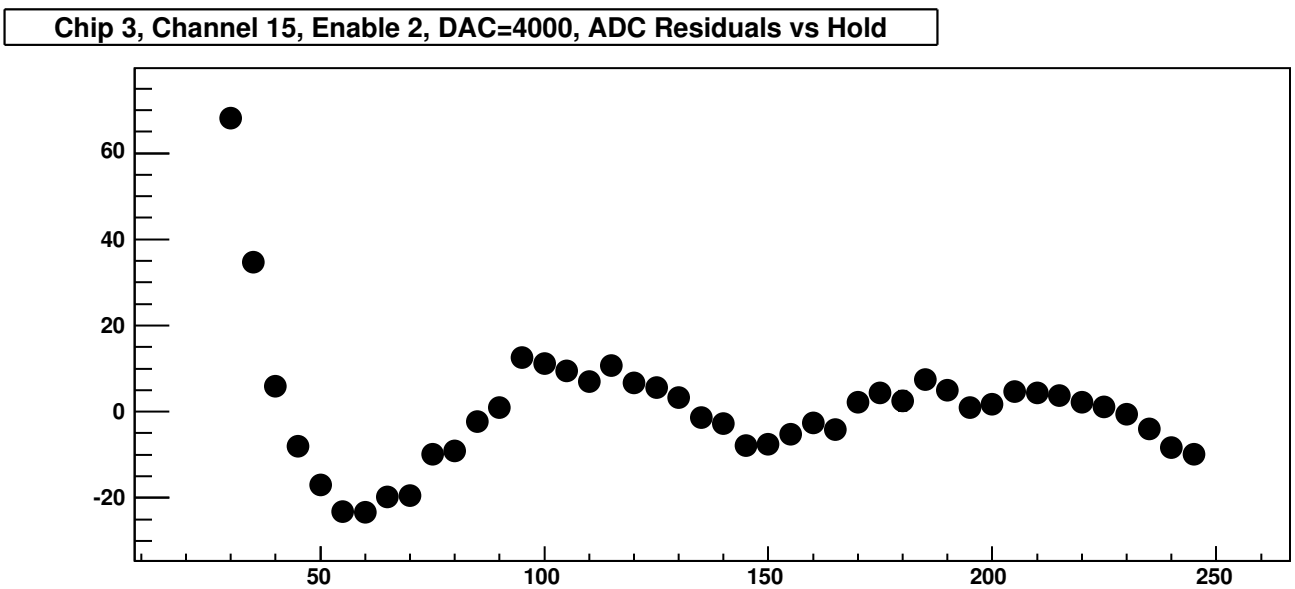


Chip 3, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

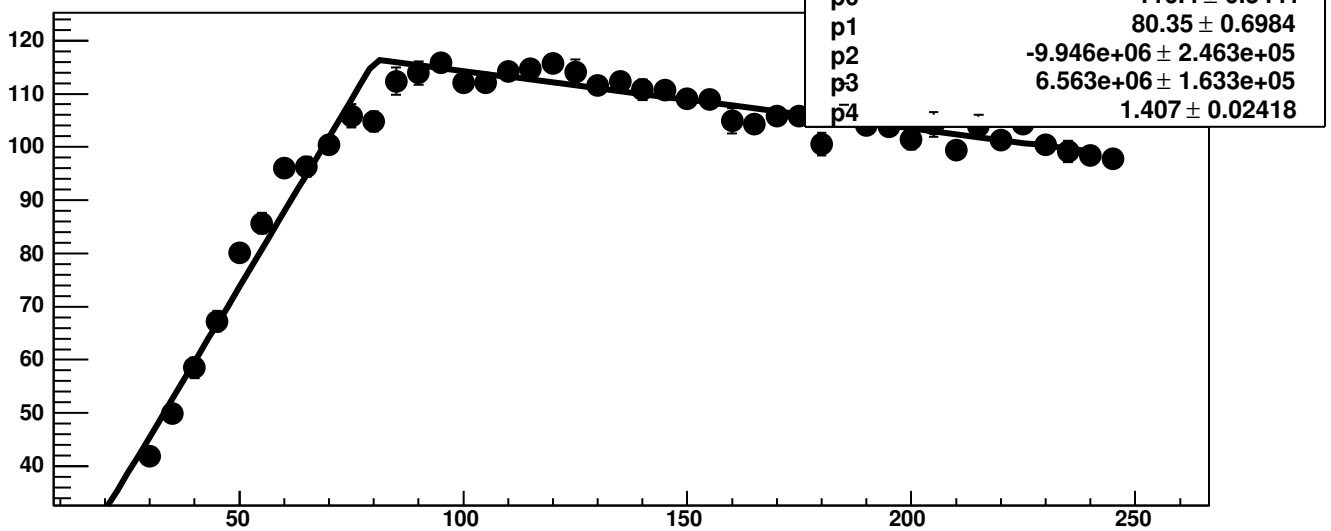



Chip 3, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 2754 / 38
p0 77 ± 0.9966
p1 146.6 ± 0.5022
p2 -4.214e+07 ± 1.265e+06
p3 2.078e+07 ± 6.114e+05
p4 2.538 ± 0.009764
150
100
50
0
-50
-100
-150
-200
50
100
150
200
250

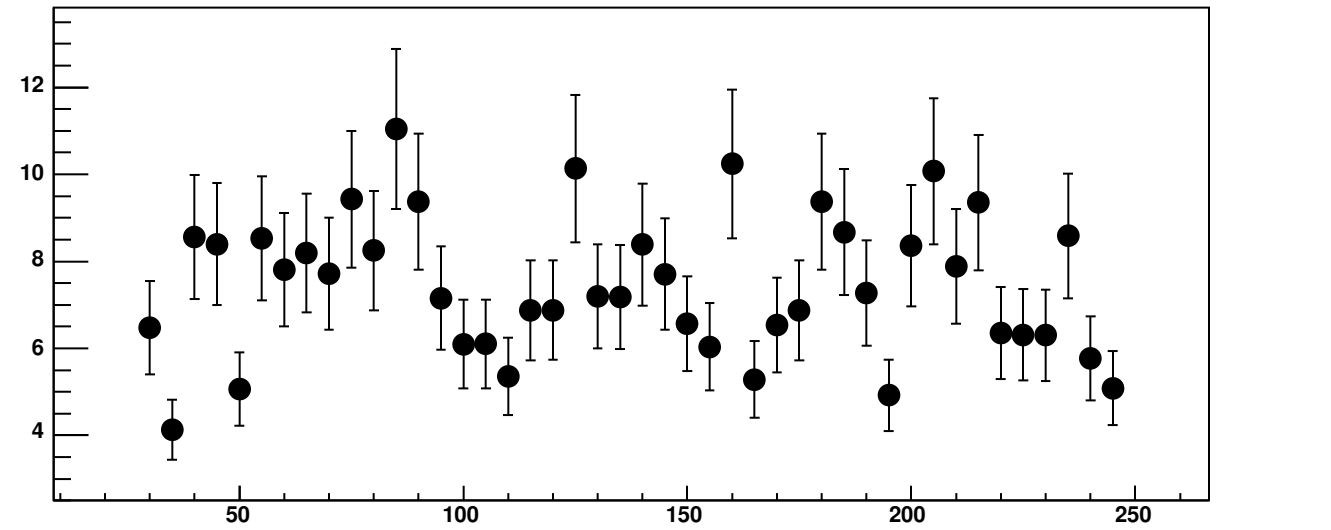

Chip 3, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 3, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold
60
40
20
0
-20
50
100
150
200
250

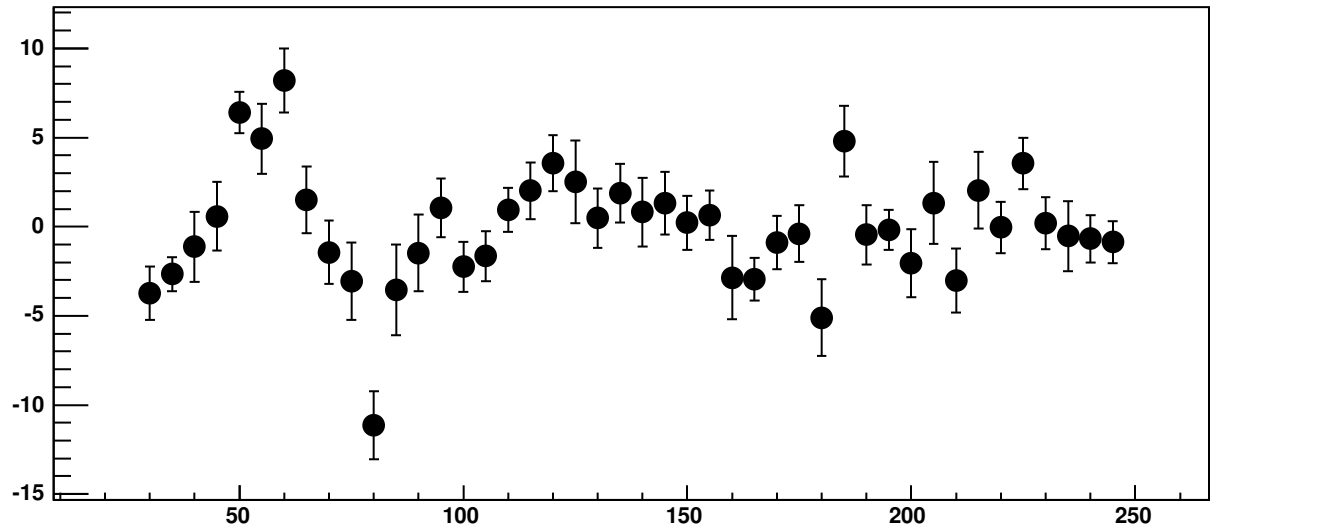
Chip 3, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

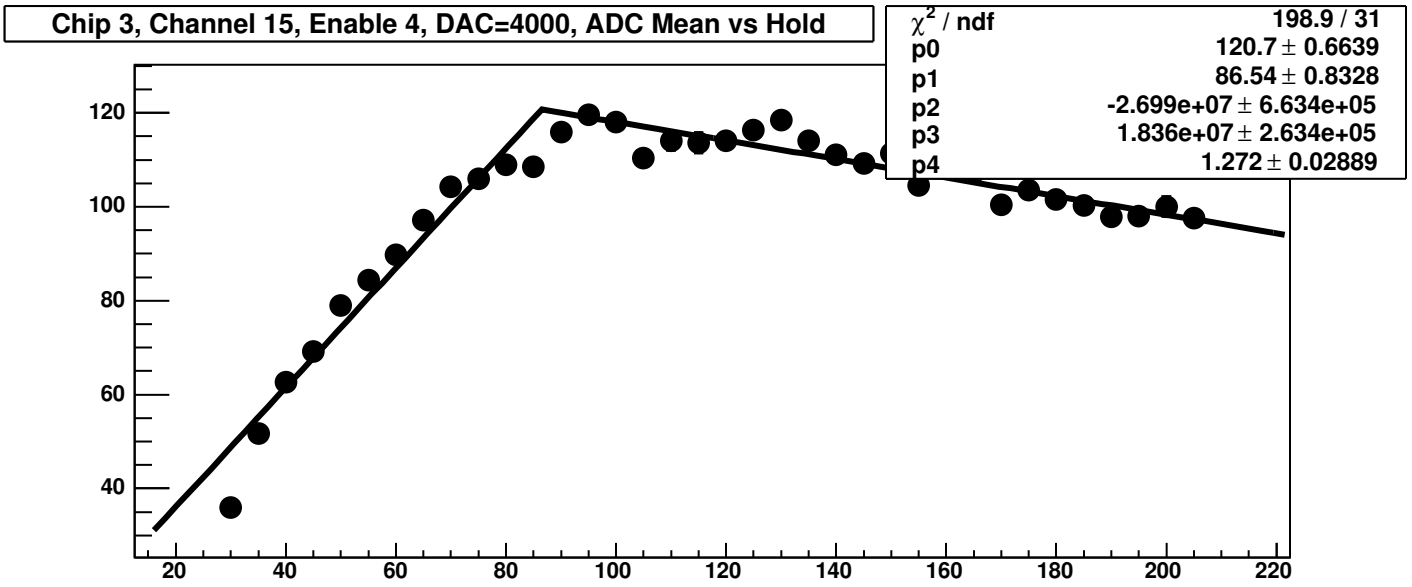


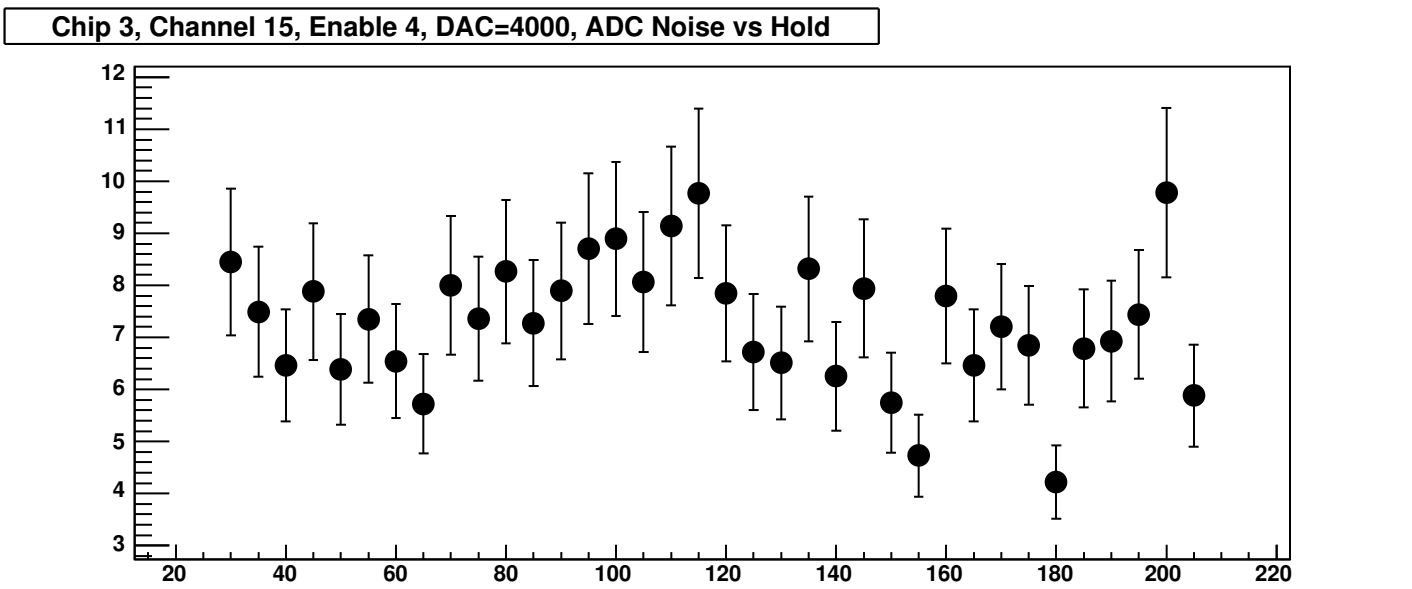
Chip 3, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold

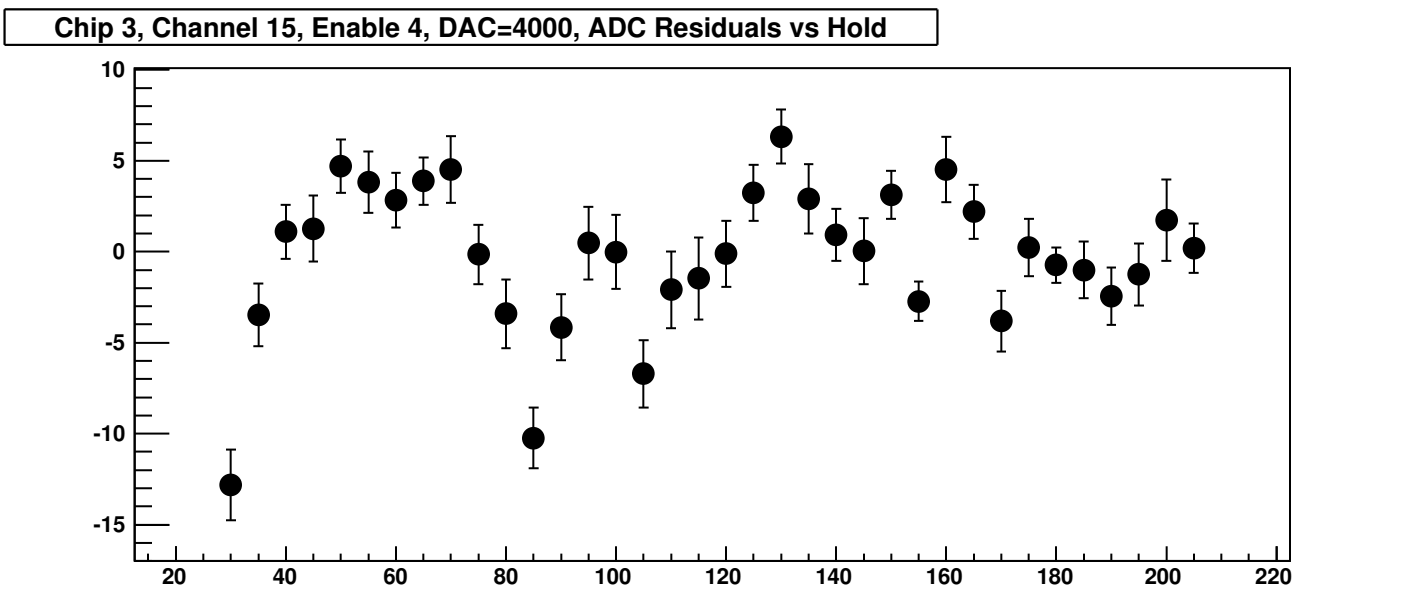


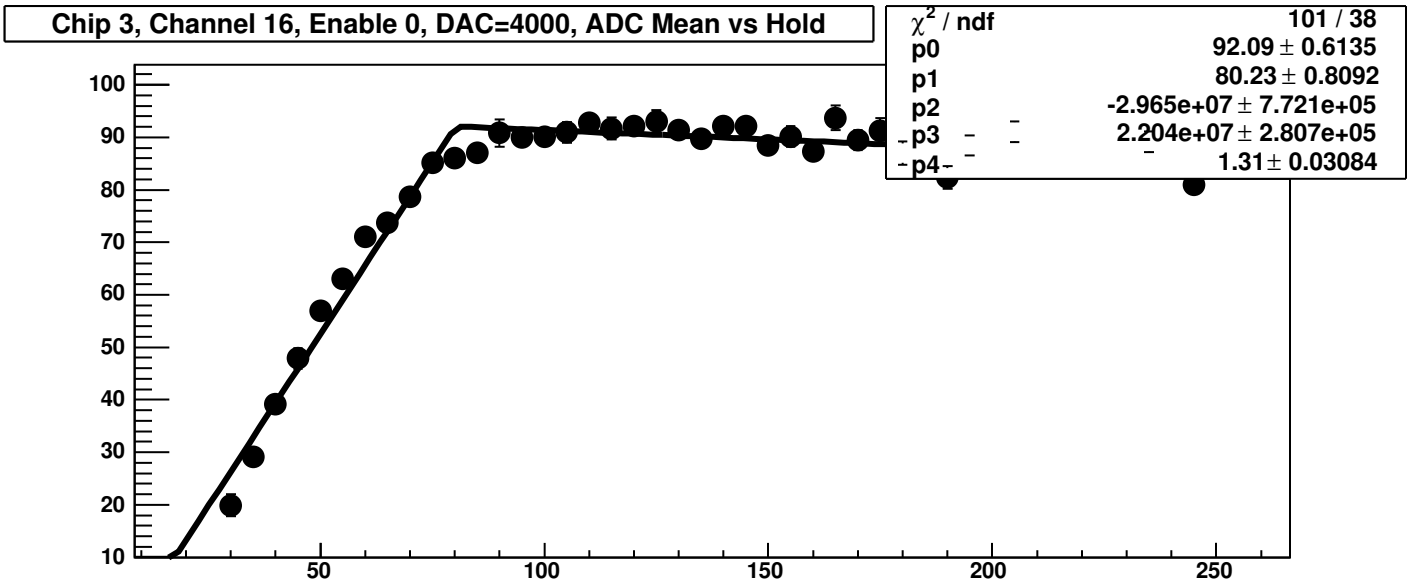
Chip 3, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold

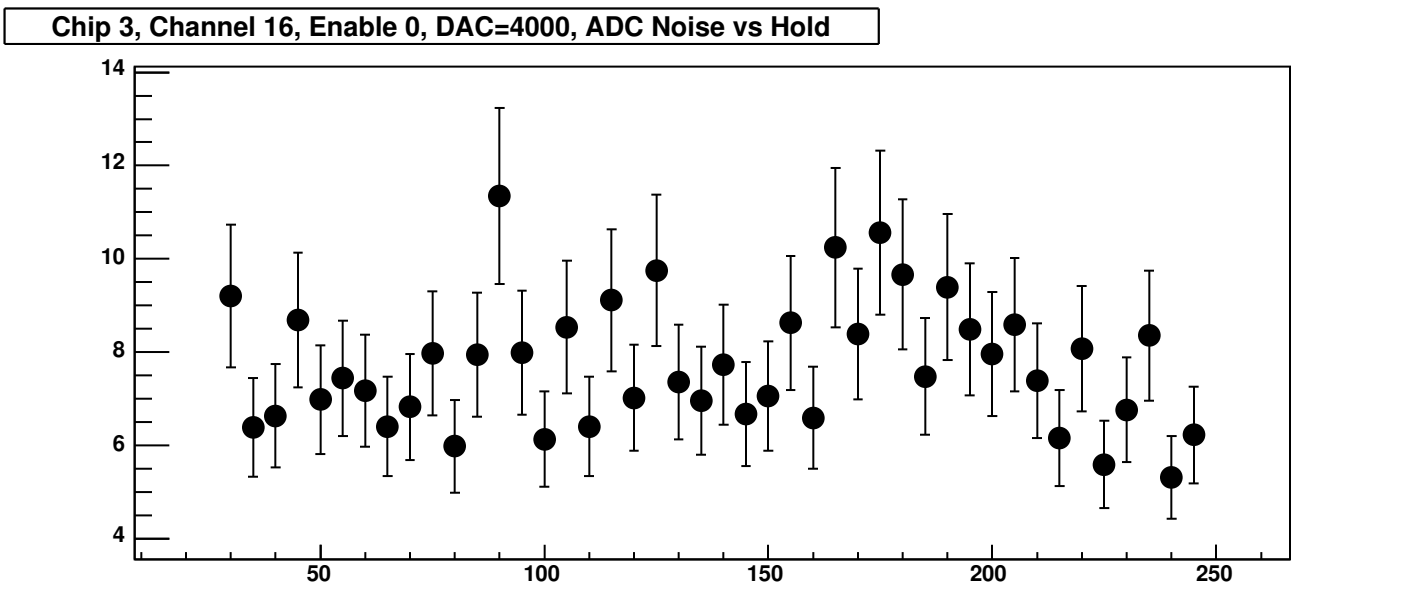


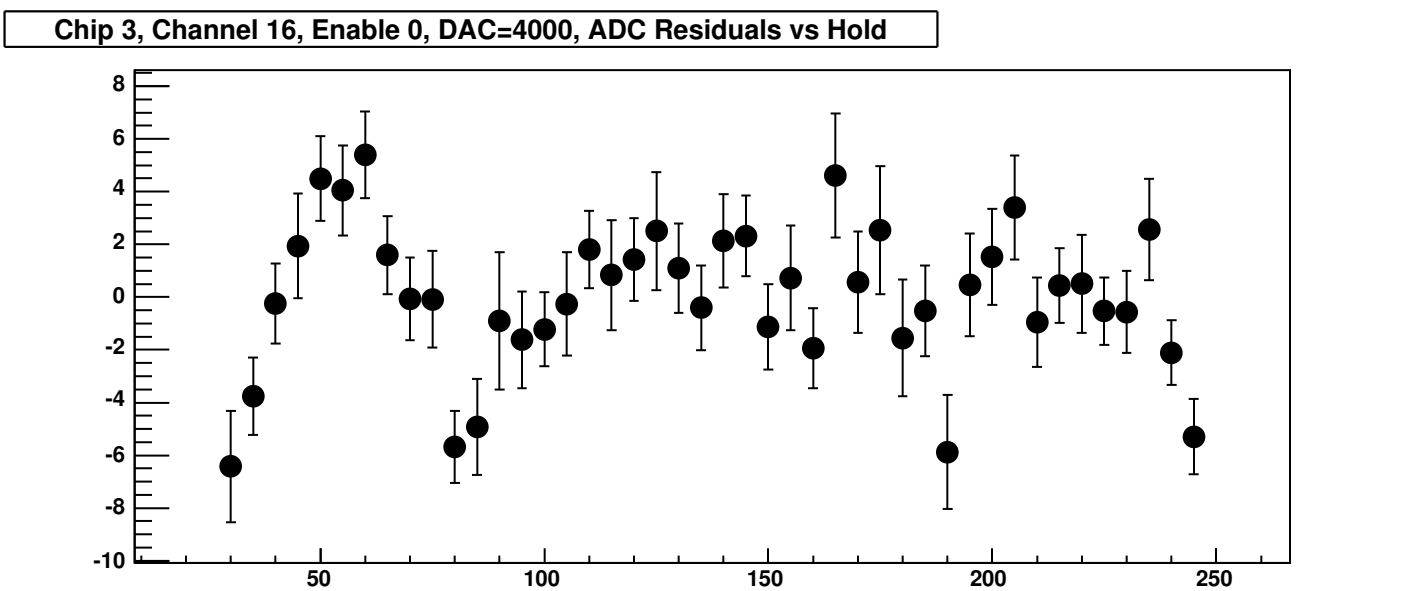

Chip 3, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 198.9 / 31
p0 120.7 ± 0.6639
p1 86.54 ± 0.8328
p2 -2.699e+07 ± 6.634e+05
p3 1.836e+07 ± 2.634e+05
p4 1.272 ± 0.02889

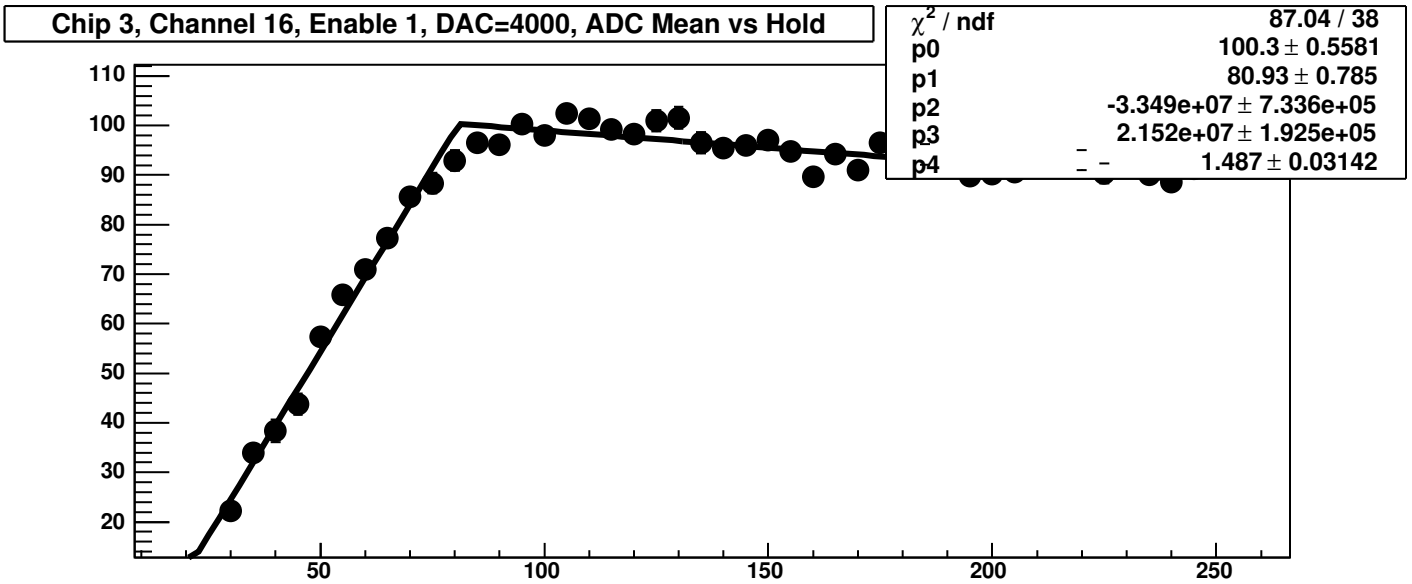

Chip 3, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold

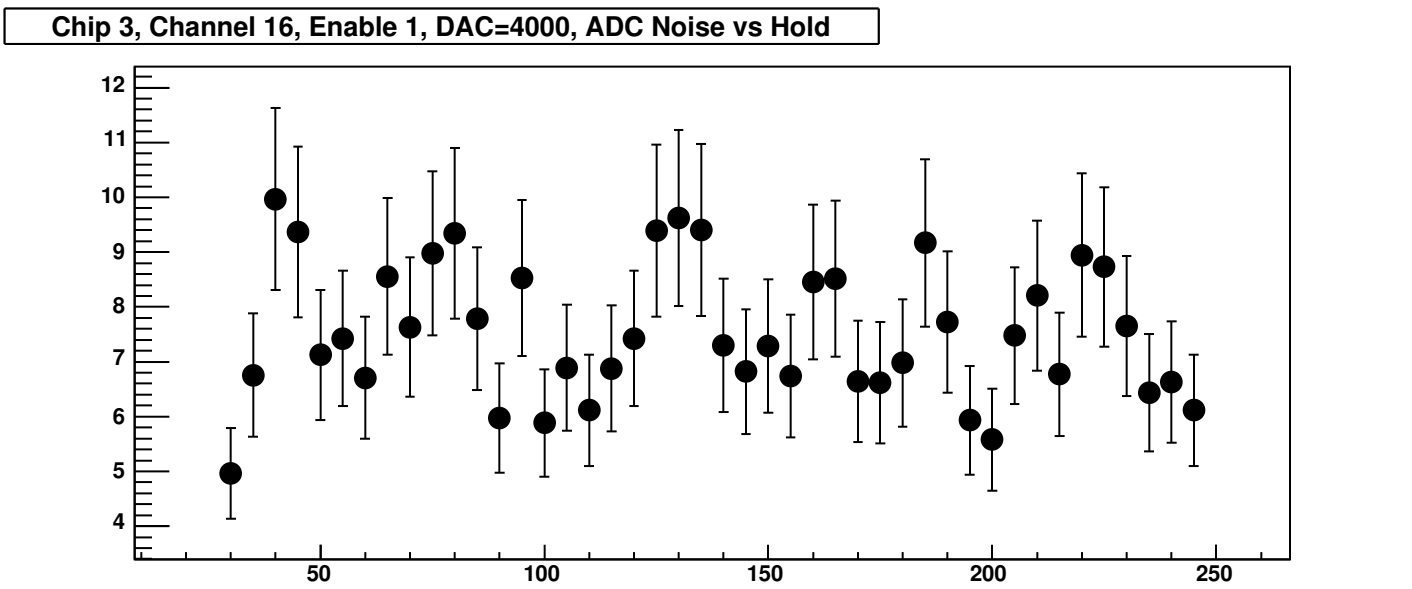

Chip 3, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

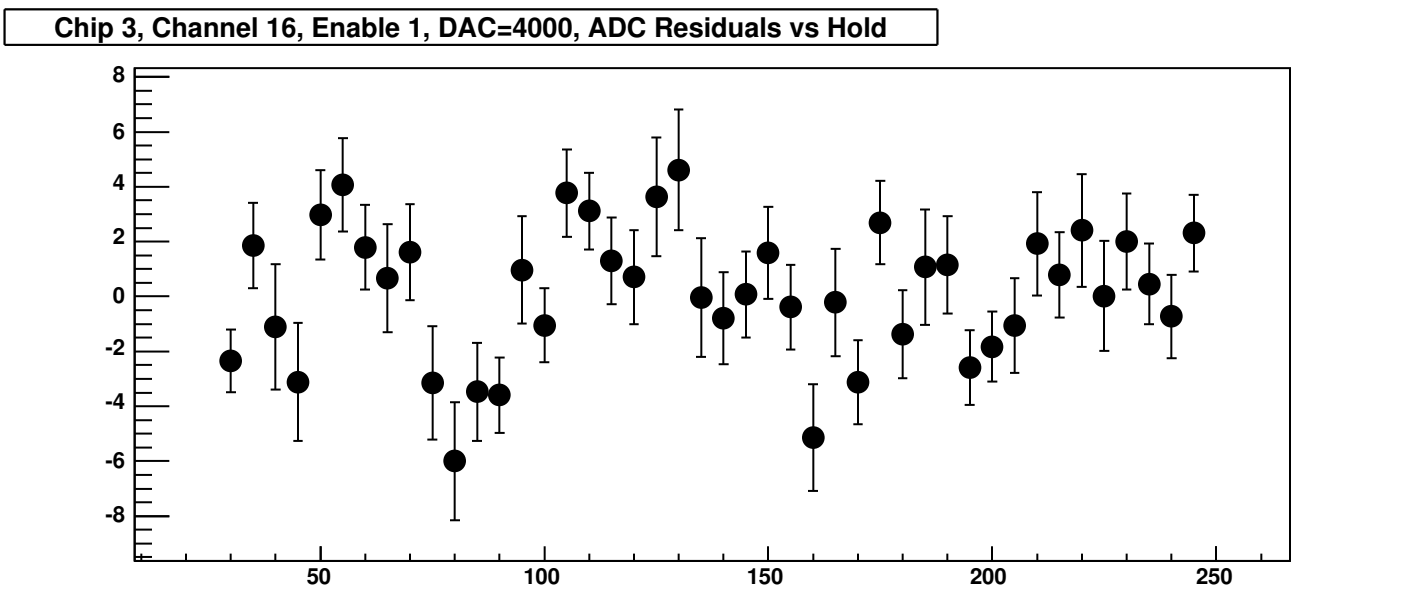

Chip 3, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 101 / 38
p0 92.09 ± 0.6135
p1 80.23 ± 0.8092
p2 -2.965e+07 ± 7.721e+05
p3 2.204e+07 ± 2.807e+05
p4 1.31 ± 0.03084
100
90
80
70
60
50
40
30
20
10
50
100
150
200
250

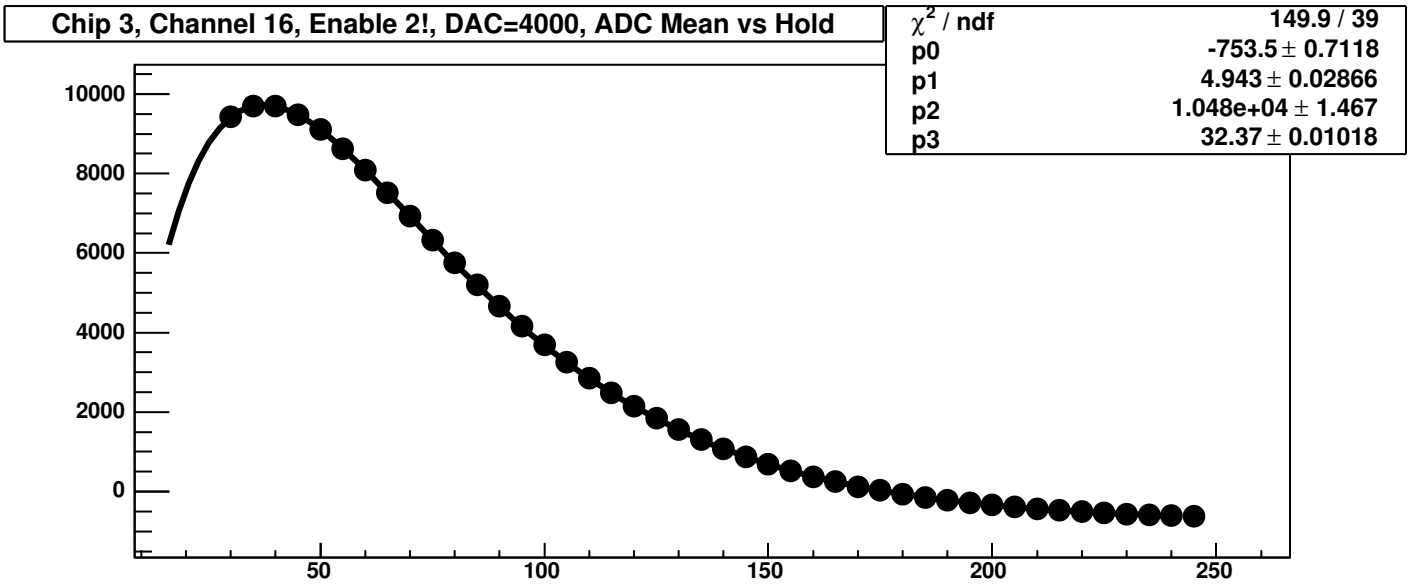

Chip 3, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250

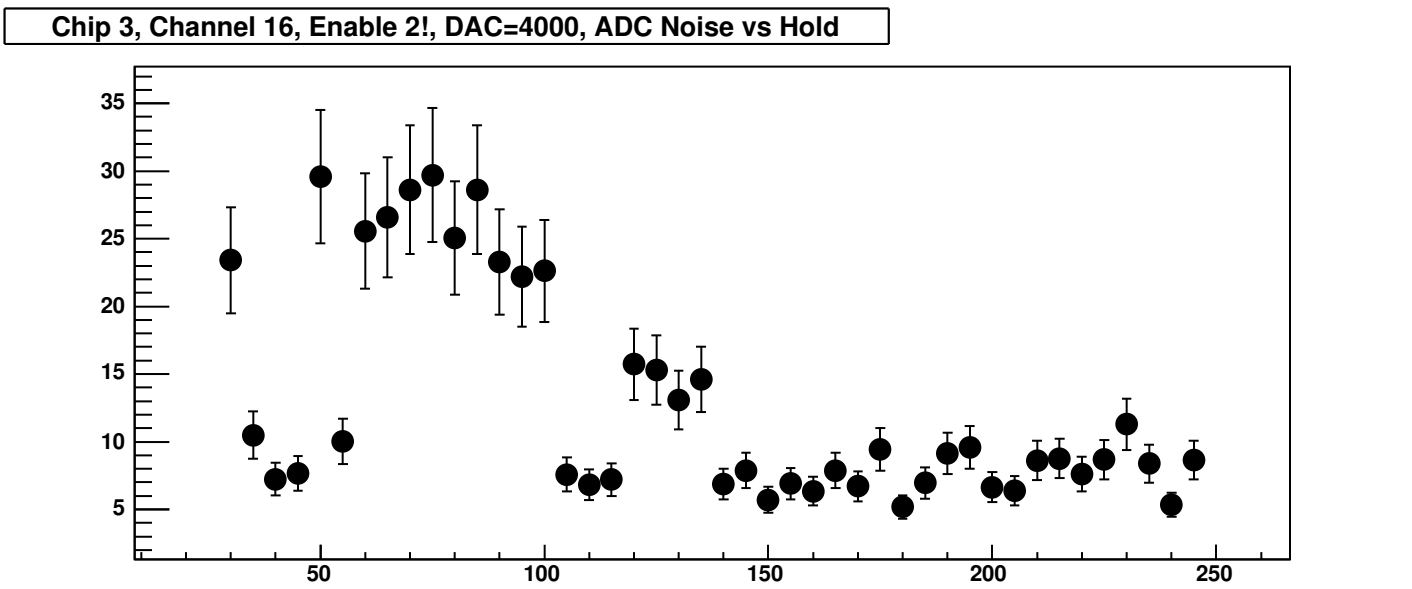

Chip 3, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
50
100
150
200
250

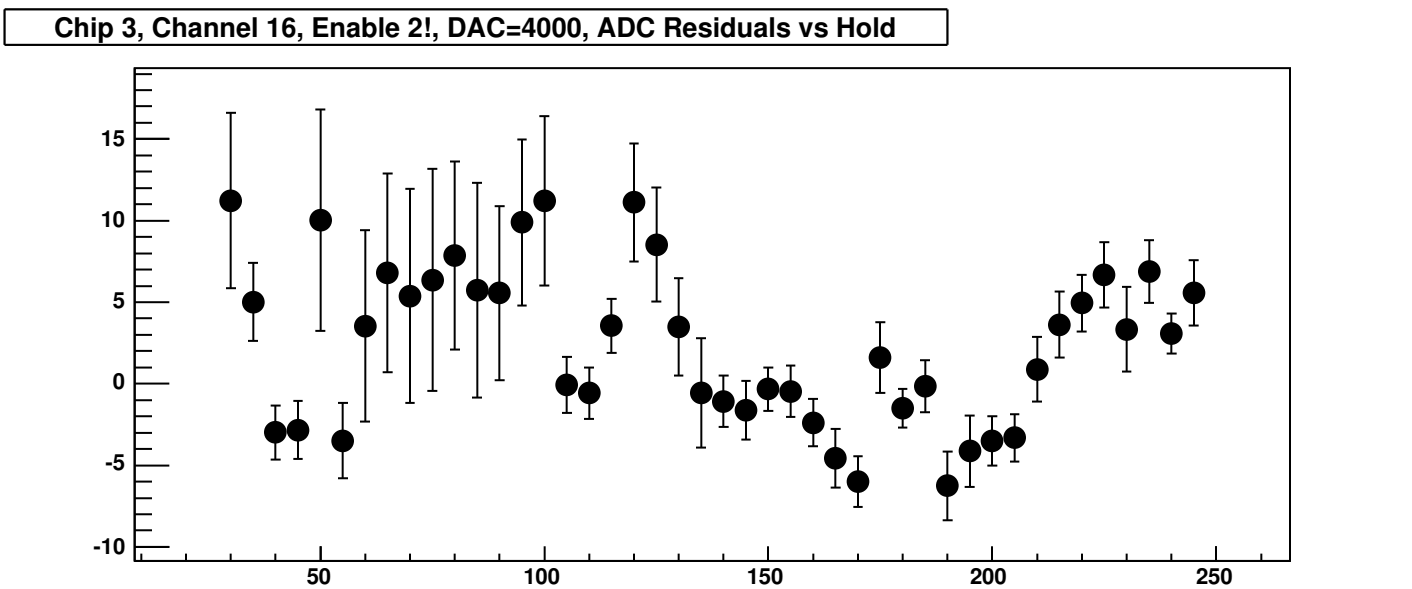

Chip 3, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 87.04 / 38
p0 100.3 ± 0.5581
p1 80.93 ± 0.785
p2 -3.349e+07 ± 7.336e+05
p3 2.152e+07 ± 1.925e+05
p4 1.487 ± 0.03142
110
100
90
80
70
60
50
40
30
20
50
100
150
200
250


Chip 3, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

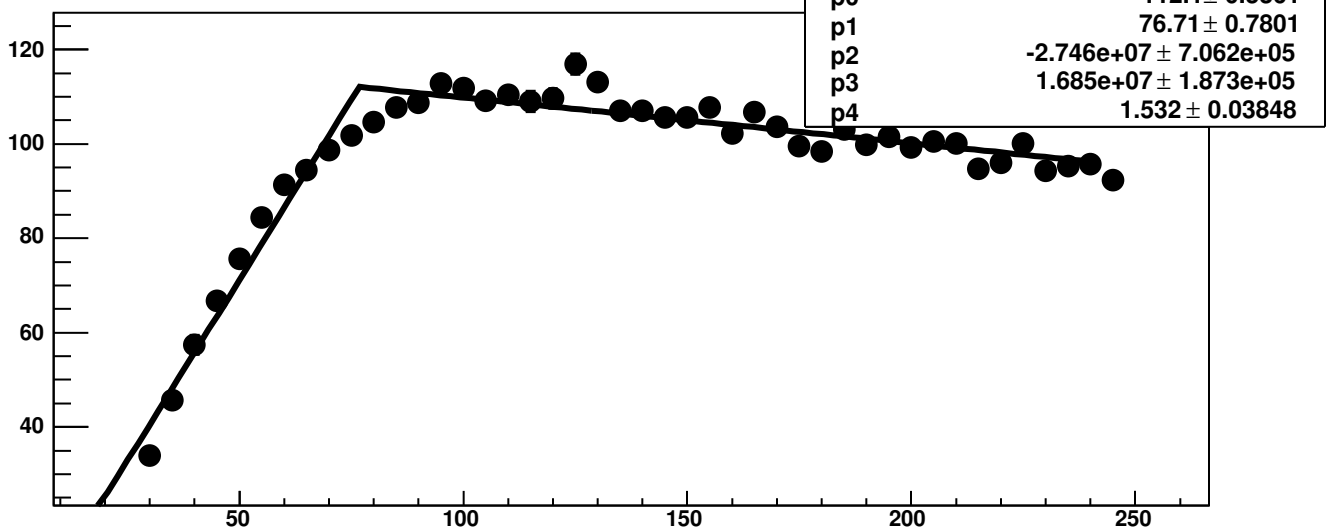

Chip 3, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250


Chip 3, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 149.9 / 39
p0 -753.5 ± 0.7118
p1 4.943 ± 0.02866
p2 1.048e+04 ± 1.467
p3 32.37 ± 0.01018
10000
8000
6000
4000
2000
0
50
100
150
200
250

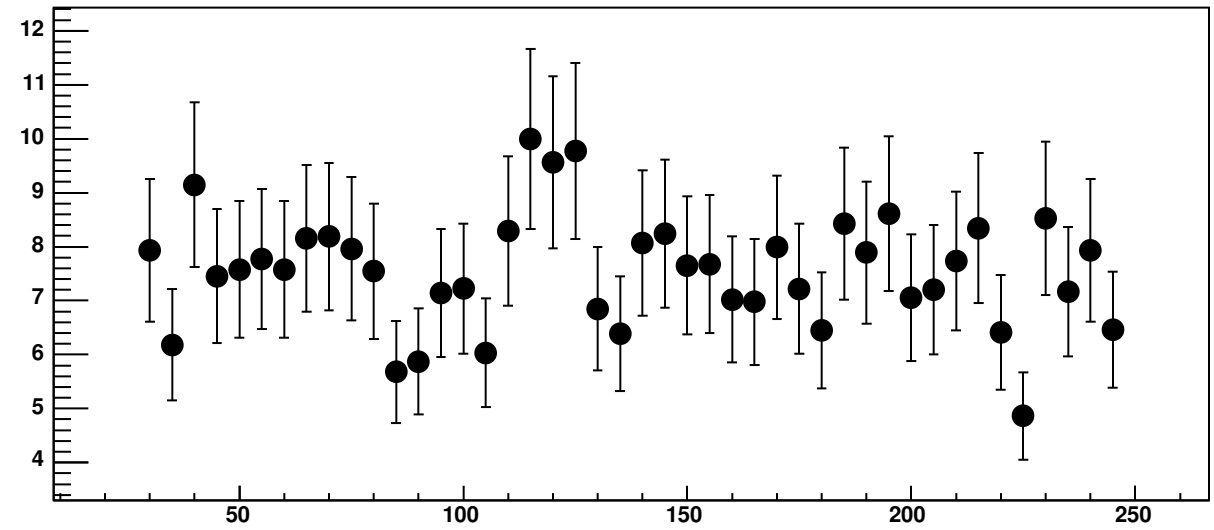

Chip 3, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold
35
30
25
20
15
10
5
50
100
150
200
250


Chip 3, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
50
100
150
200
250

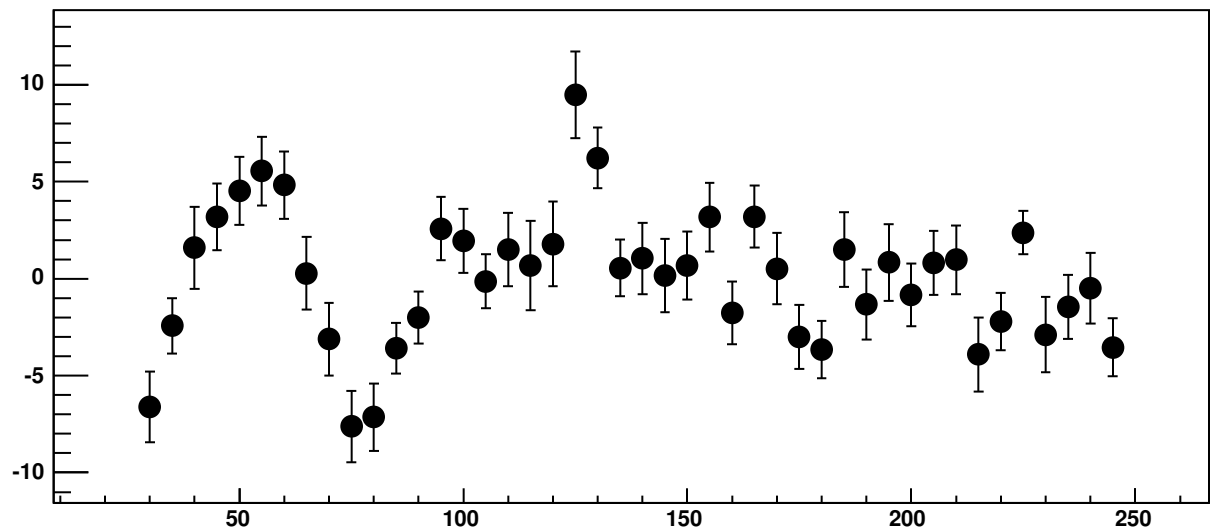
Chip 3, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold



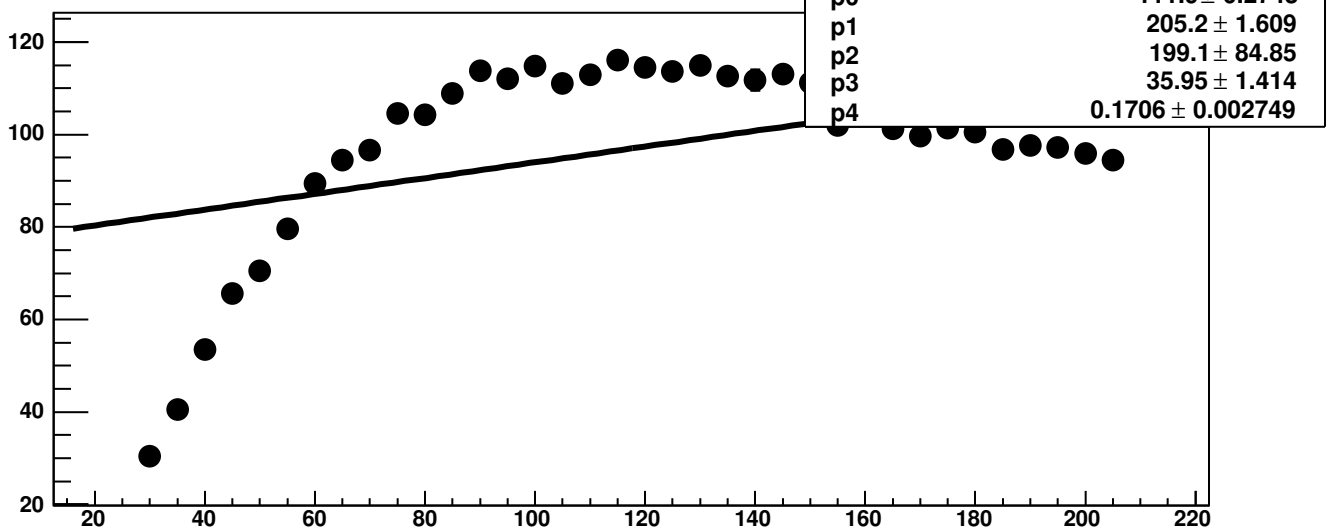
Chip 3, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



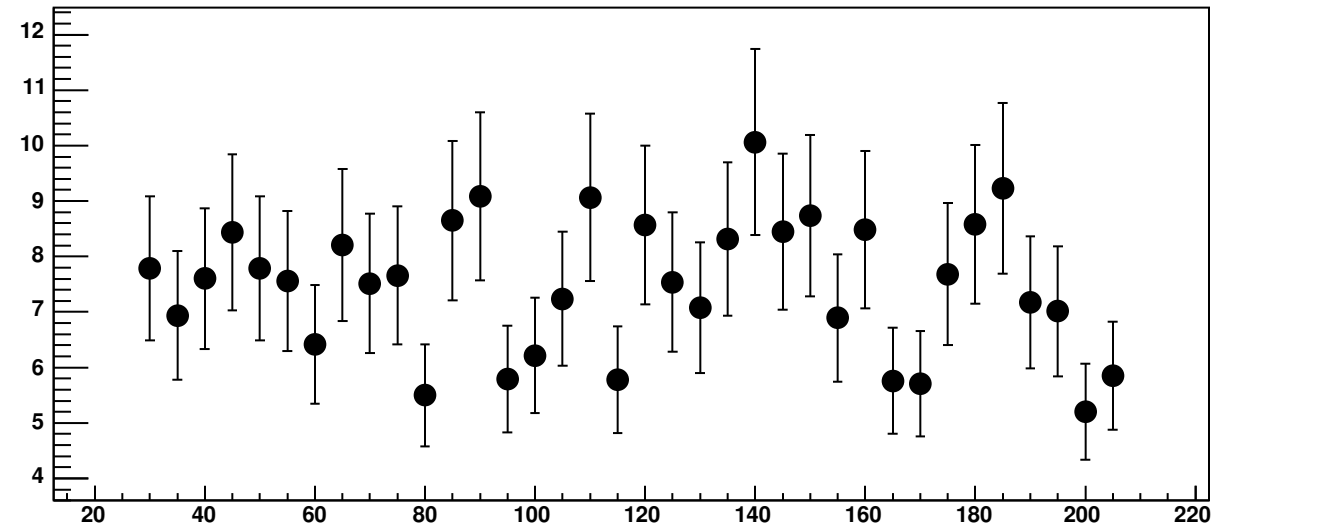
Chip 3, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold



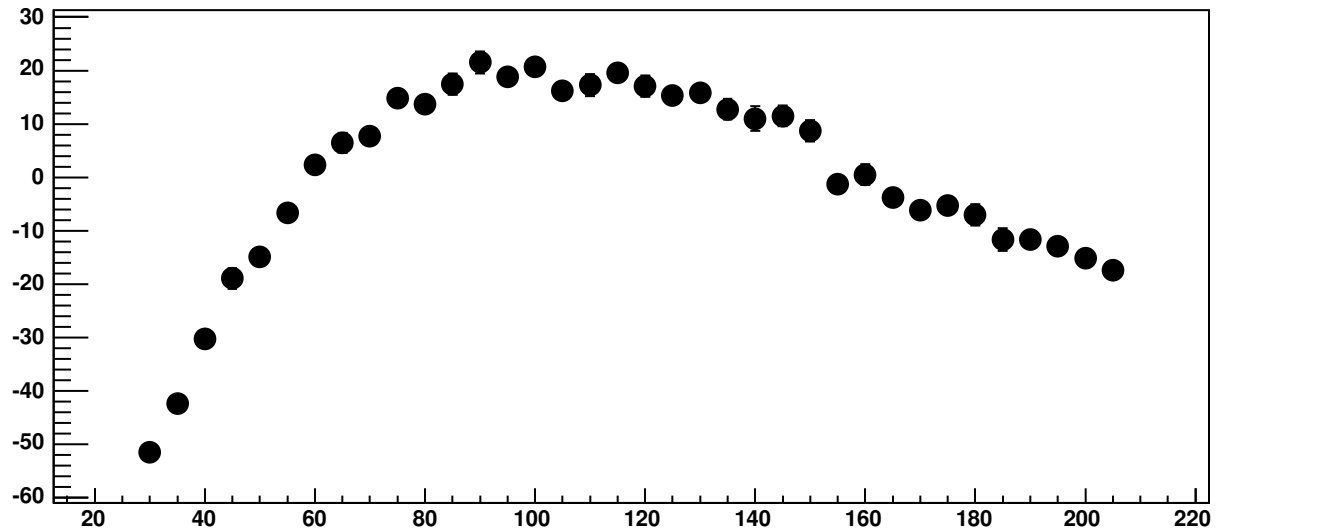
Chip 3, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold



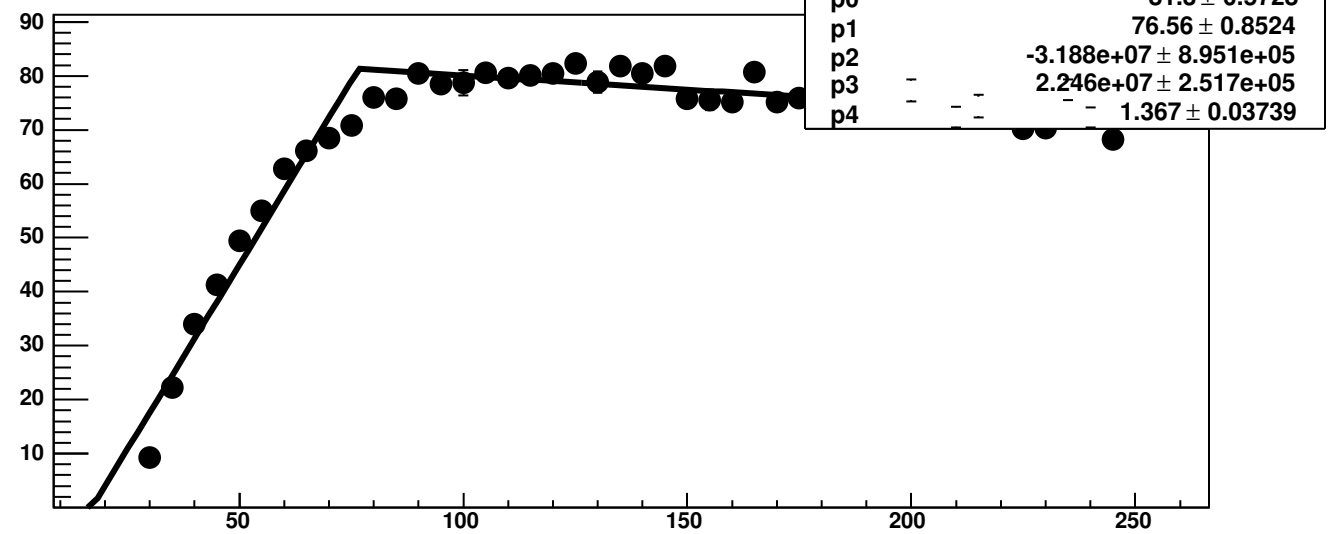
Chip 3, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



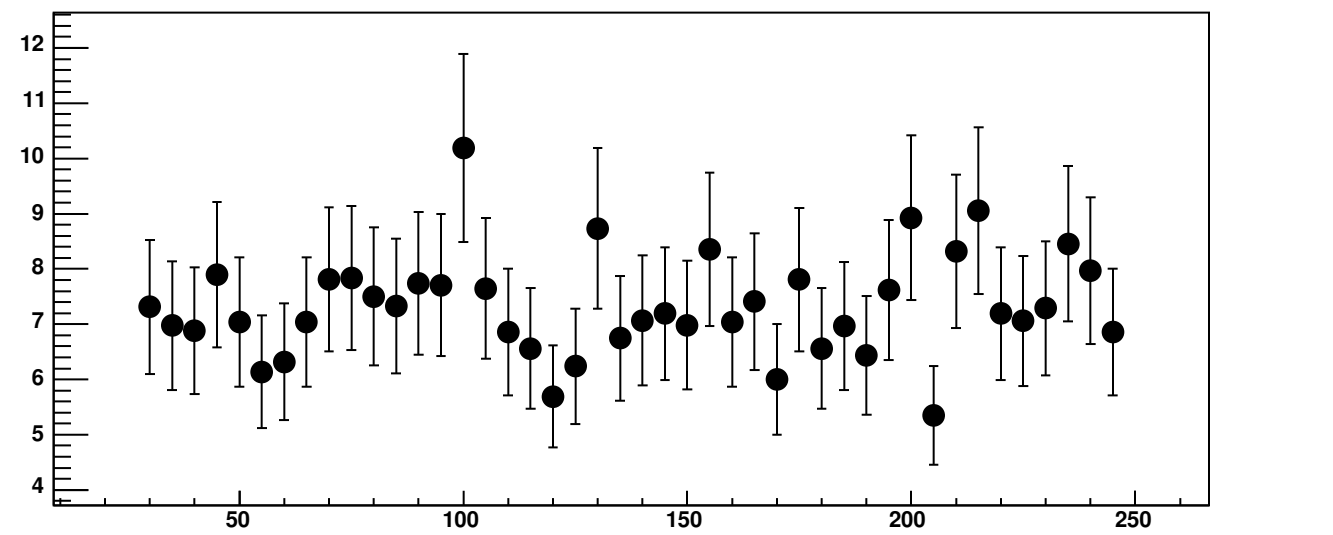
Chip 3, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold



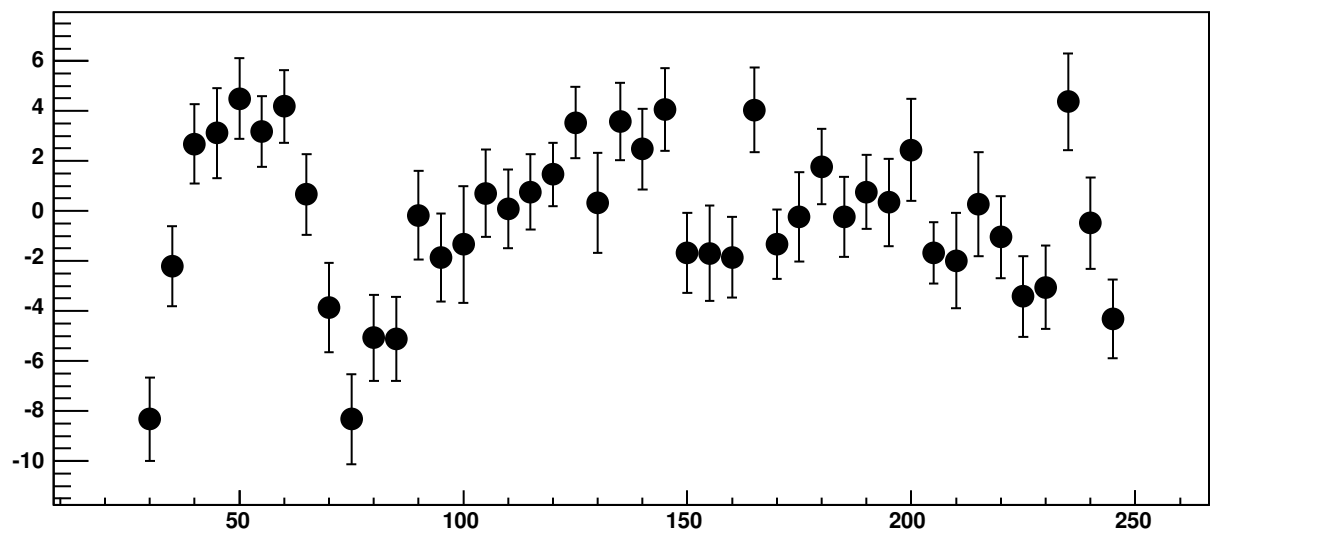
Chip 3, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold



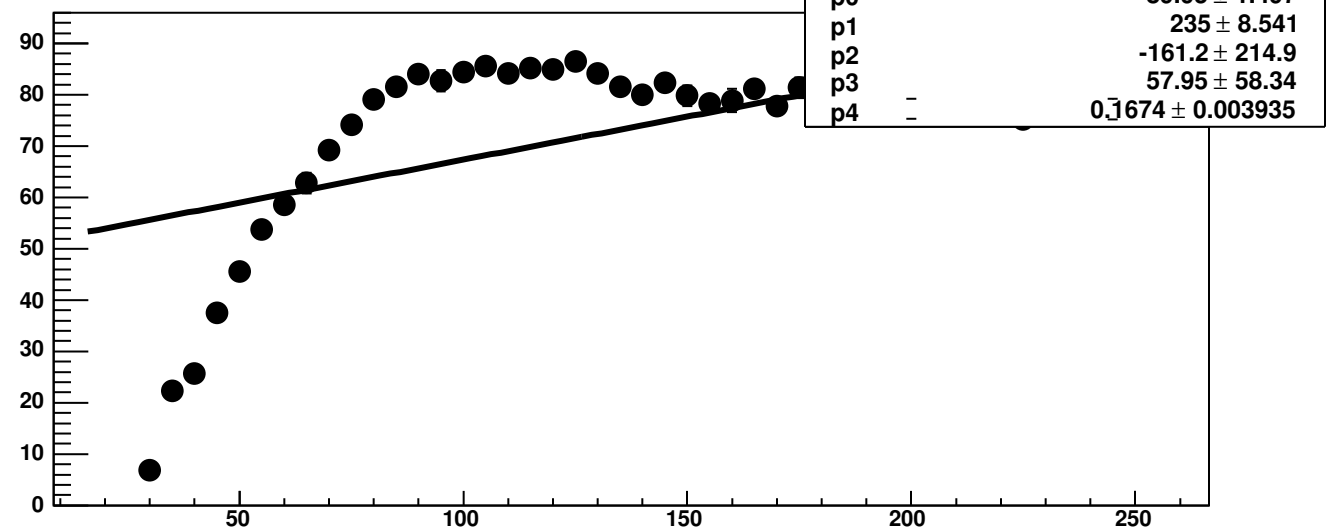
Chip 3, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



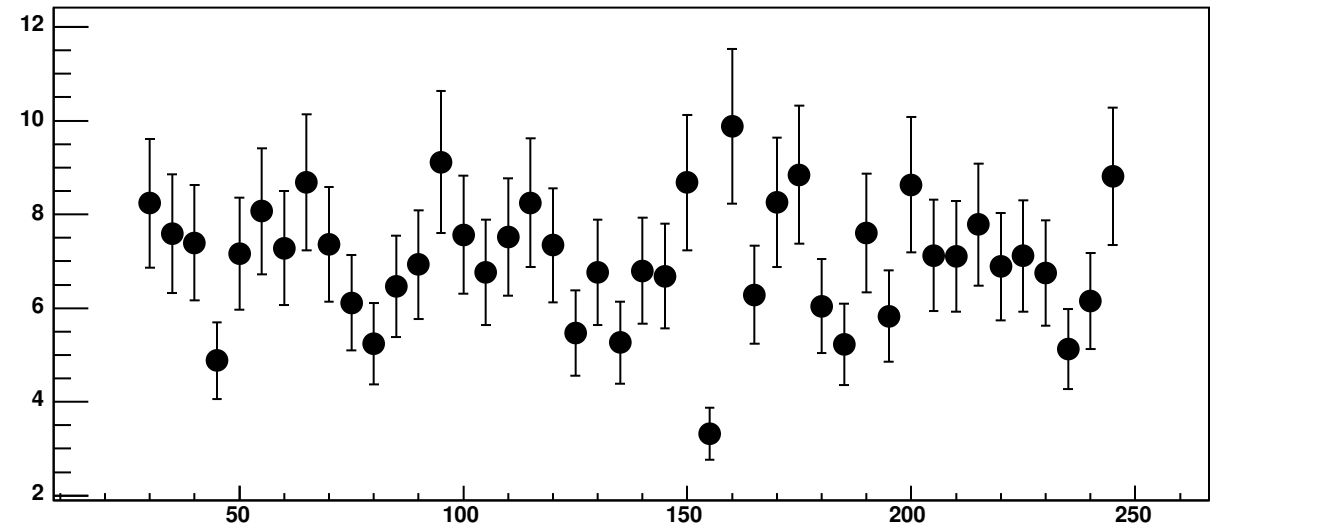
Chip 3, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold



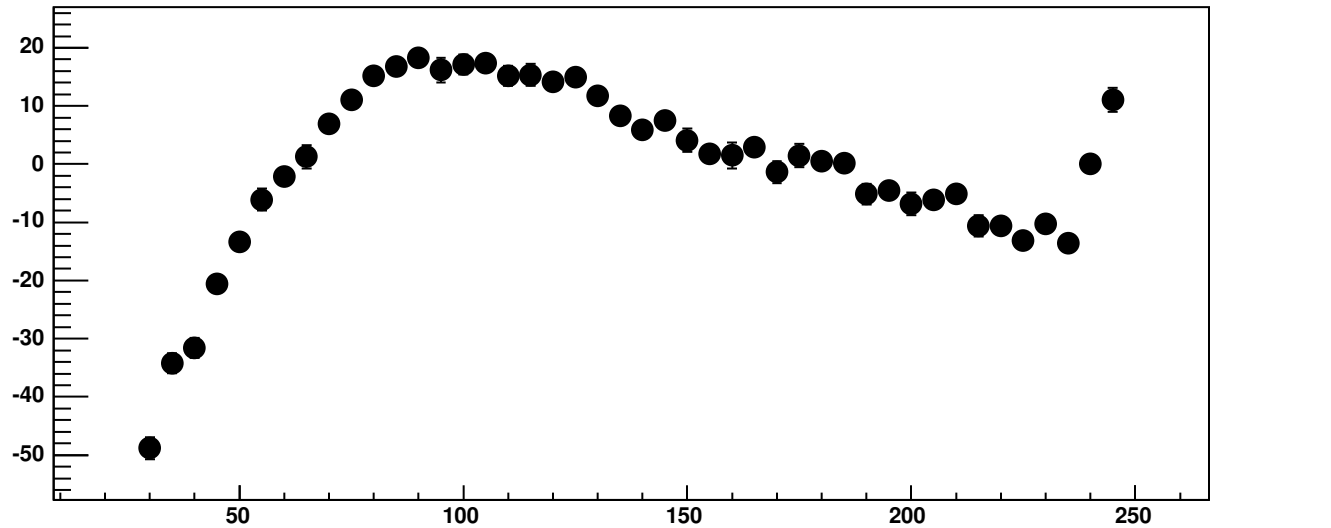
Chip 3, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold



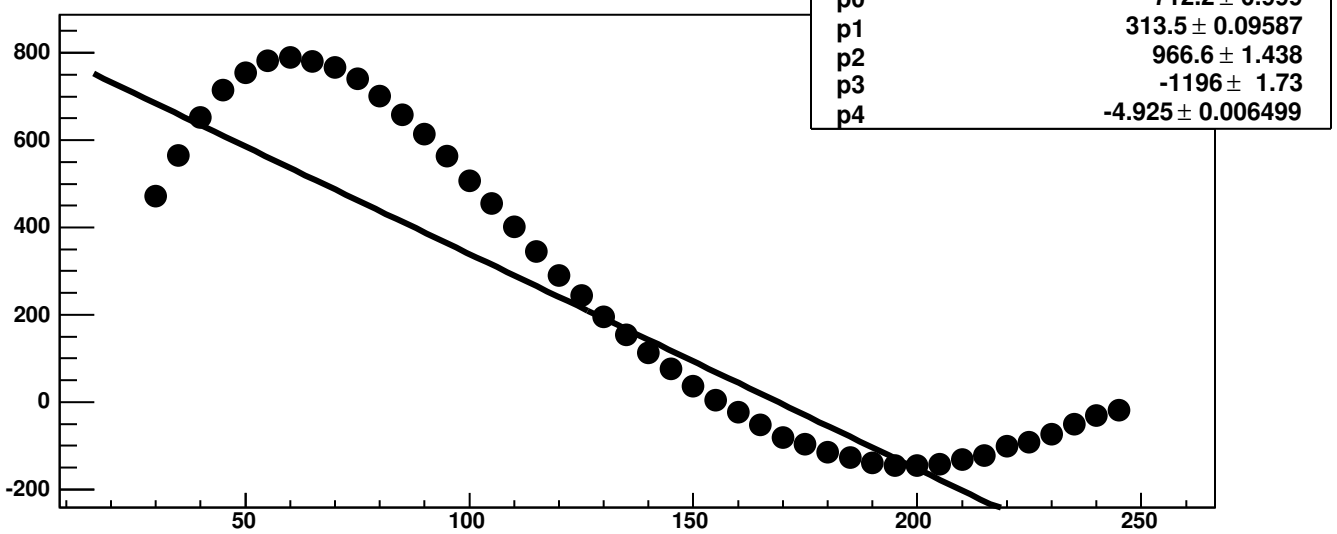
Chip 3, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



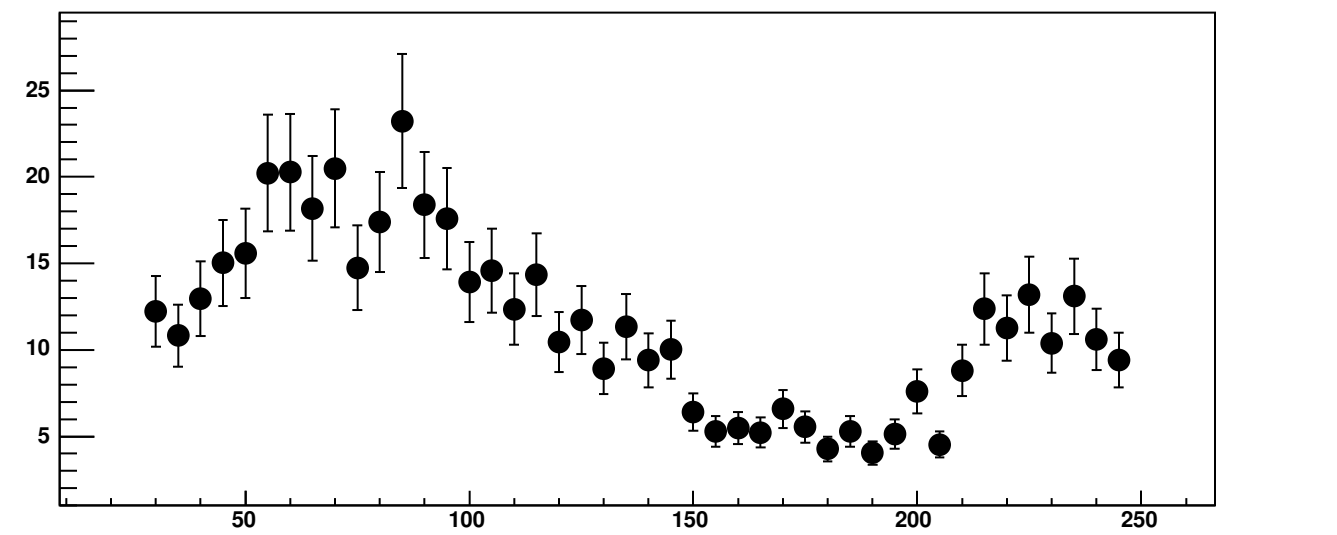
Chip 3, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold



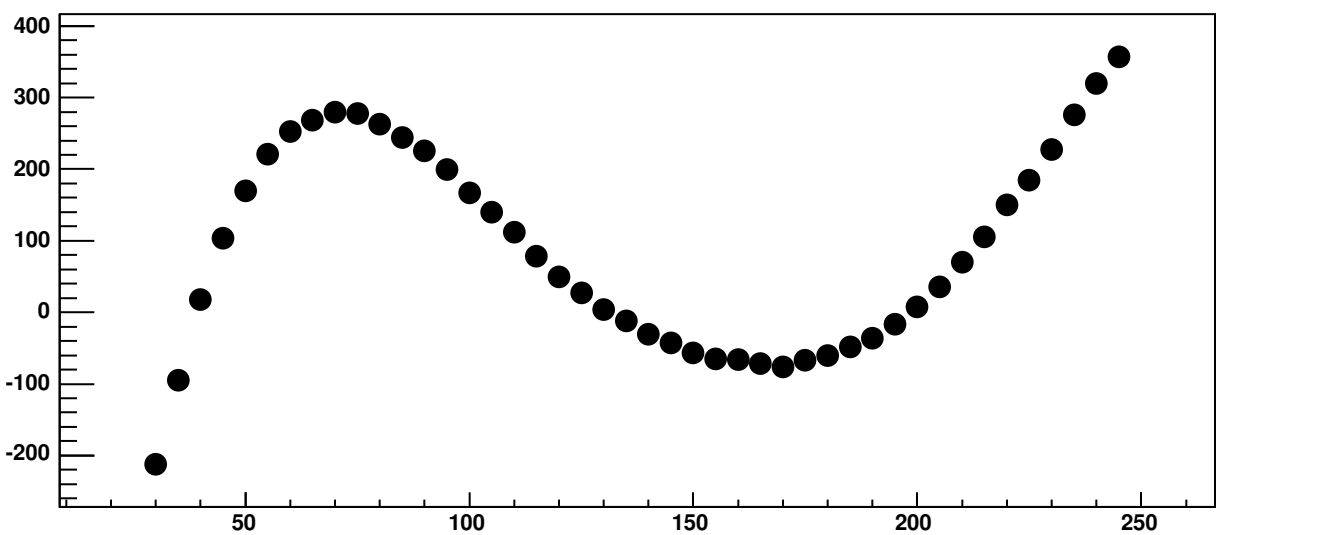
Chip 3, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold

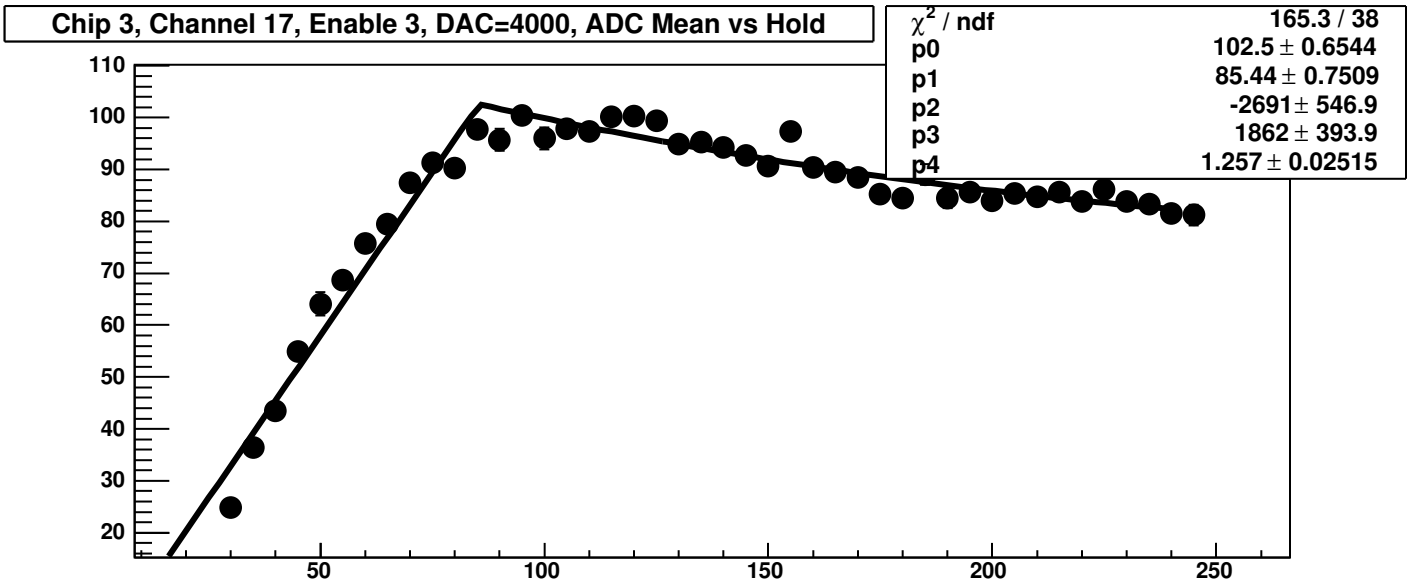


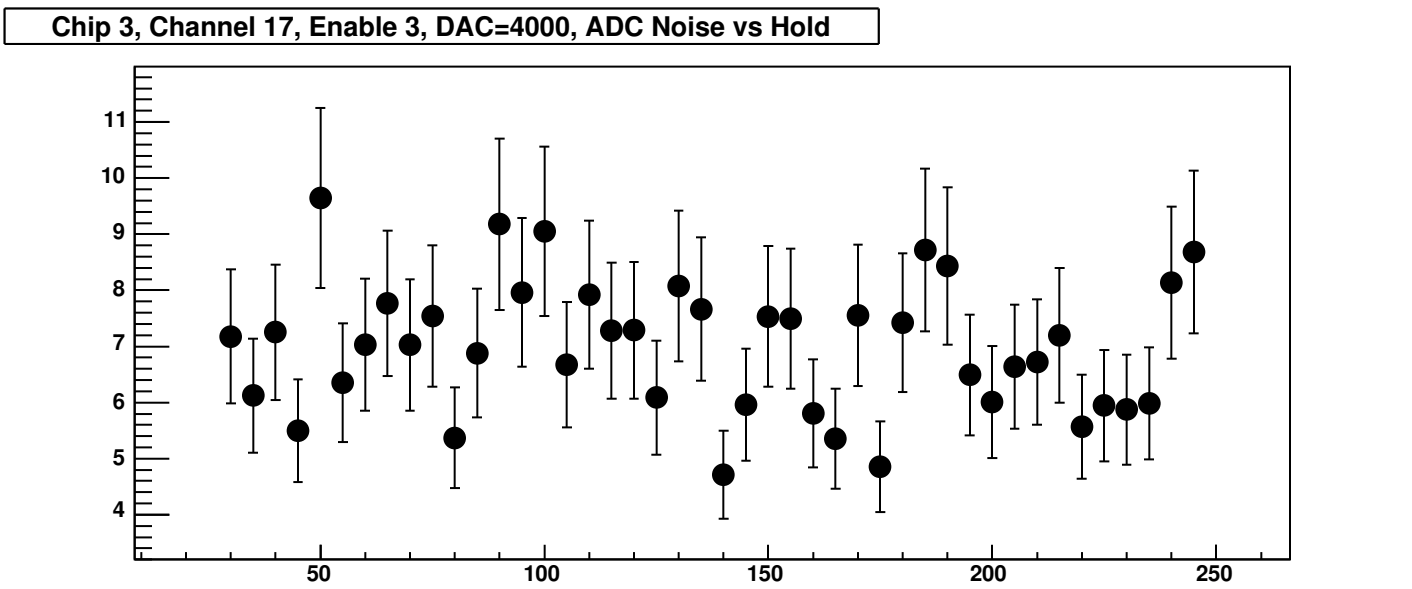
Chip 3, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold

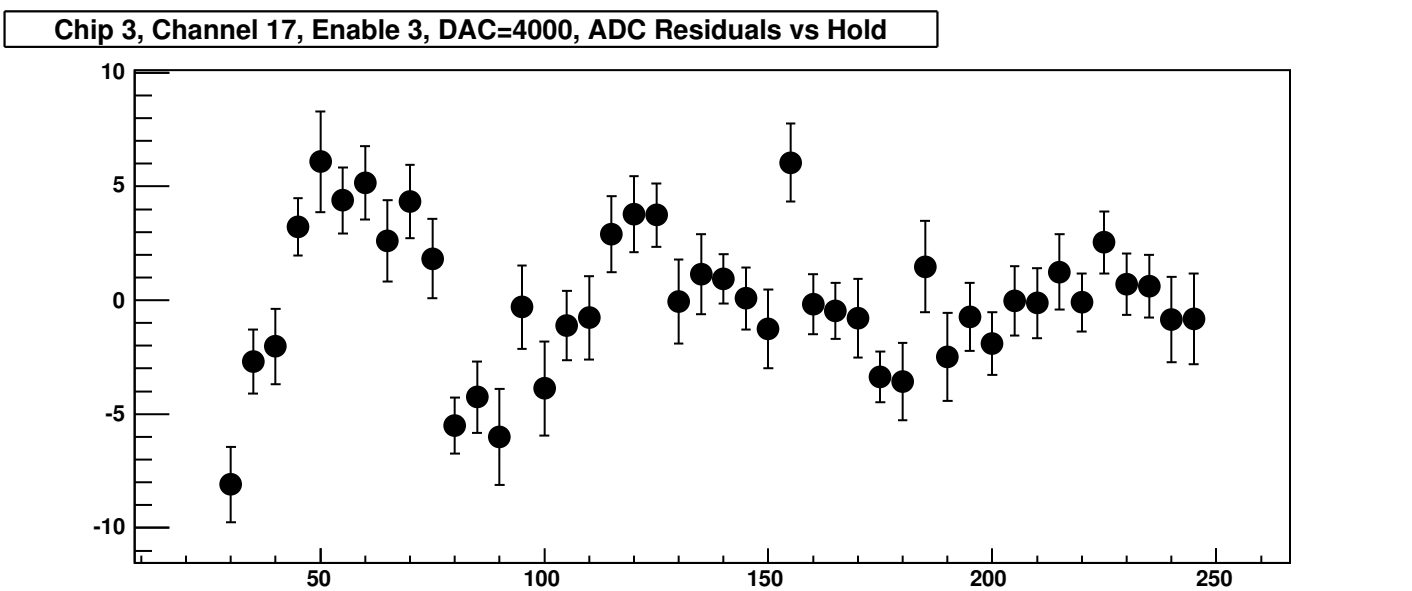


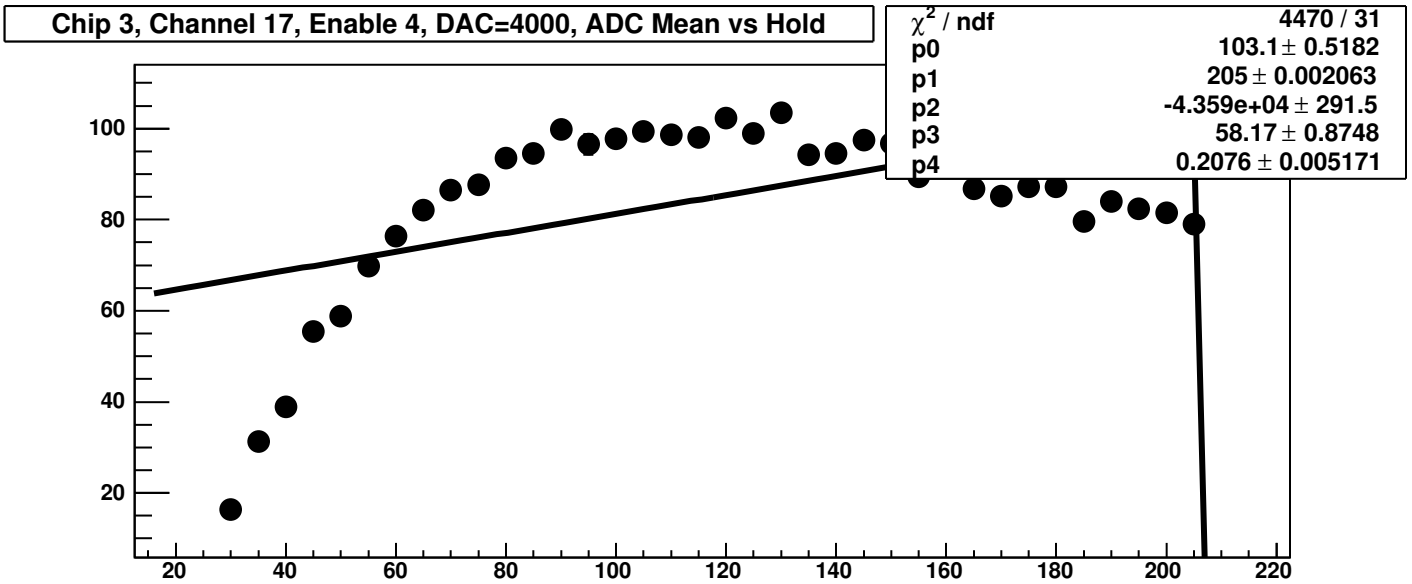
Chip 3, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold

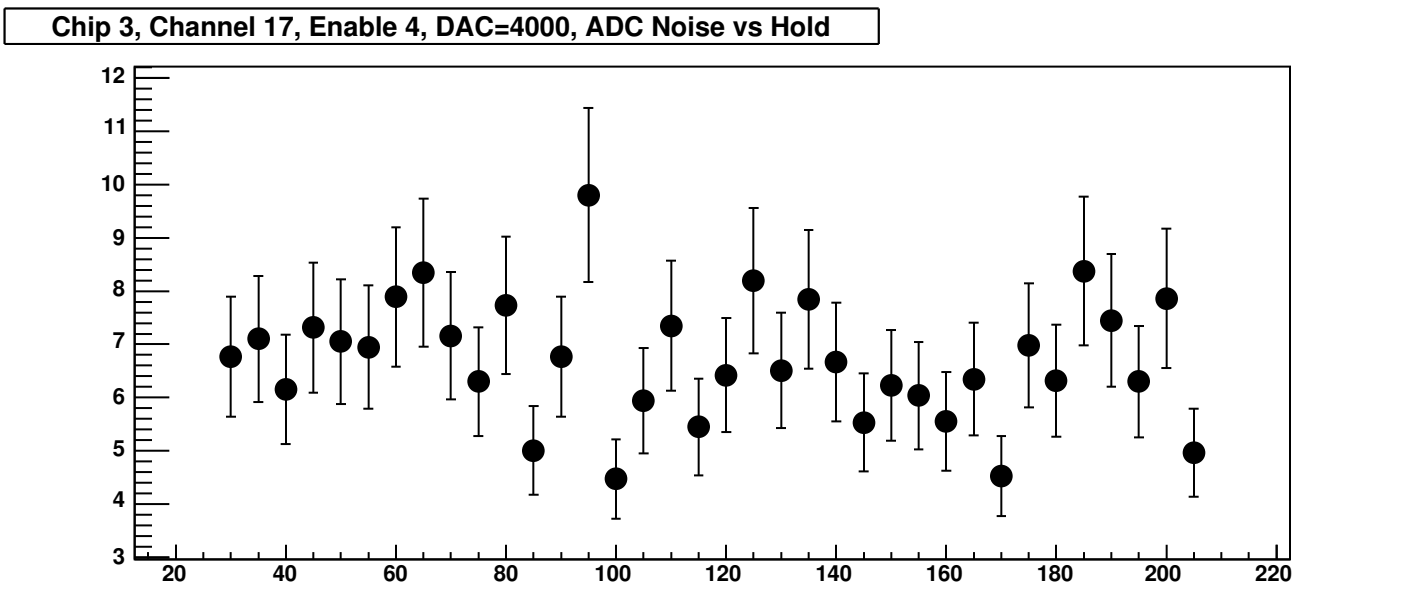


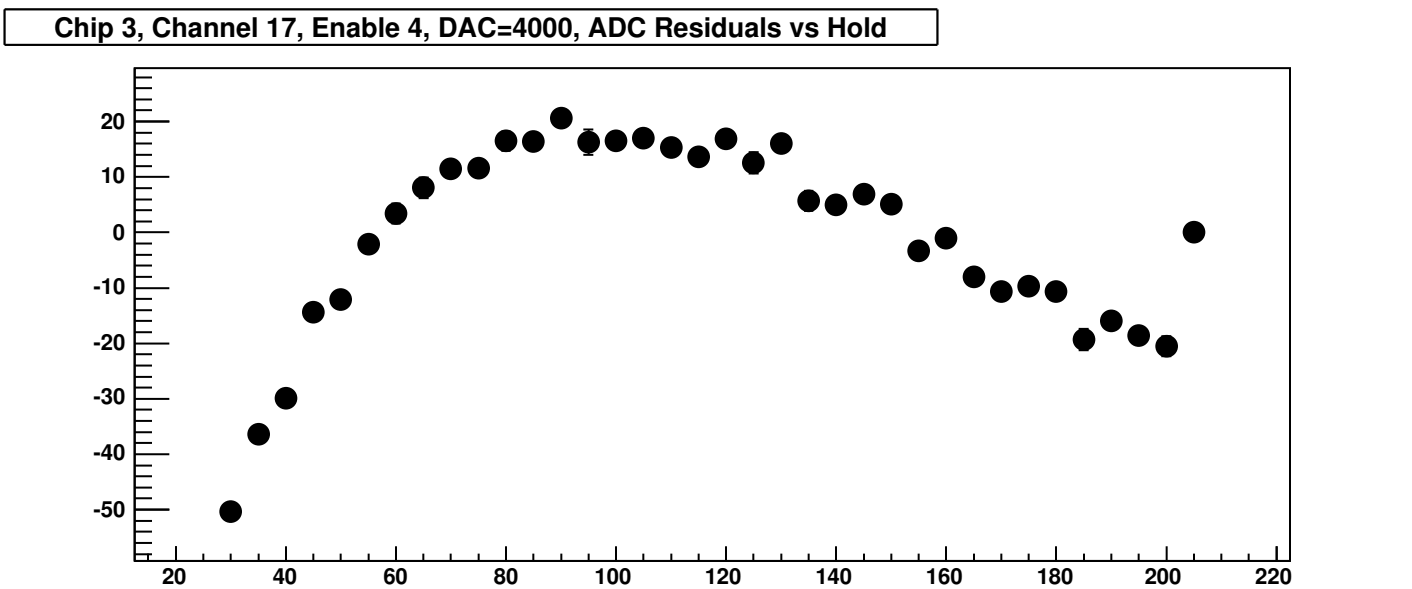

Chip 3, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 165.3 / 38
p0 102.5 ± 0.6544
p1 85.44 ± 0.7509
p2 -2691 ± 546.9
p3 1862 ± 393.9
p4 1.257 ± 0.02515


Chip 3, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold


Chip 3, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold

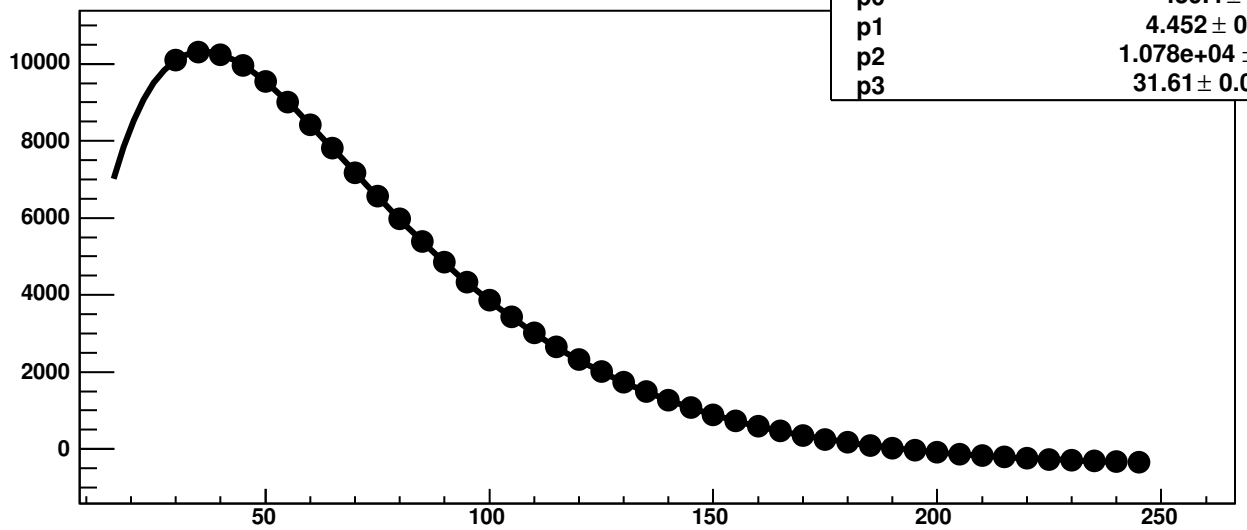

Chip 3, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 4470 / 31
p0 103.1 ± 0.5182
p1 205 ± 0.002063
p2 -4.359e+04 ± 291.5
p3 58.17 ± 0.8748
p4 0.2076 ± 0.005171


Chip 3, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold

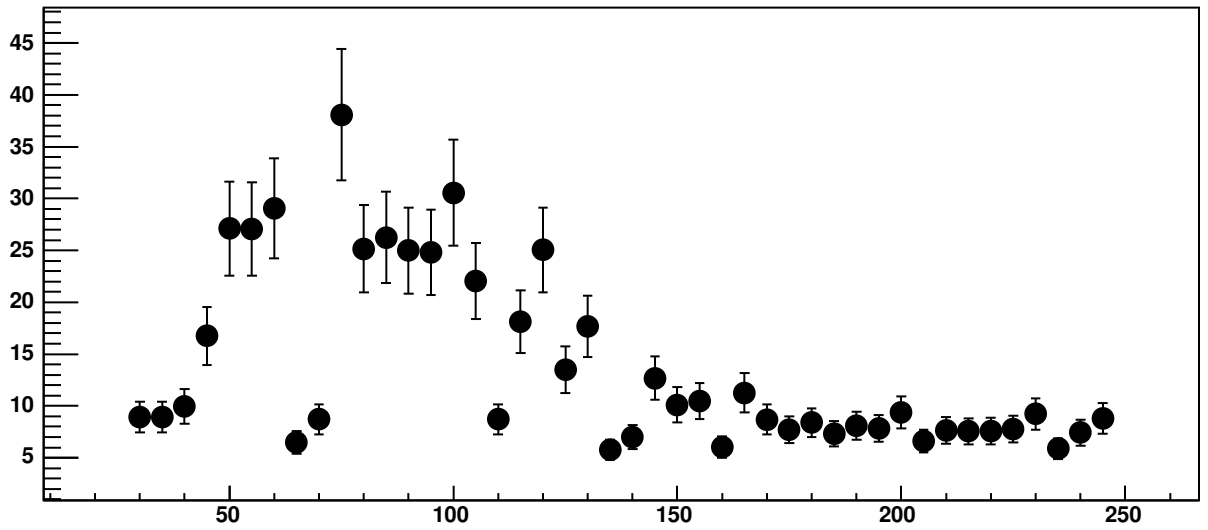

Chip 3, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

Chip 4, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold

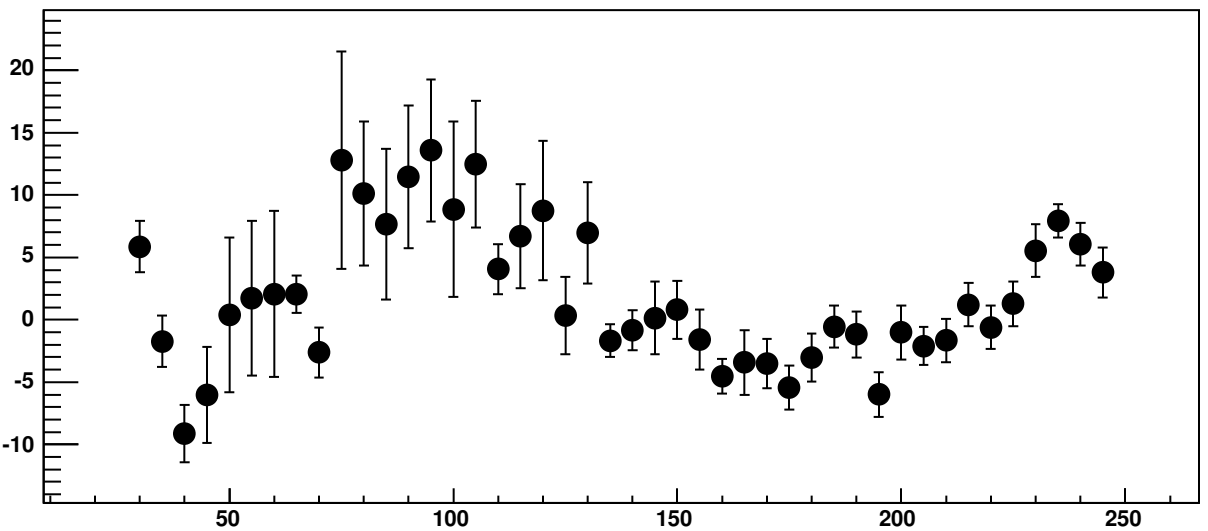
χ^2 / ndf	167.2 / 39
p0	-459.4 ± 0.7039
p1	4.452 ± 0.02077
p2	1.078e+04 ± 1.201
p3	31.61 ± 0.009518



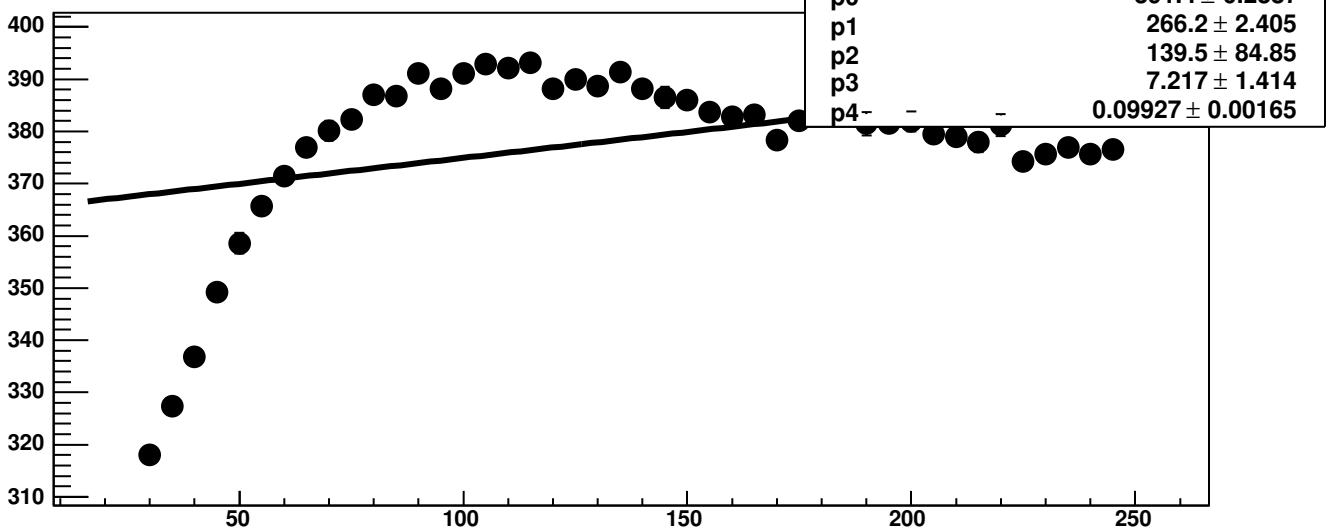
Chip 4, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold



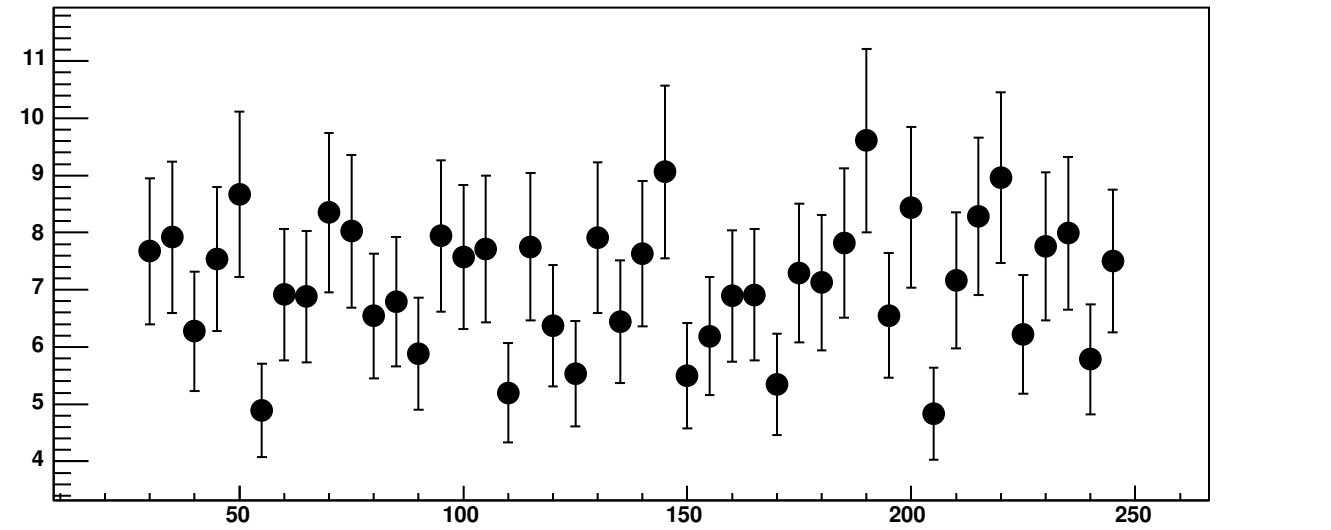
Chip 4, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold



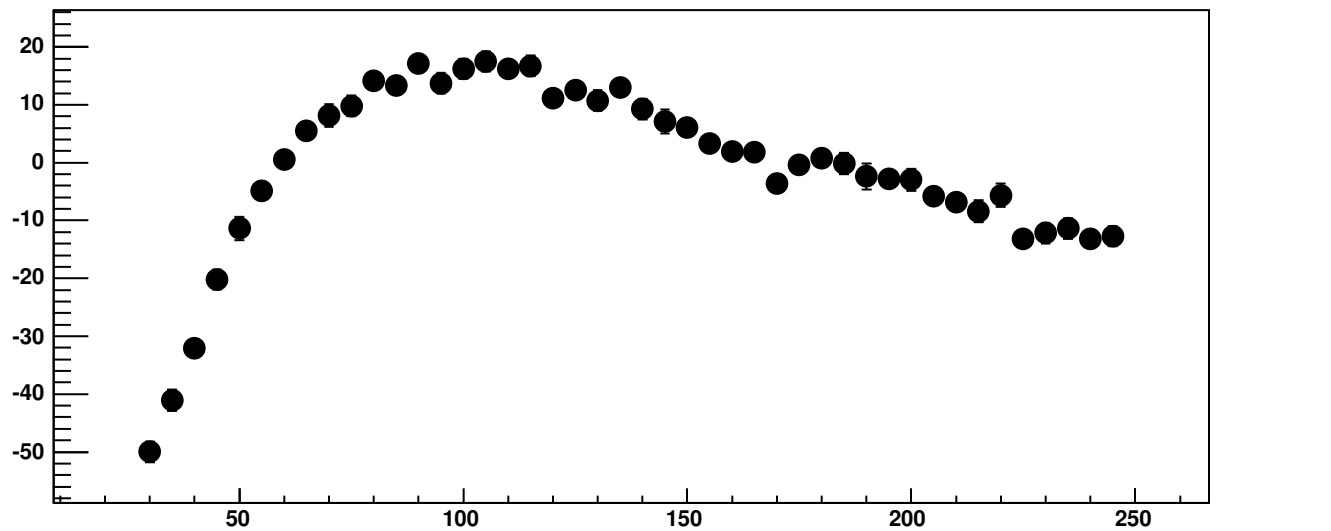
Chip 4, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold



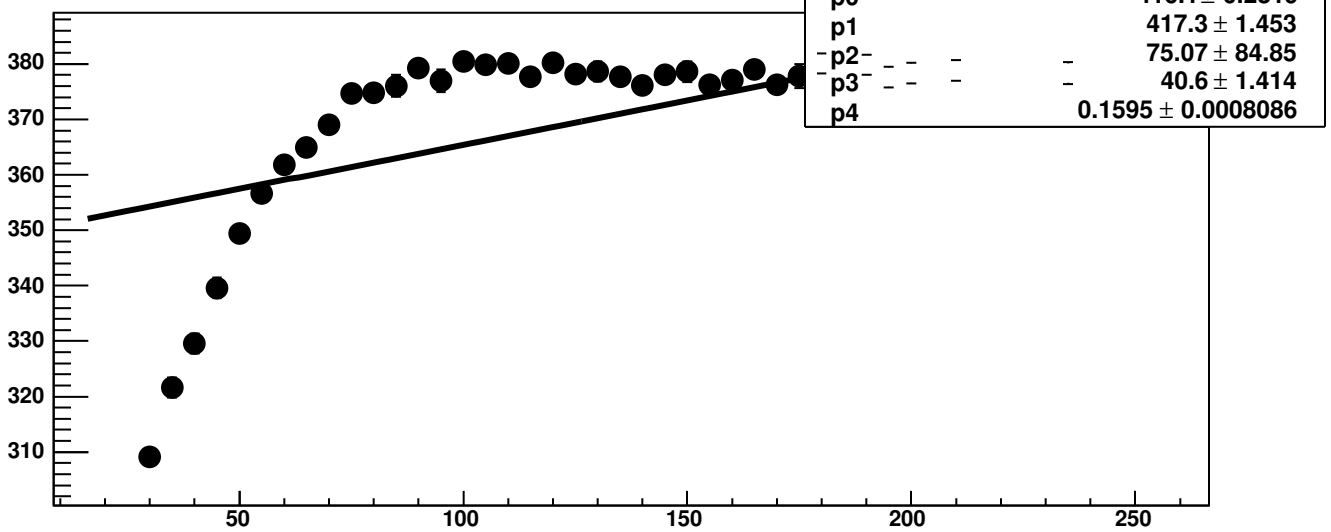
Chip 4, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



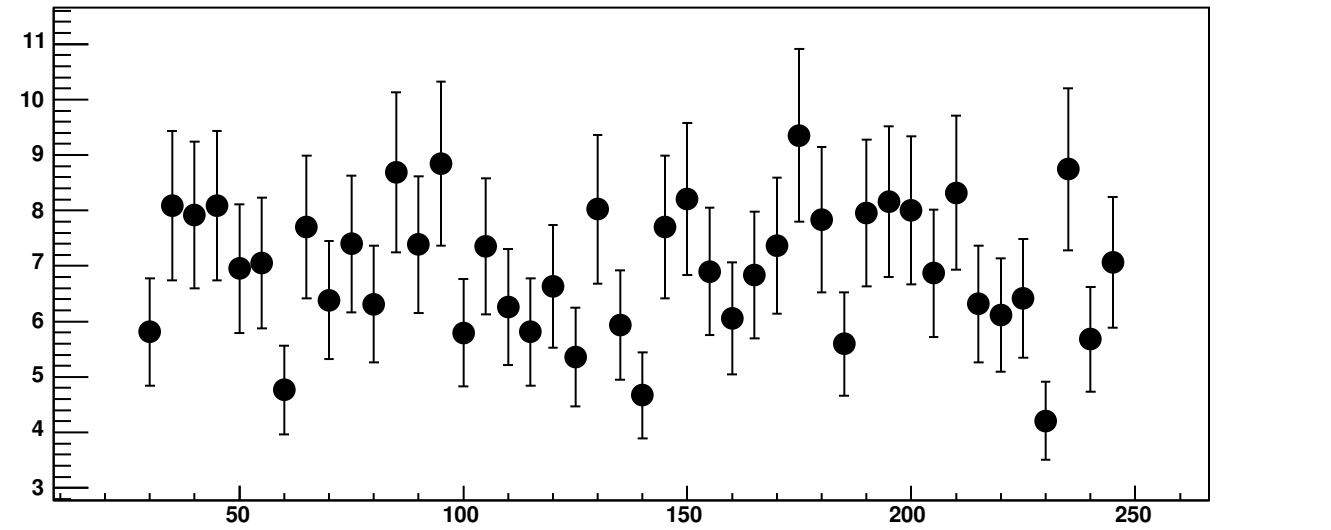
Chip 4, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold



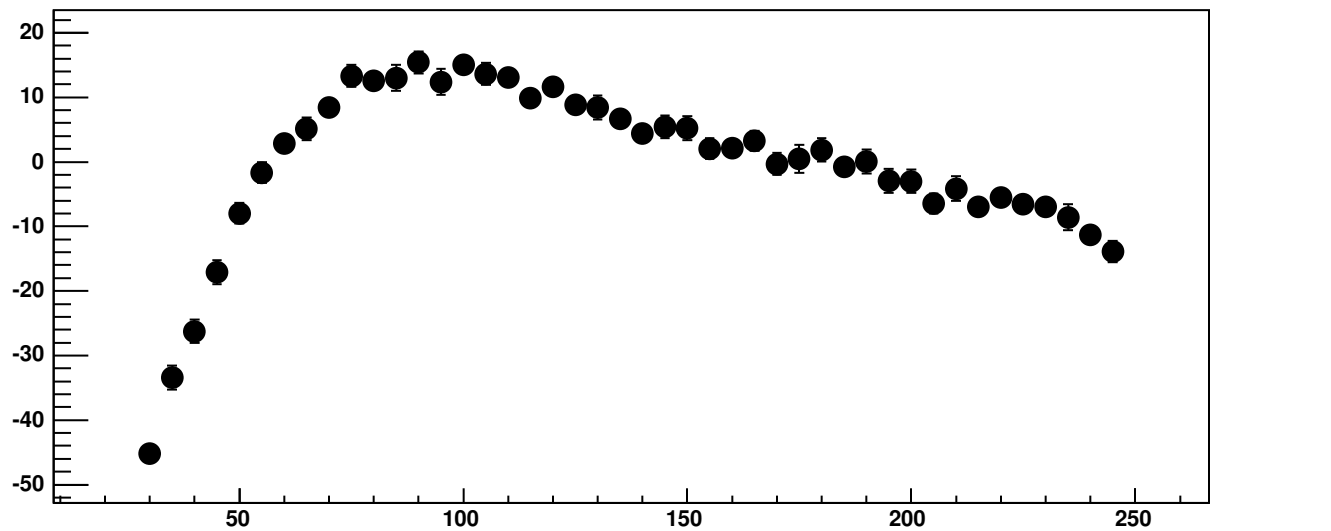
Chip 4, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold



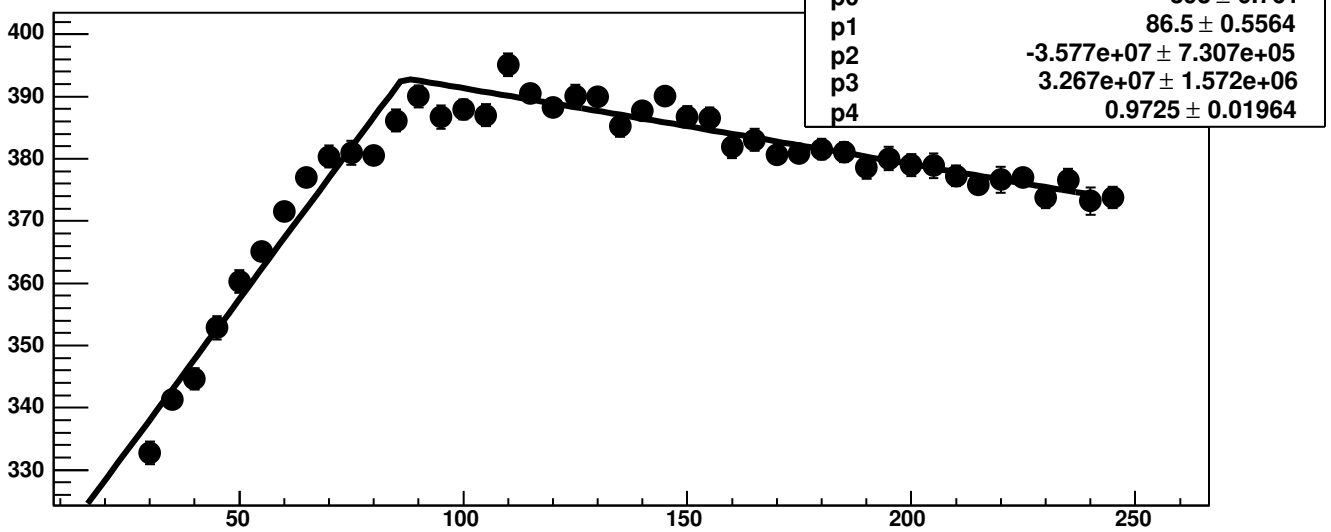
Chip 4, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



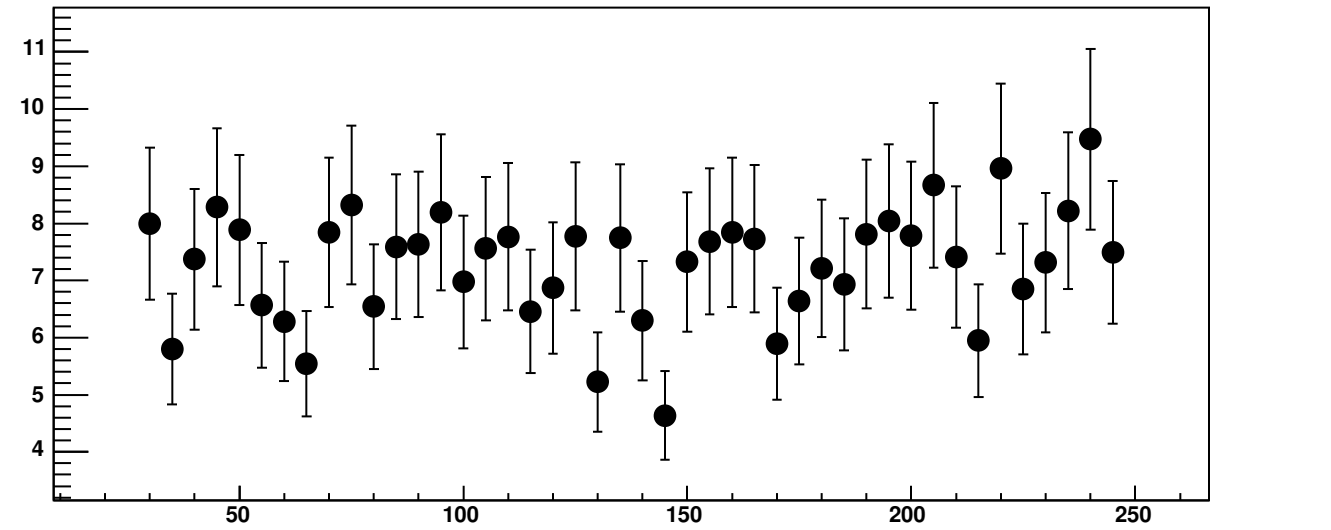
Chip 4, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold



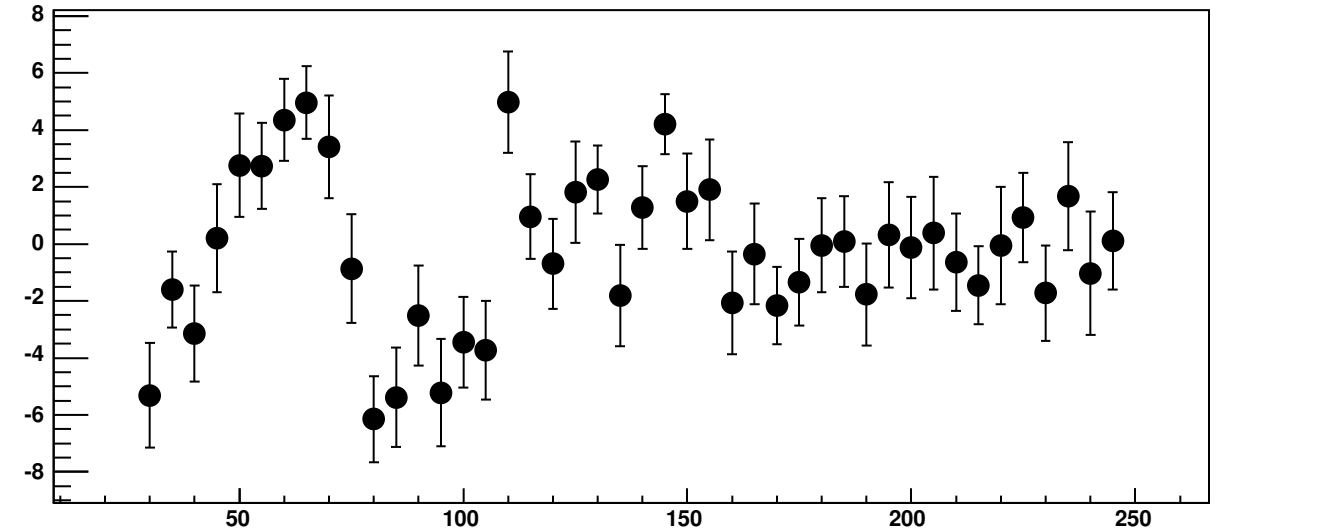
Chip 4, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold

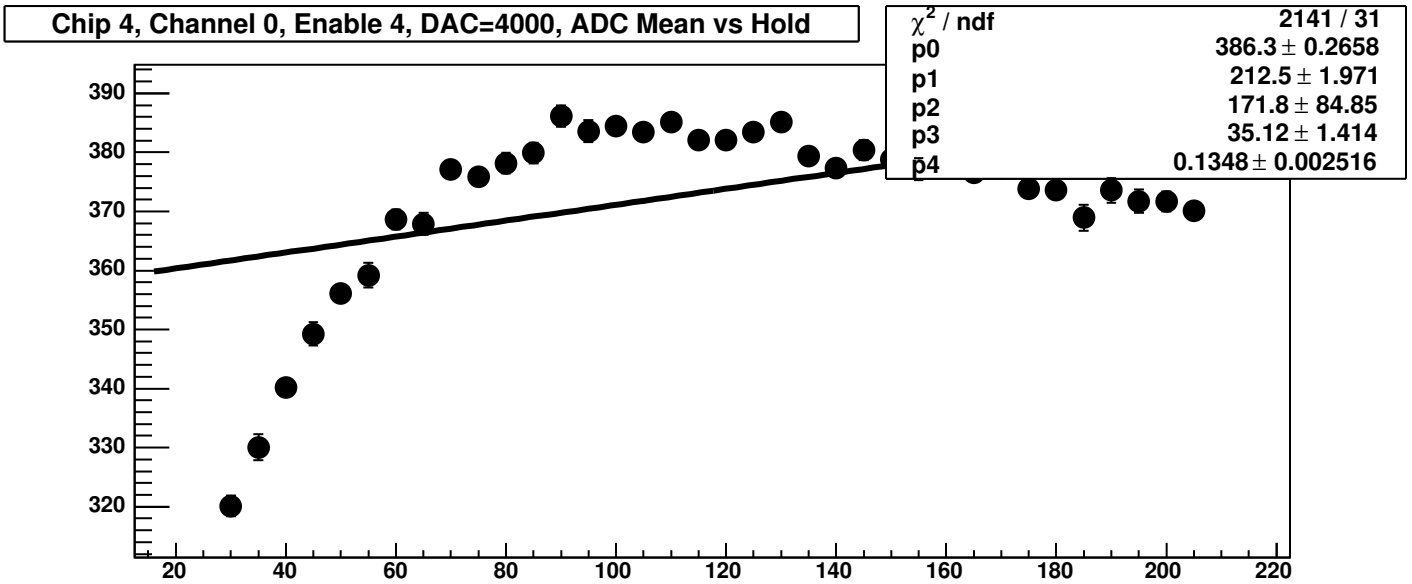


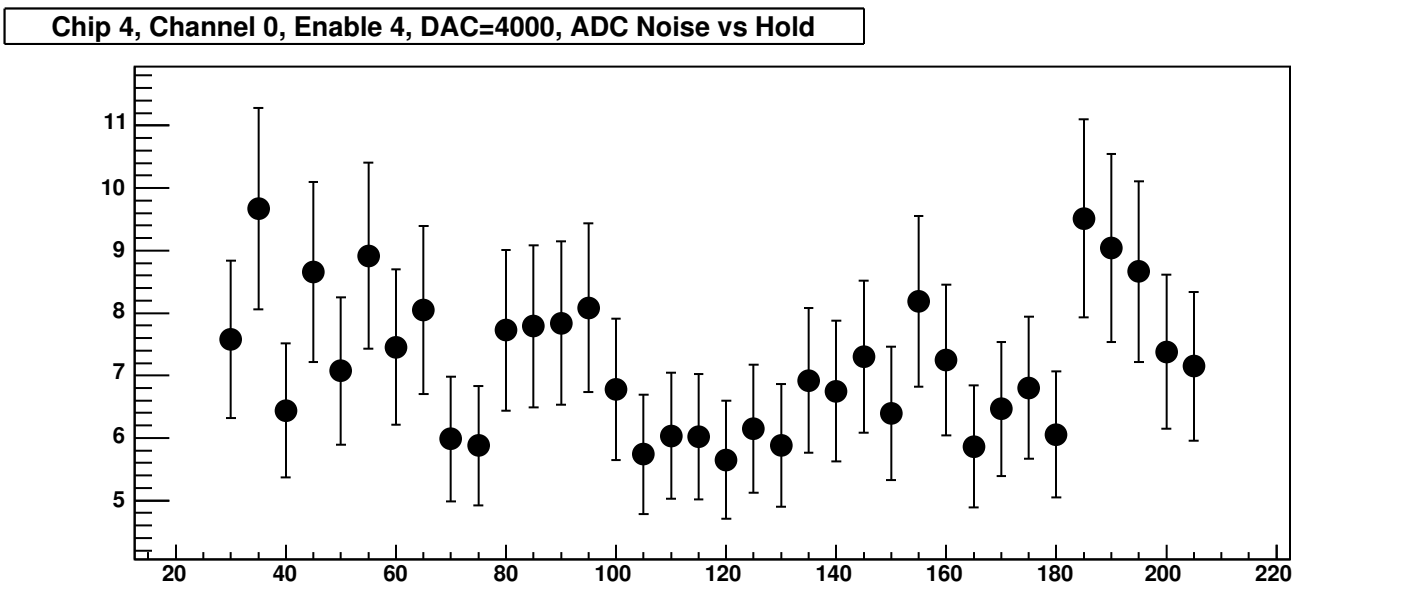
Chip 4, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold

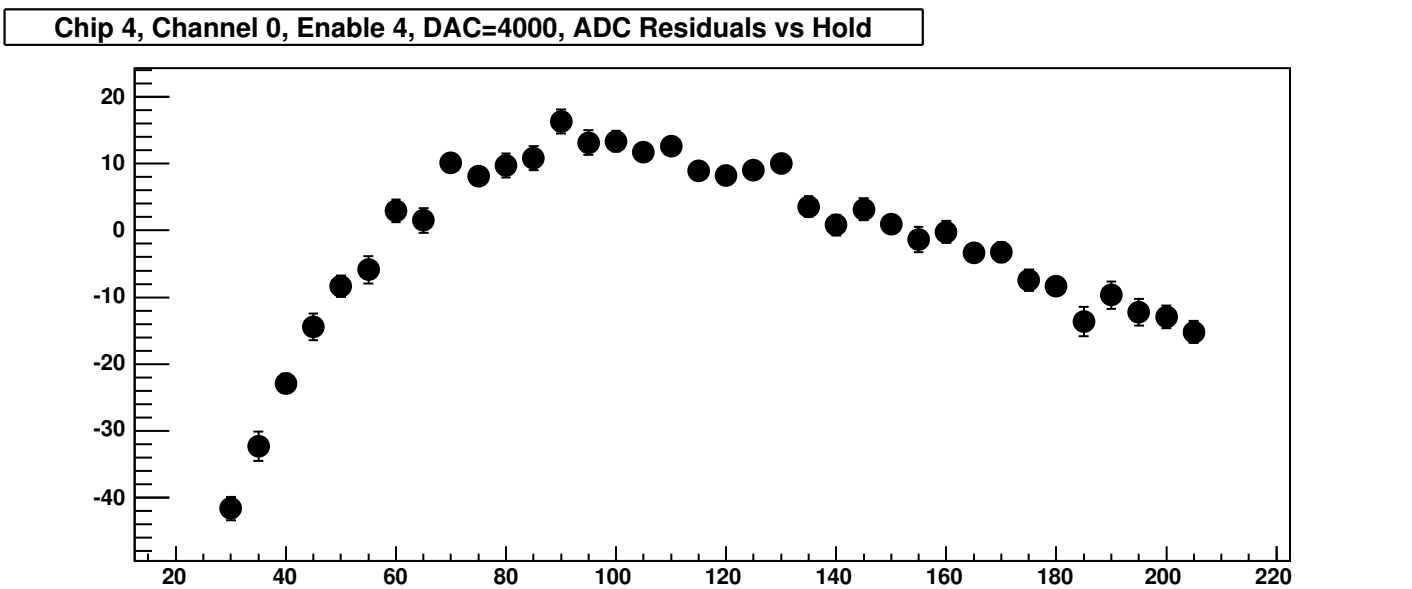


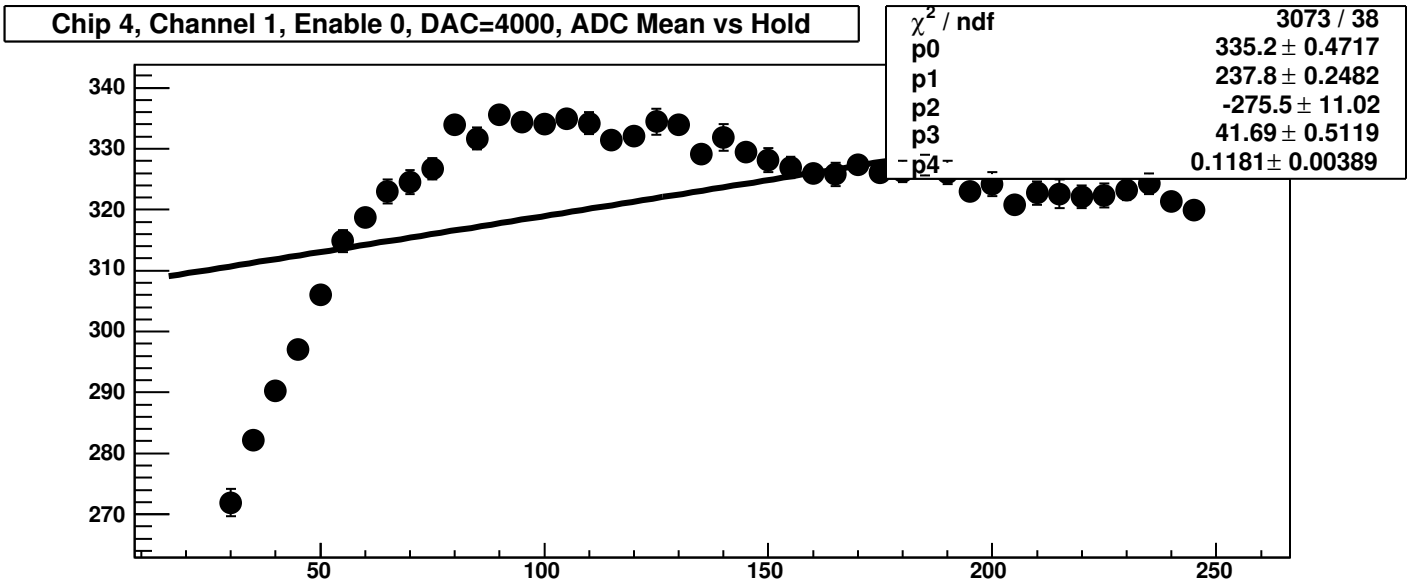
Chip 4, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold

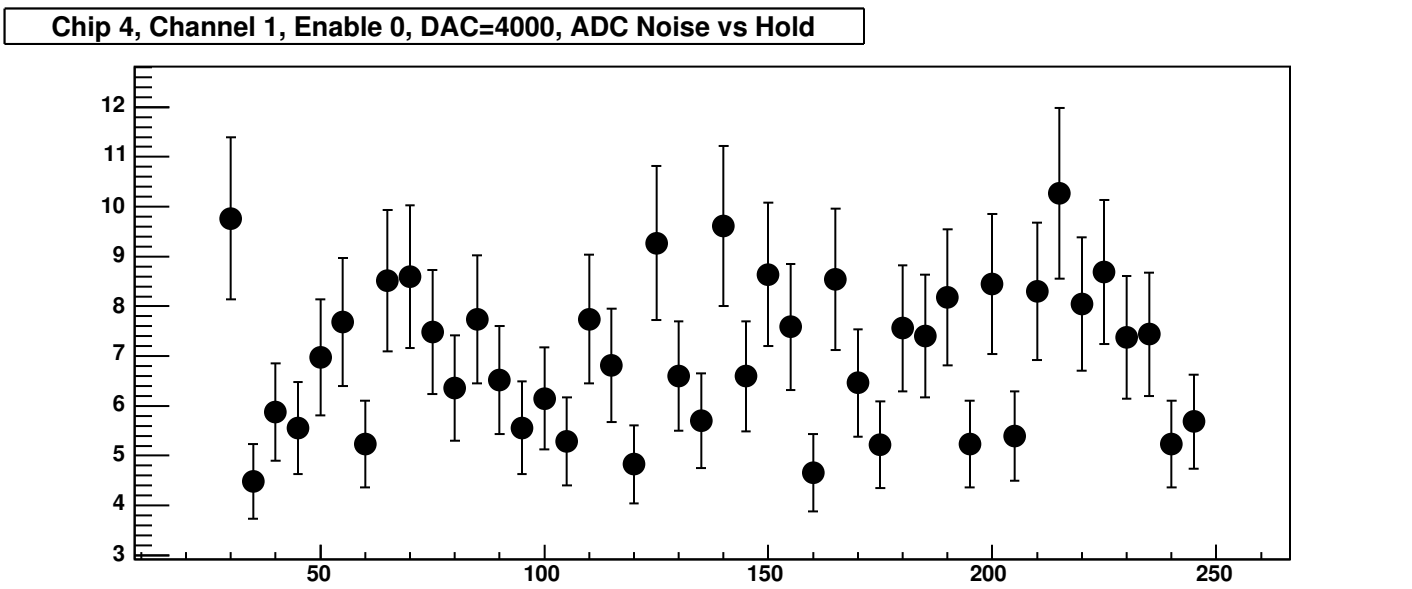


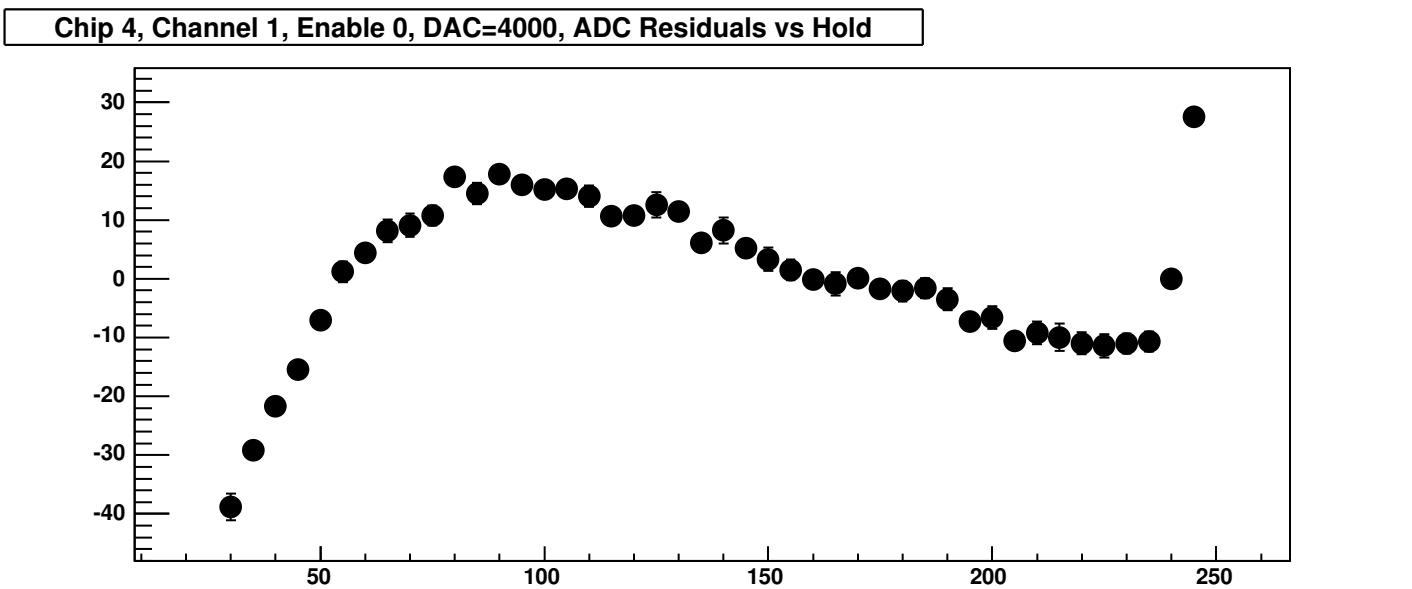

Chip 4, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 2141 / 31
p0 386.3 ± 0.2658
p1 212.5 ± 1.971
p2 171.8 ± 84.85
p3 35.12 ± 1.414
p4 0.1348 ± 0.002516
390
380
370
360
350
340
330
320
20
40
60
80
100
120
140
160
180
200
220


Chip 4, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
20
40
60
80
100
120
140
160
180
200
220

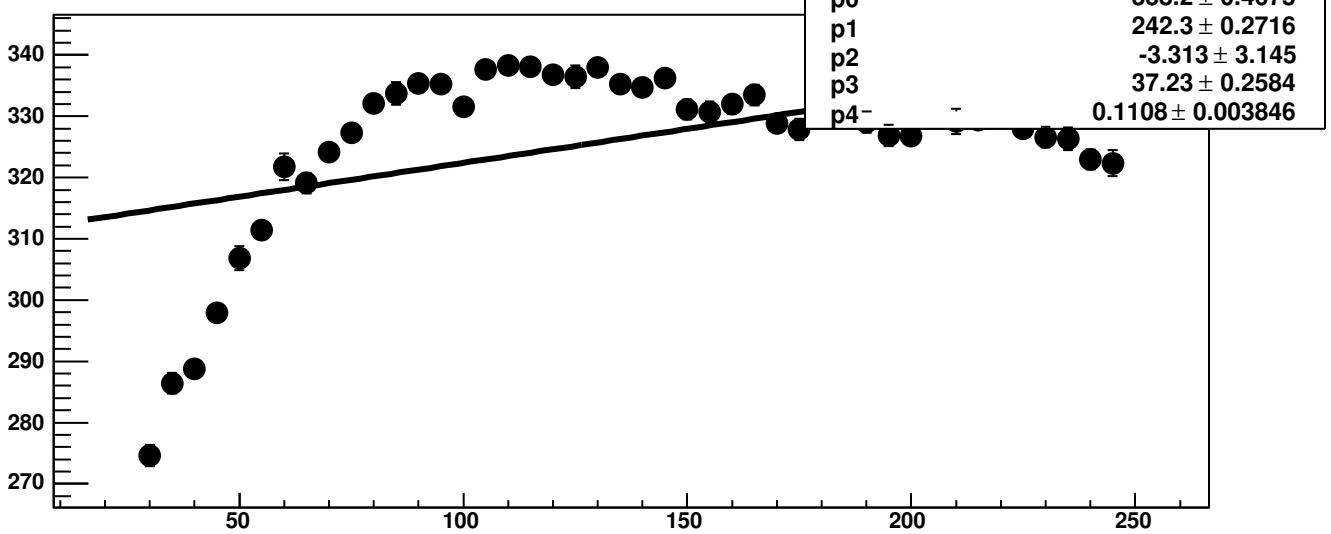

Chip 4, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
20
40
60
80
100
120
140
160
180
200
220


Chip 4, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3073 / 38
p0 335.2 ± 0.4717
p1 237.8 ± 0.2482
p2 -275.5 ± 11.02
p3 41.69 ± 0.5119
p4 0.1181 ± 0.00389
340
330
320
310
300
290
280
270
50
100
150
200
250

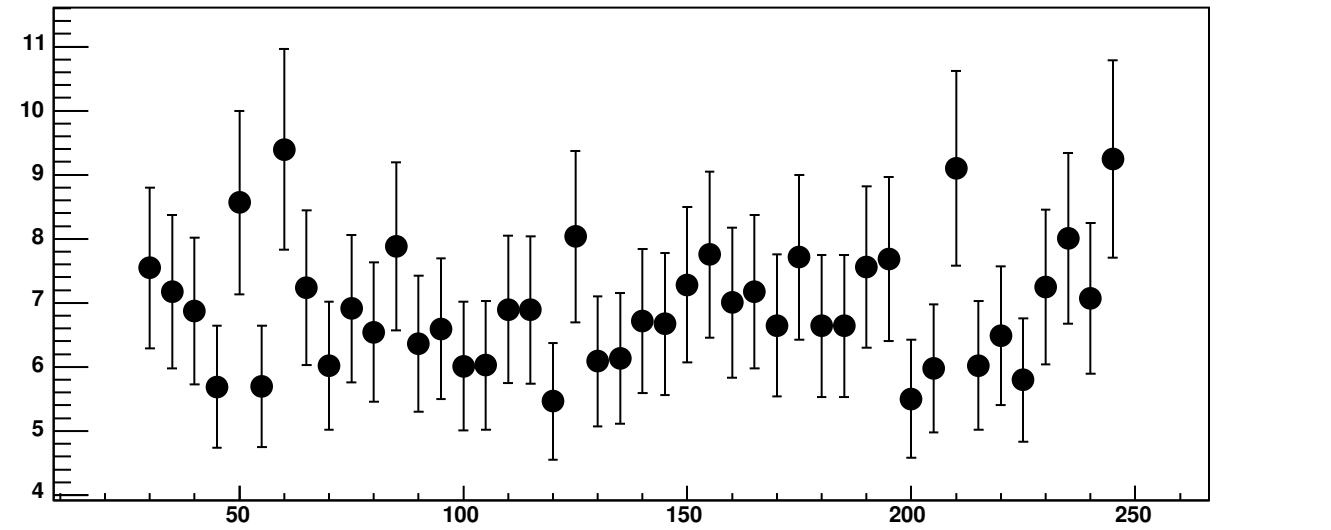

Chip 4, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250


Chip 4, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
-20
-30
-40
50
100
150
200
250

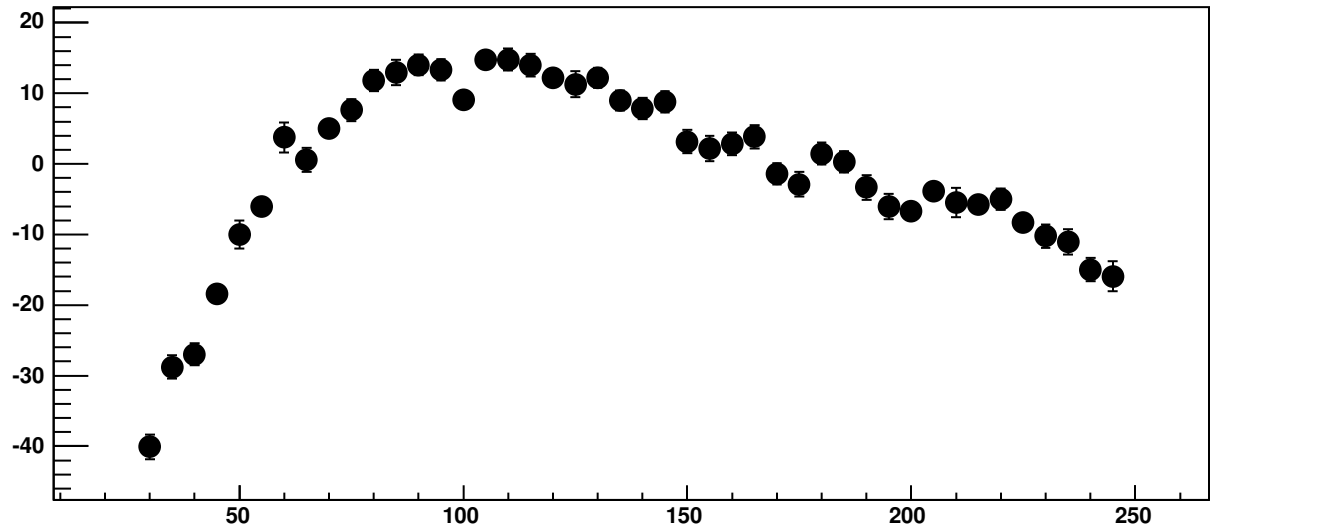
Chip 4, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold



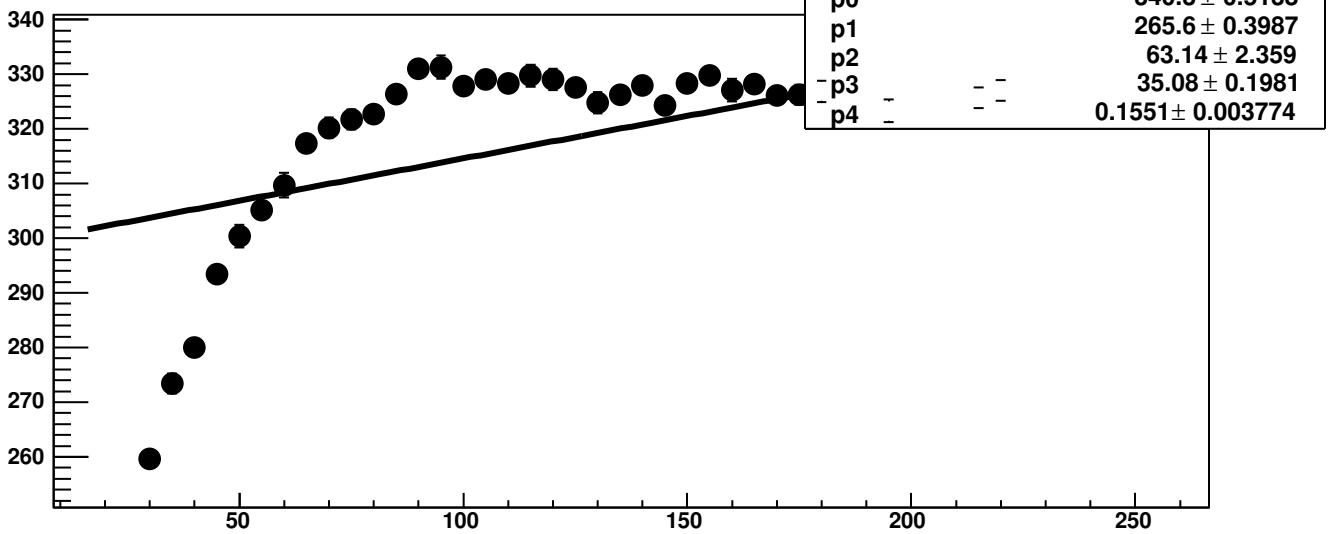
Chip 4, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



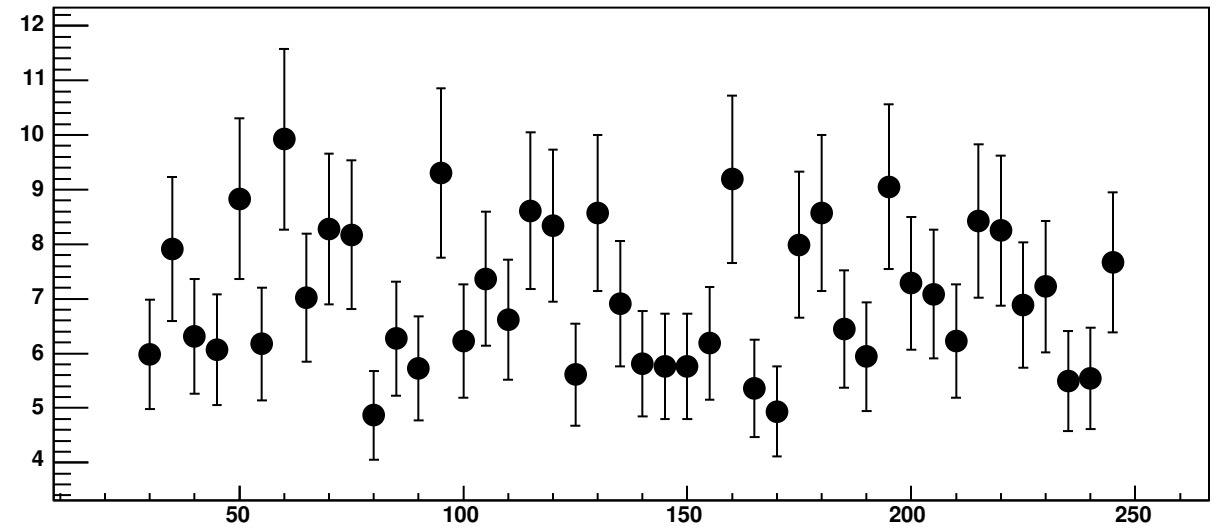
Chip 4, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold



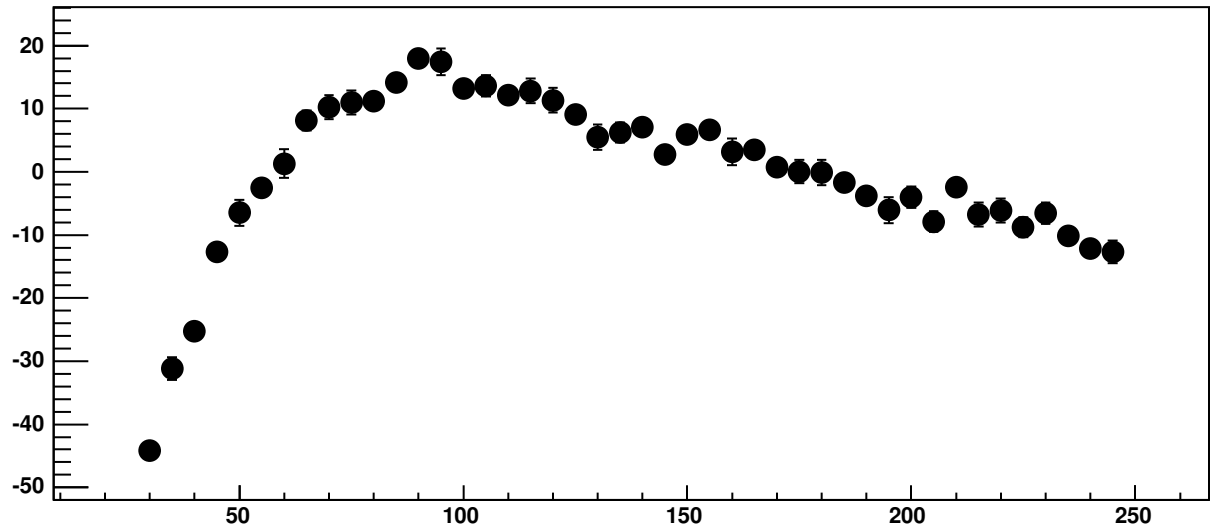
Chip 4, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

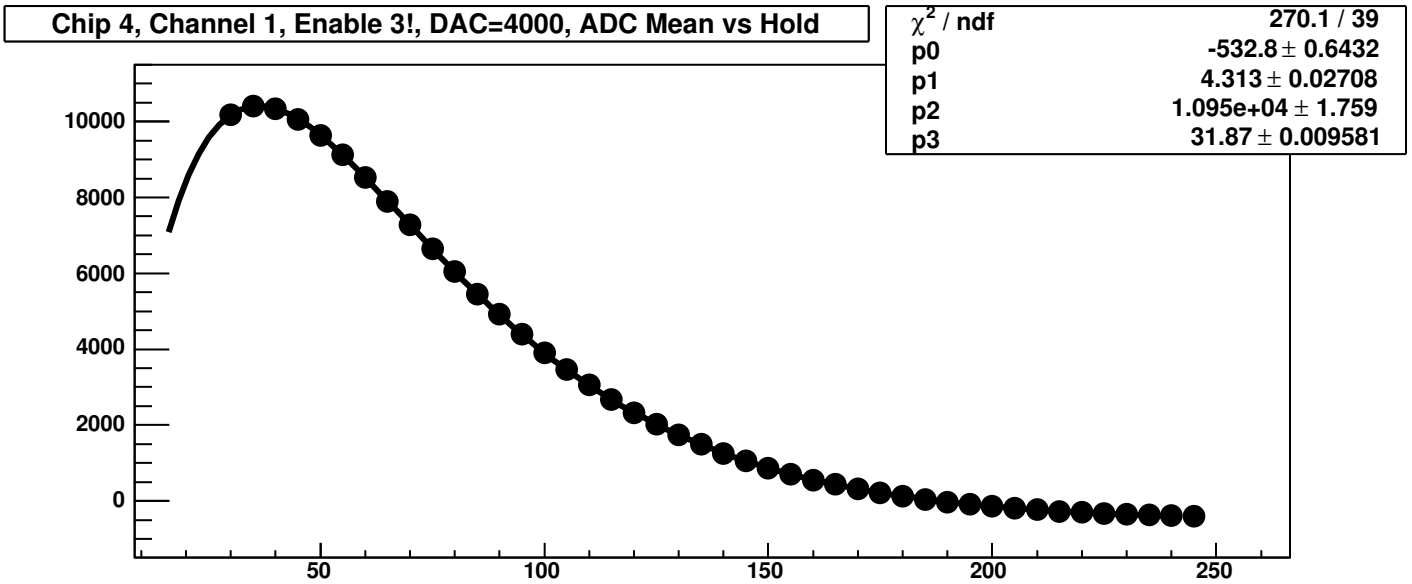


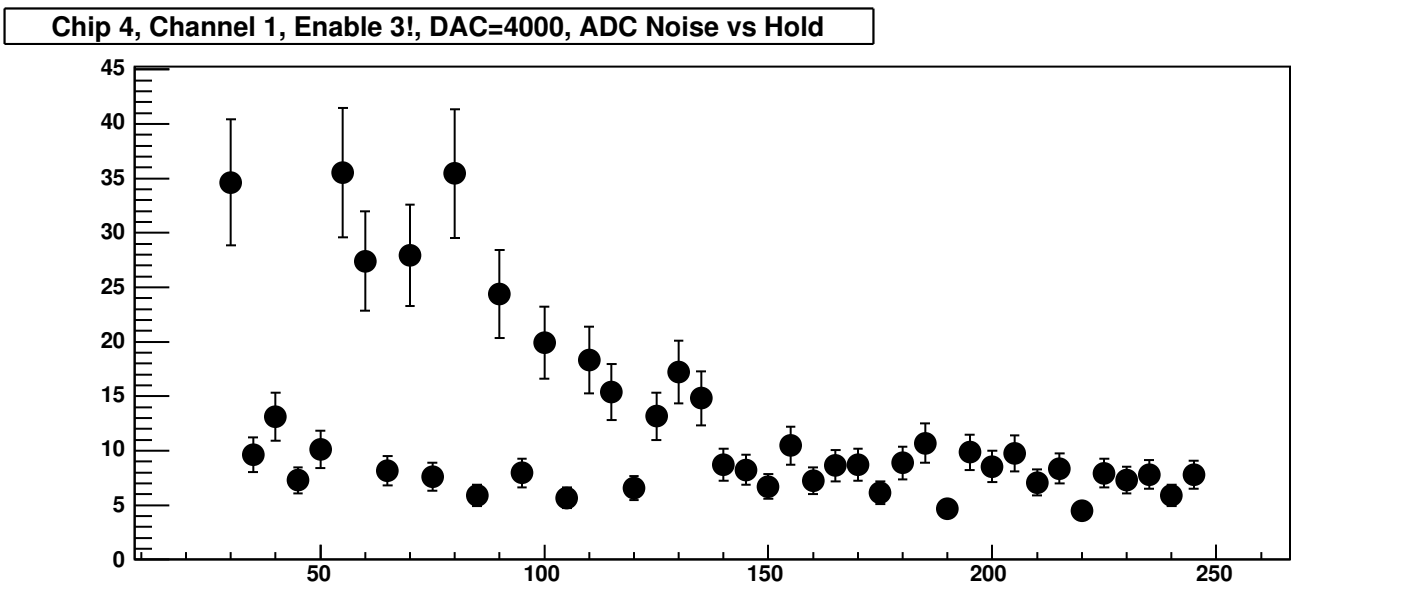
Chip 4, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold

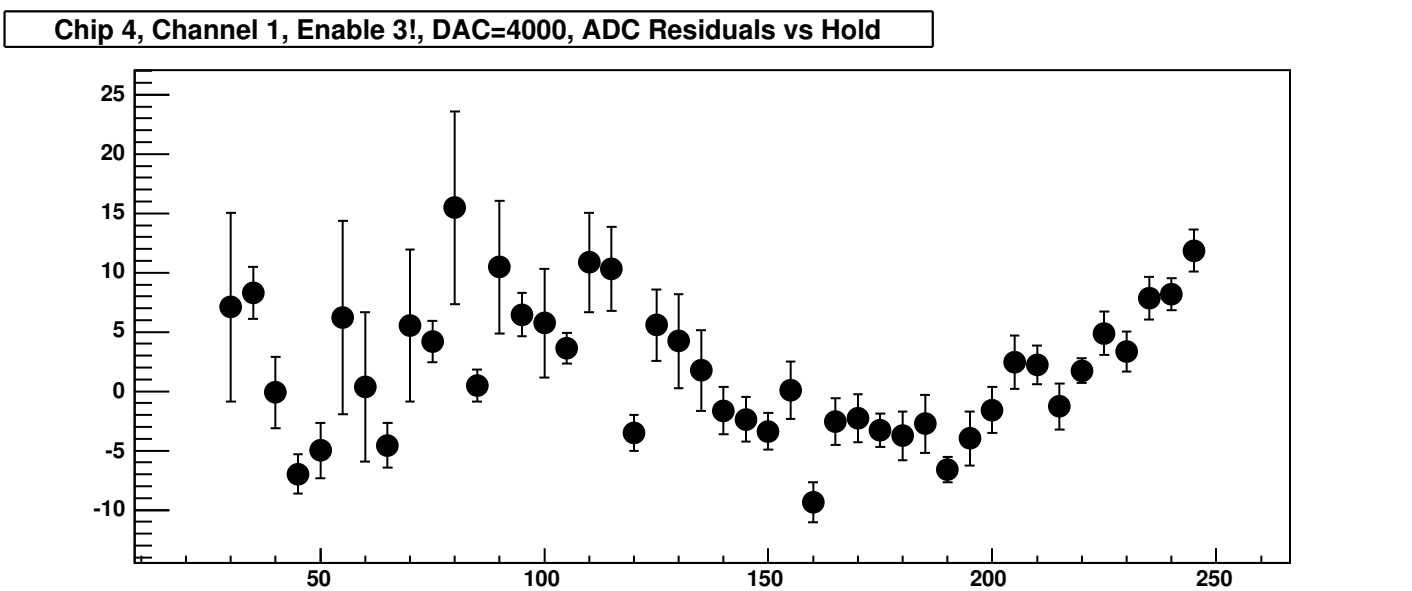


Chip 4, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

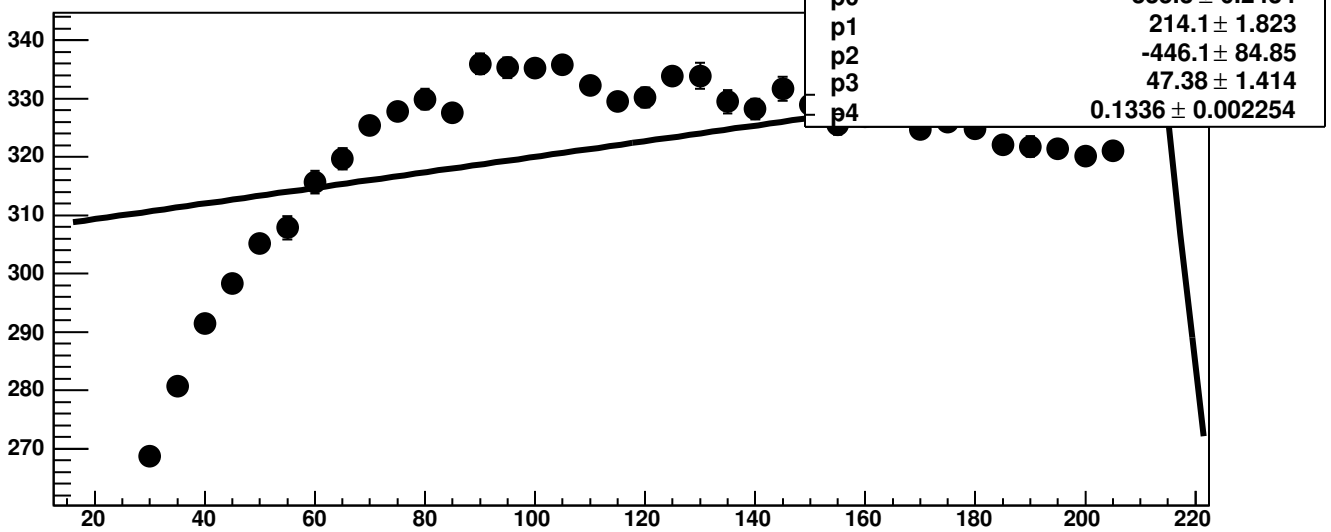



Chip 4, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 270.1 / 39
p0 -532.8 ± 0.6432
p1 4.313 ± 0.02708
p2 1.095e+04 ± 1.759
p3 31.87 ± 0.009581
10000
8000
6000
4000
2000
0
50
100
150
200
250

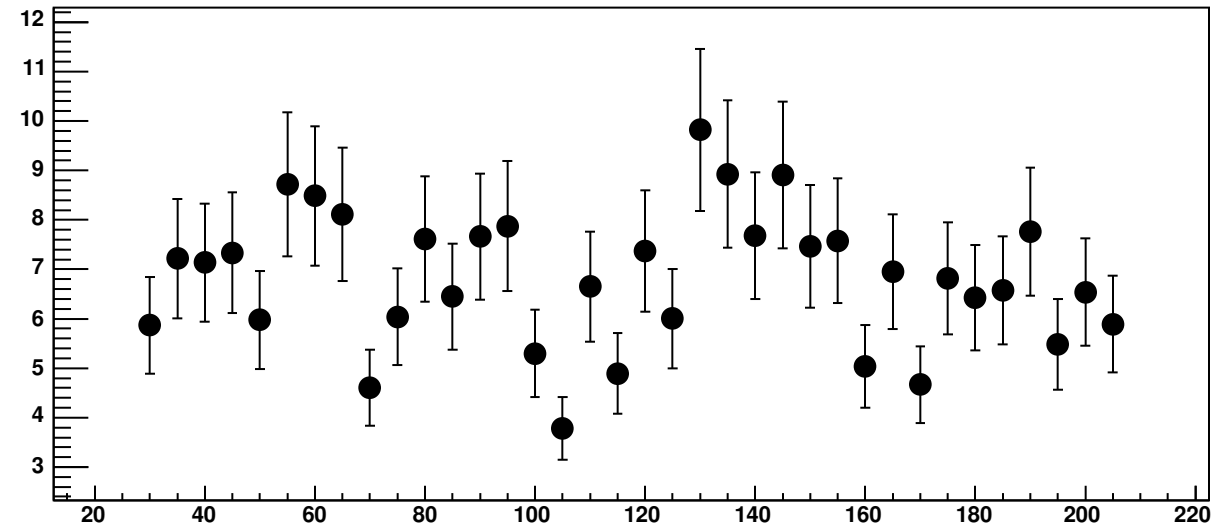

Chip 4, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold
45
40
35
30
25
20
15
10
5
0
50
100
150
200
250


Chip 4, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
50
100
150
200
250

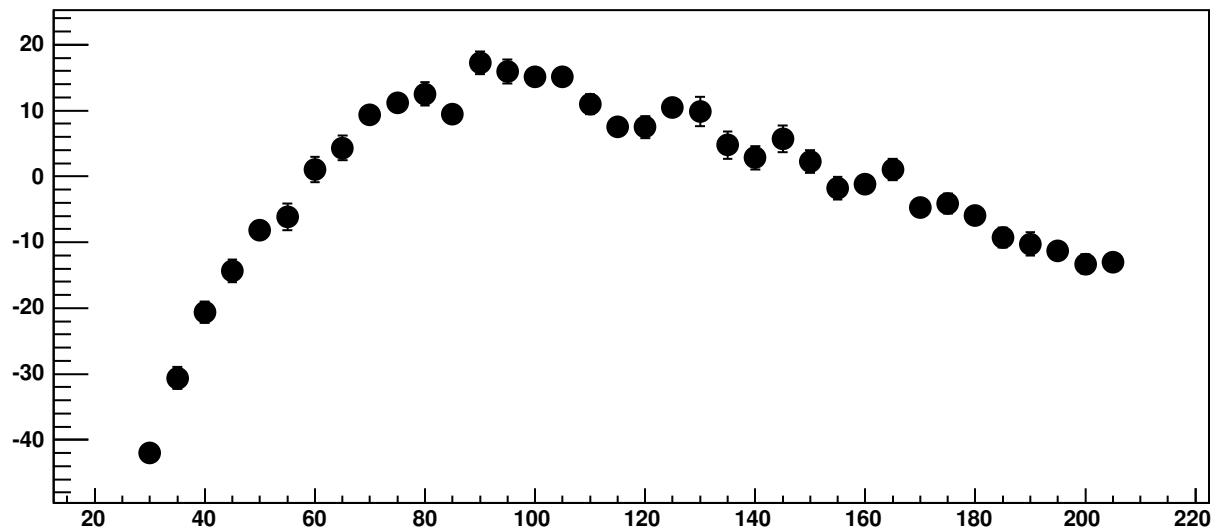
Chip 4, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold

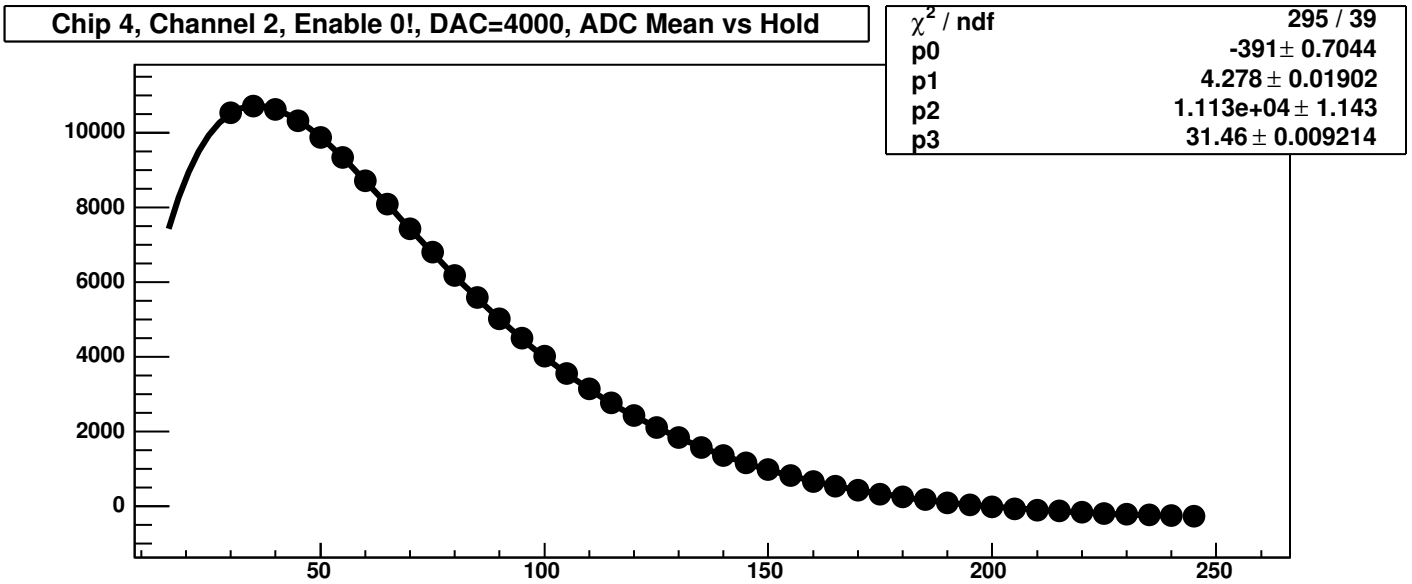


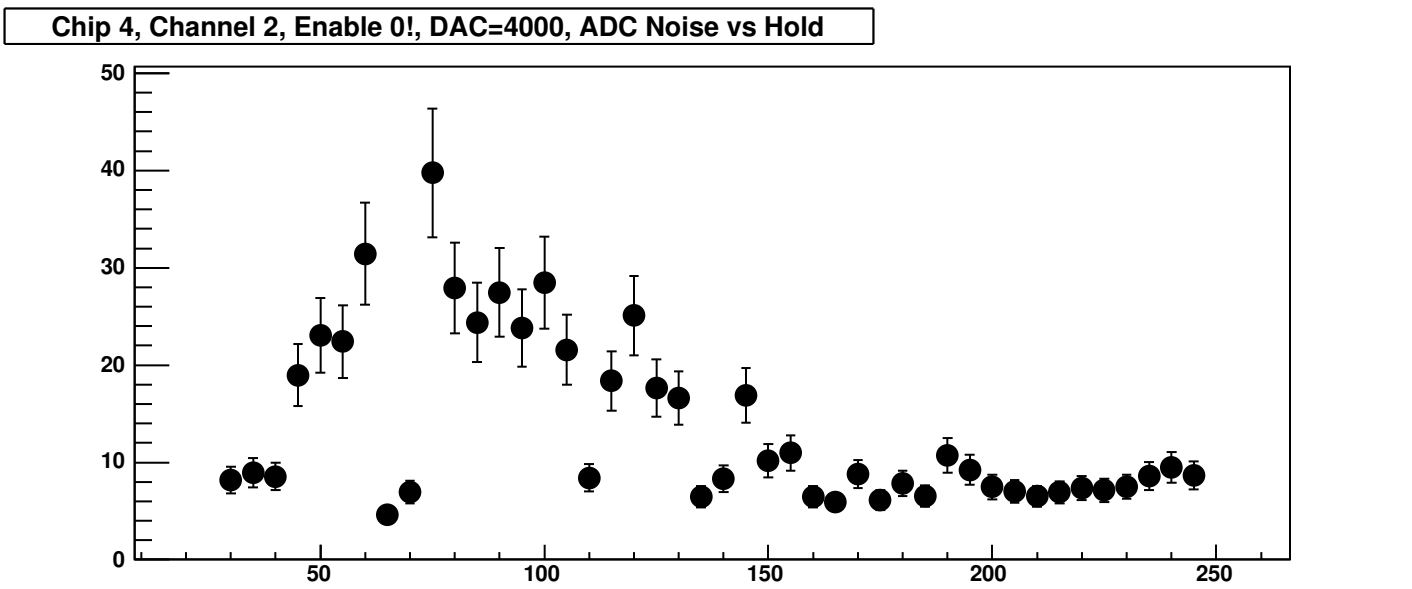
Chip 4, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold

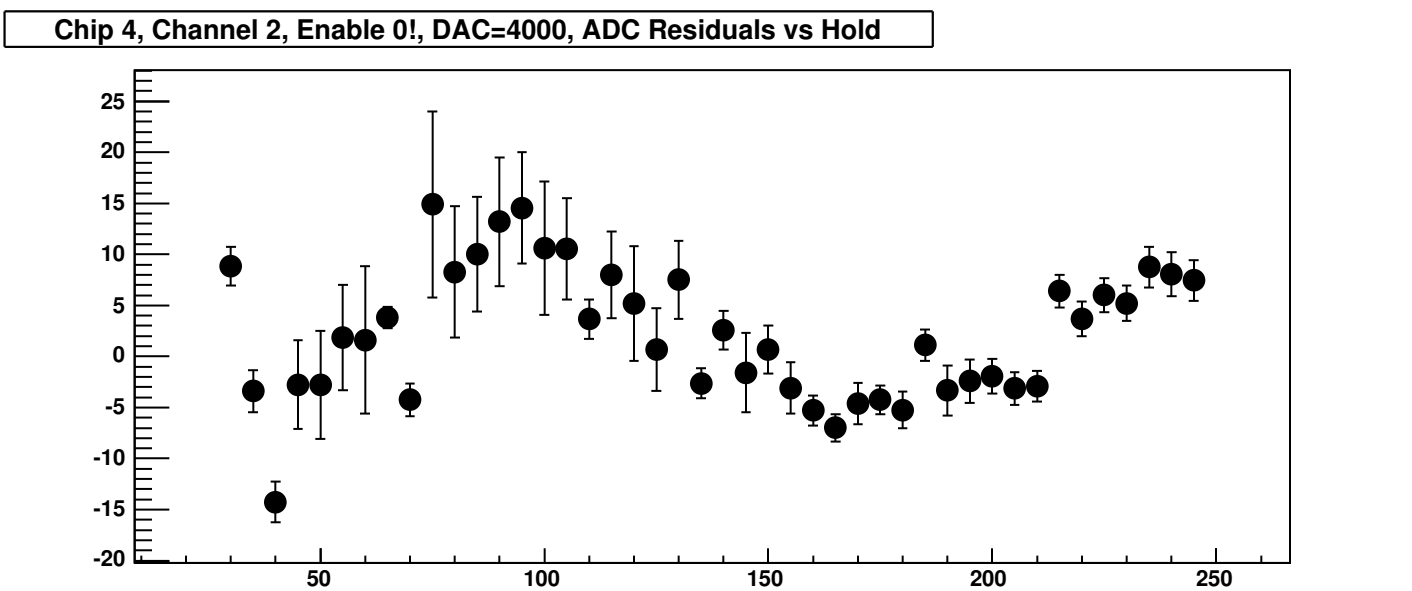


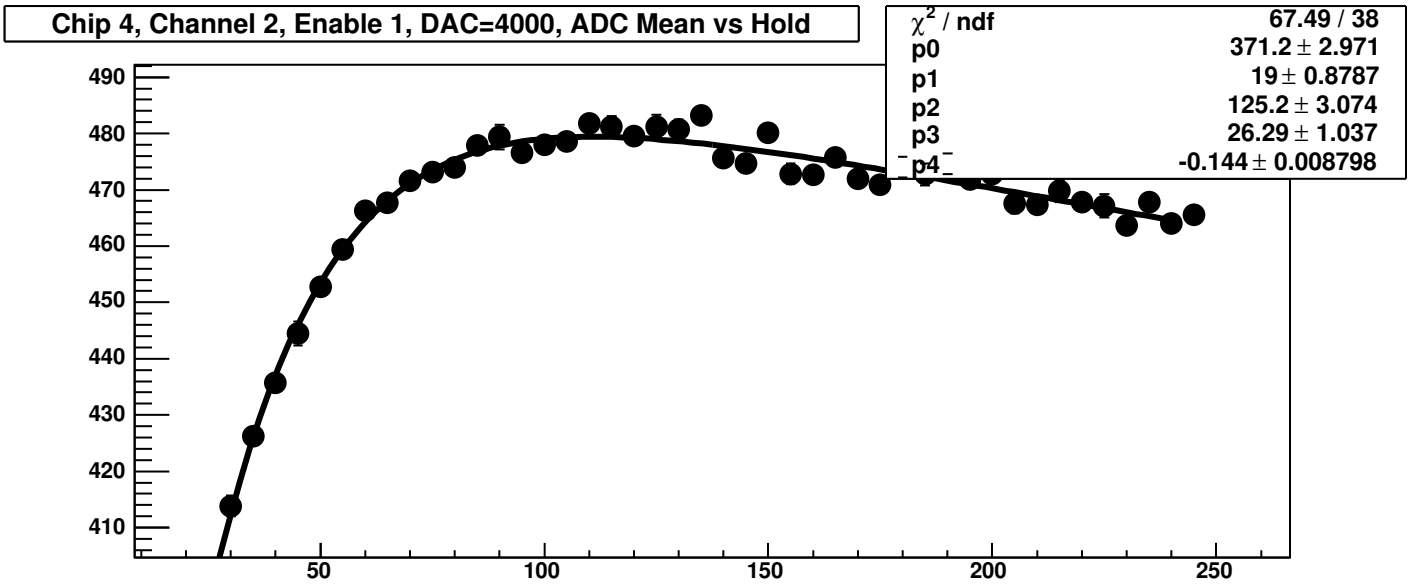
Chip 4, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

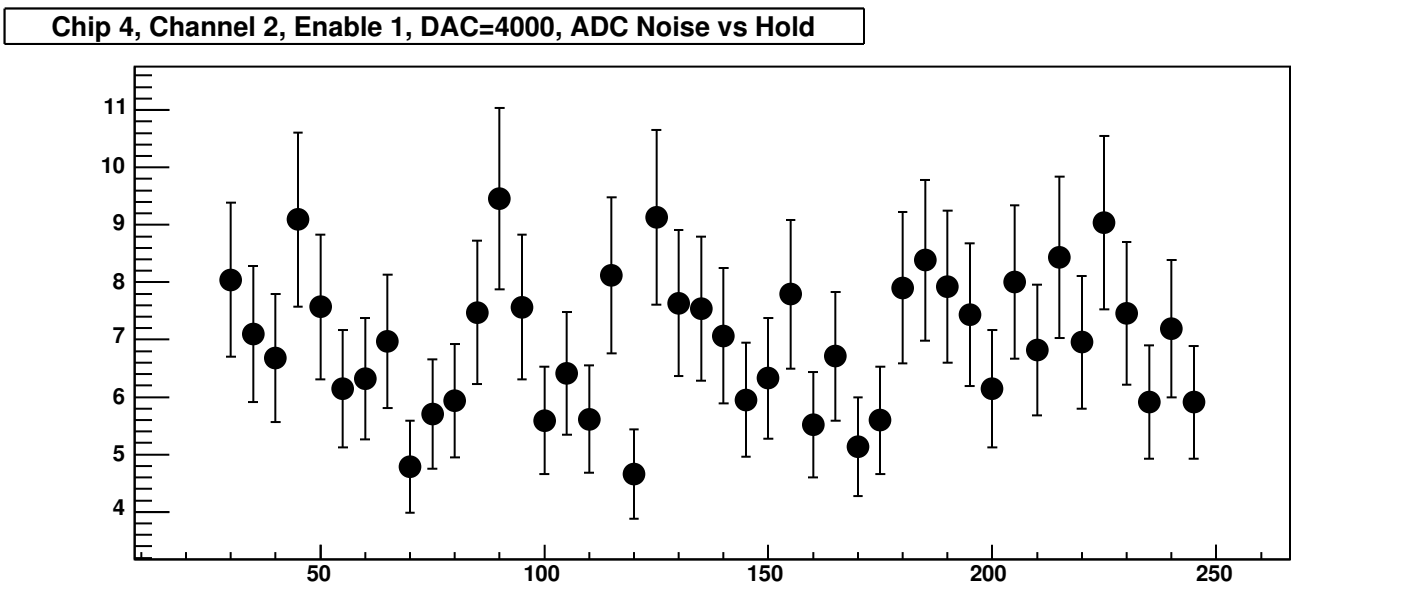


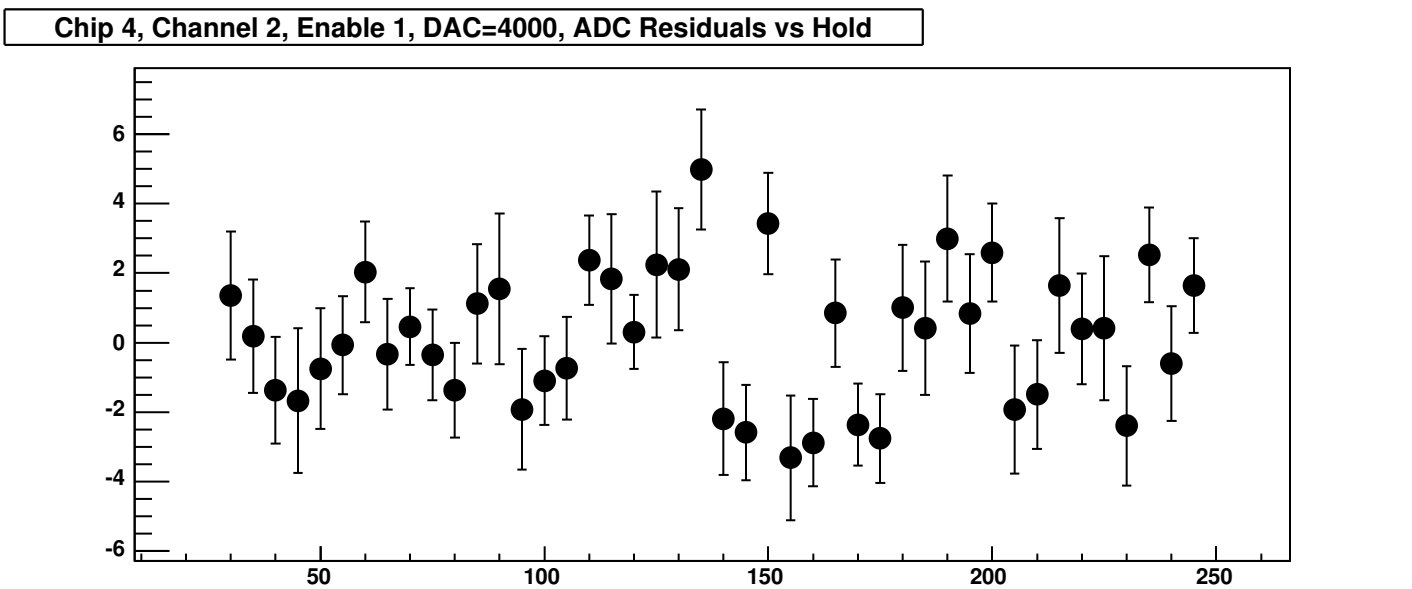

Chip 4, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 295 / 39
p0 -391 ± 0.7044
p1 4.278 ± 0.01902
p2 1.113e+04 ± 1.143
p3 31.46 ± 0.009214
10000
8000
6000
4000
2000
0
50
100
150
200
250


Chip 4, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
0
50
100
150
200
250

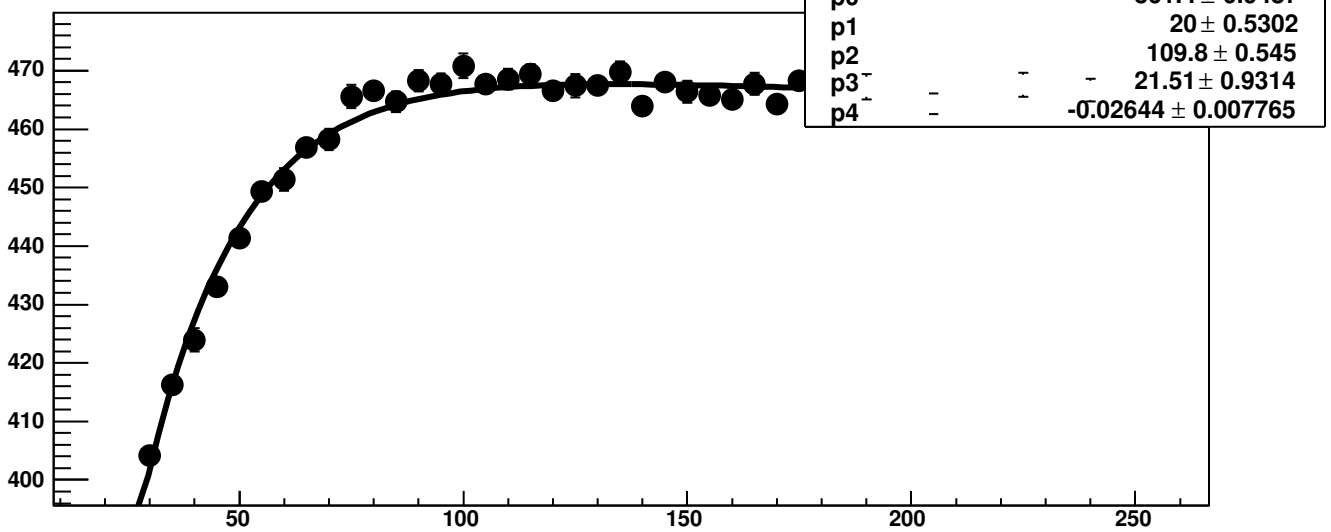

Chip 4, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
-15
-20
50
100
150
200
250


Chip 4, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 67.49 / 38
p0 371.2 ± 2.971
p1 19 ± 0.8787
p2 125.2 ± 3.074
p3 26.29 ± 1.037
p4 -0.144 ± 0.008798
490
480
470
460
450
440
430
420
410
50
100
150
200
250

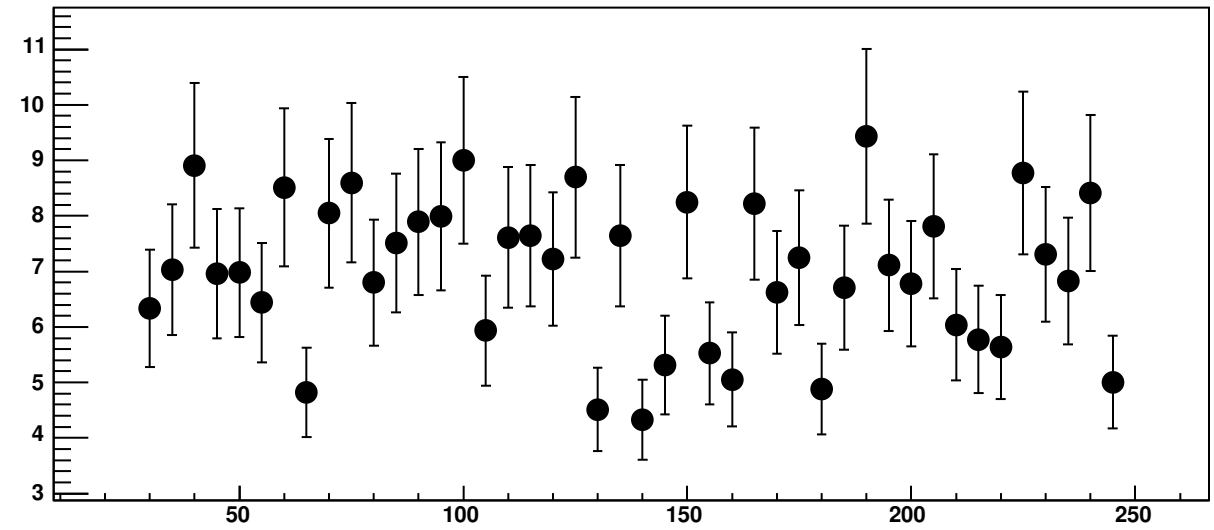

Chip 4, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 4, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
50
100
150
200
250

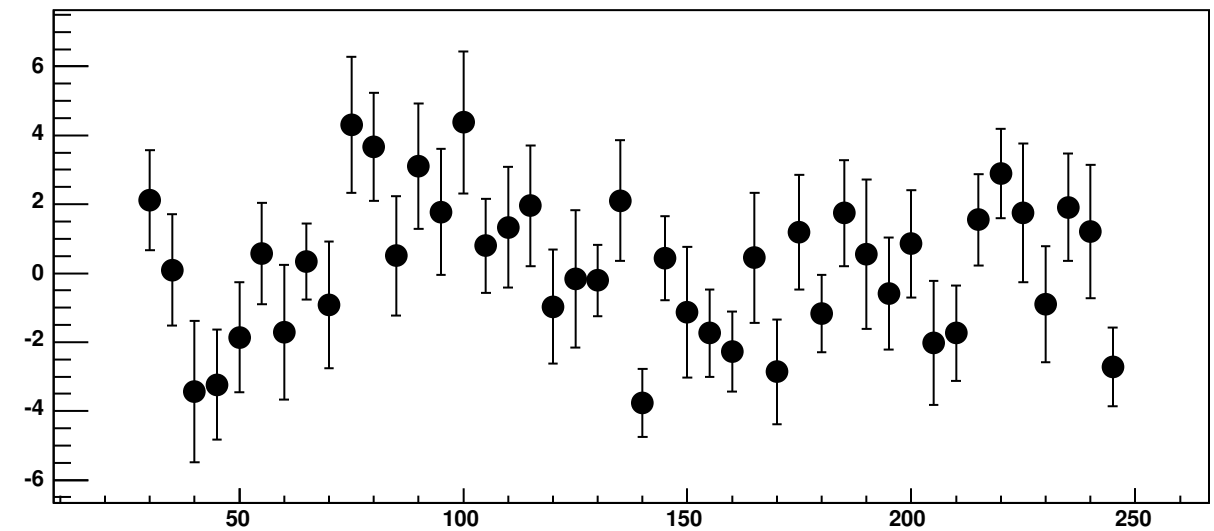
Chip 4, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

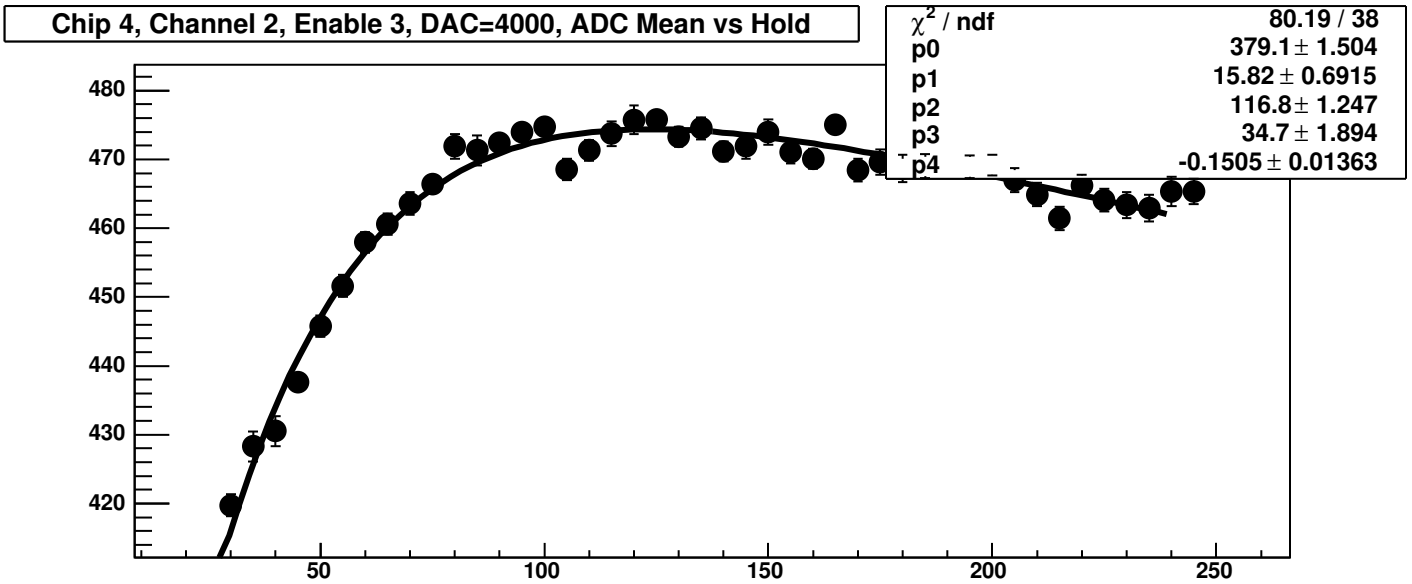


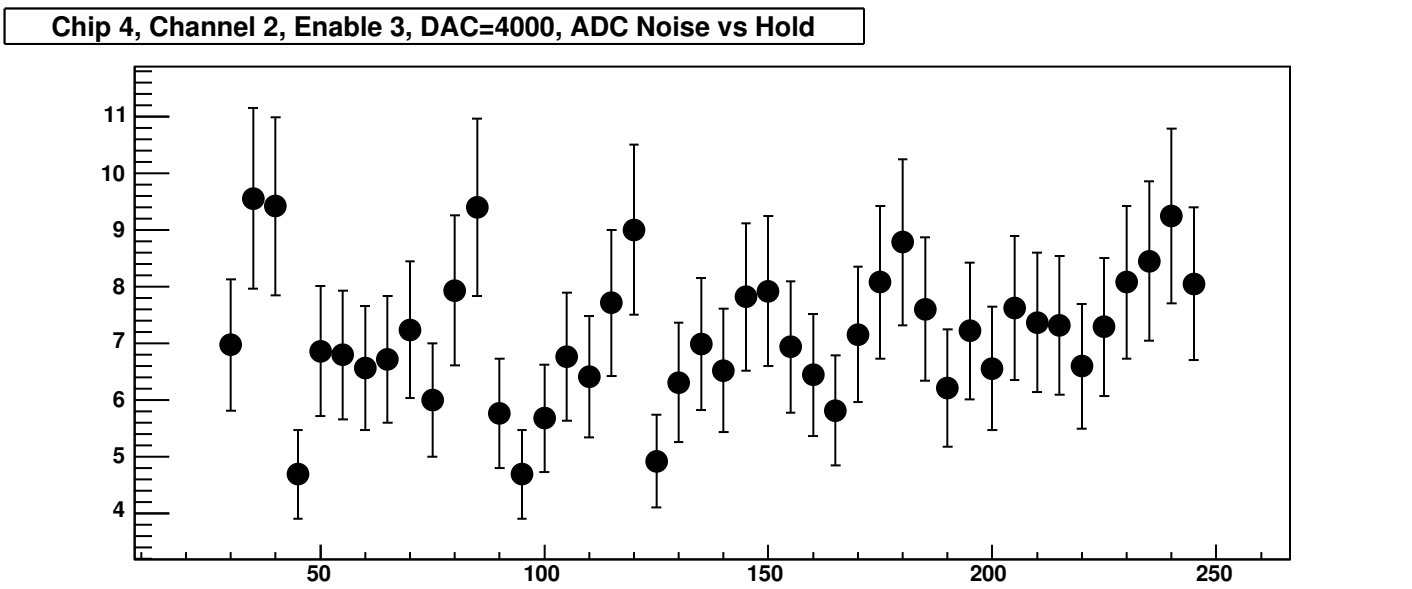
Chip 4, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold

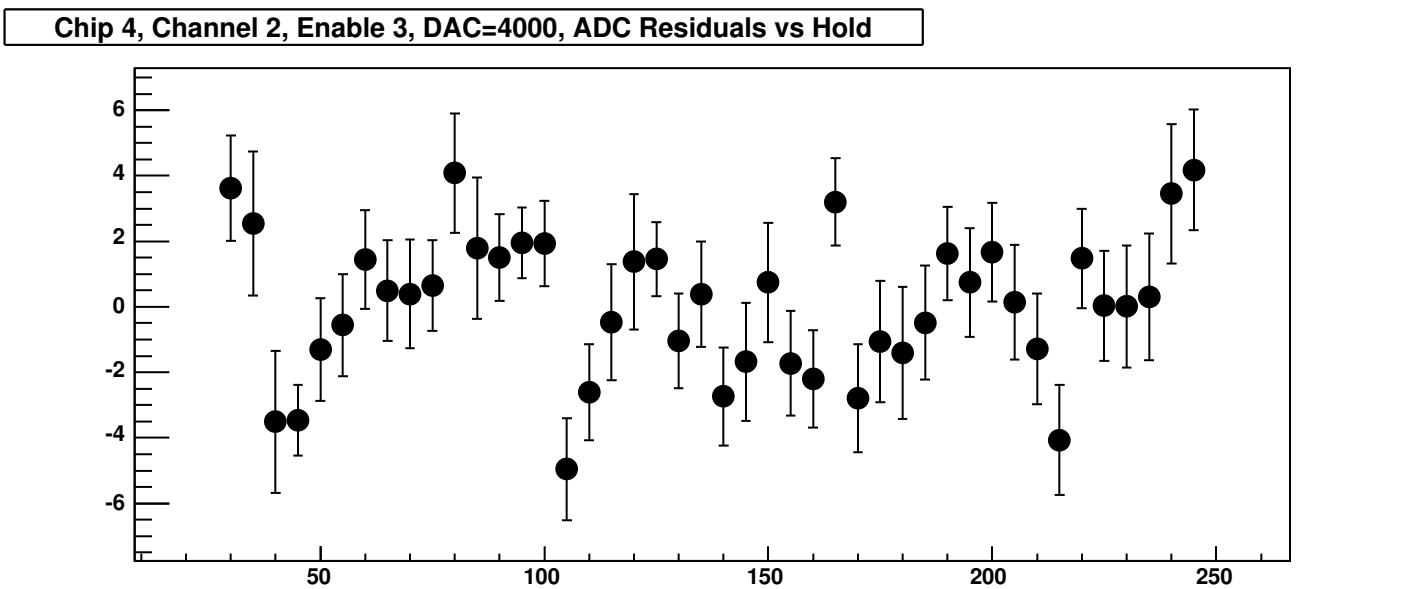


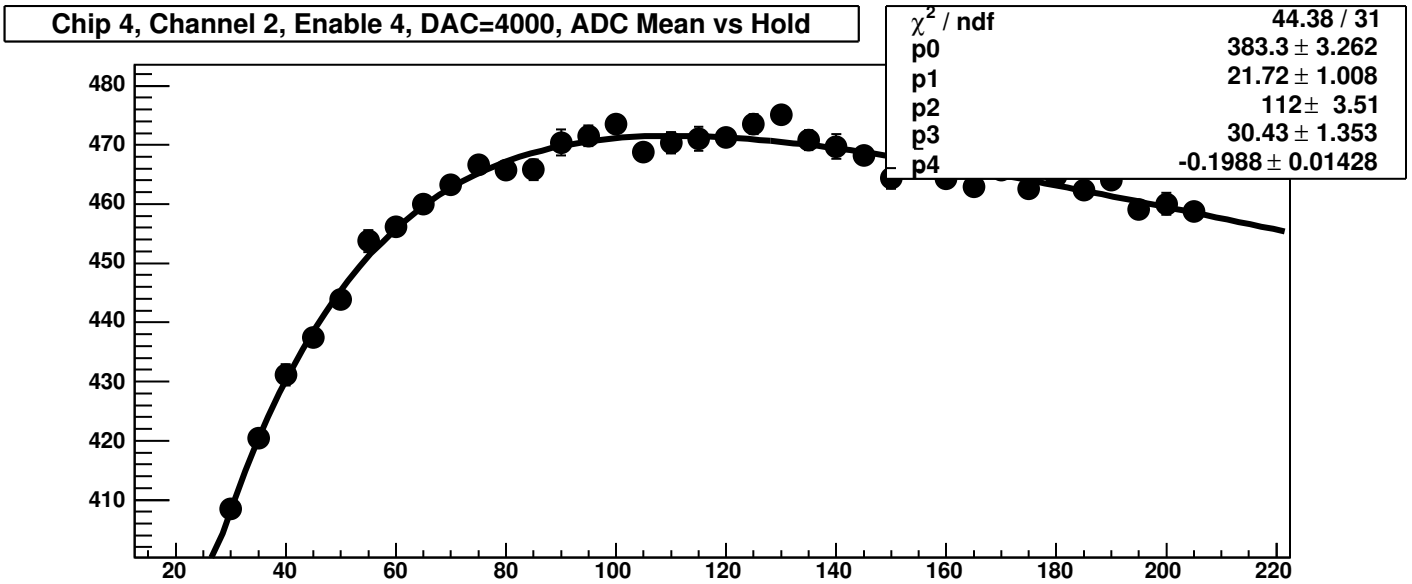
Chip 4, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

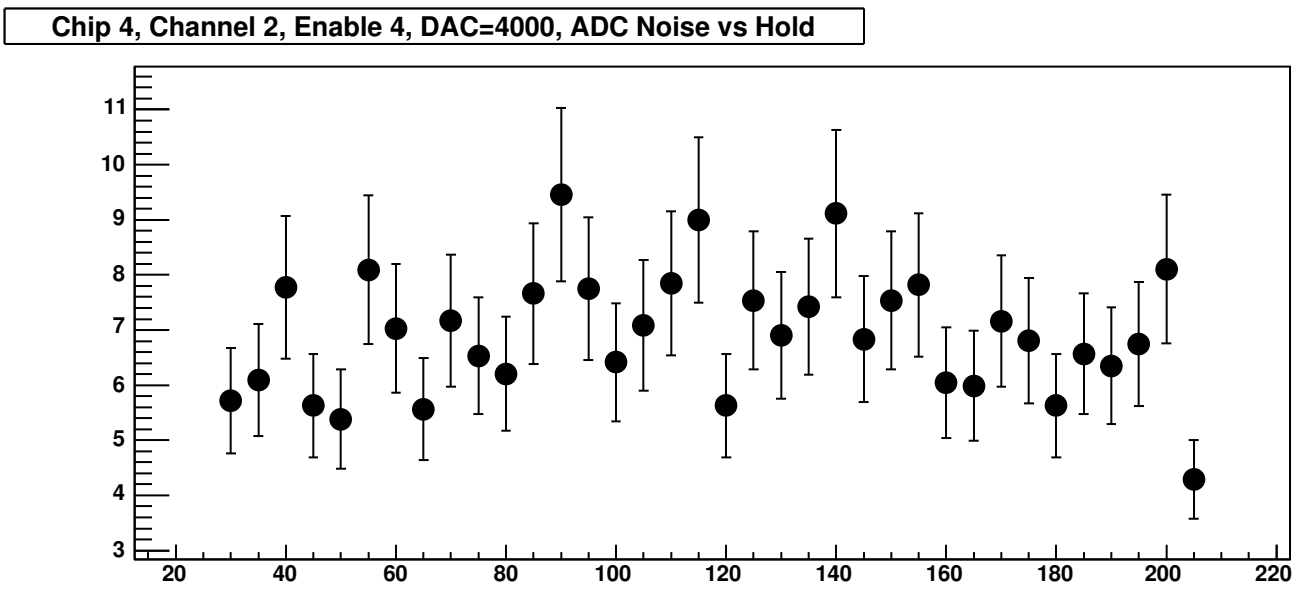


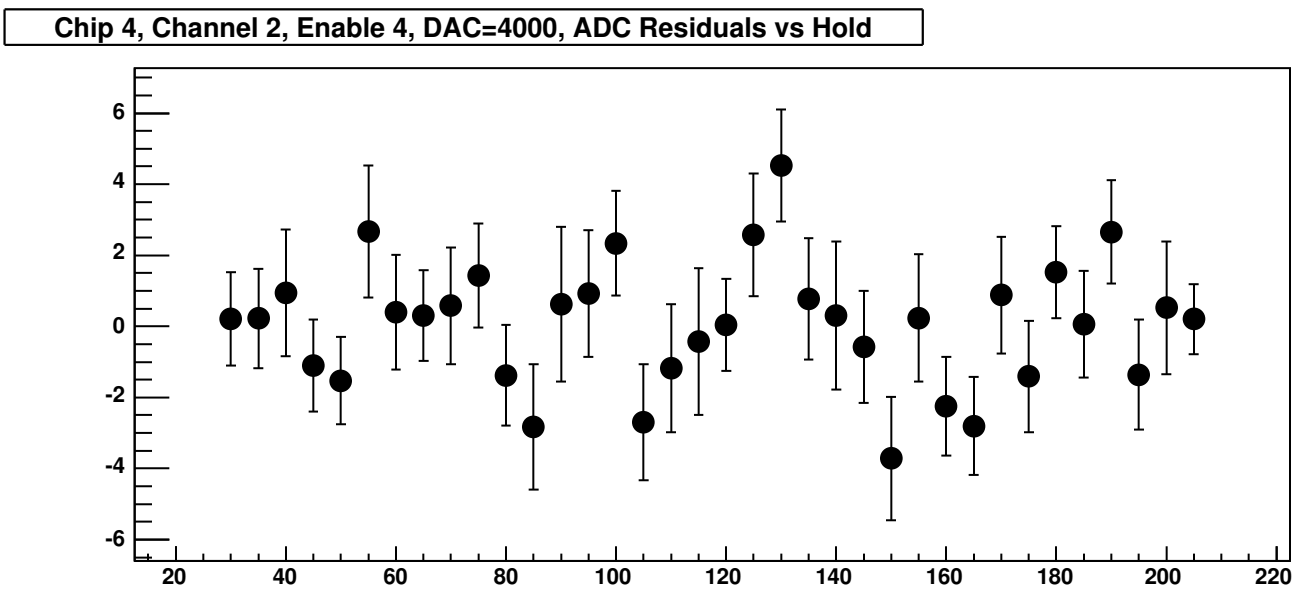

Chip 4, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 80.19 / 38
p0 379.1 ± 1.504
p1 15.82 ± 0.6915
p2 116.8 ± 1.247
p3 34.7 ± 1.894
p4 -0.1505 ± 0.01363
480
470
460
450
440
430
420
50
100
150
200
250


Chip 4, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250

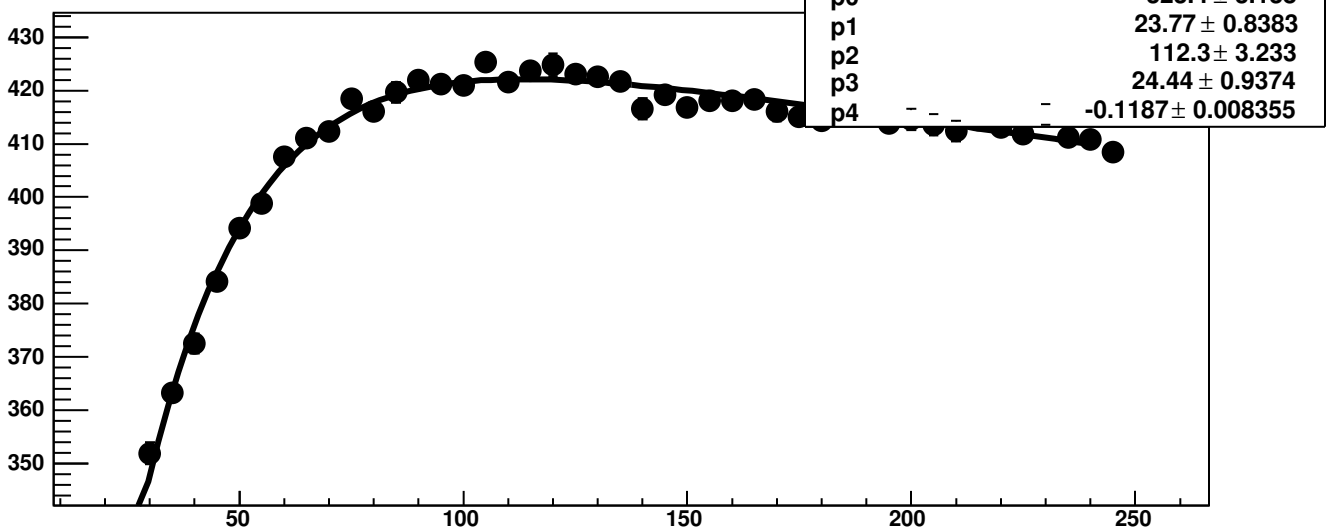

Chip 4, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
50
100
150
200
250


Chip 4, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 44.38 / 31
p0 383.3 ± 3.262
p1 21.72 ± 1.008
p2 112 ± 3.51
p3 30.43 ± 1.353
p4 -0.1988 ± 0.01428
480
470
460
450
440
430
420
410
20
40
60
80
100
120
140
160
180
200
220

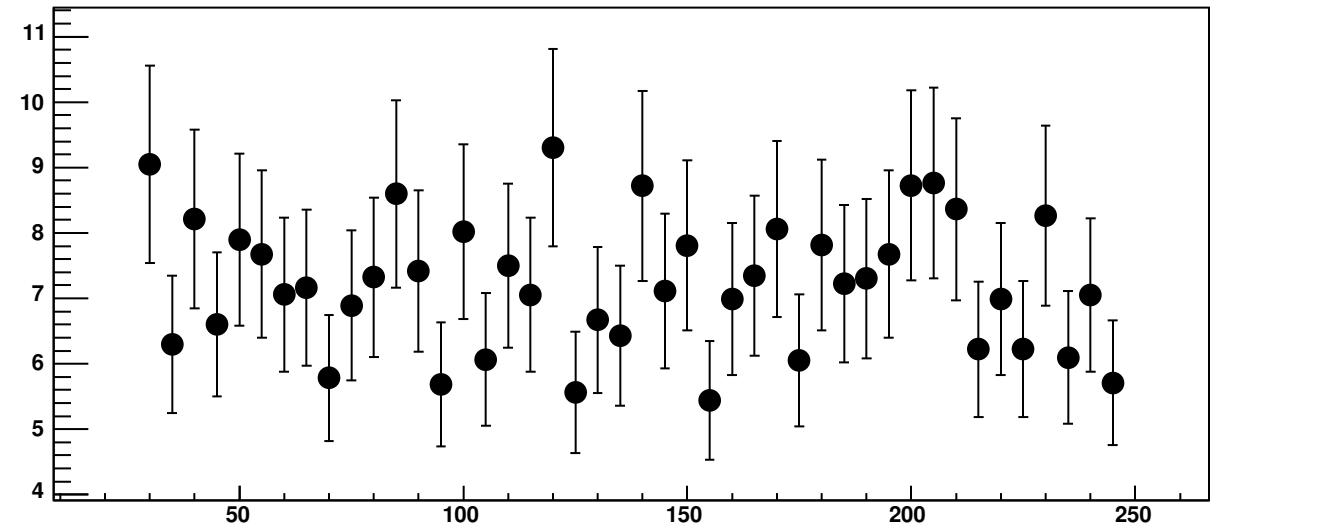

Chip 4, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
20
40
60
80
100
120
140
160
180
200
220


Chip 4, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
20
40
60
80
100
120
140
160
180
200
220

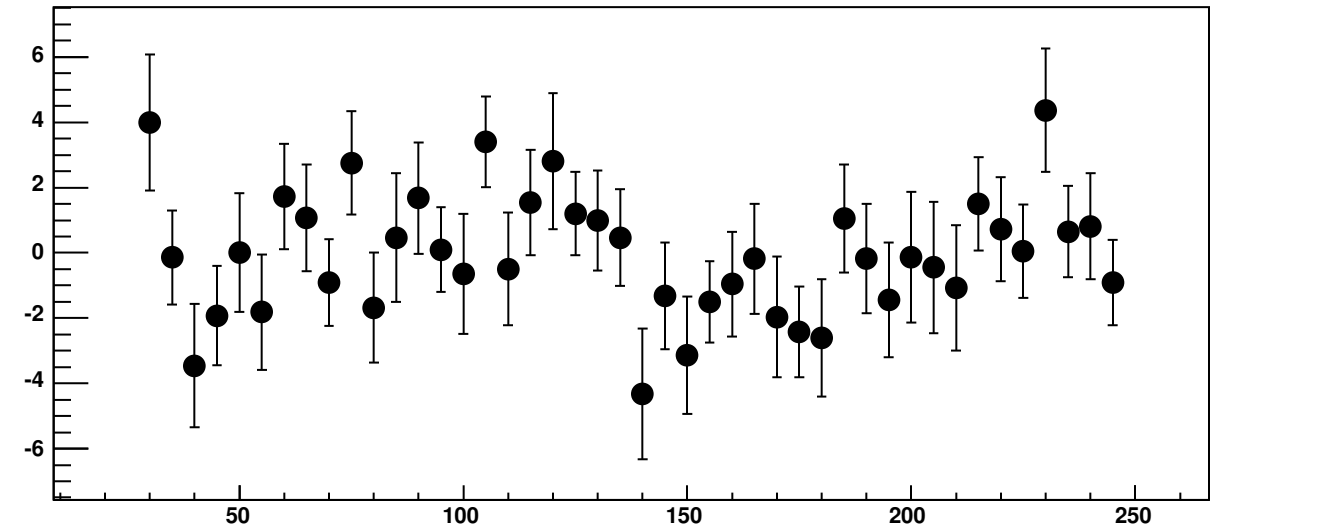
Chip 4, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold



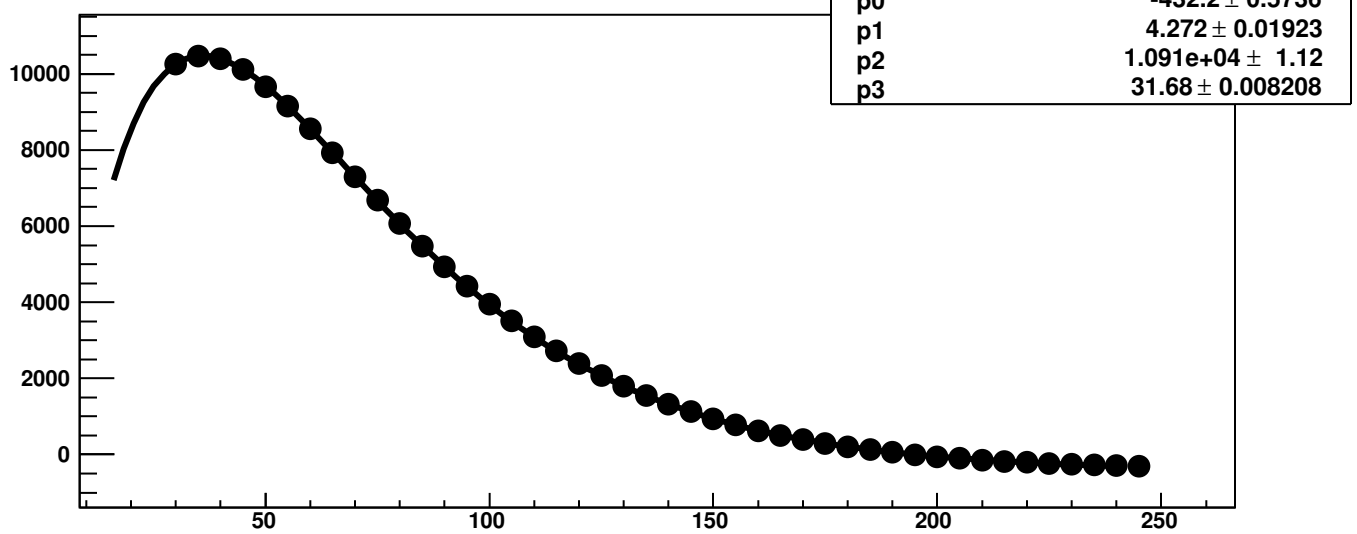
Chip 4, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



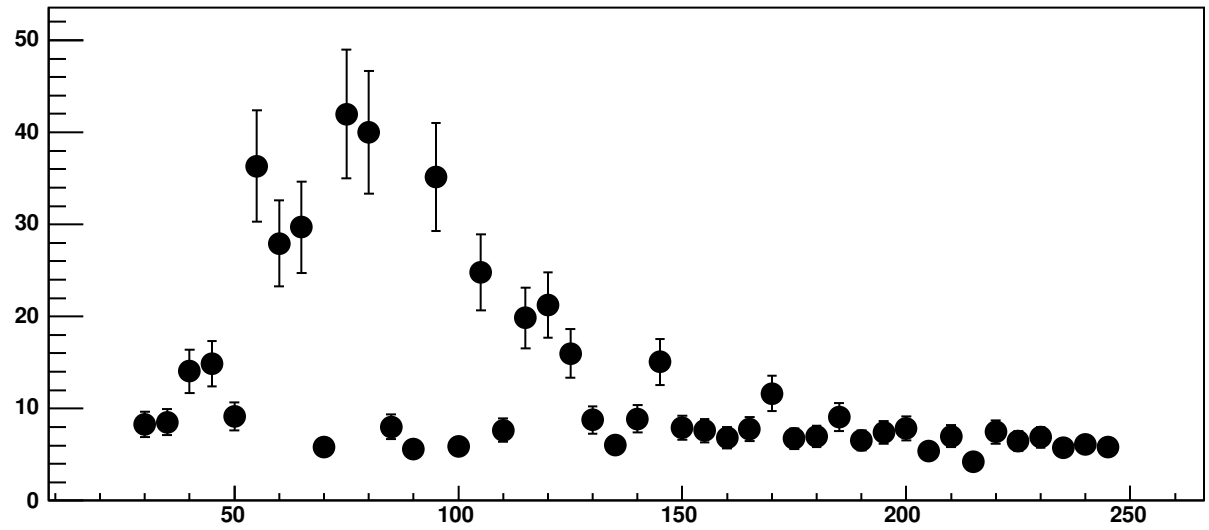
Chip 4, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold



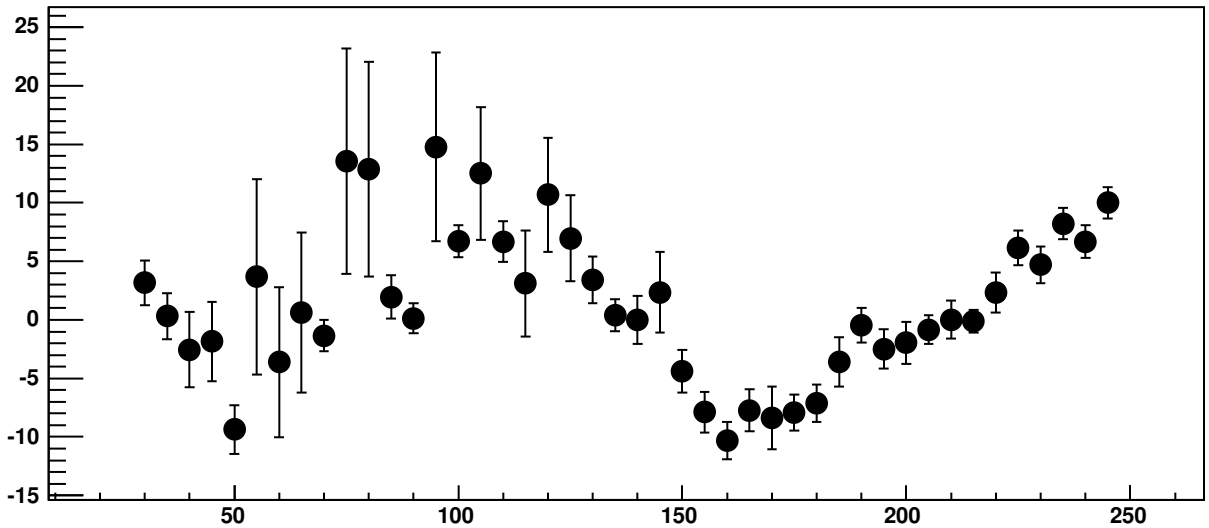
Chip 4, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold



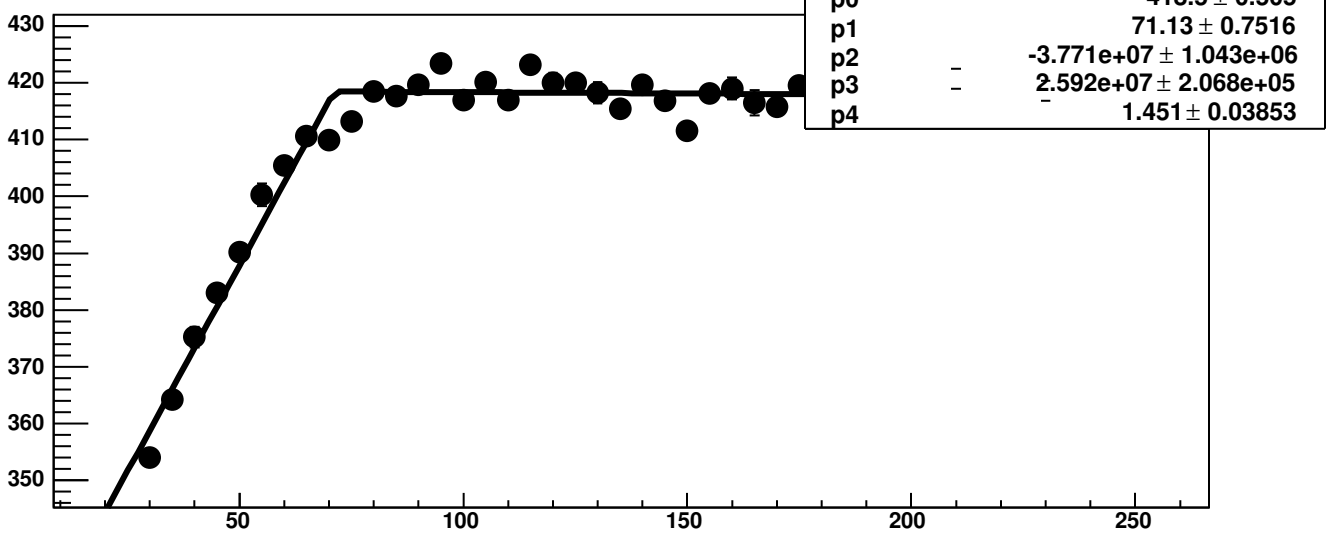
Chip 4, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold



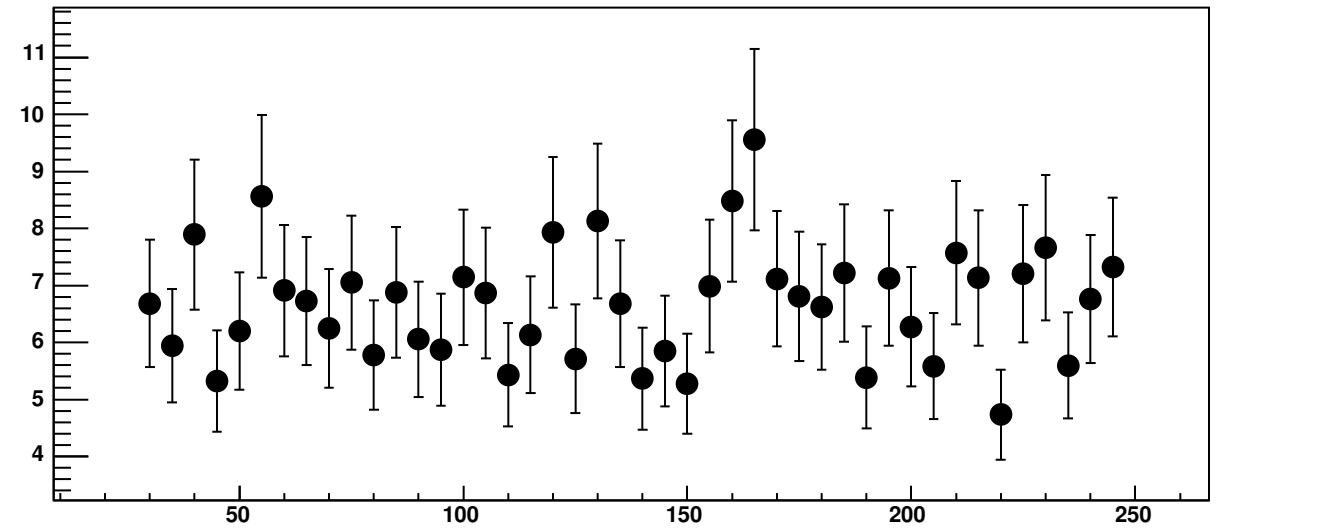
Chip 4, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold



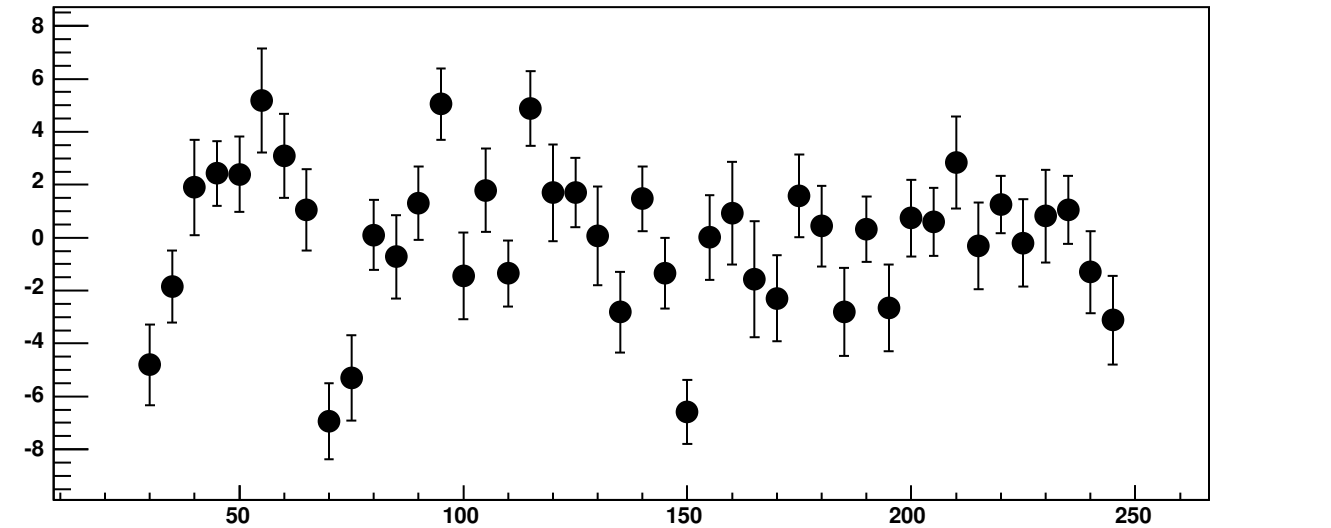
Chip 4, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold



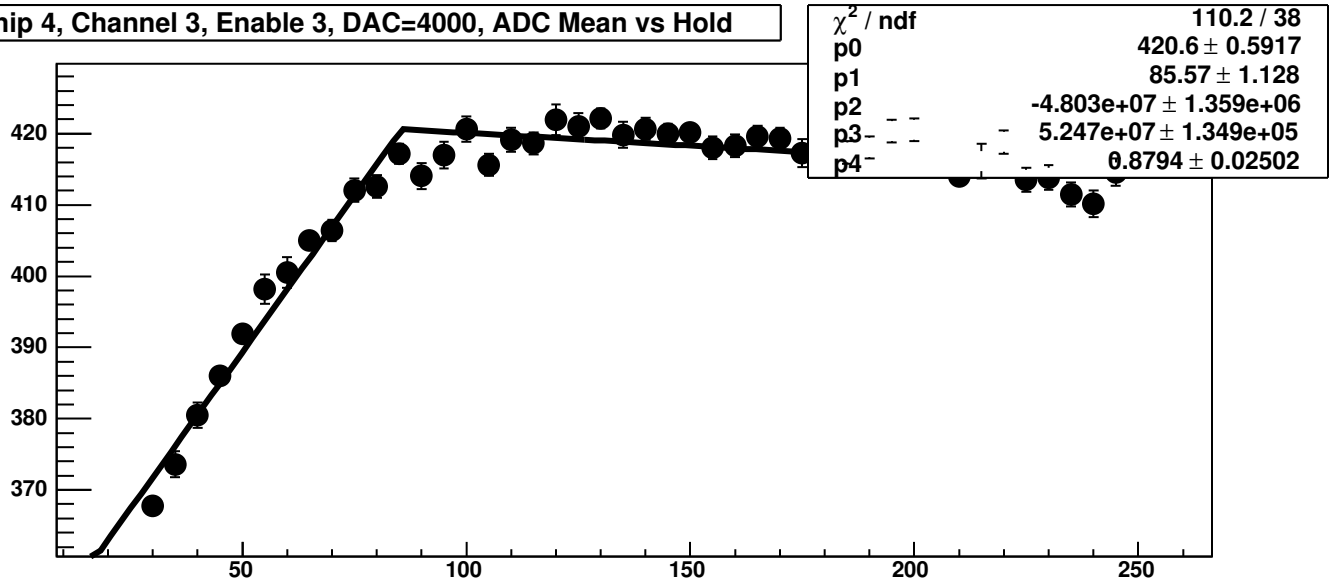
Chip 4, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



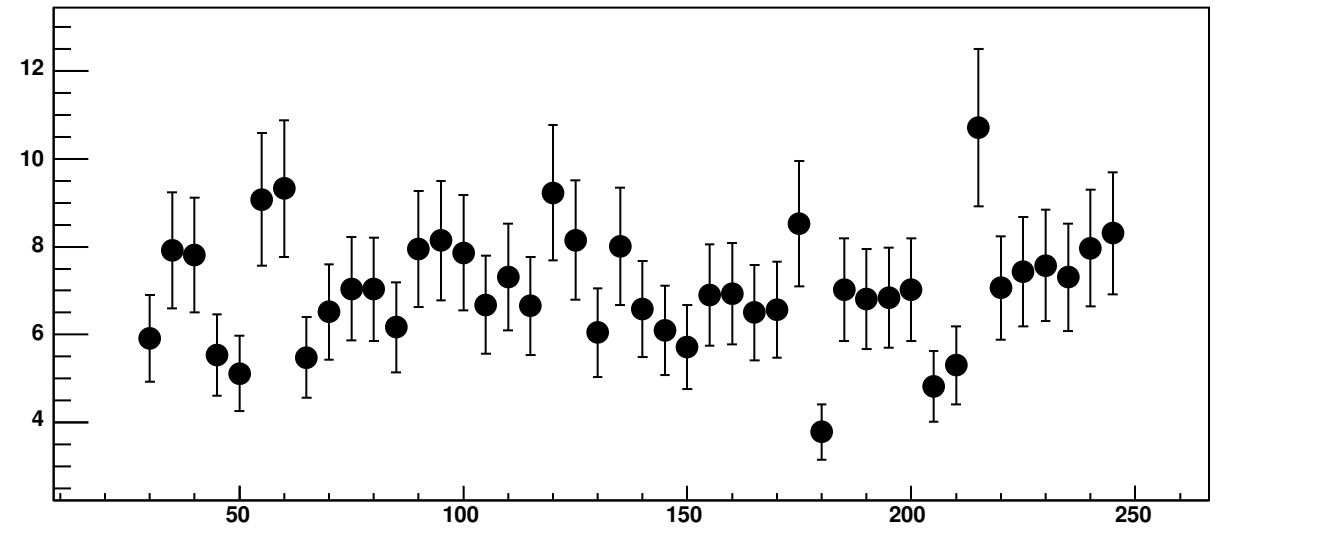
Chip 4, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold



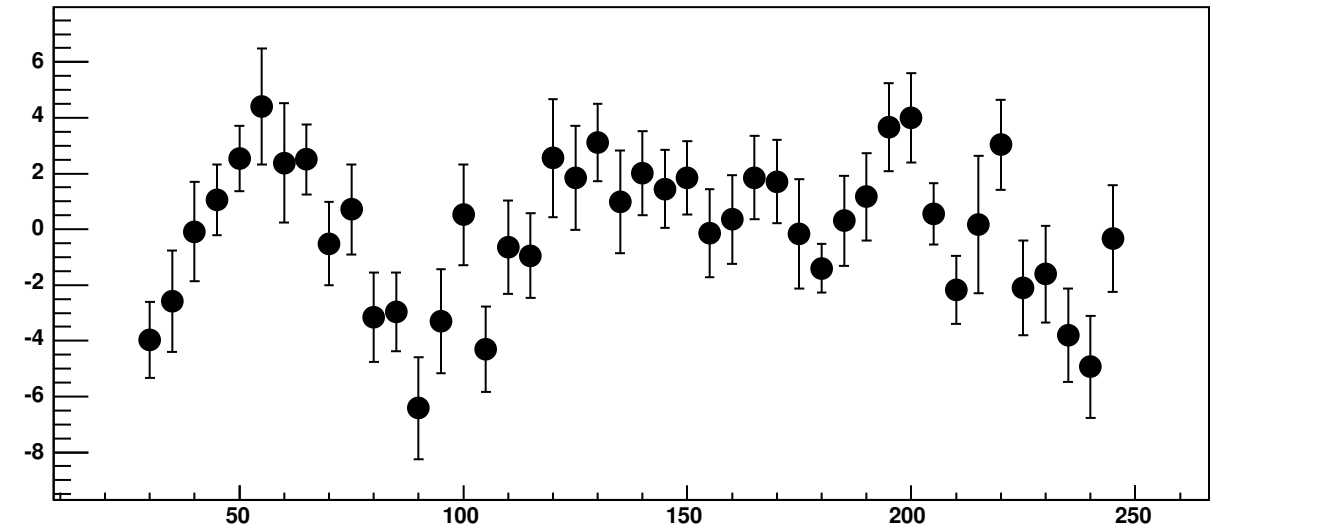
Chip 4, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold



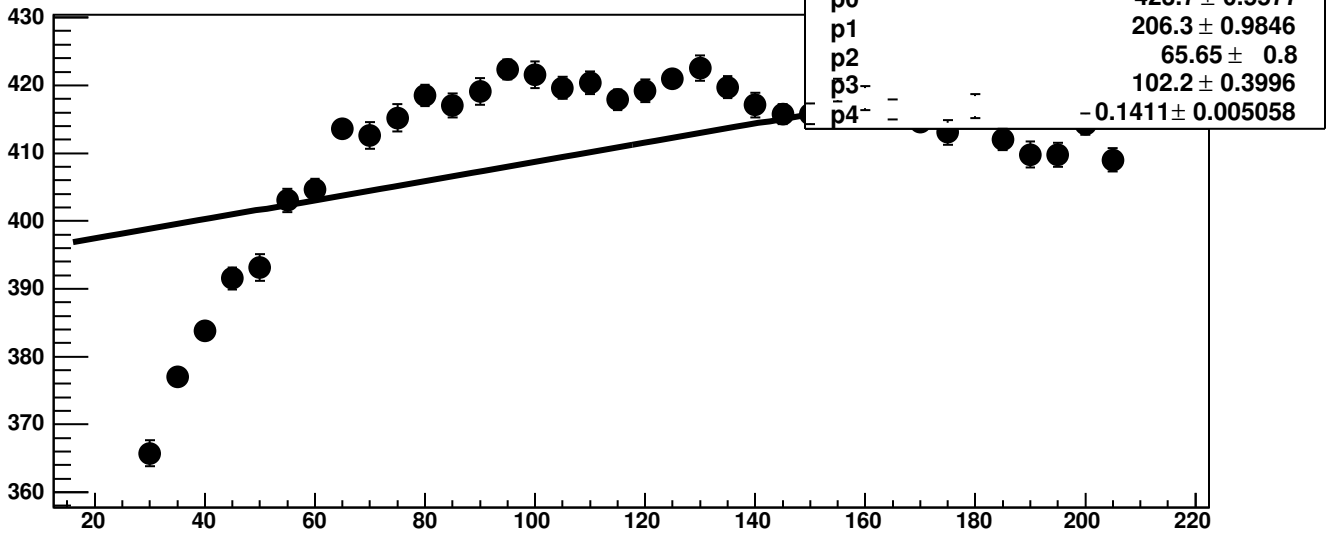
Chip 4, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



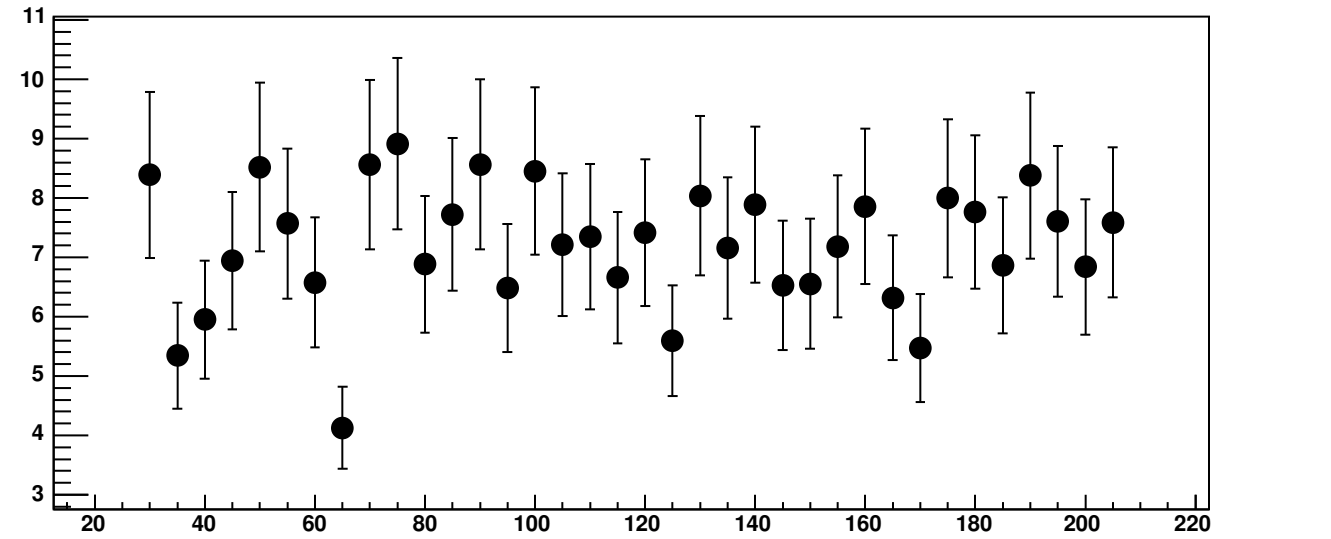
Chip 4, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold



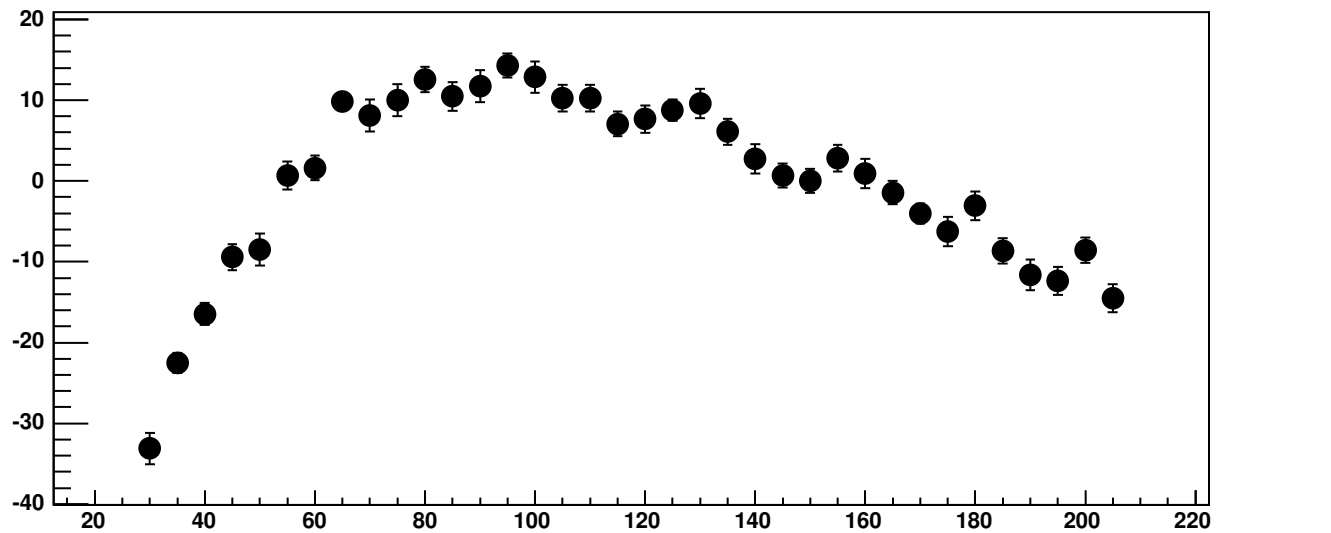
Chip 4, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold



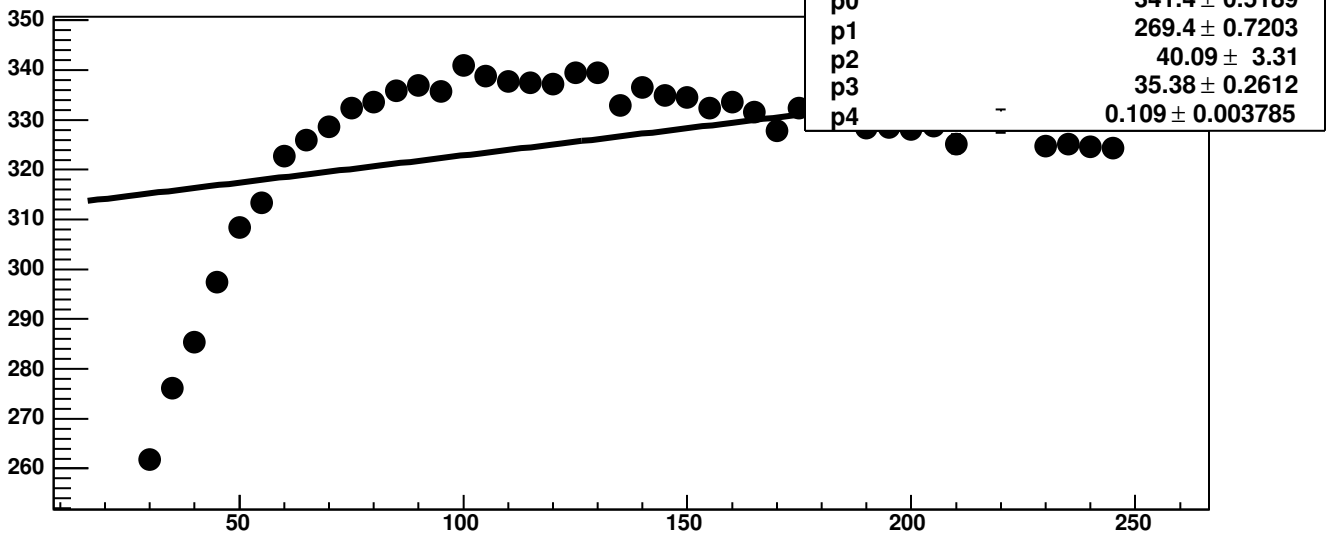
Chip 4, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold



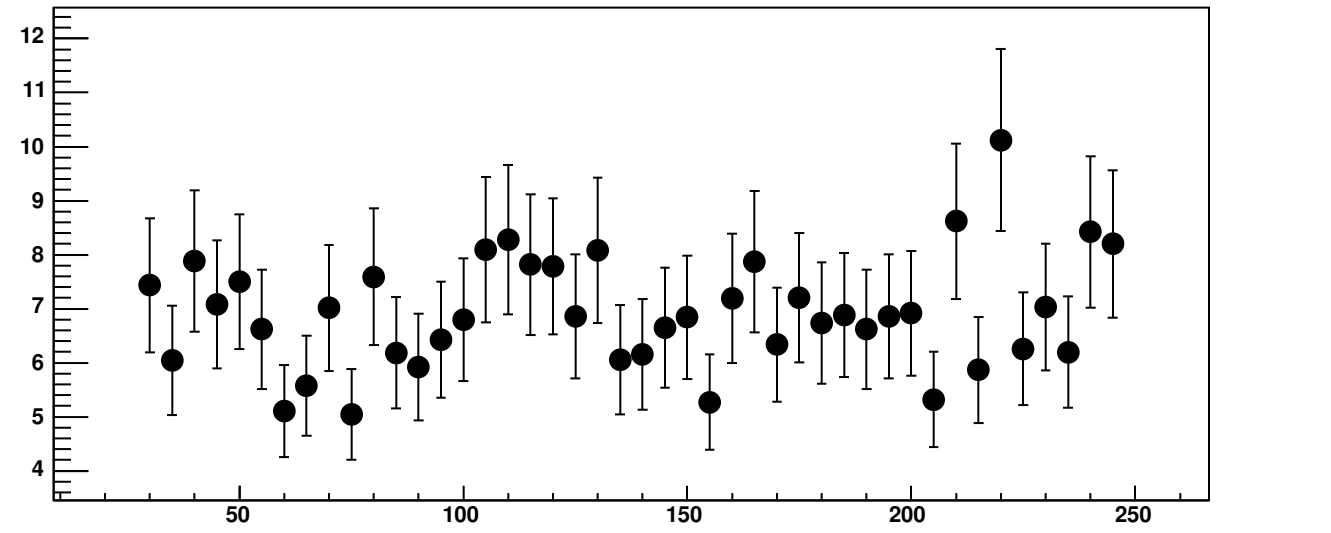
Chip 4, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold



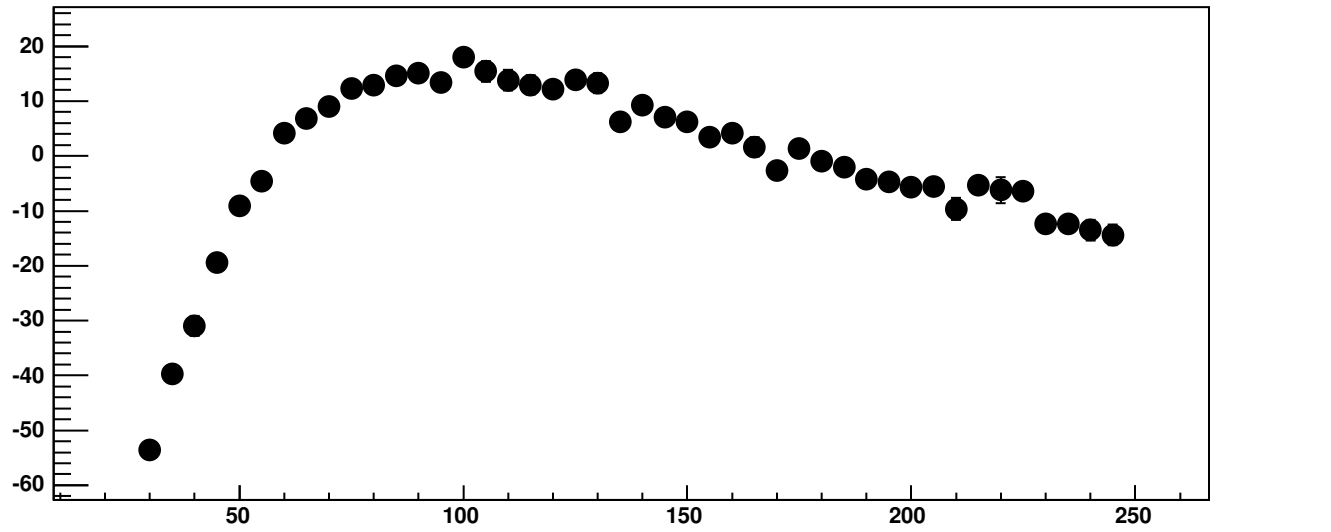
Chip 4, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold



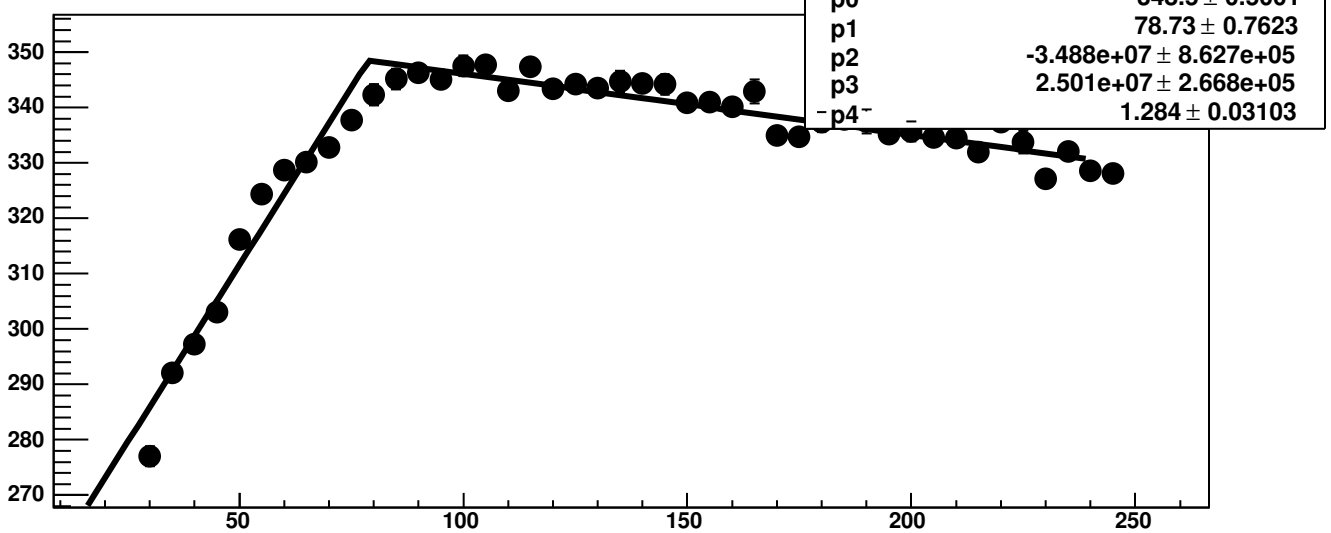
Chip 4, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



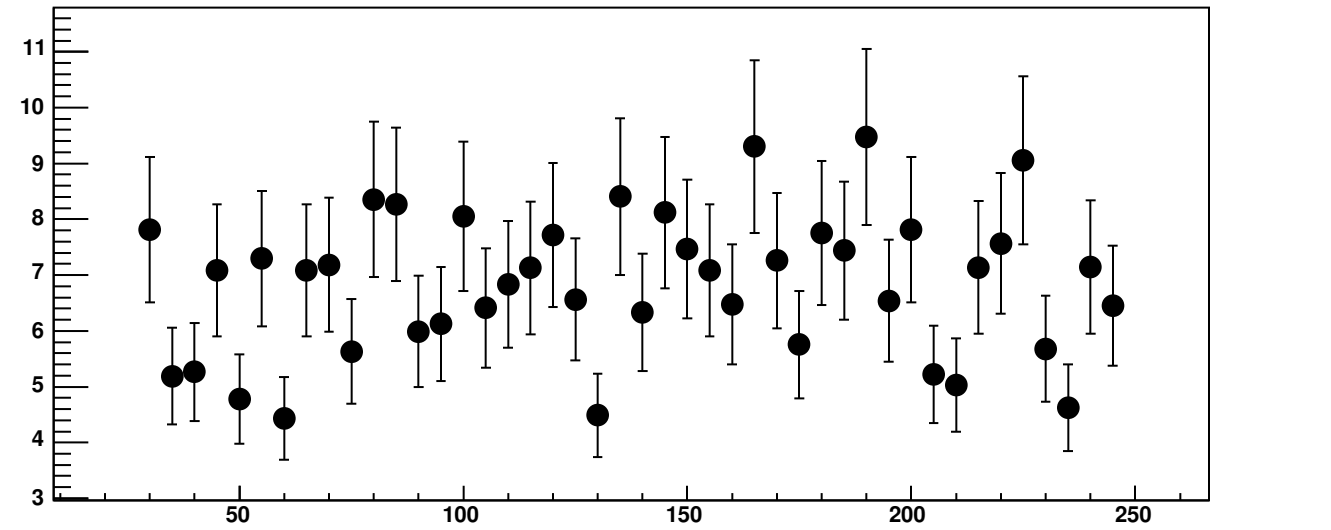
Chip 4, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold



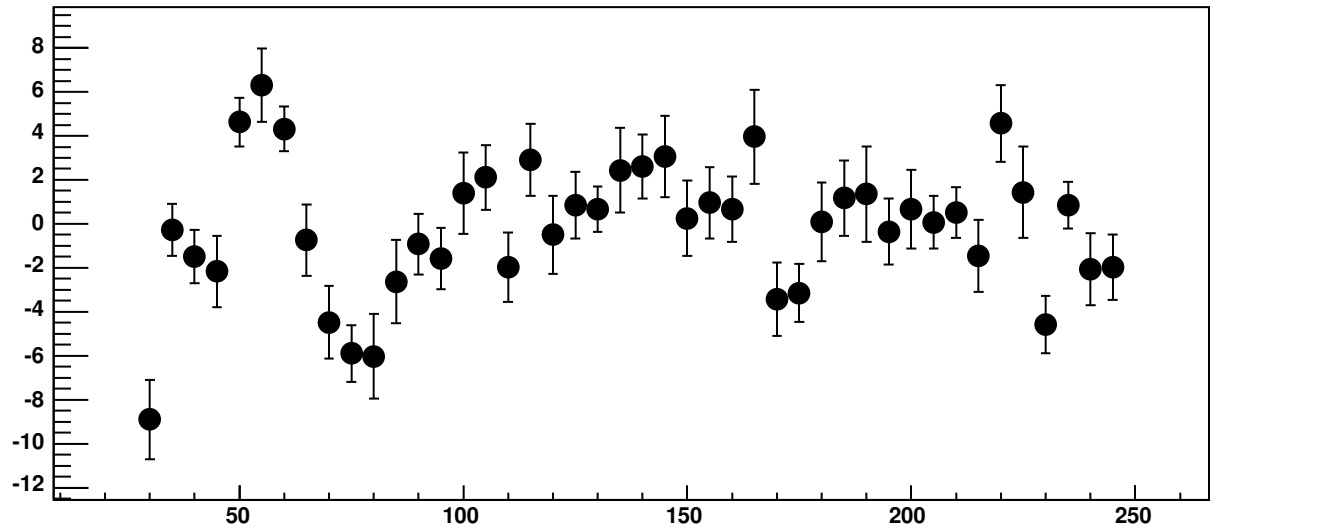
Chip 4, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold



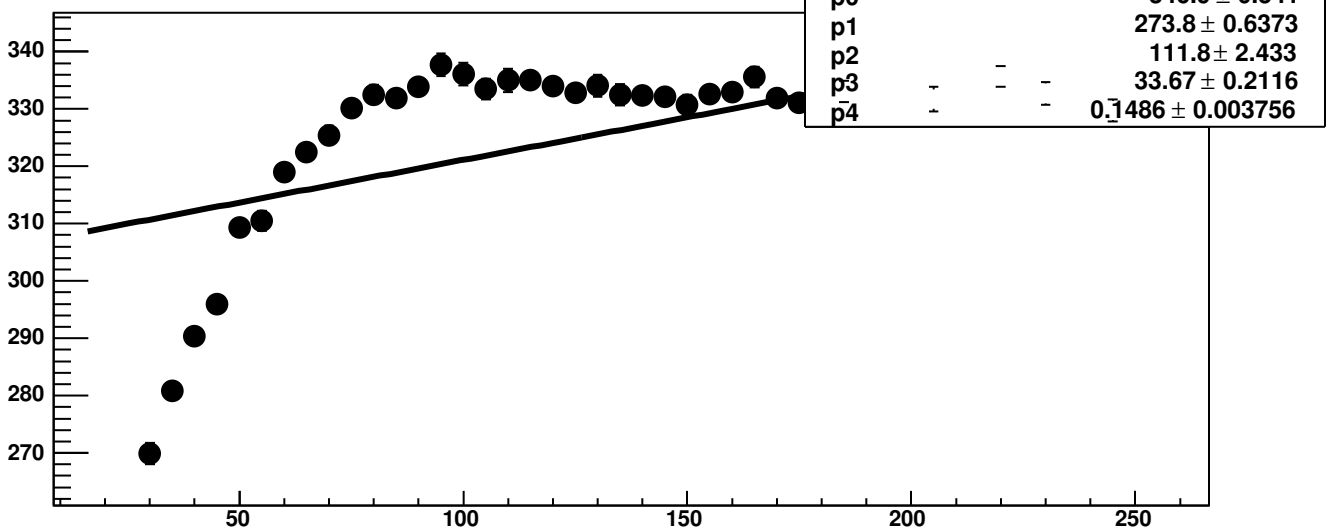
Chip 4, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold



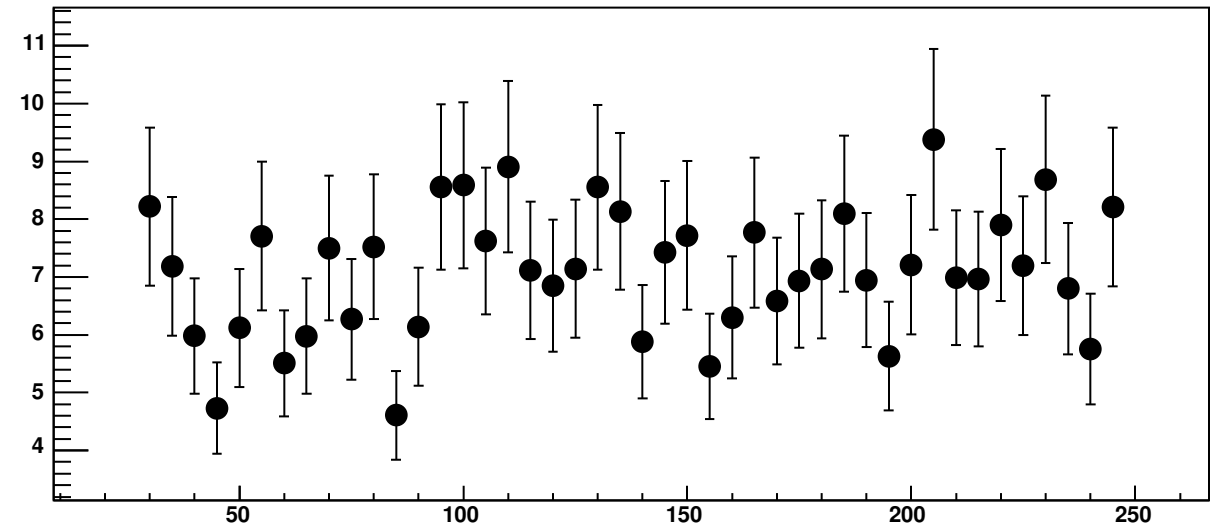
Chip 4, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold



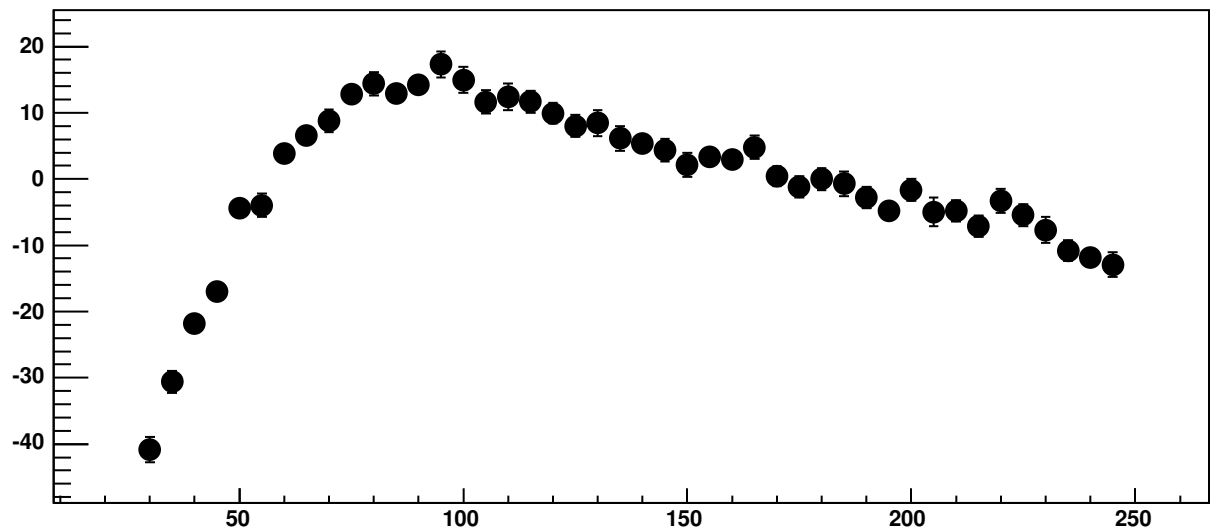
Chip 4, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold



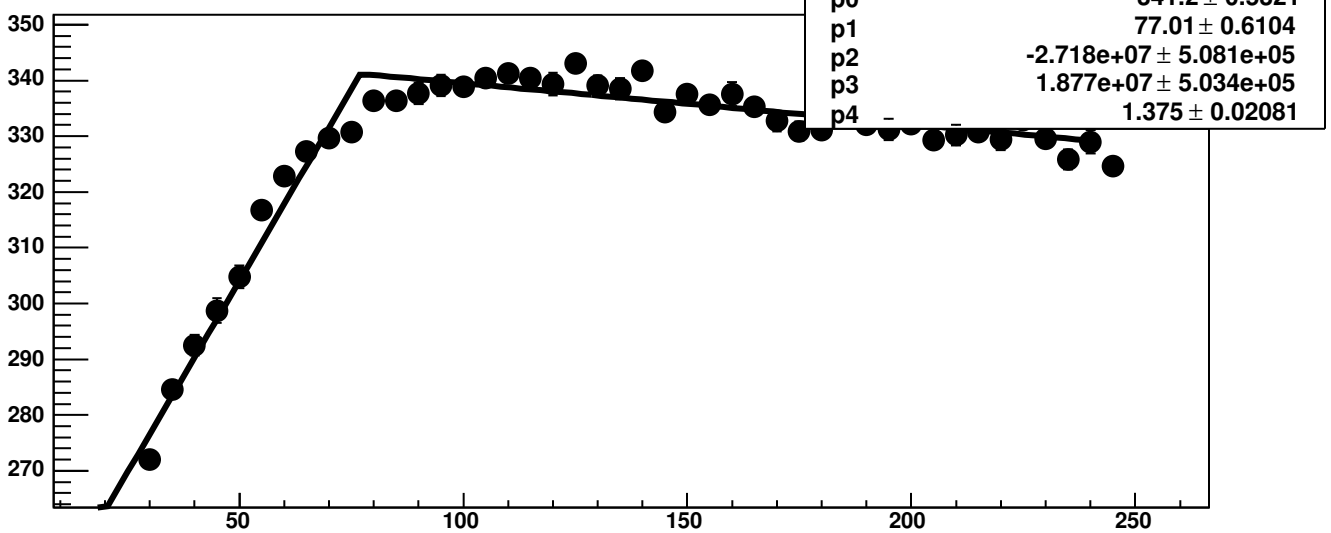
Chip 4, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



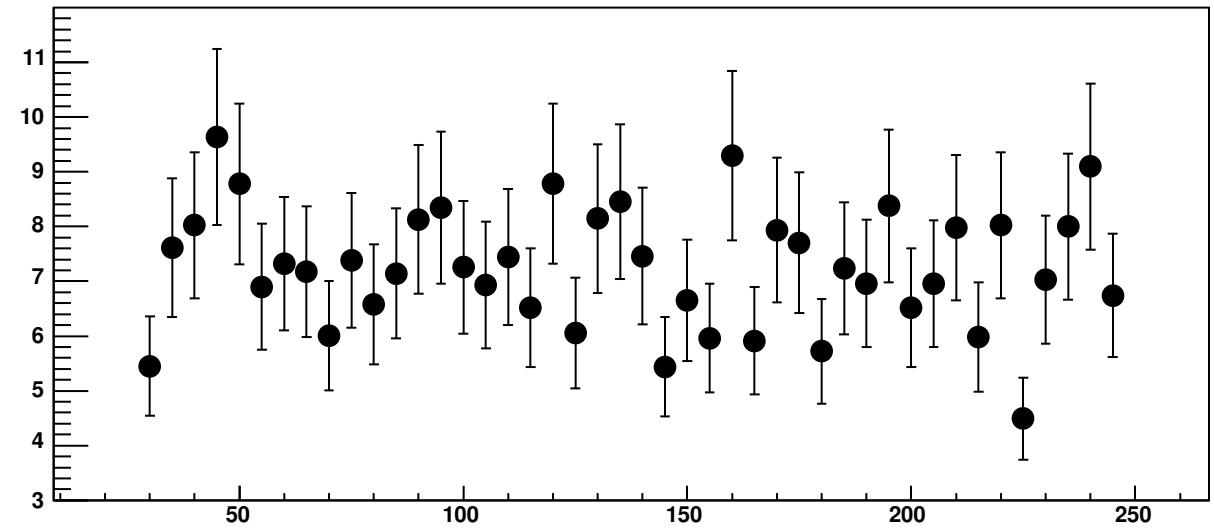
Chip 4, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold



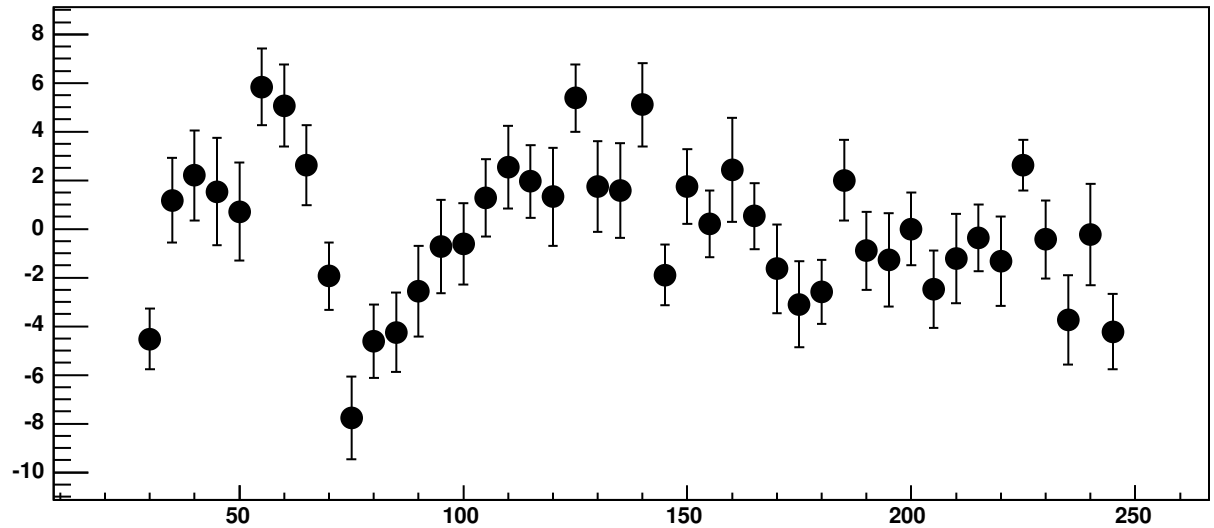
Chip 4, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

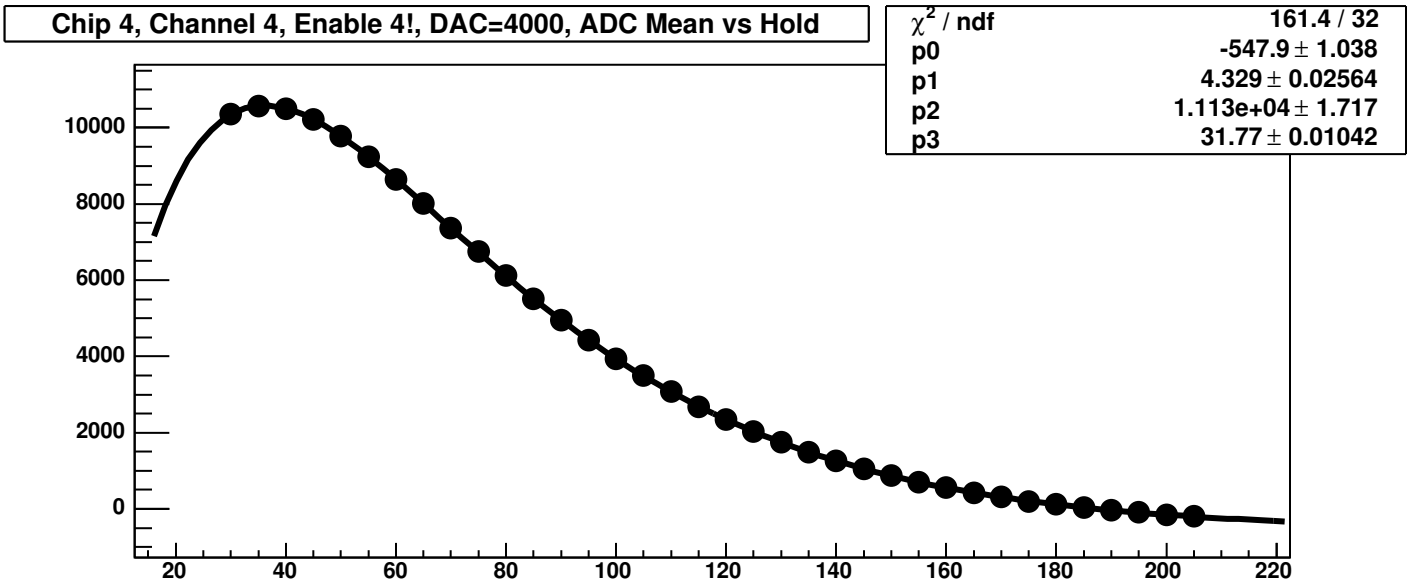


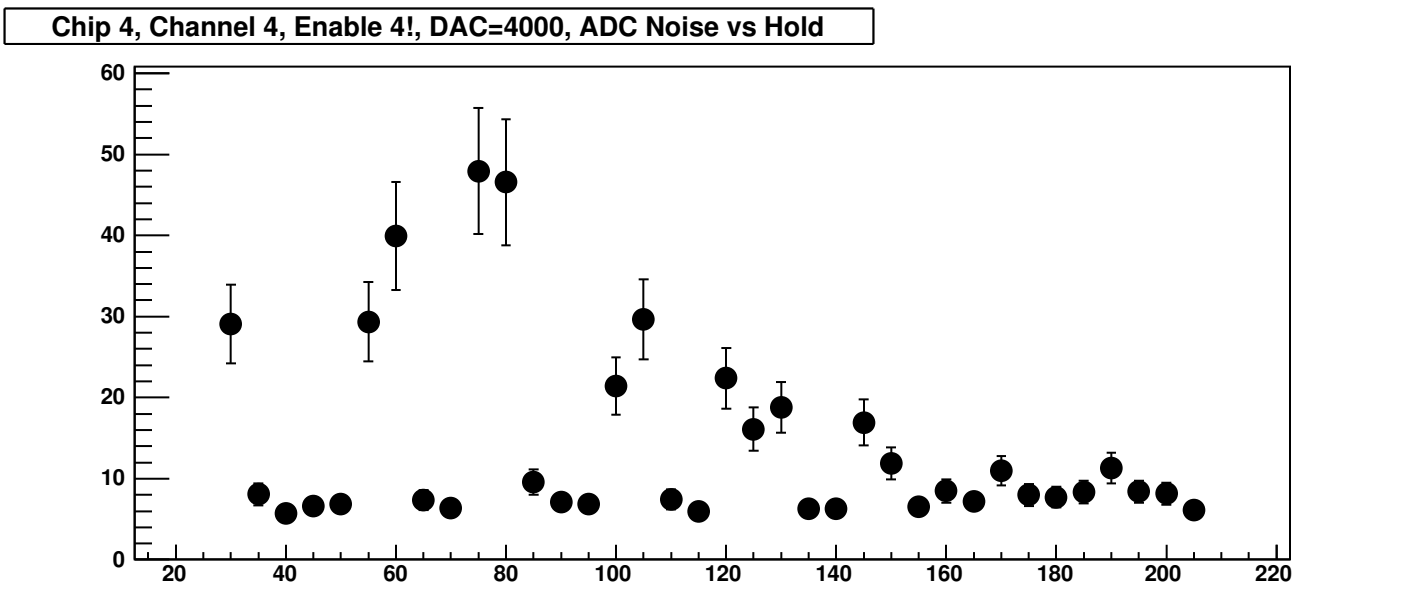
Chip 4, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold

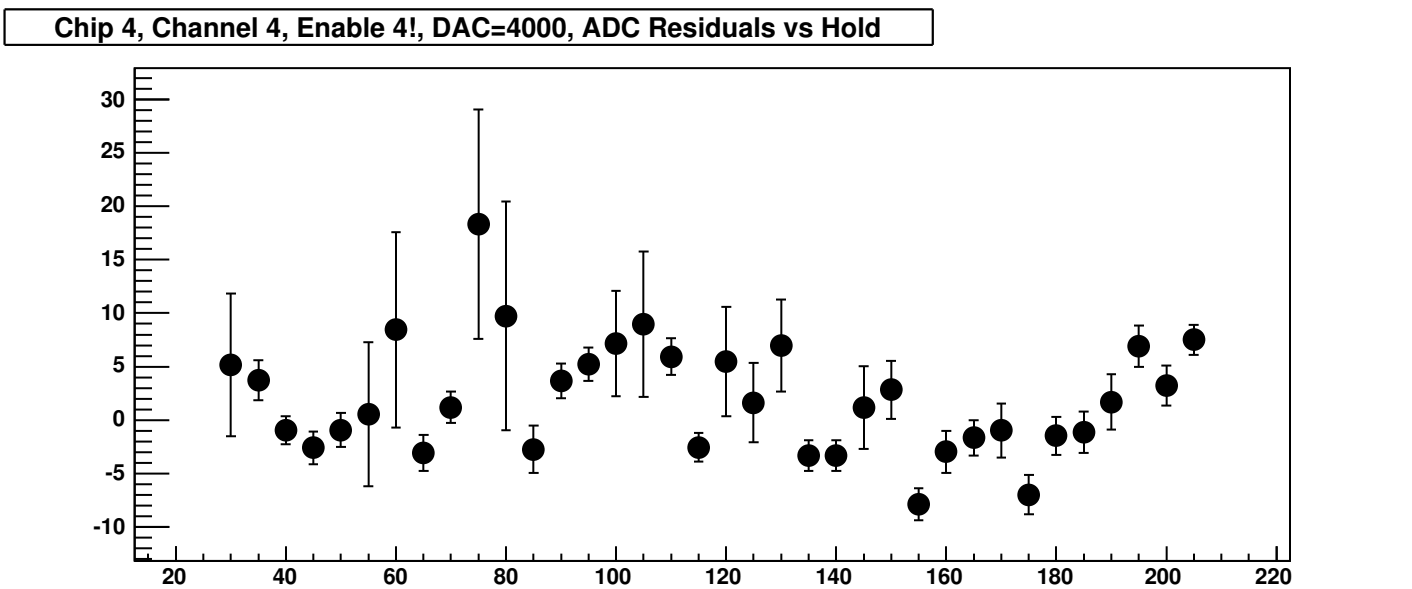


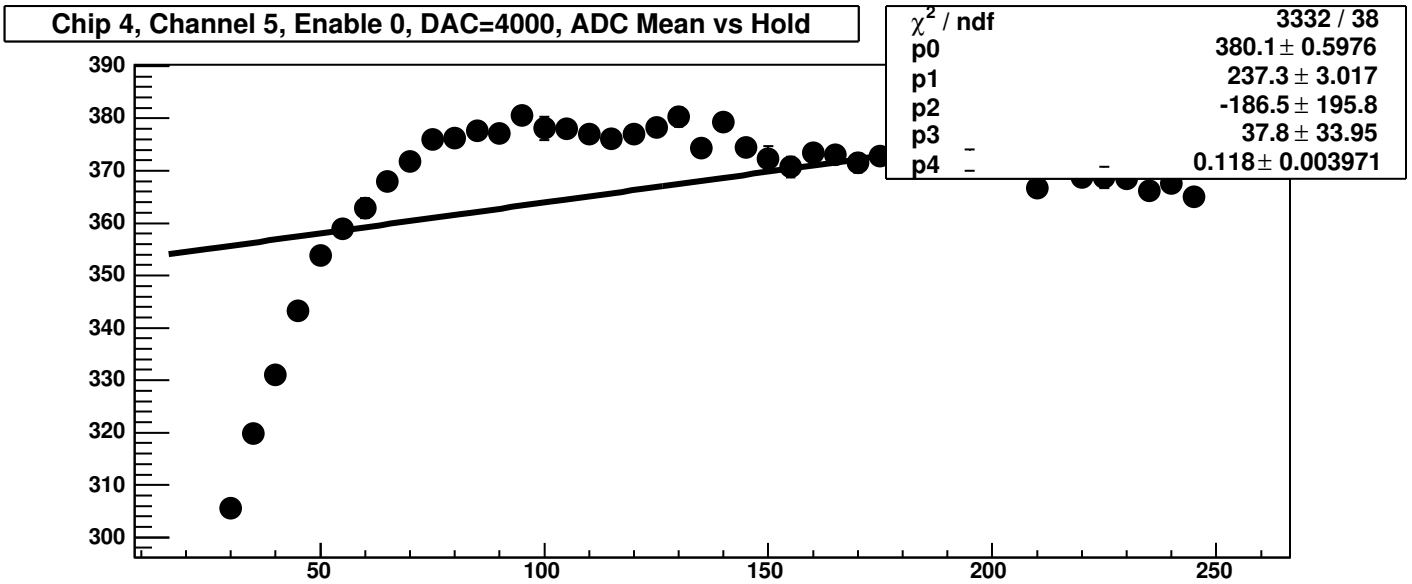
Chip 4, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

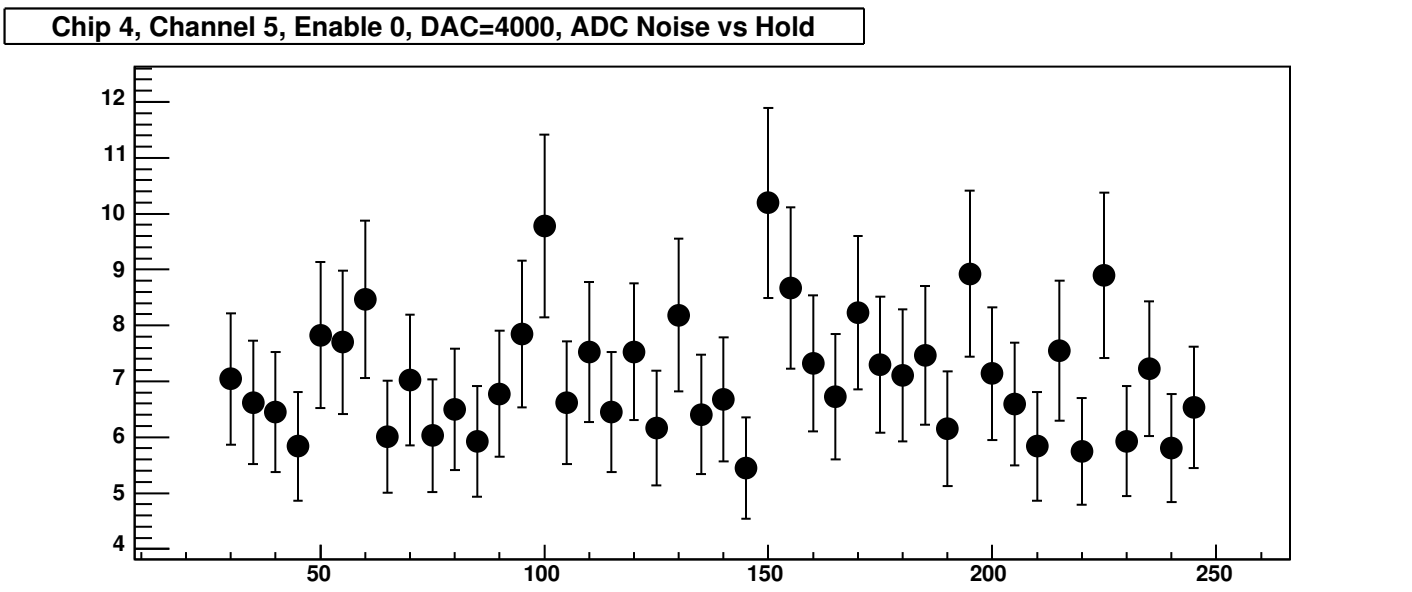


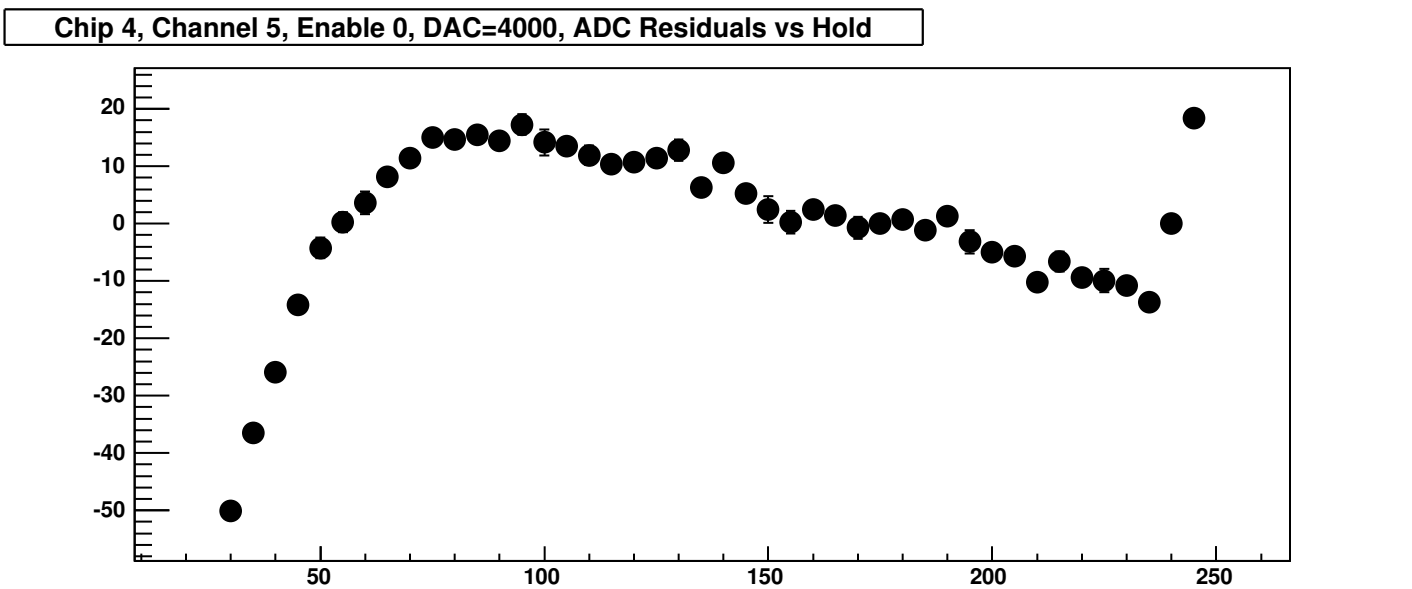

Chip 4, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 161.4 / 32
p0 -547.9 ± 1.038
p1 4.329 ± 0.02564
p2 1.113e+04 ± 1.717
p3 31.77 ± 0.01042
10000
8000
6000
4000
2000
0
20
40
60
80
100
120
140
160
180
200
220


Chip 4, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold
60
50
40
30
20
10
0
20
40
60
80
100
120
140
160
180
200
220


Chip 4, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
20
40
60
80
100
120
140
160
180
200
220

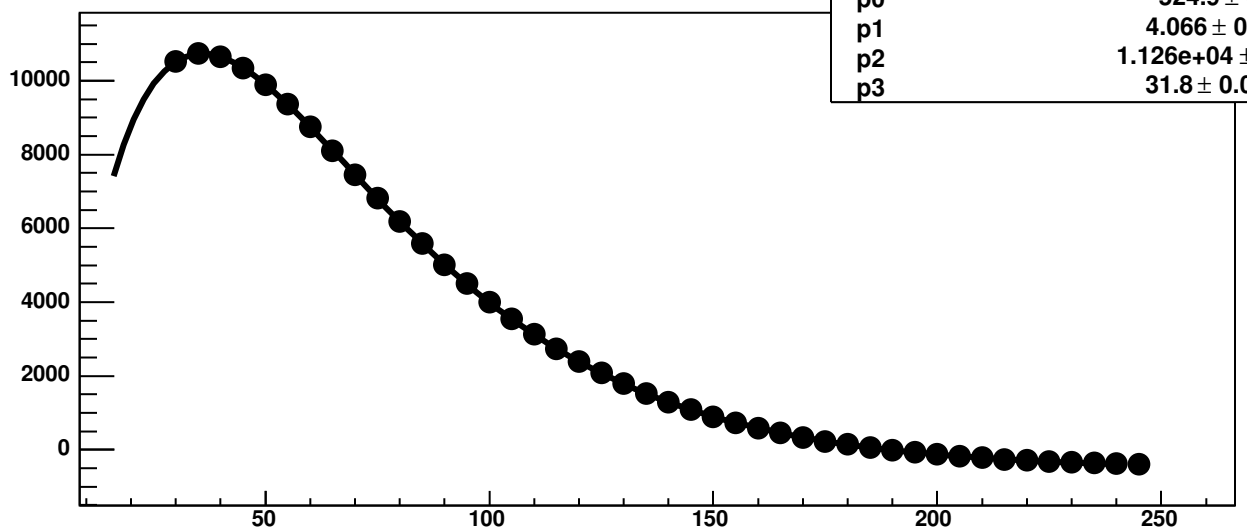

Chip 4, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3332 / 38
p0 380.1 ± 0.5976
p1 237.3 ± 3.017
p2 -186.5 ± 195.8
p3 37.8 ± 33.95
p4 0.118 ± 0.003971
390
380
370
360
350
340
330
320
310
300
50
100
150
200
250


Chip 4, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

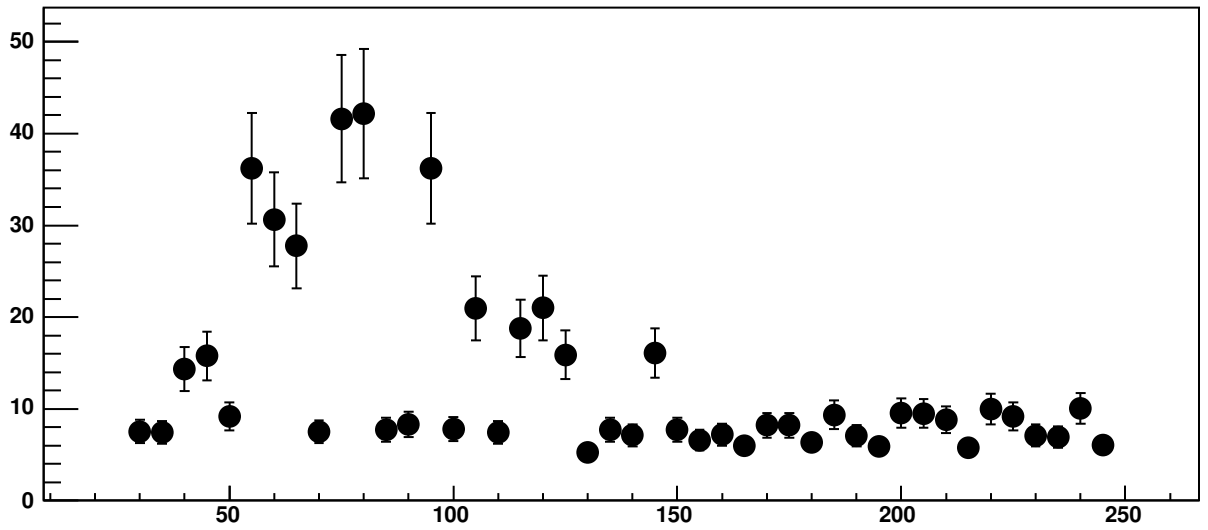

Chip 4, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
-50
50
100
150
200
250

Chip 4, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold

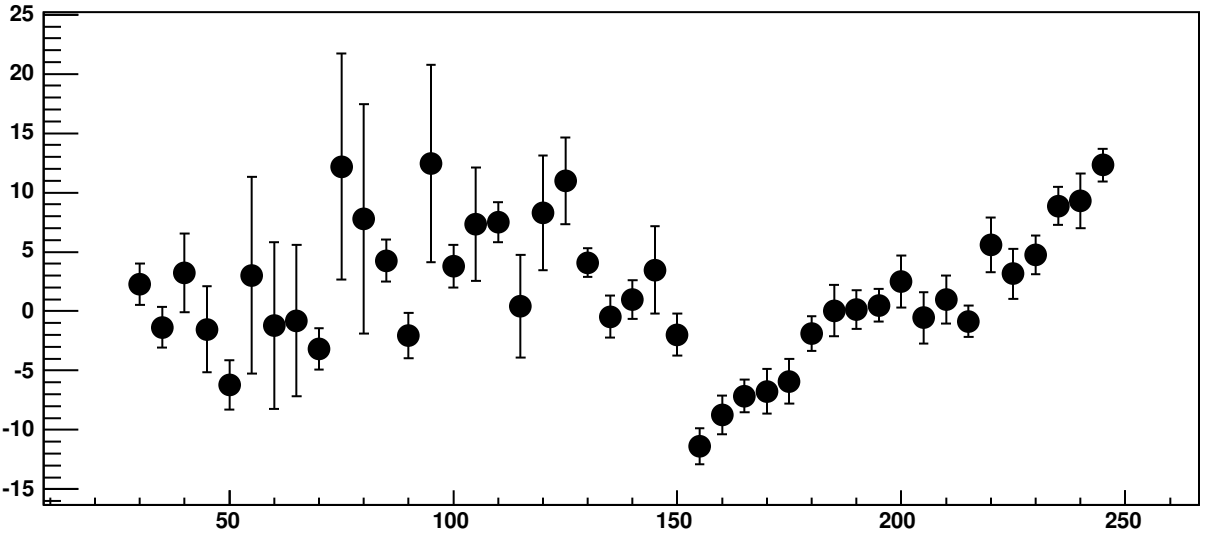
χ^2 / ndf	281.5 / 39
p0	-524.9 ± 0.6885
p1	4.066 ± 0.01906
p2	1.126e+04 ± 1.089
p3	31.8 ± 0.008346



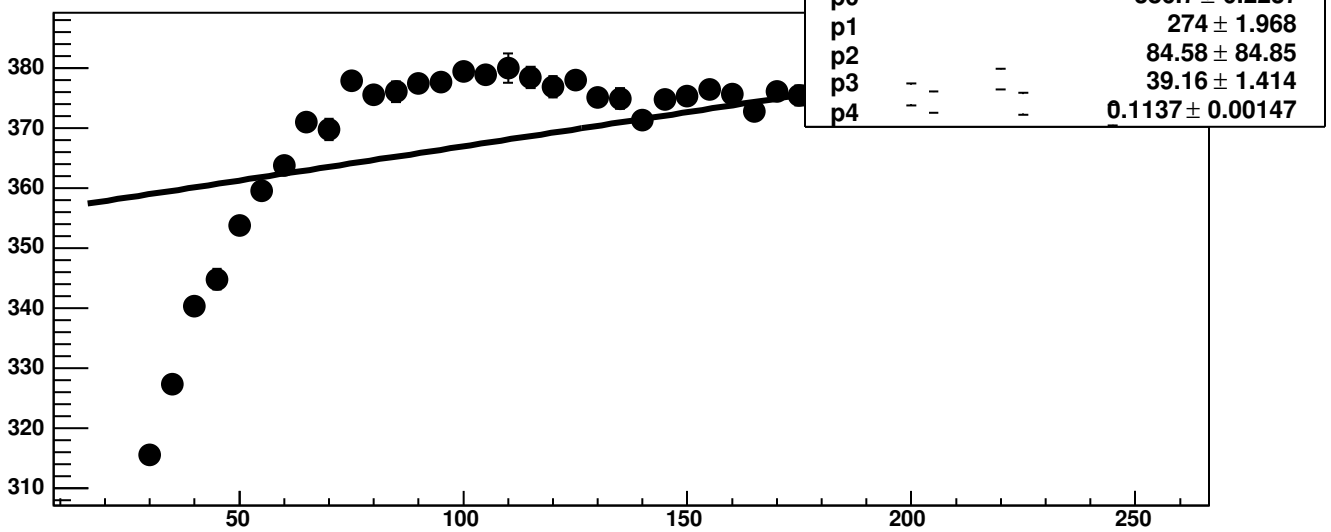
Chip 4, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold



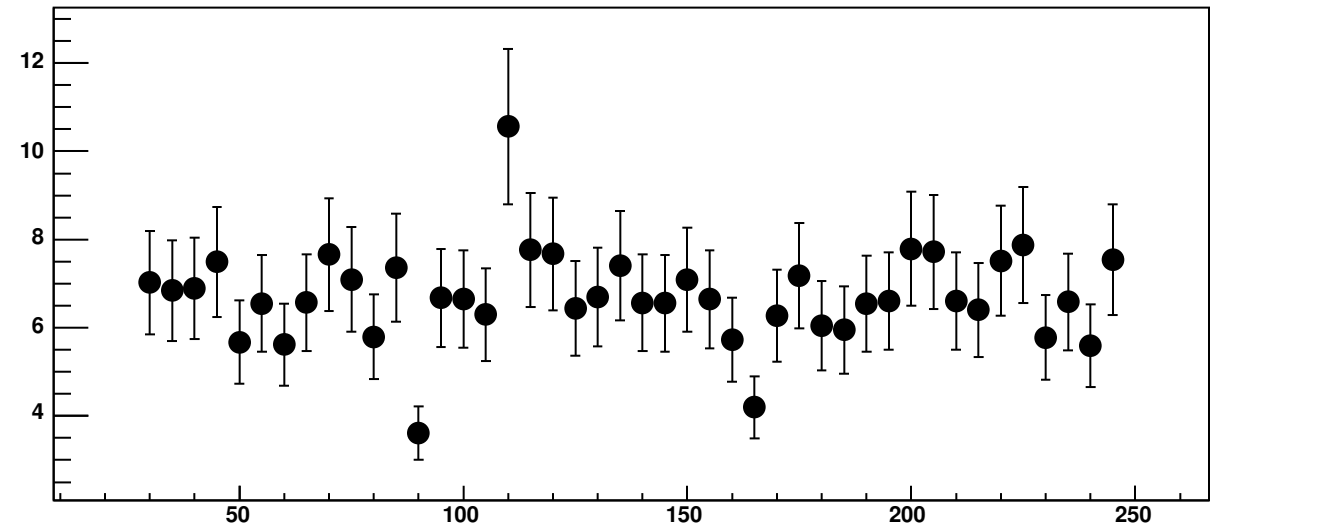
Chip 4, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold



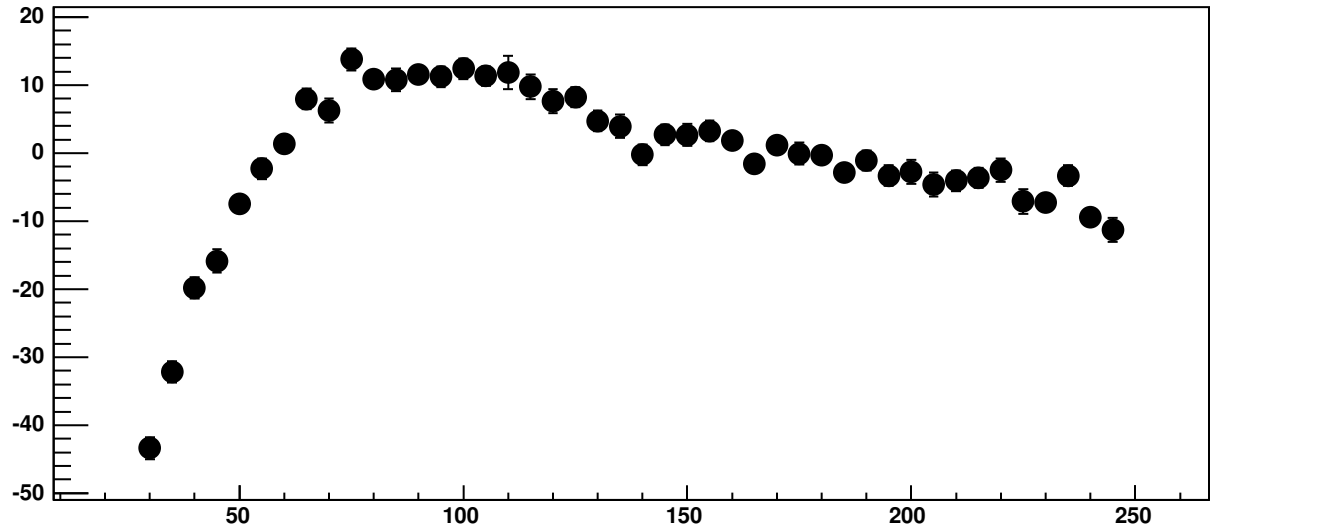
Chip 4, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

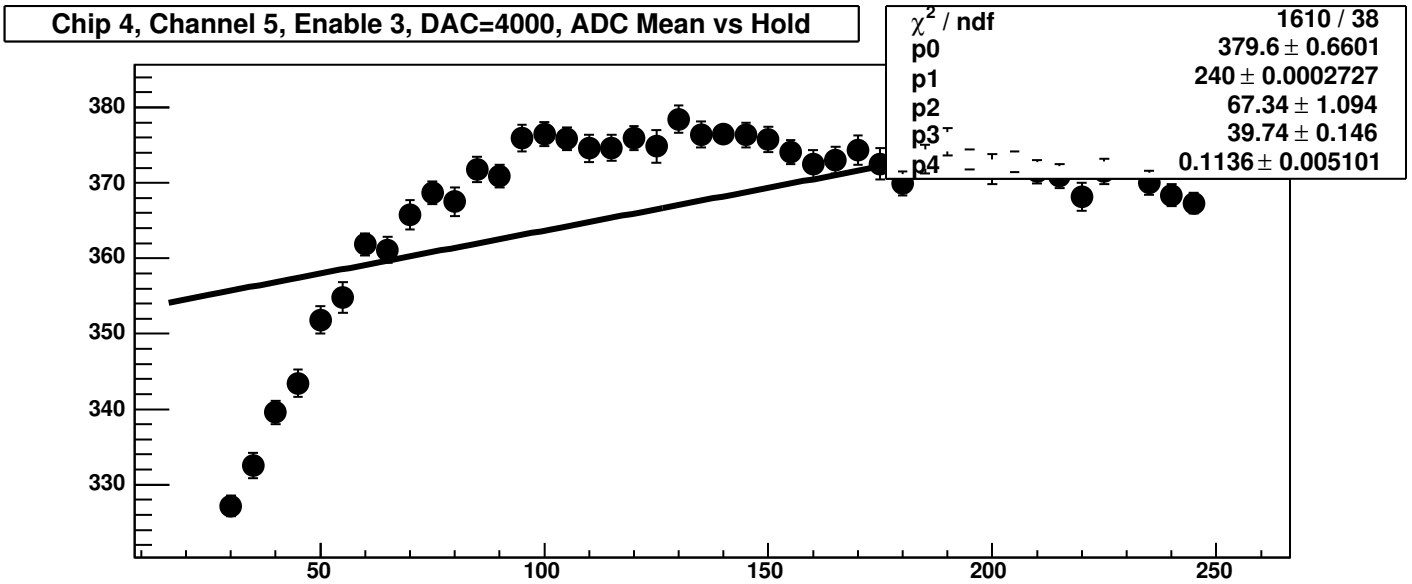


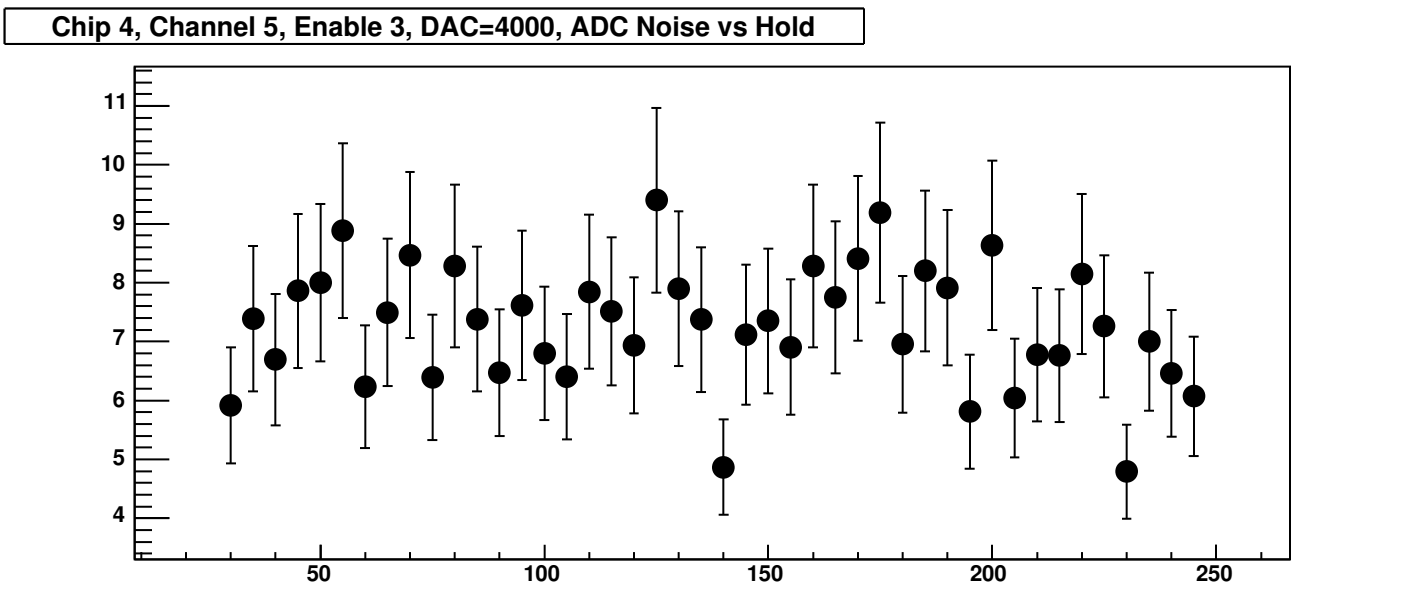
Chip 4, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold

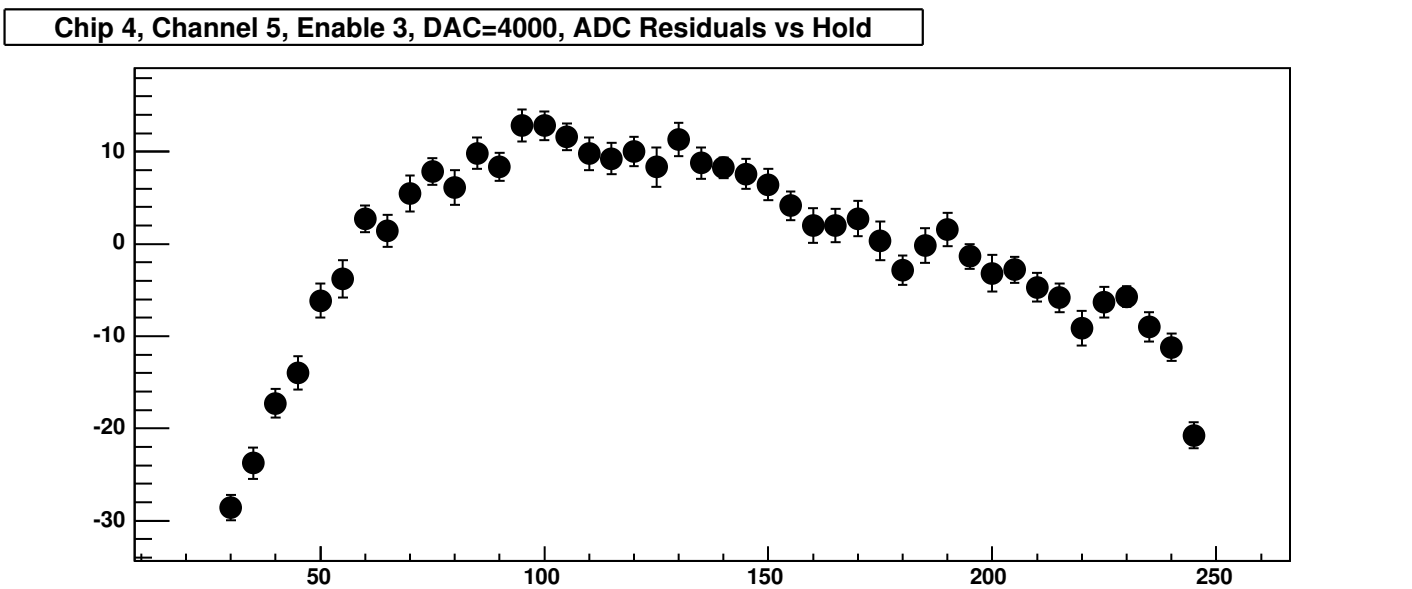


Chip 4, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

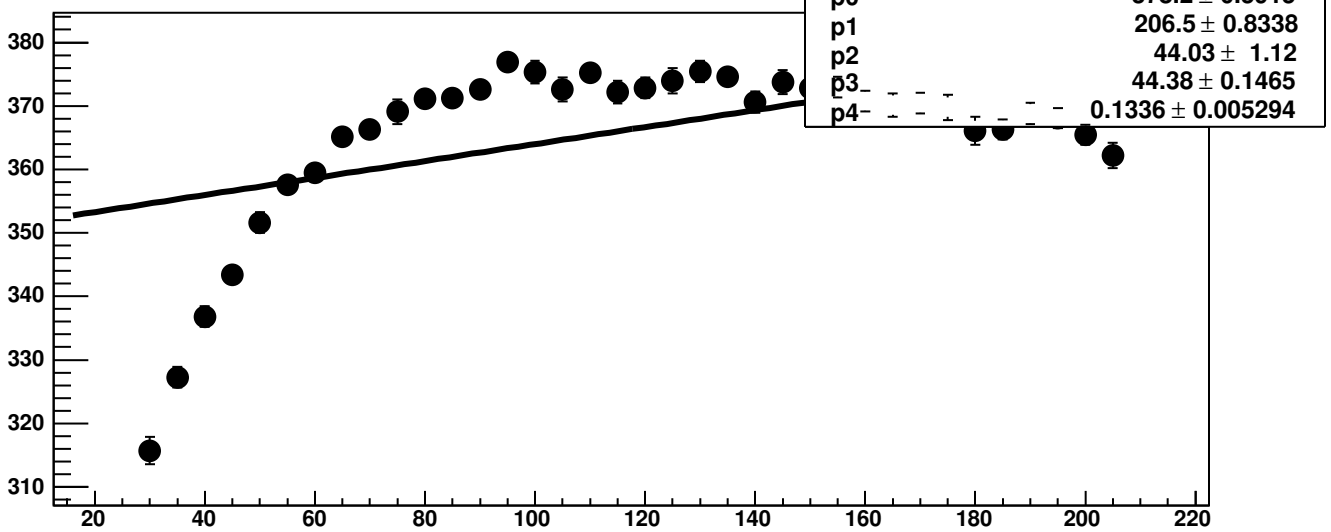



Chip 4, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 1610 / 38
p0 379.6 ± 0.6601
p1 240 ± 0.0002727
p2 67.34 ± 1.094
p3 39.74 ± 0.146
p4 0.1136 ± 0.005101
380
370
360
350
340
330
50
100
150
200
250

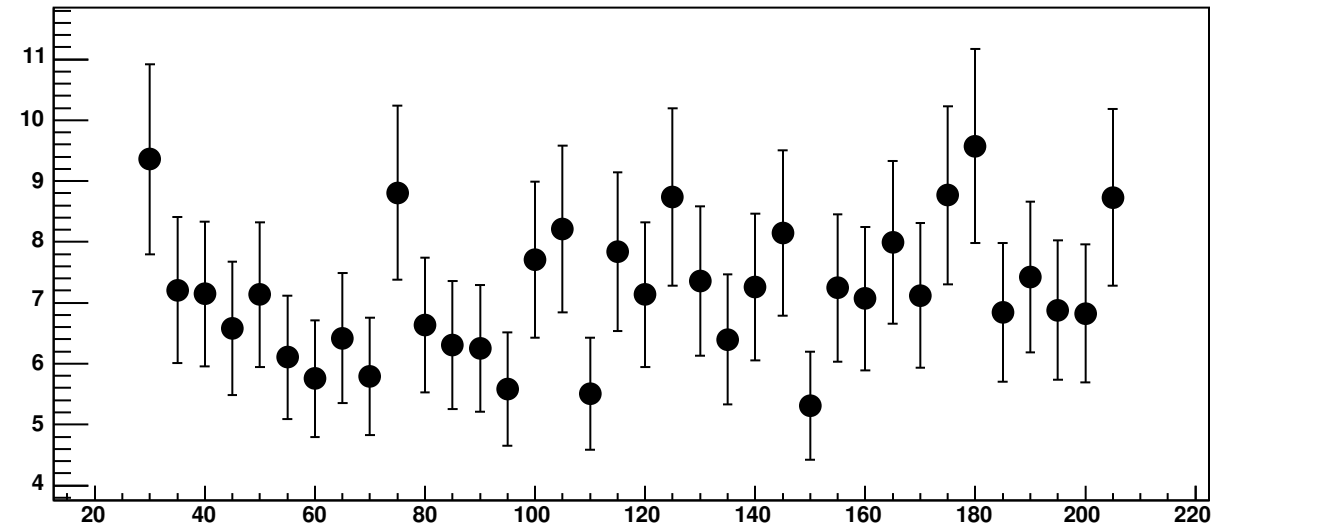

Chip 4, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 4, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold
10
0
-10
-20
-30
50
100
150
200
250

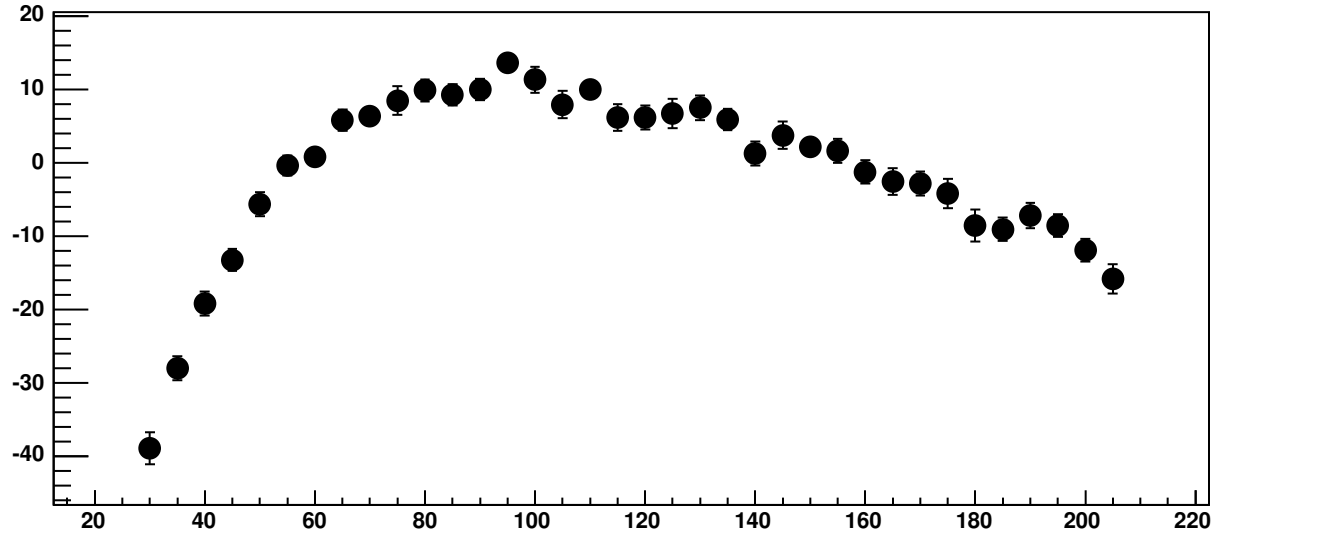
Chip 4, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold

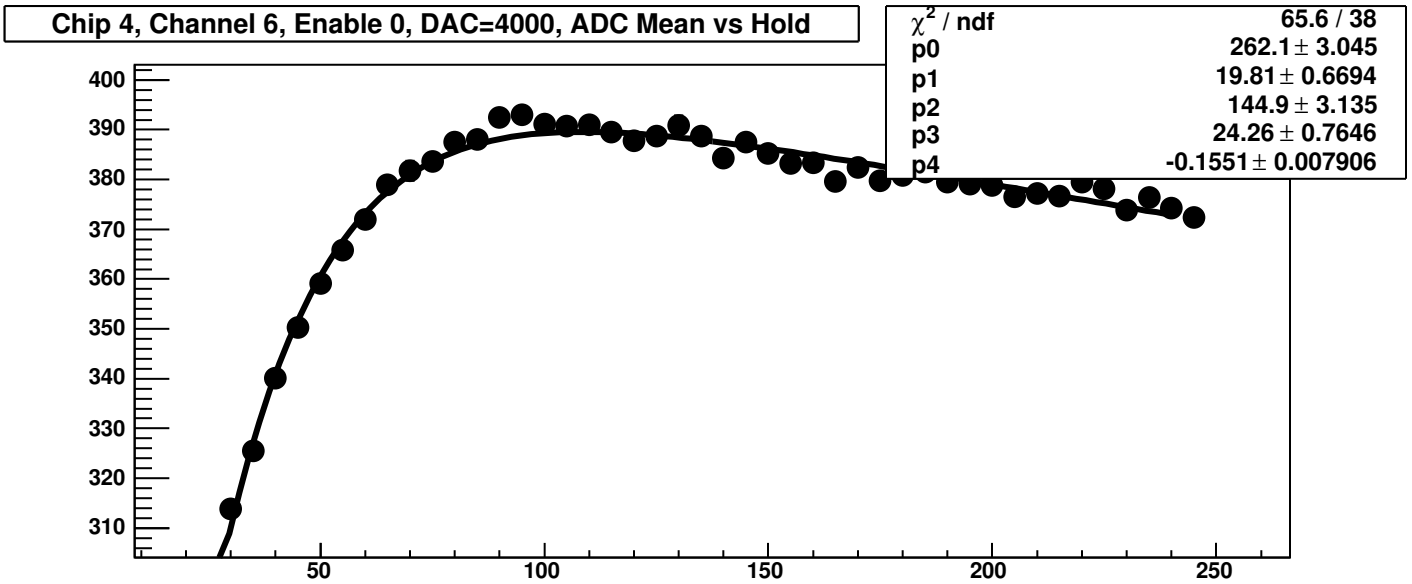


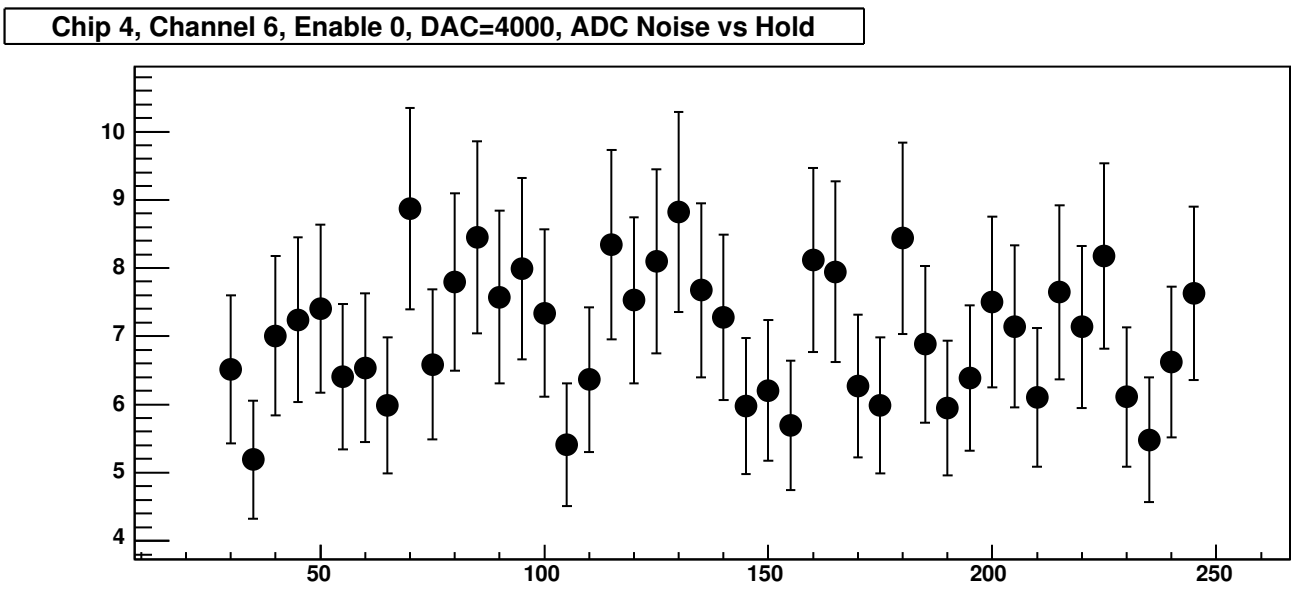
Chip 4, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold

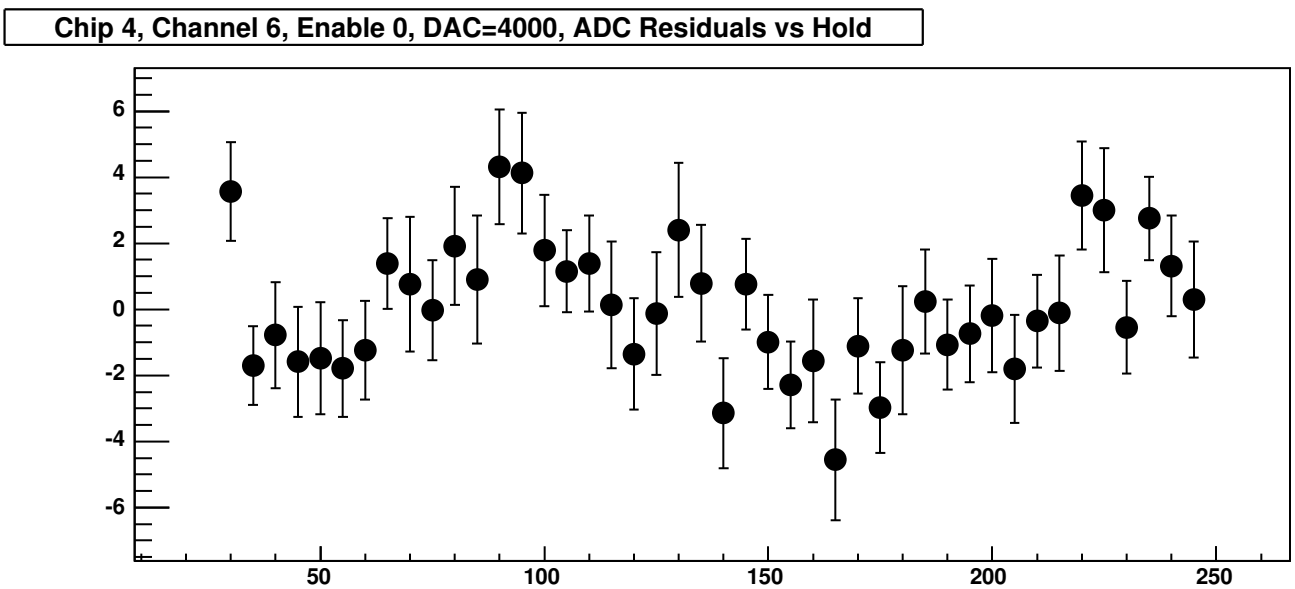


Chip 4, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold

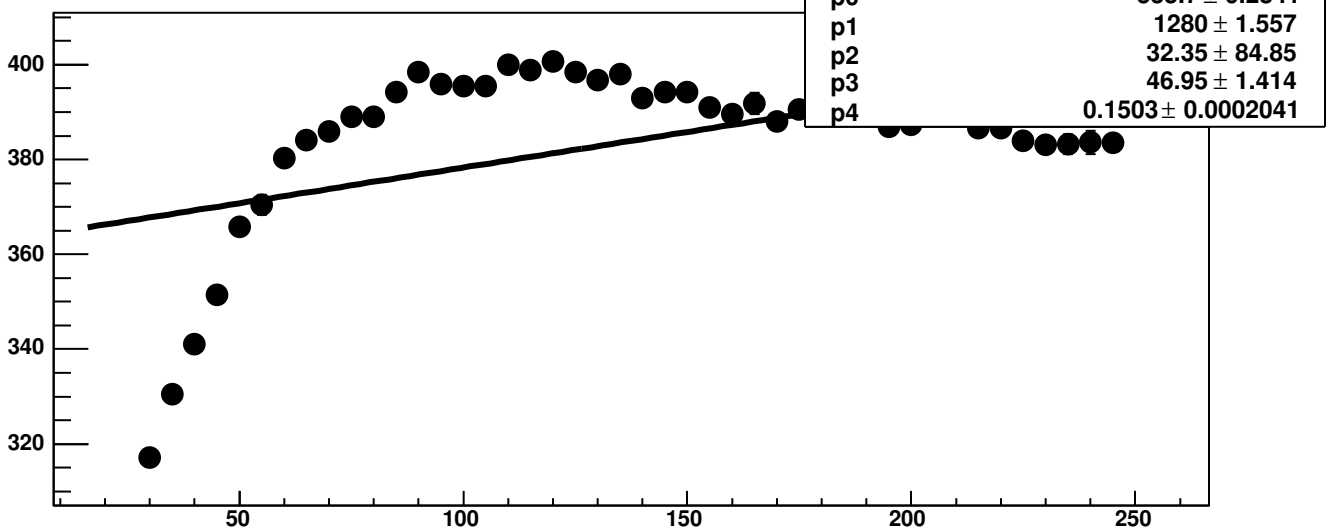



Chip 4, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 65.6 / 38
p0 262.1 ± 3.045
p1 19.81 ± 0.6694
p2 144.9 ± 3.135
p3 24.26 ± 0.7646
p4 -0.1551 ± 0.007906
400
390
380
370
360
350
340
330
320
310
50
100
150
200
250

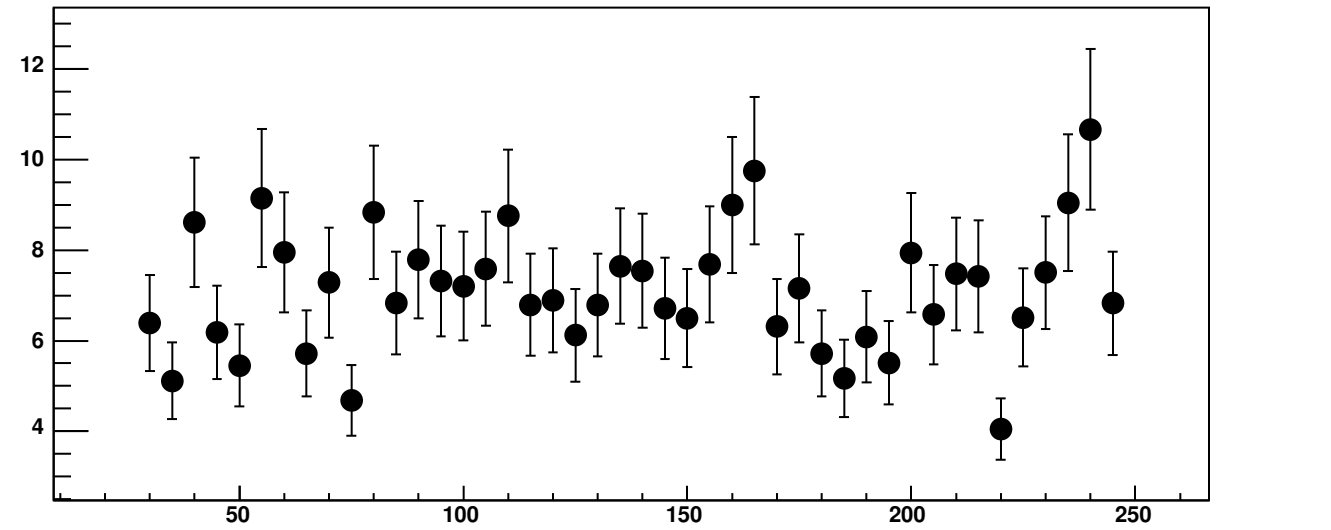

Chip 4, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold
10
9
8
7
6
5
4
50
100
150
200
250


Chip 4, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
50
100
150
200
250

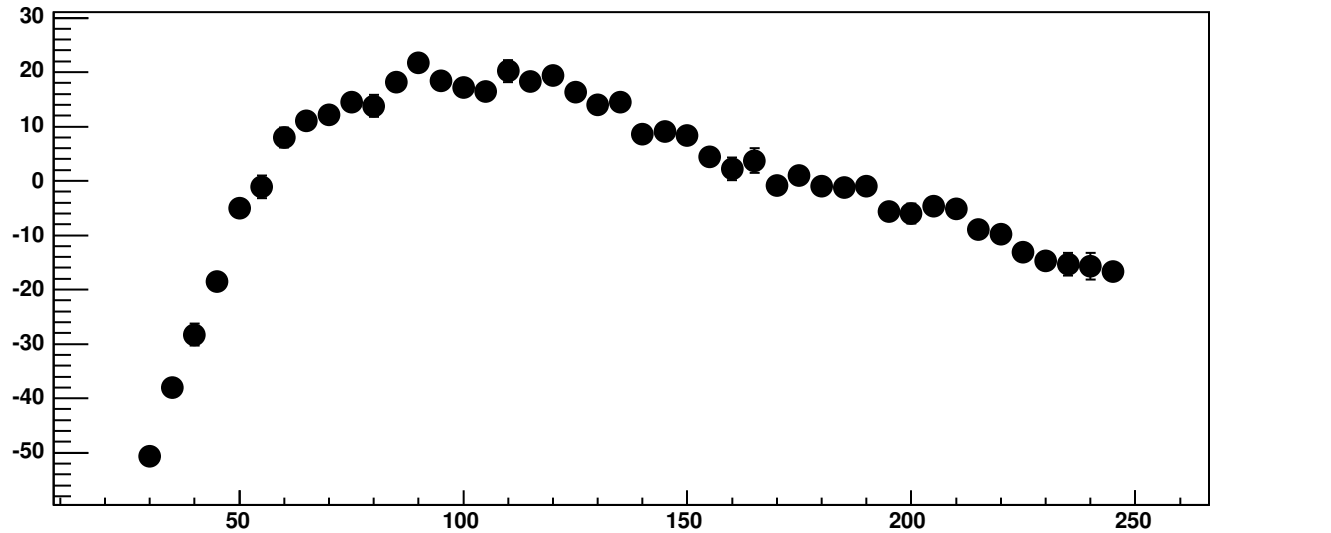
Chip 4, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold

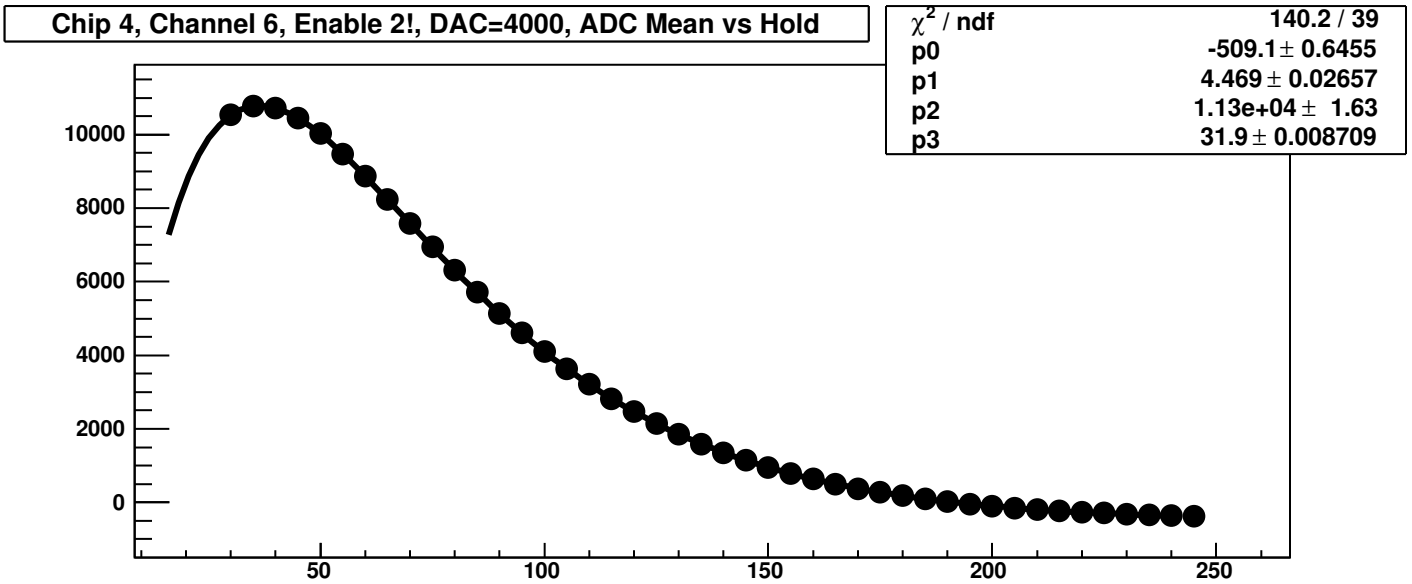


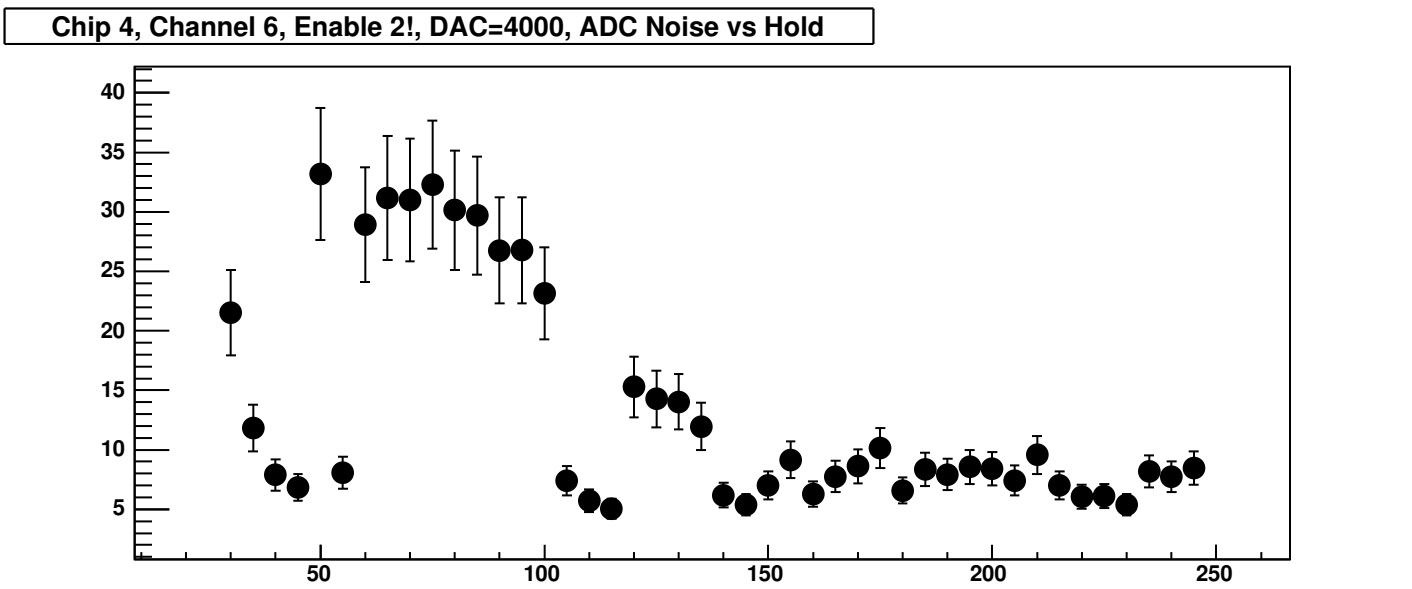
Chip 4, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold

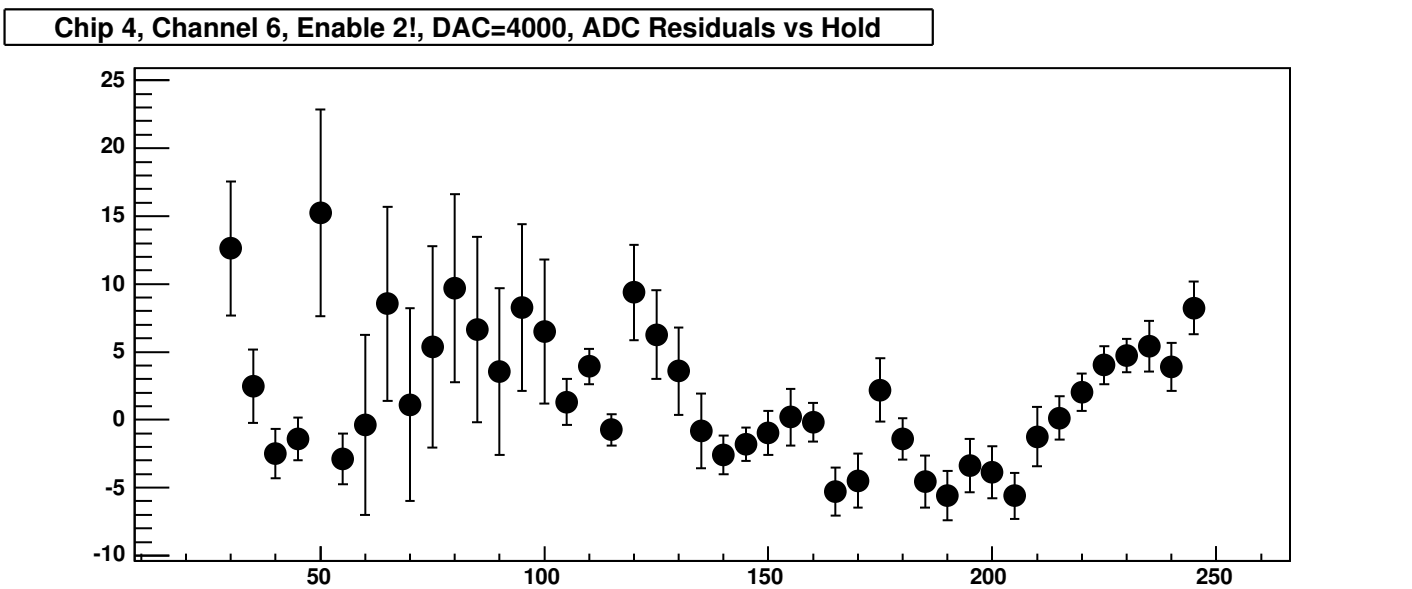


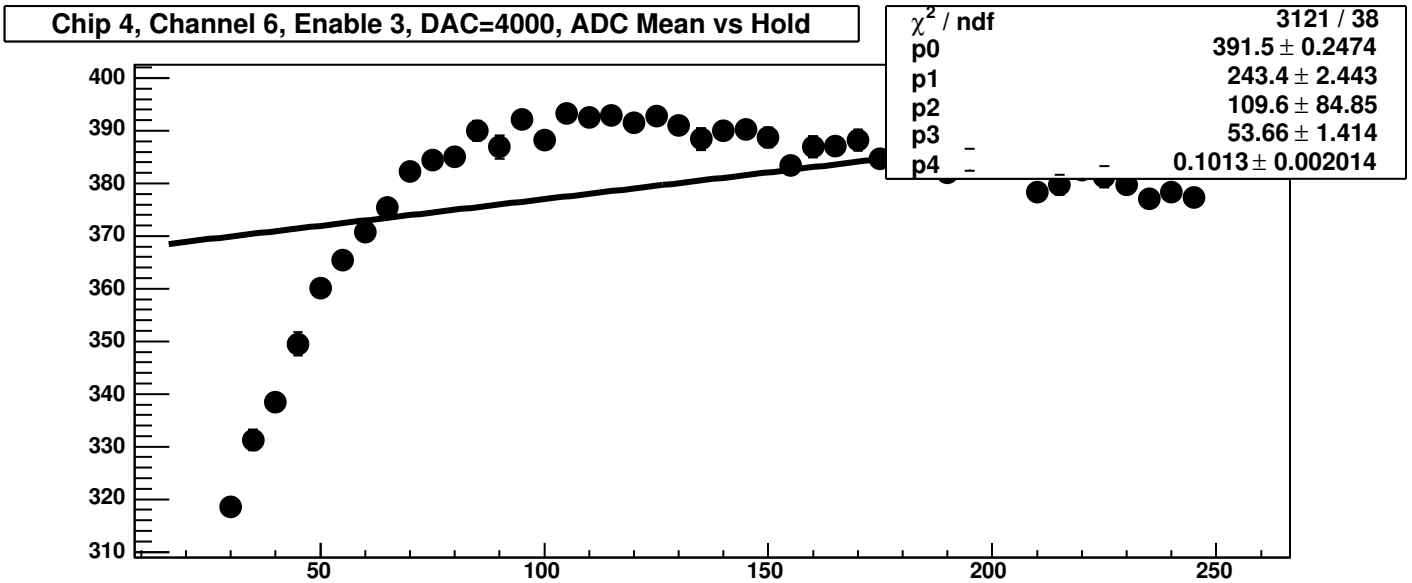
Chip 4, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold

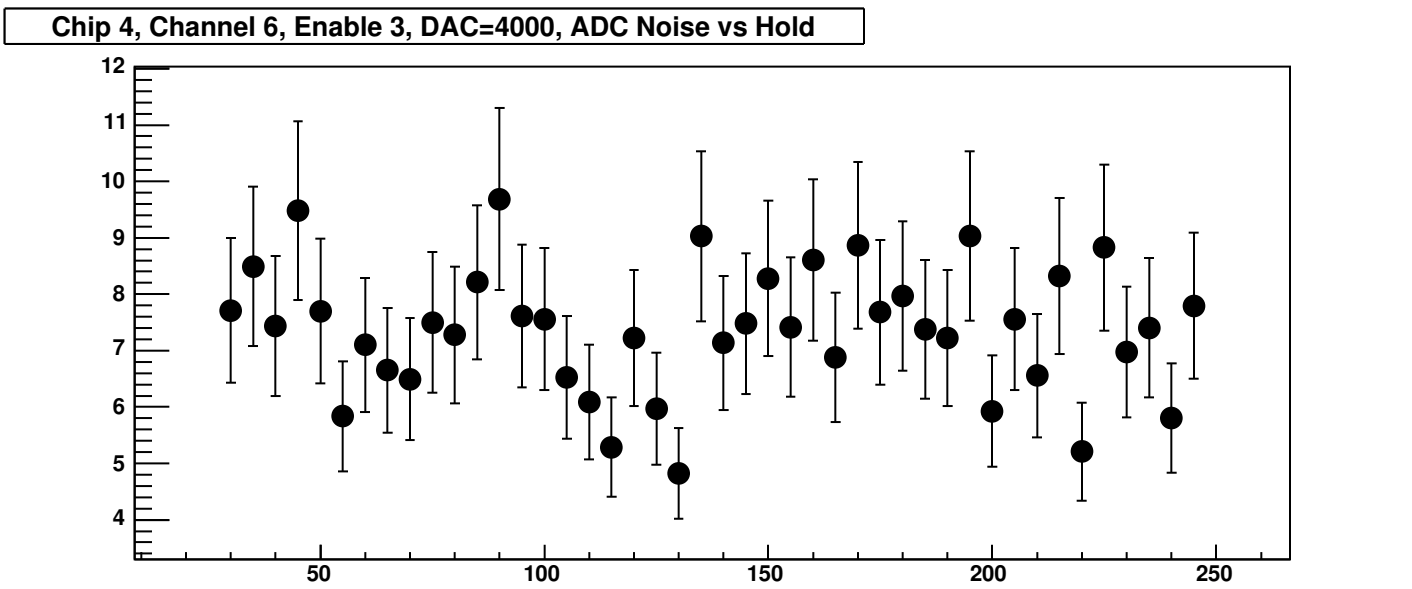


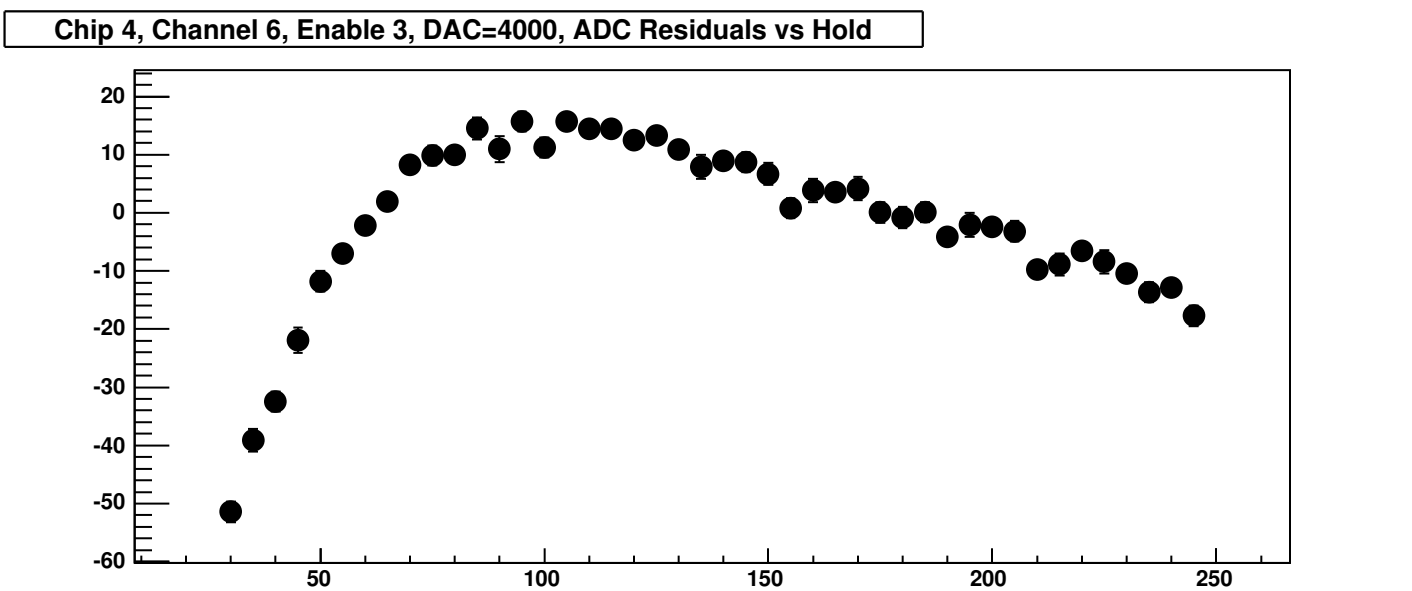

Chip 4, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 140.2 / 39
p0 -509.1 ± 0.6455
p1 4.469 ± 0.02657
p2 1.13e+04 ± 1.63
p3 31.9 ± 0.008709
10000
8000
6000
4000
2000
0
50
100
150
200
250


Chip 4, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold
40
35
30
25
20
15
10
5
50
100
150
200
250

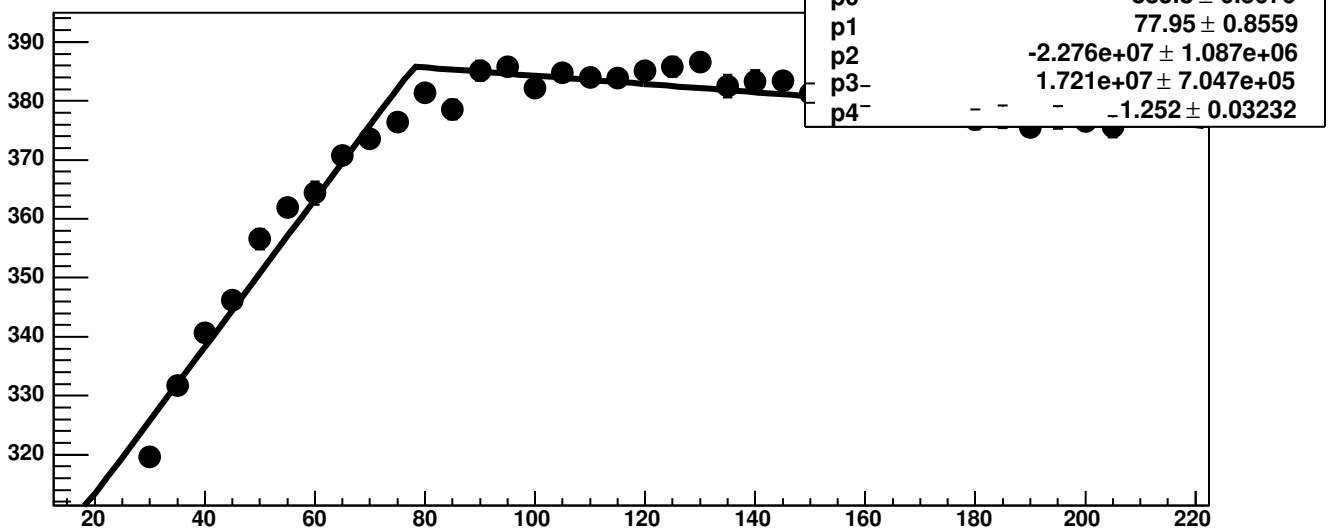

Chip 4, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
50
100
150
200
250


Chip 4, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 3121 / 38
p0 391.5 ± 0.2474
p1 243.4 ± 2.443
p2 109.6 ± 84.85
p3 53.66 ± 1.414
p4 0.1013 ± 0.002014
400
390
380
370
360
350
340
330
320
310
50
100
150
200
250

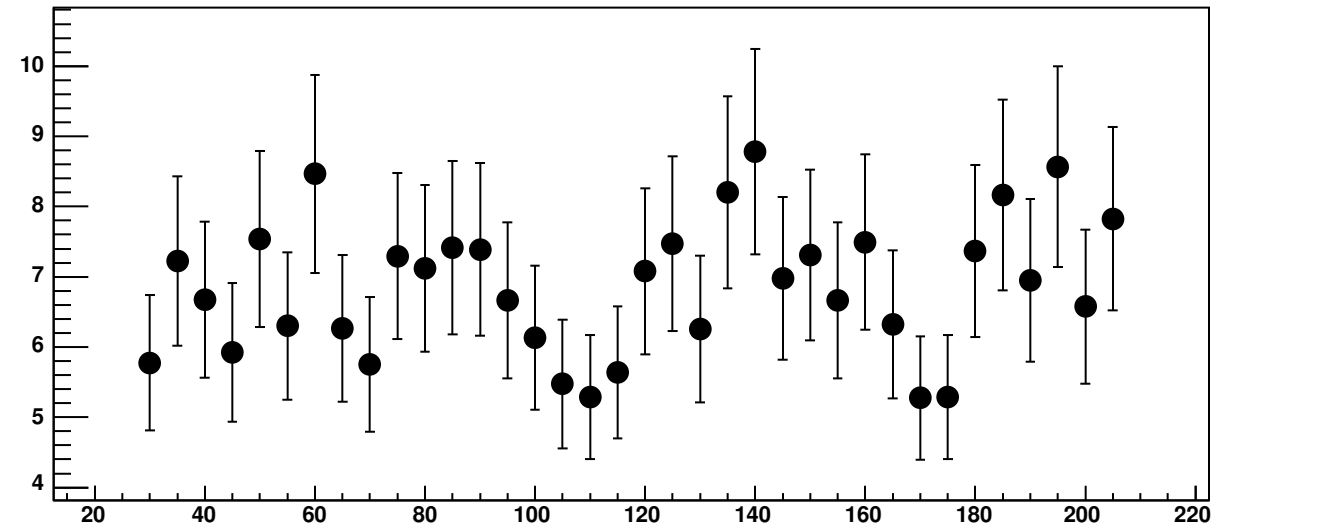

Chip 4, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 4, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
-50
-60
50
100
150
200
250

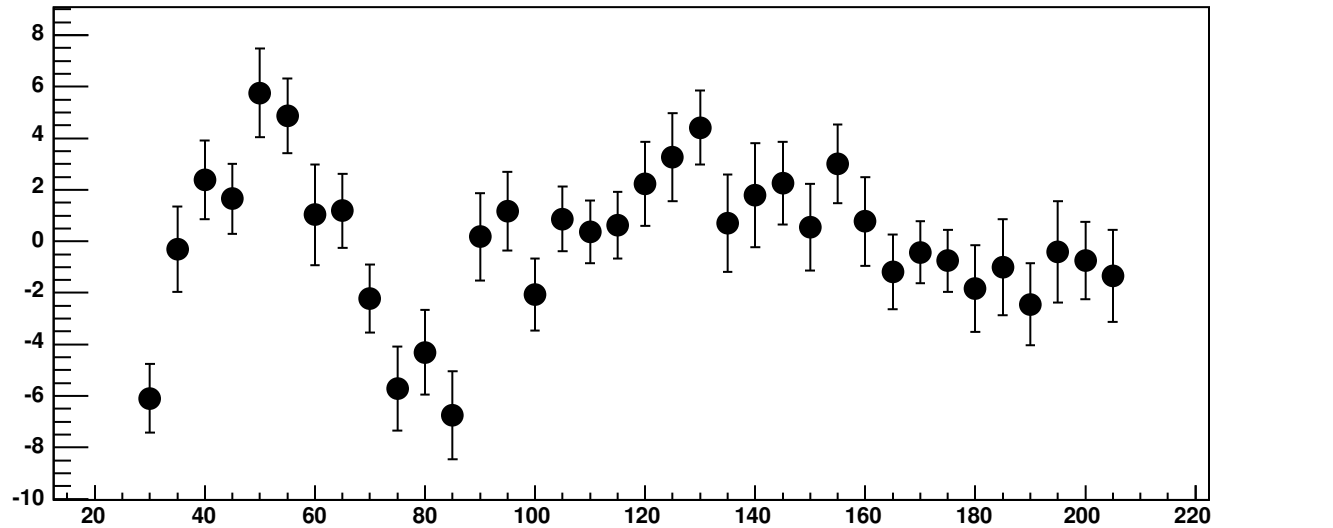
Chip 4, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

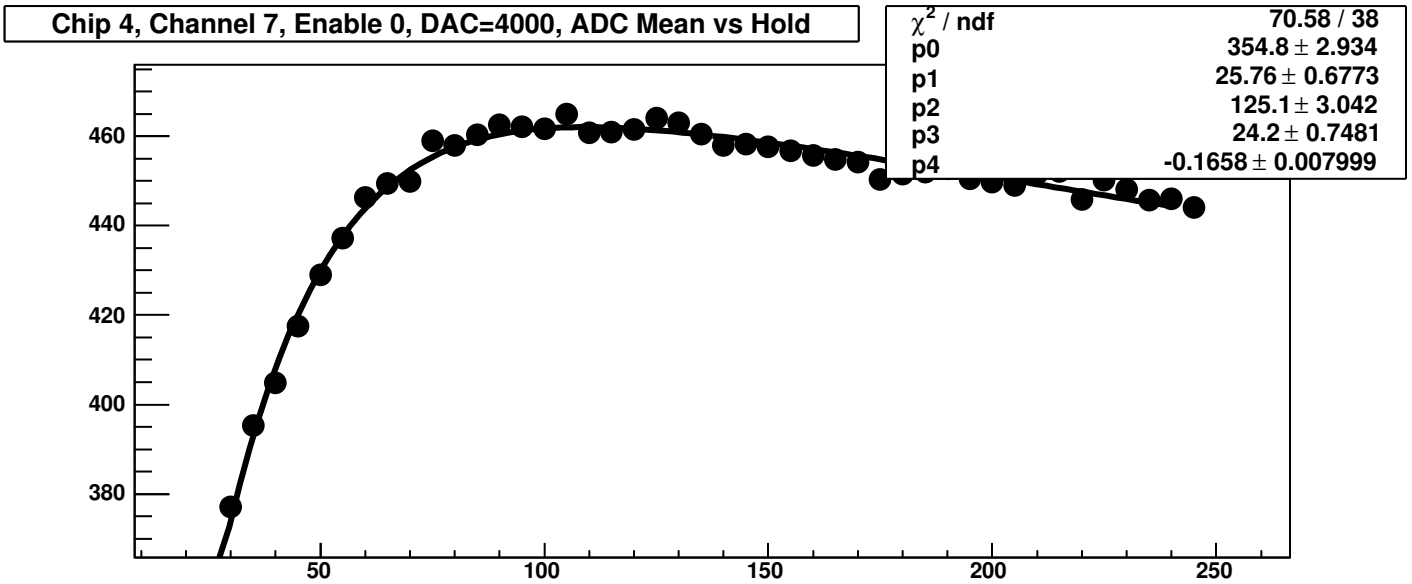


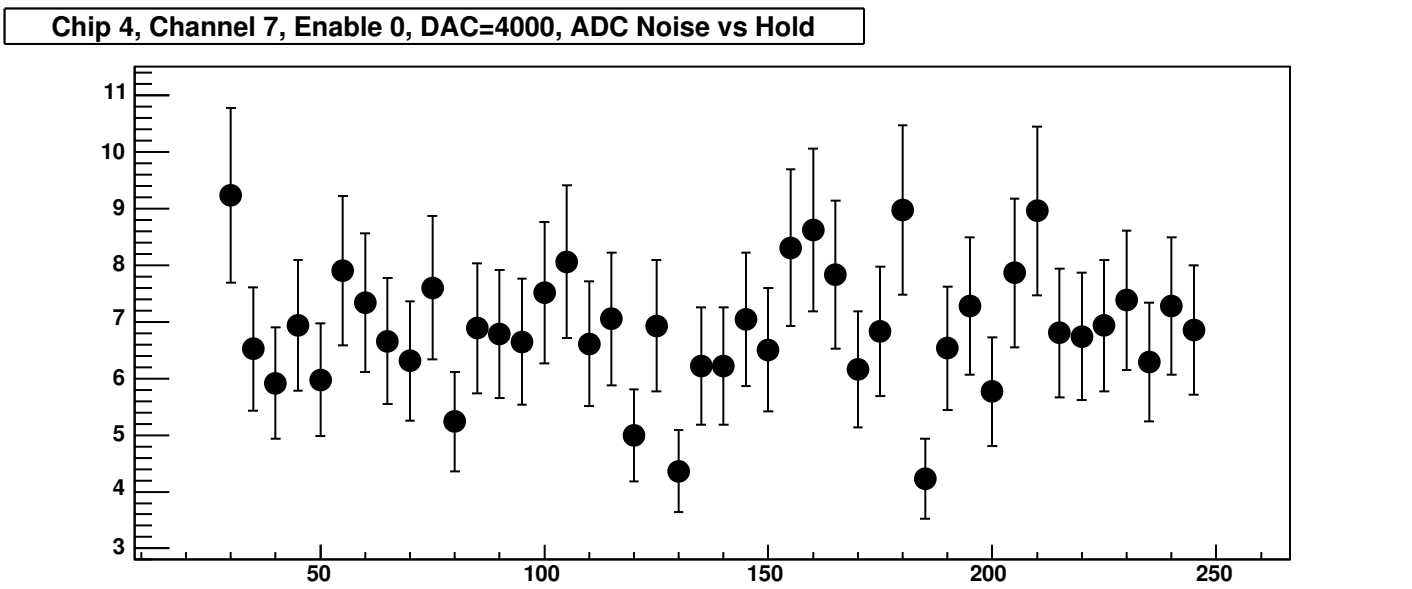
Chip 4, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold

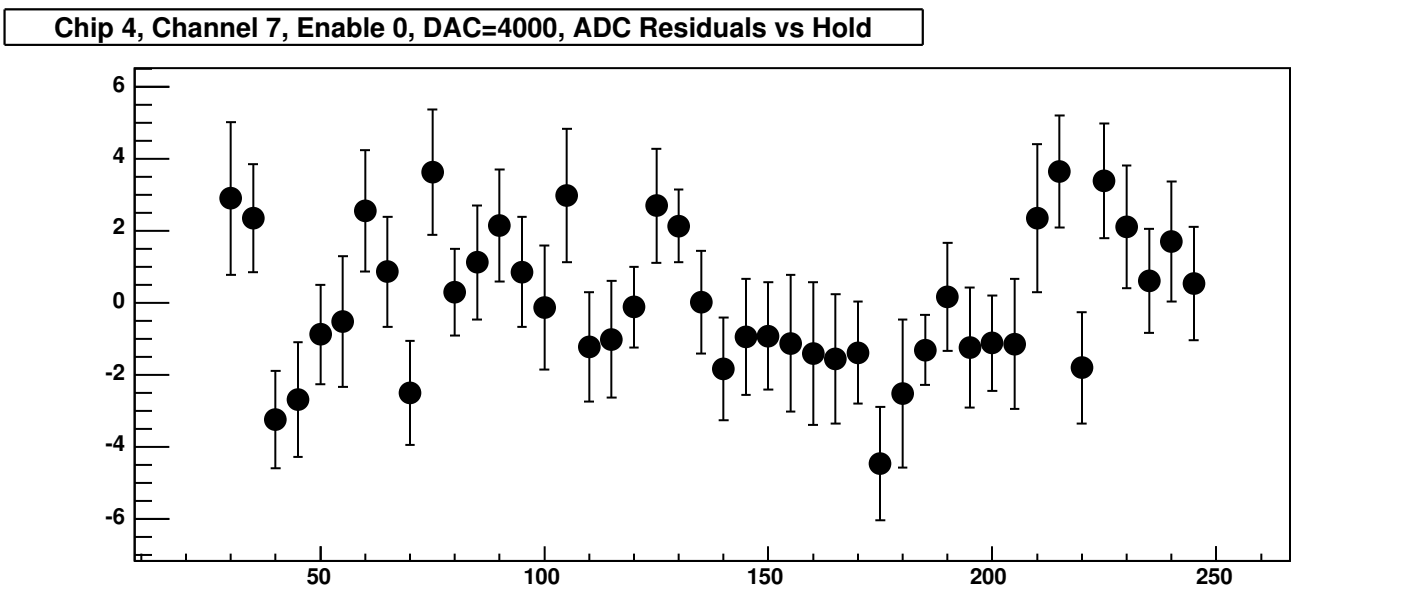


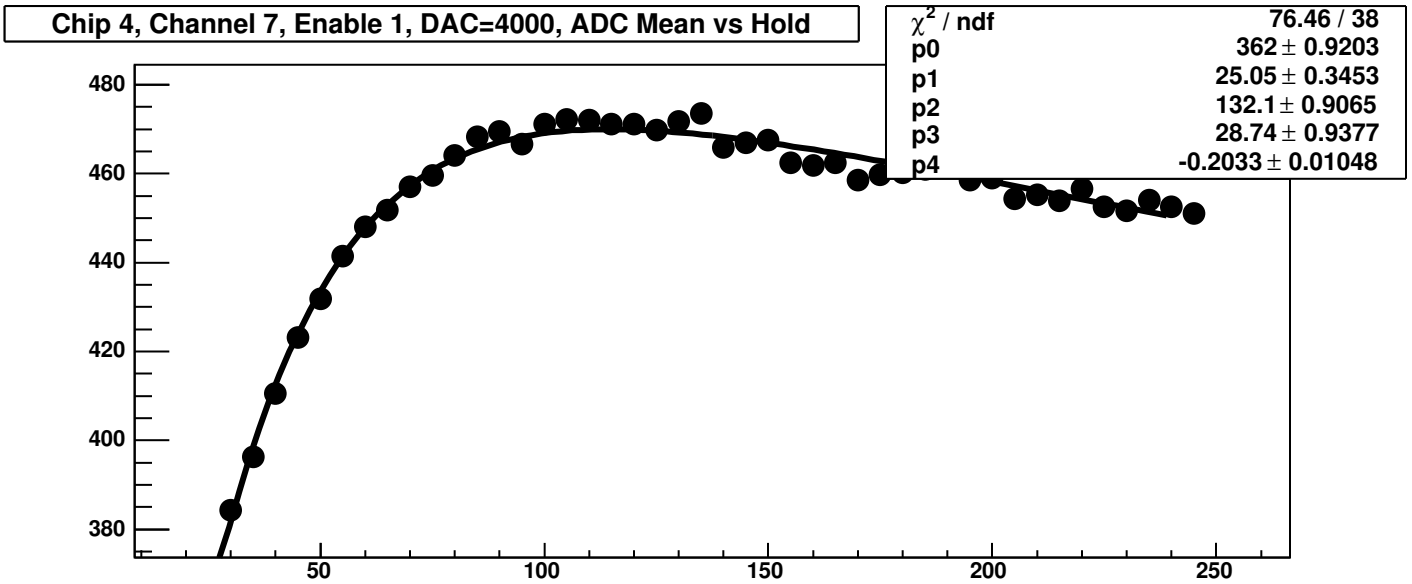
Chip 4, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

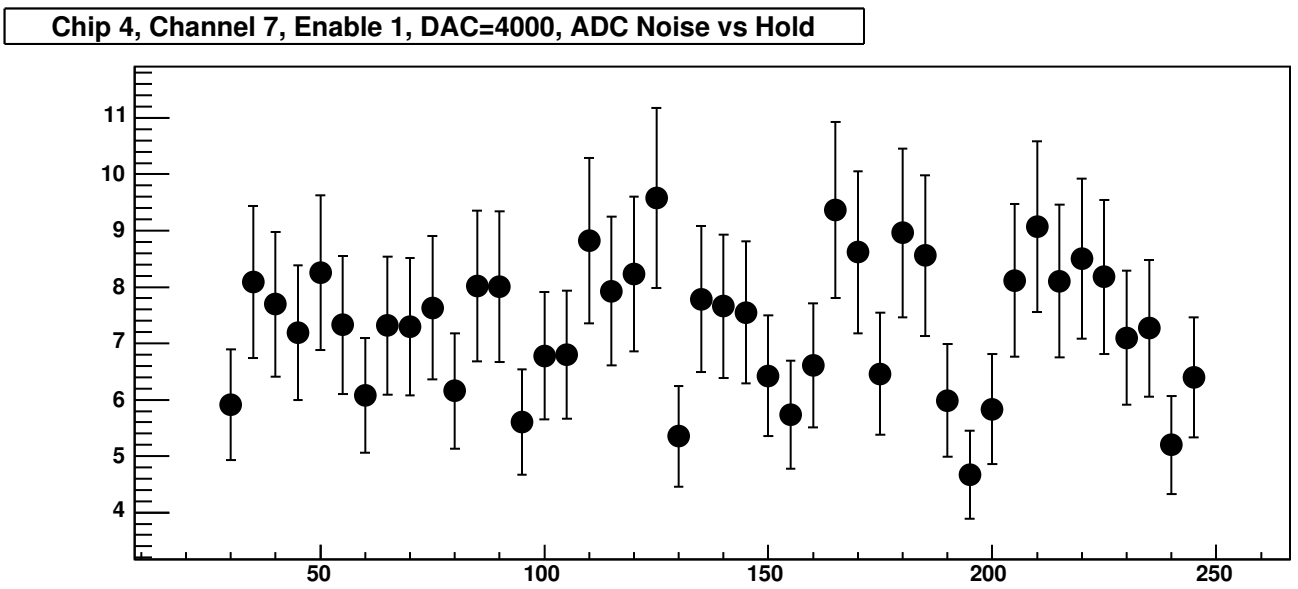


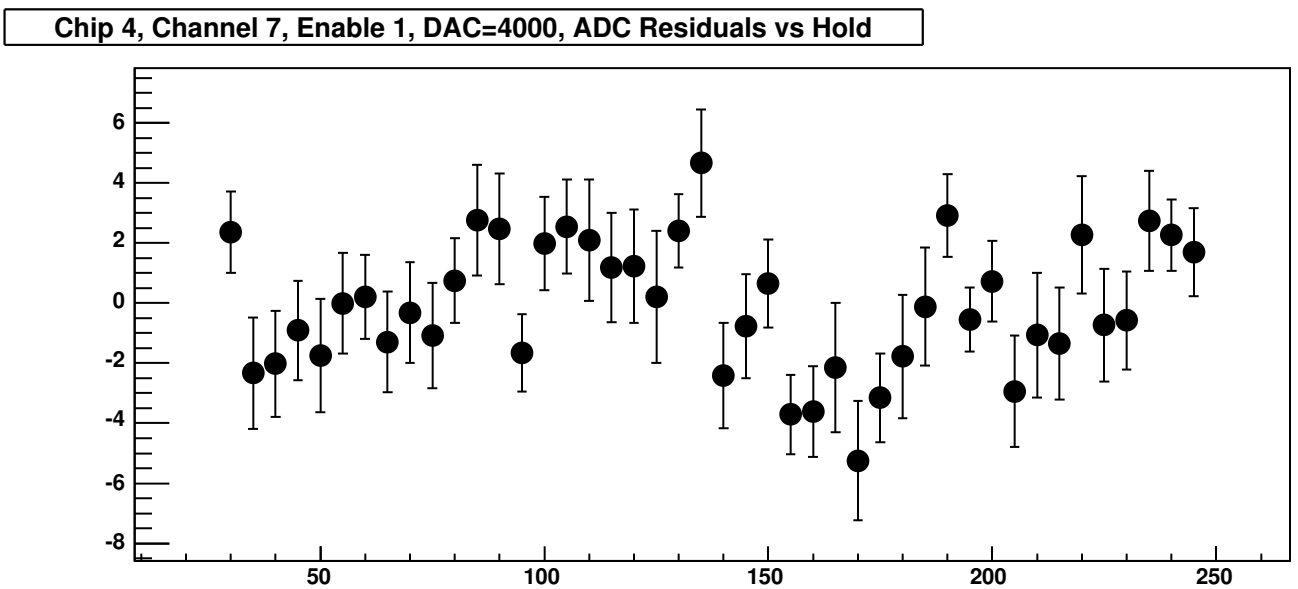

Chip 4, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 70.58 / 38
p0 354.8 ± 2.934
p1 25.76 ± 0.6773
p2 125.1 ± 3.042
p3 24.2 ± 0.7481
p4 -0.1658 ± 0.007999
460
440
420
400
380
50
100
150
200
250


Chip 4, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
50
100
150
200
250

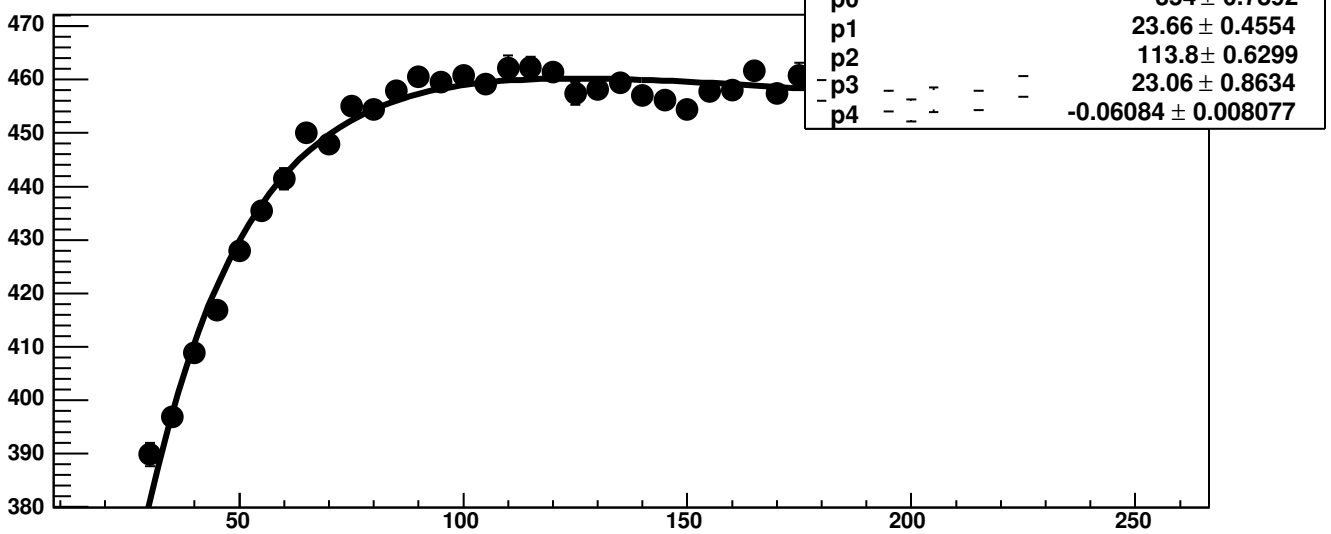

Chip 4, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
50
100
150
200
250


Chip 4, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 76.46 / 38
p0 362 ± 0.9203
p1 25.05 ± 0.3453
p2 132.1 ± 0.9065
p3 28.74 ± 0.9377
p4 -0.2033 ± 0.01048
480
460
440
420
400
380
50
100
150
200
250

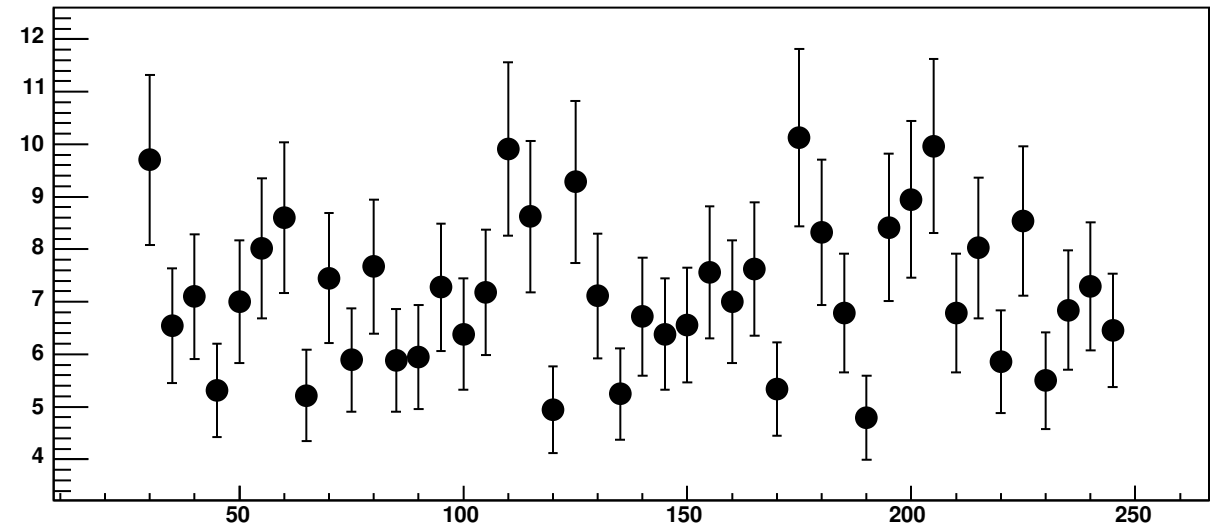

Chip 4, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 4, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

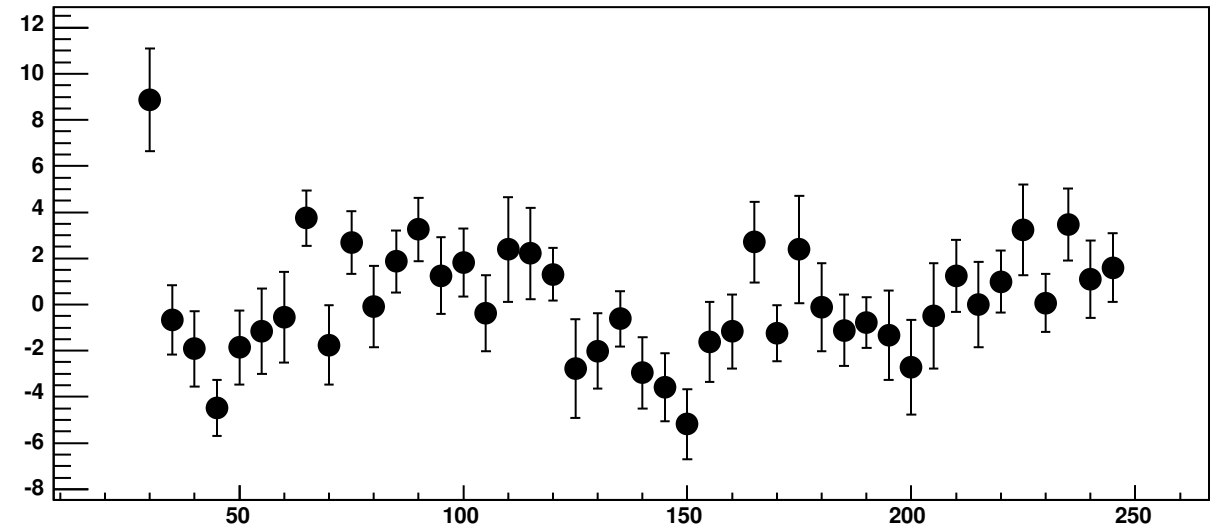
Chip 4, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold



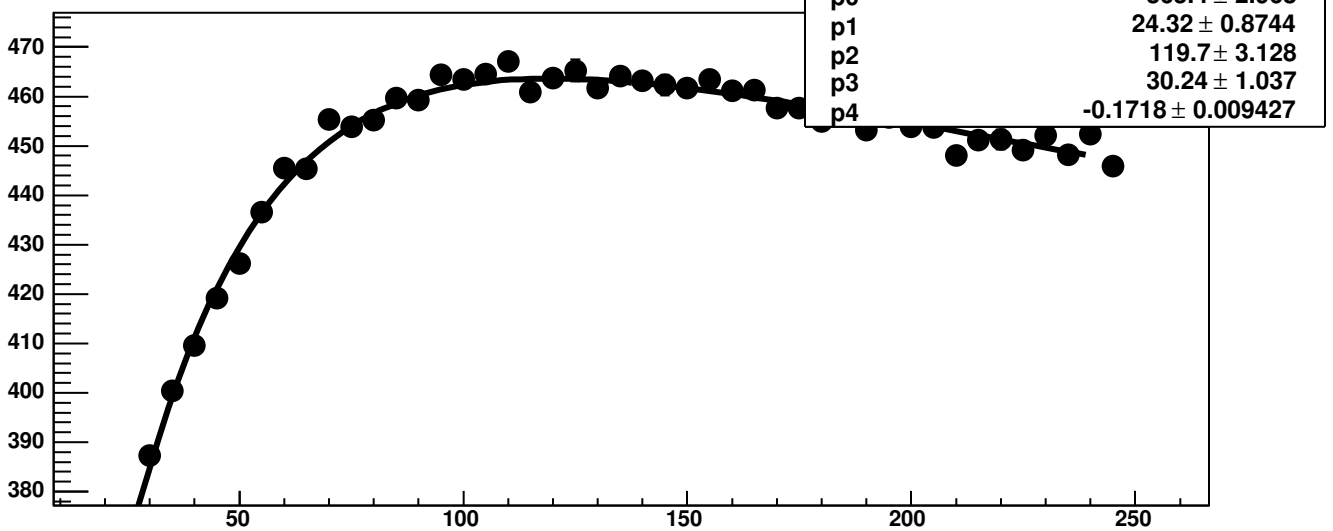
Chip 4, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold



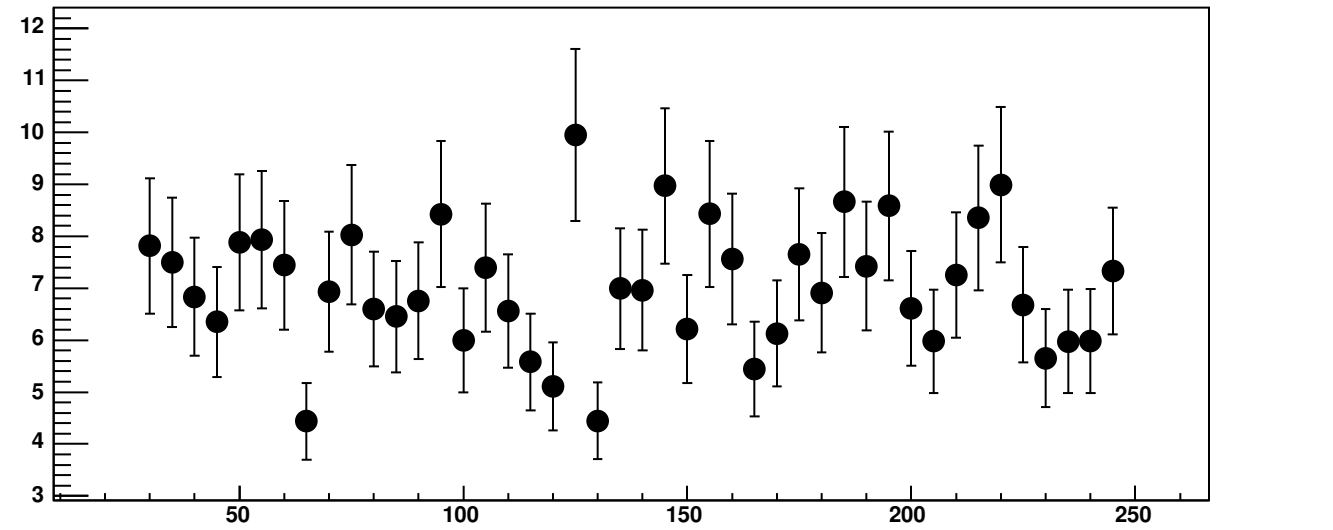
Chip 4, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold



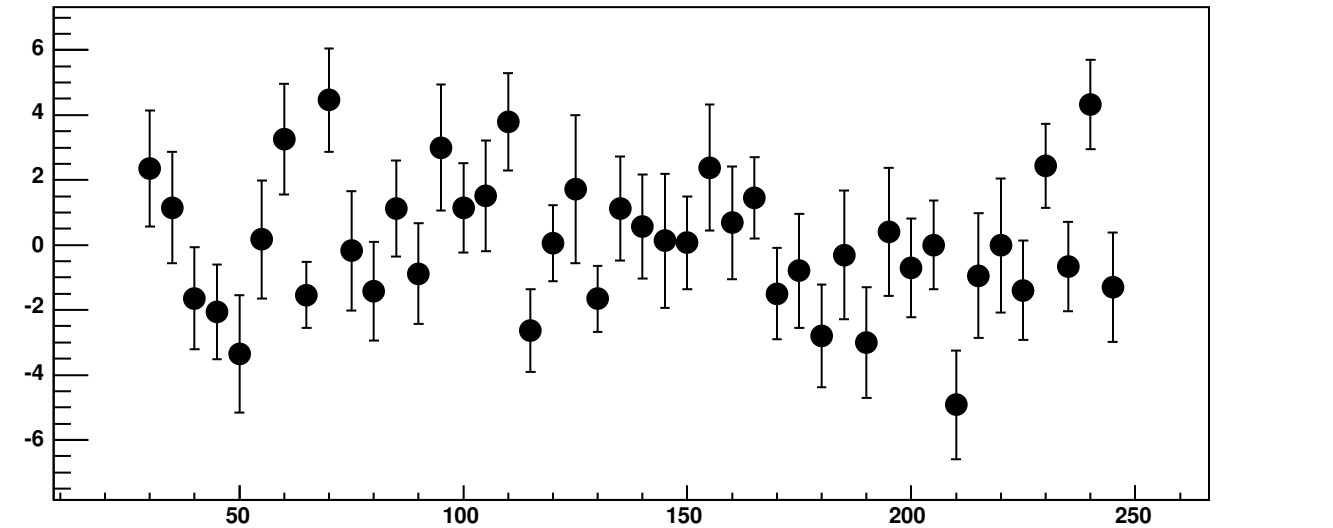
Chip 4, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

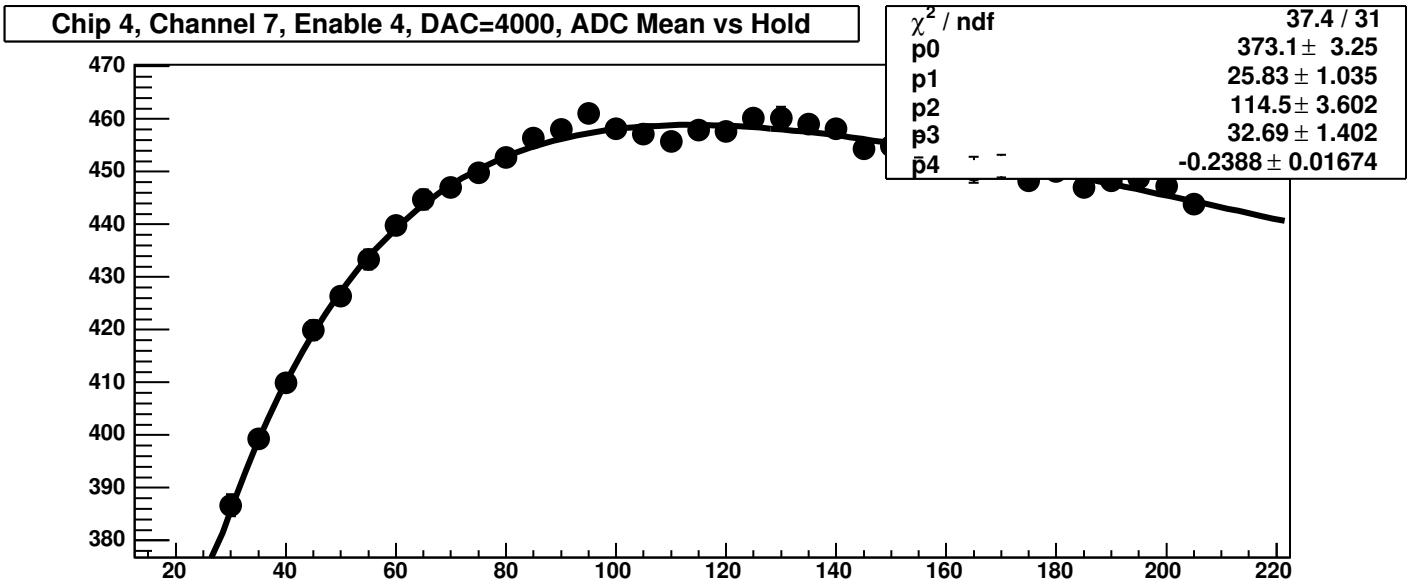


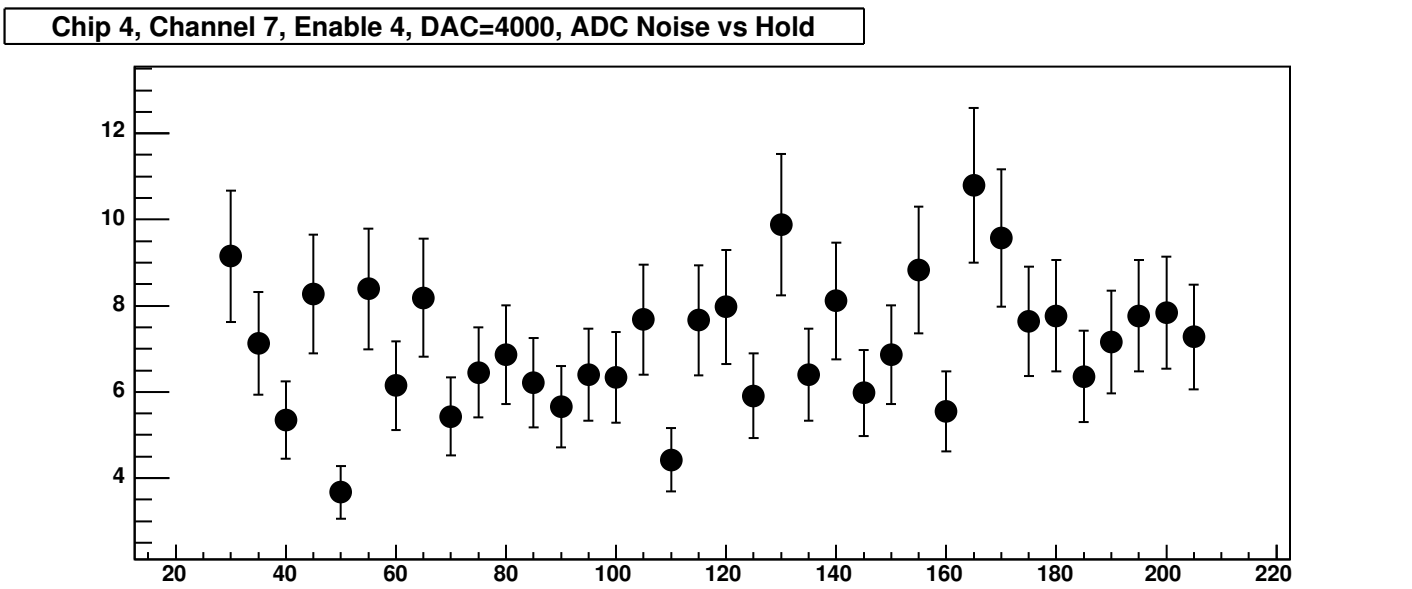
Chip 4, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold

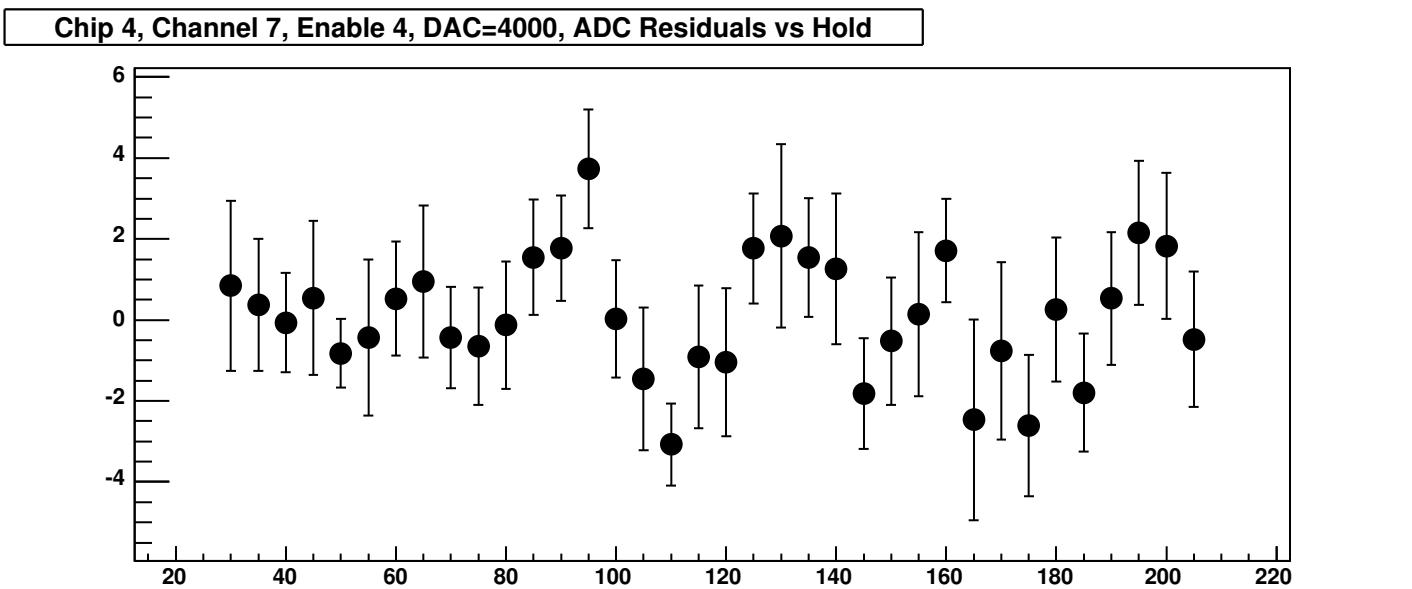


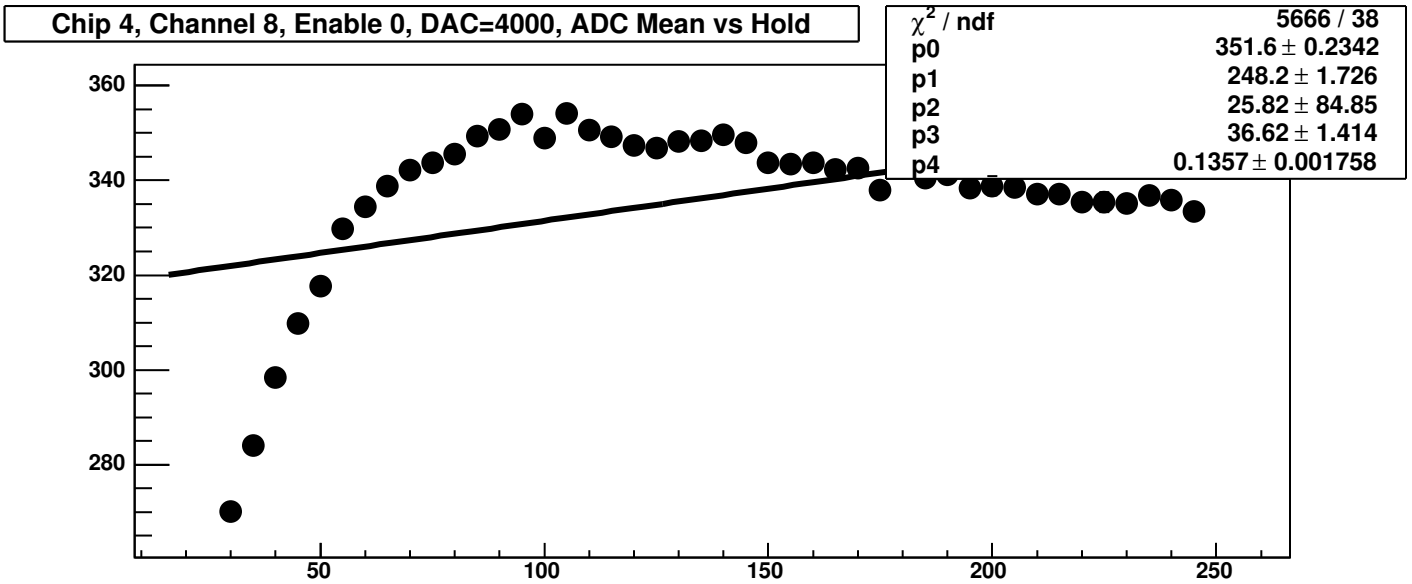
Chip 4, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

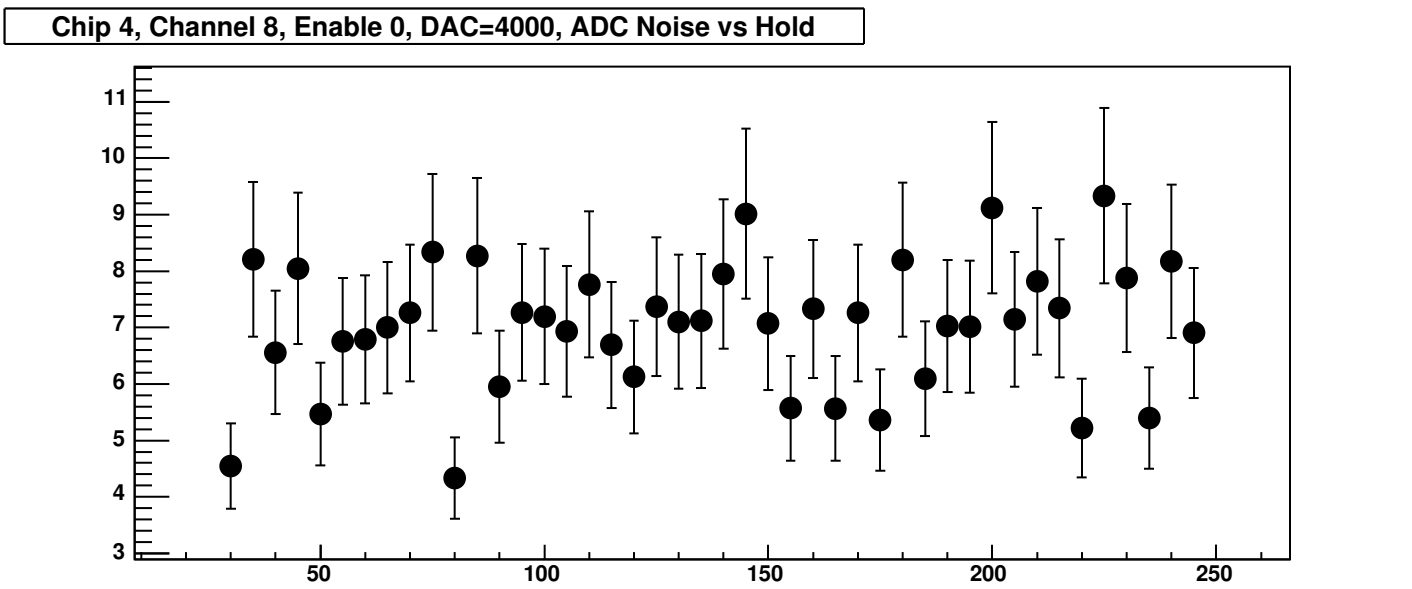


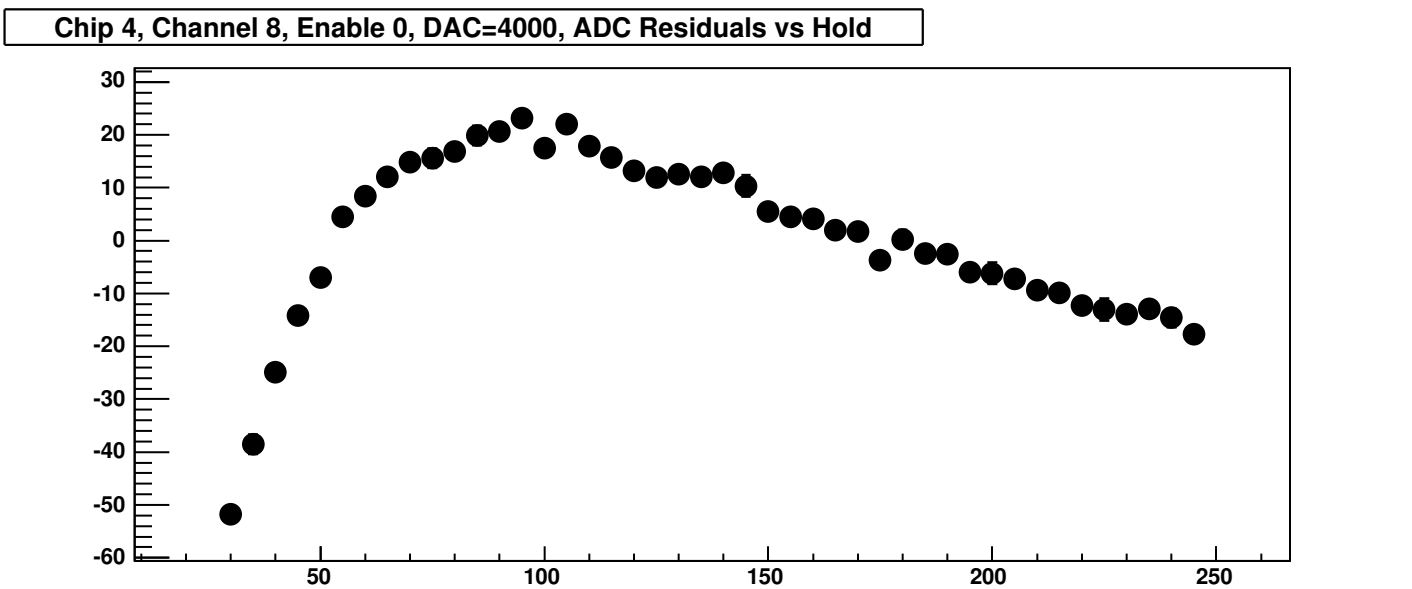

Chip 4, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 37.4 / 31
p0 373.1 ± 3.25
p1 25.83 ± 1.035
p2 114.5 ± 3.602
p3 32.69 ± 1.402
p4 -0.2388 ± 0.01674
470
460
450
440
430
420
410
400
390
380
20
40
60
80
100
120
140
160
180
200
220

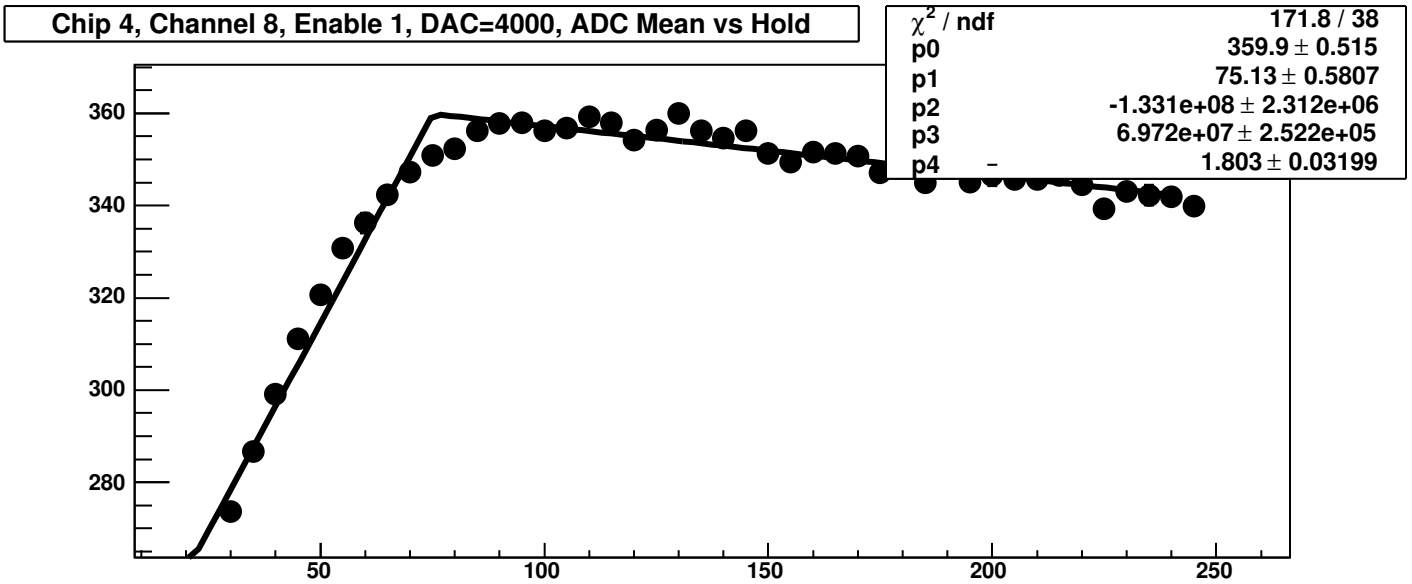

Chip 4, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
20
40
60
80
100
120
140
160
180
200
220

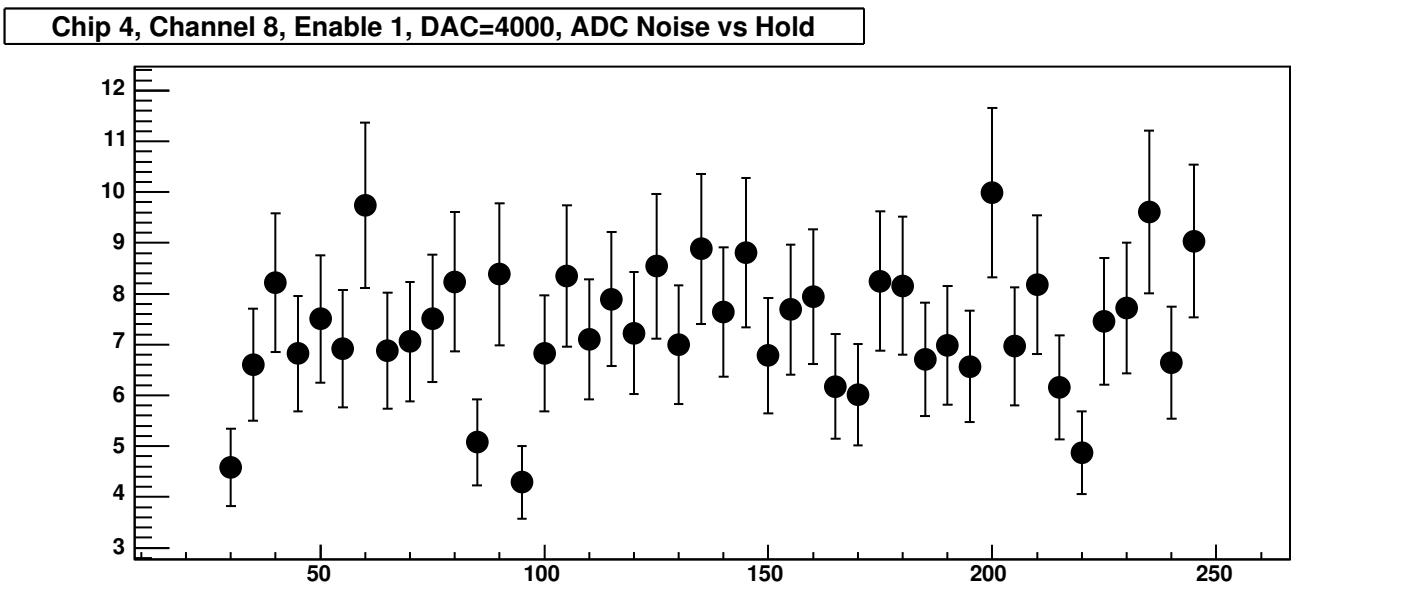

Chip 4, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
20
40
60
80
100
120
140
160
180
200
220

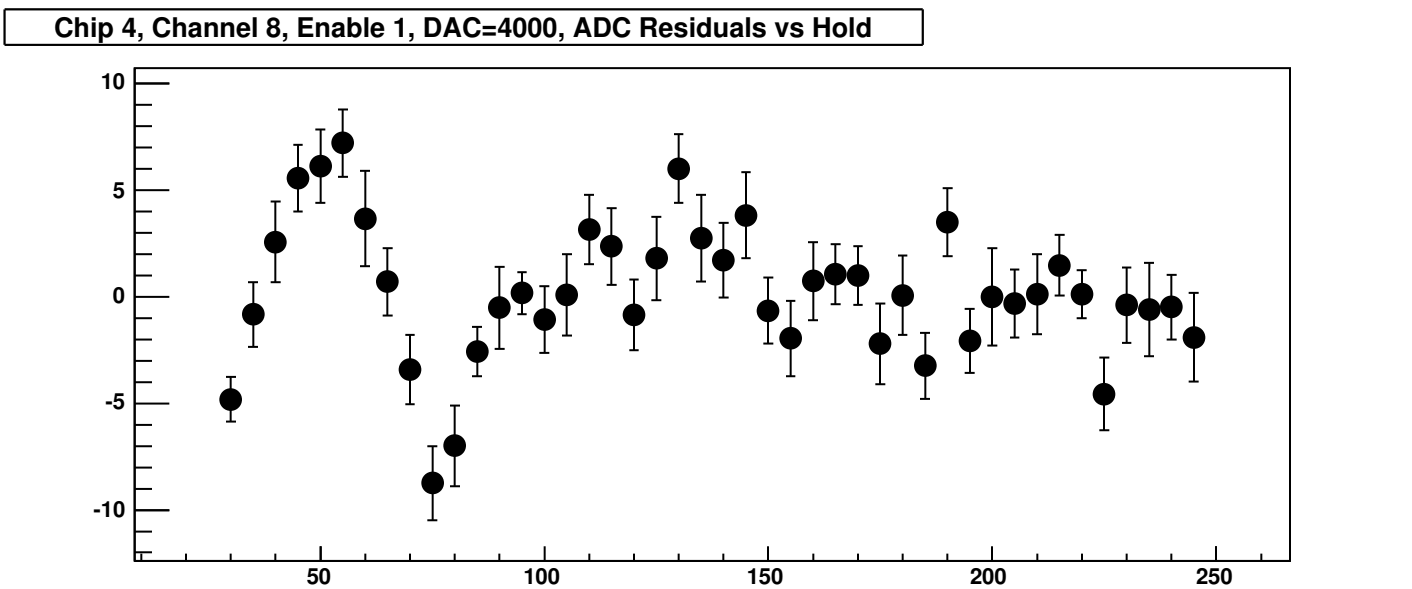

Chip 4, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 5666 / 38
p0 351.6 ± 0.2342
p1 248.2 ± 1.726
p2 25.82 ± 84.85
p3 36.62 ± 1.414
p4 0.1357 ± 0.001758
360
340
320
300
280
50
100
150
200
250

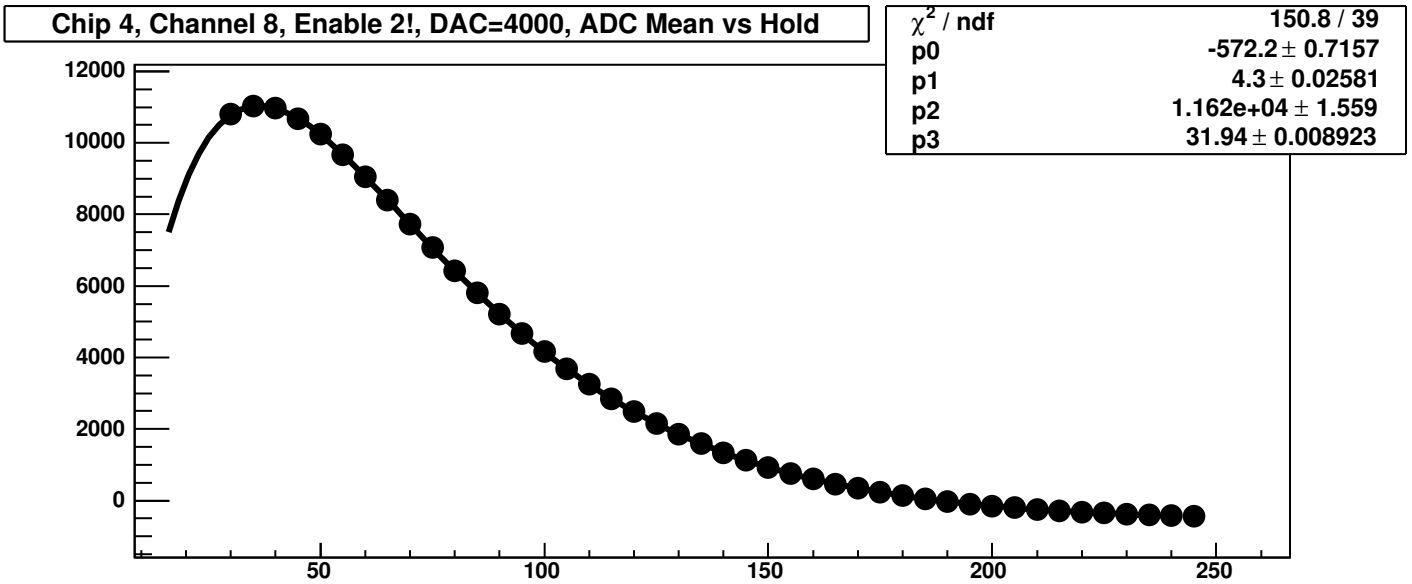

Chip 4, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
50
100
150
200
250

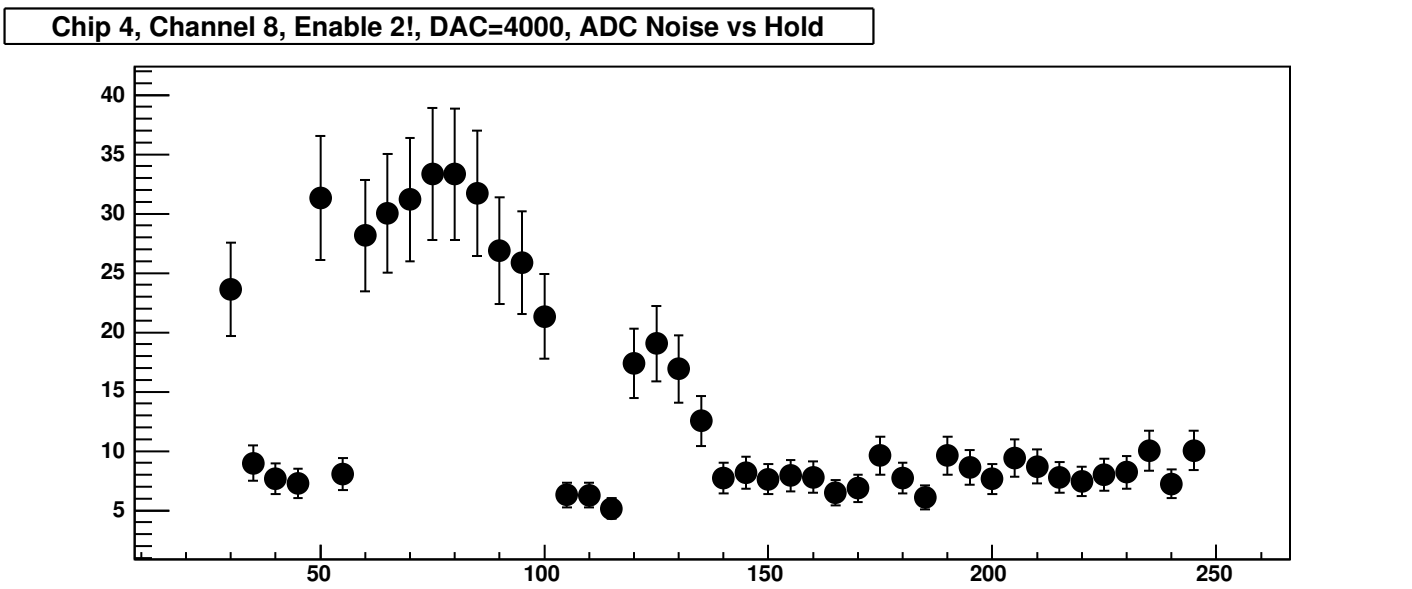

Chip 4, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
-20
-30
-40
-50
-60
50
100
150
200
250

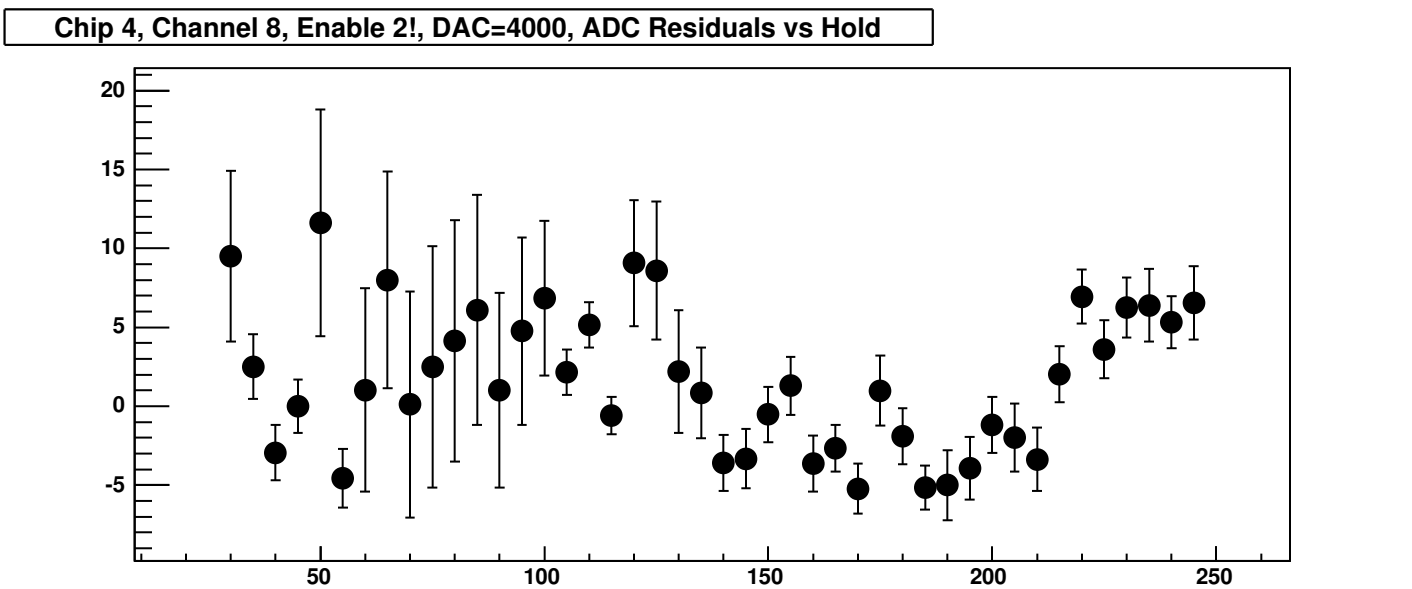

Chip 4, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 171.8 / 38
p0 359.9 ± 0.515
p1 75.13 ± 0.5807
p2 -1.331e+08 ± 2.312e+06
p3 6.972e+07 ± 2.522e+05
p4 - 1.803 ± 0.03199
360
340
320
300
280
50
100
150
200
250

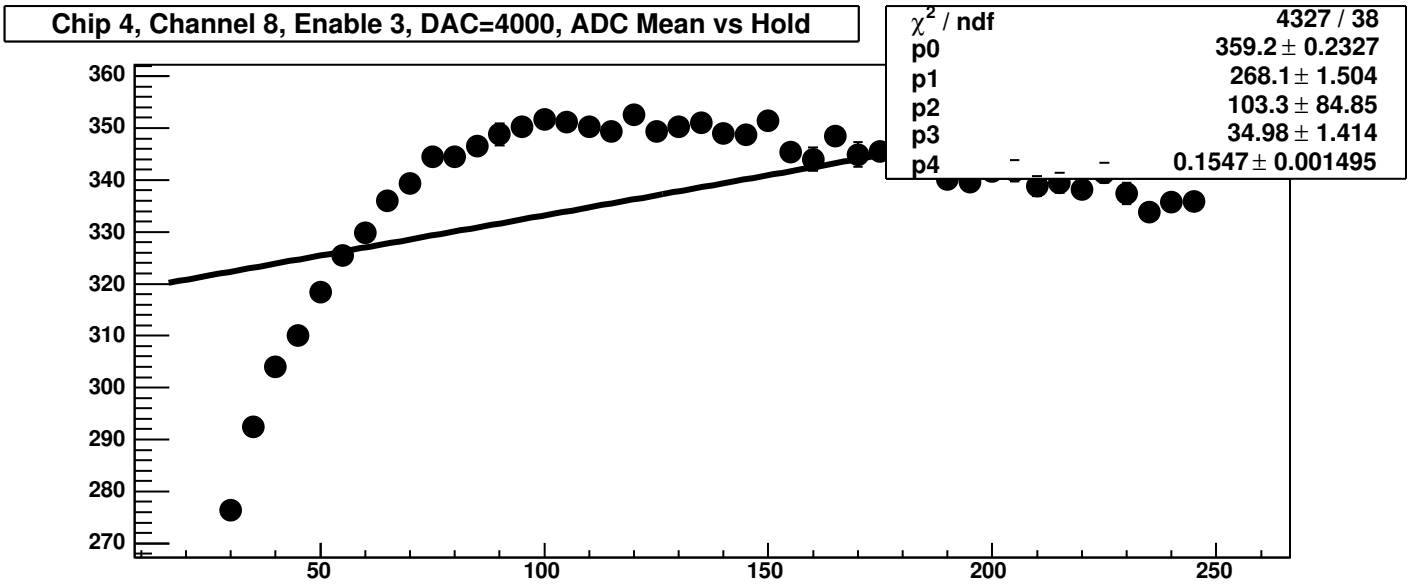

Chip 4, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250

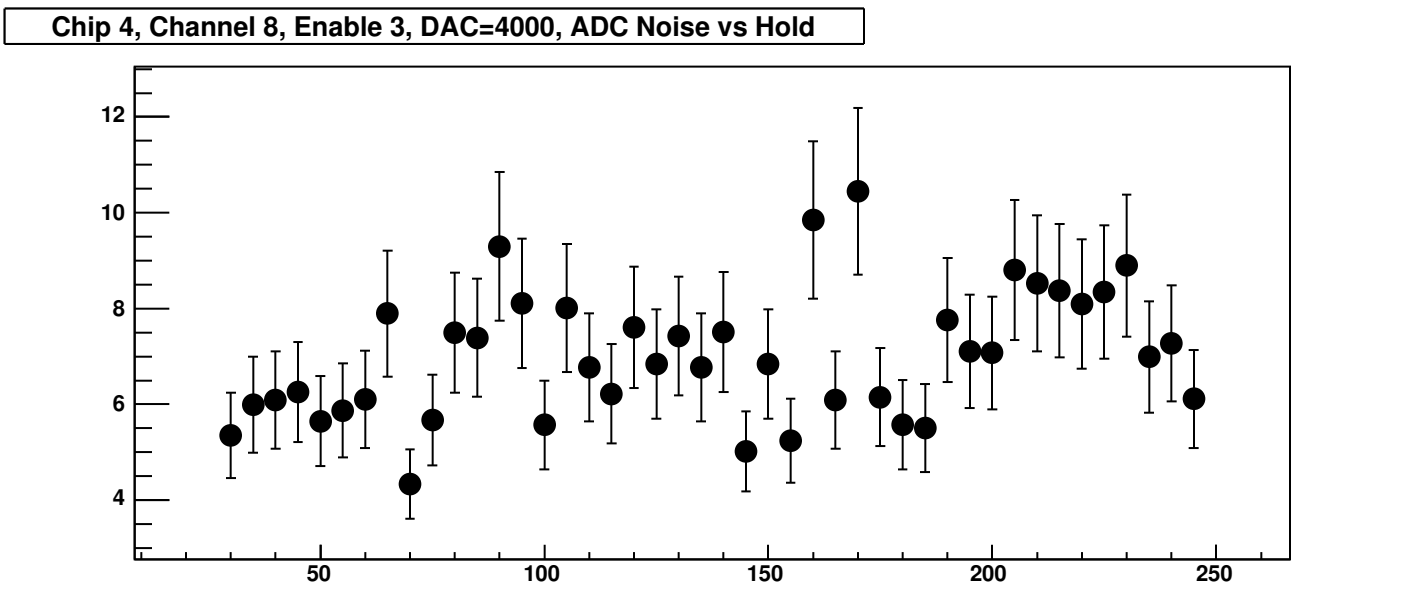

Chip 4, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

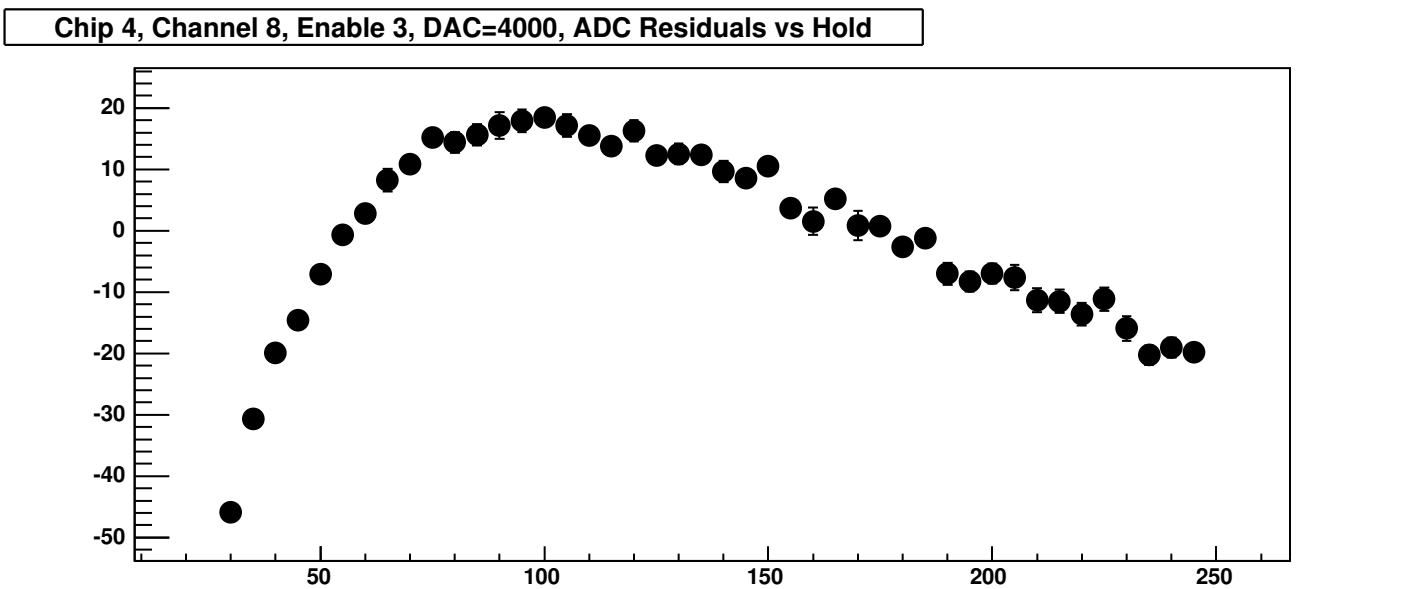

Chip 4, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 150.8 / 39
p0 -572.2 ± 0.7157
p1 4.3 ± 0.02581
p2 1.162e+04 ± 1.559
p3 31.94 ± 0.008923
12000
10000
8000
6000
4000
2000
0
50
100
150
200
250

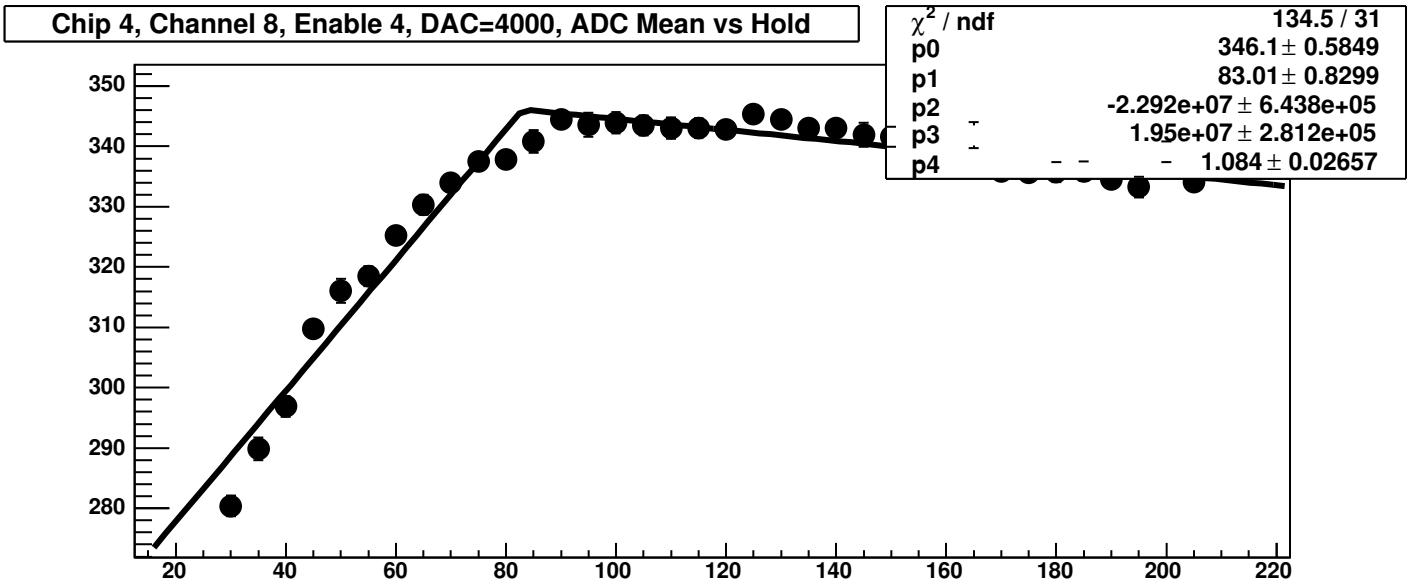

Chip 4, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold
40
35
30
25
20
15
10
5
50
100
150
200
250

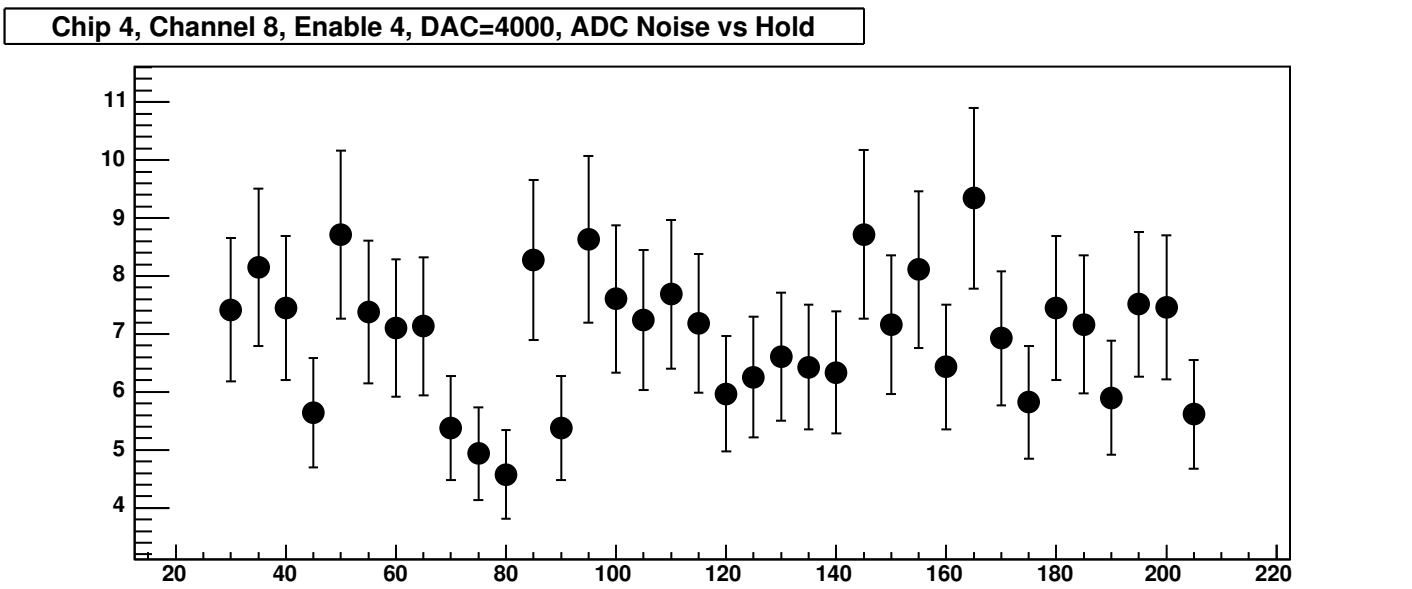

Chip 4, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
50
100
150
200
250

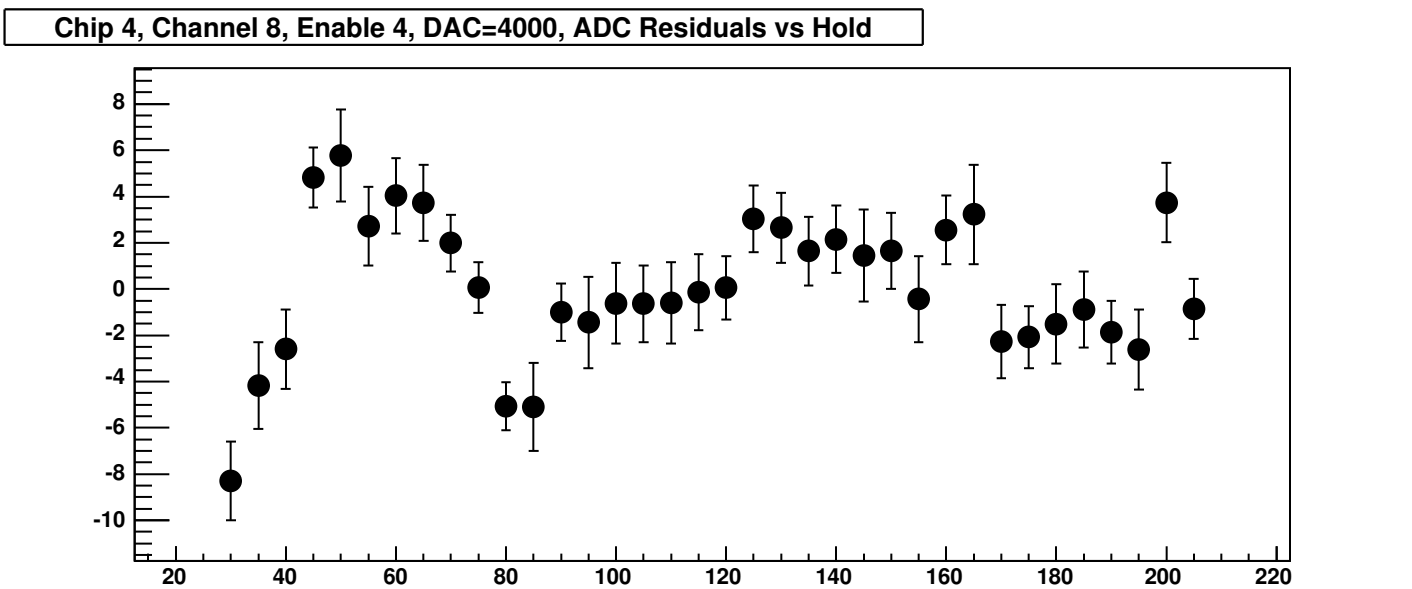

Chip 4, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 4327 / 38
p0 359.2 ± 0.2327
p1 268.1 ± 1.504
p2 103.3 ± 84.85
p3 34.98 ± 1.414
p4 0.1547 ± 0.001495
360
350
340
330
320
310
300
290
280
270
50
100
150
200
250

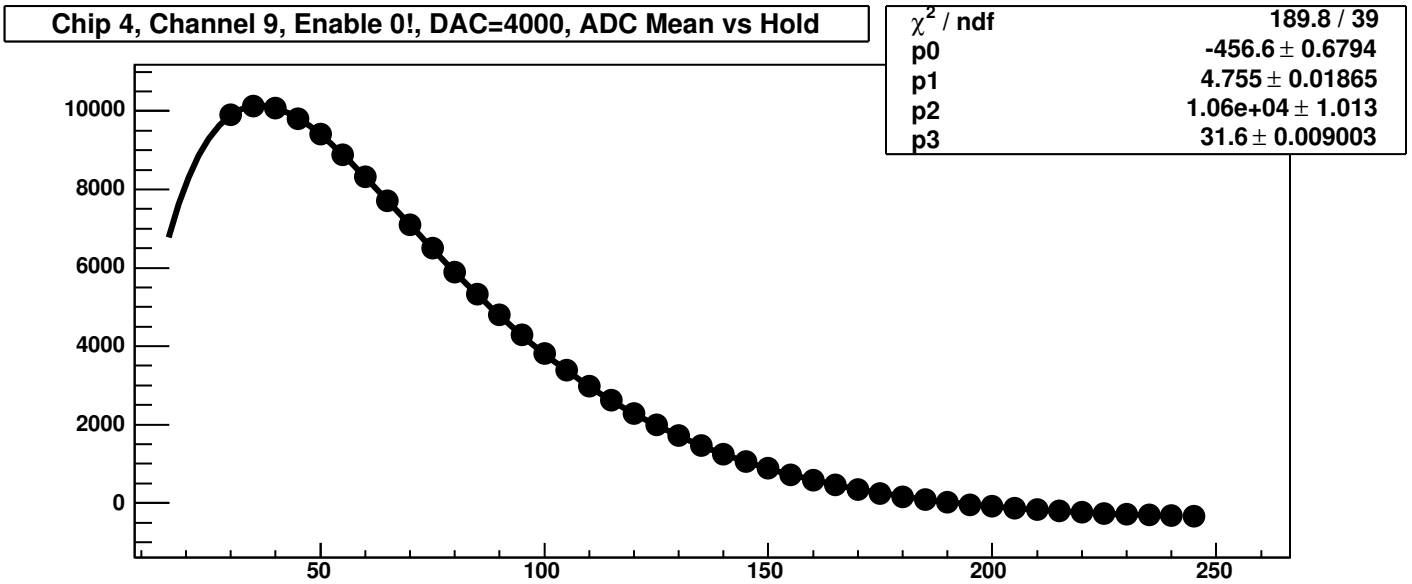

Chip 4, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250

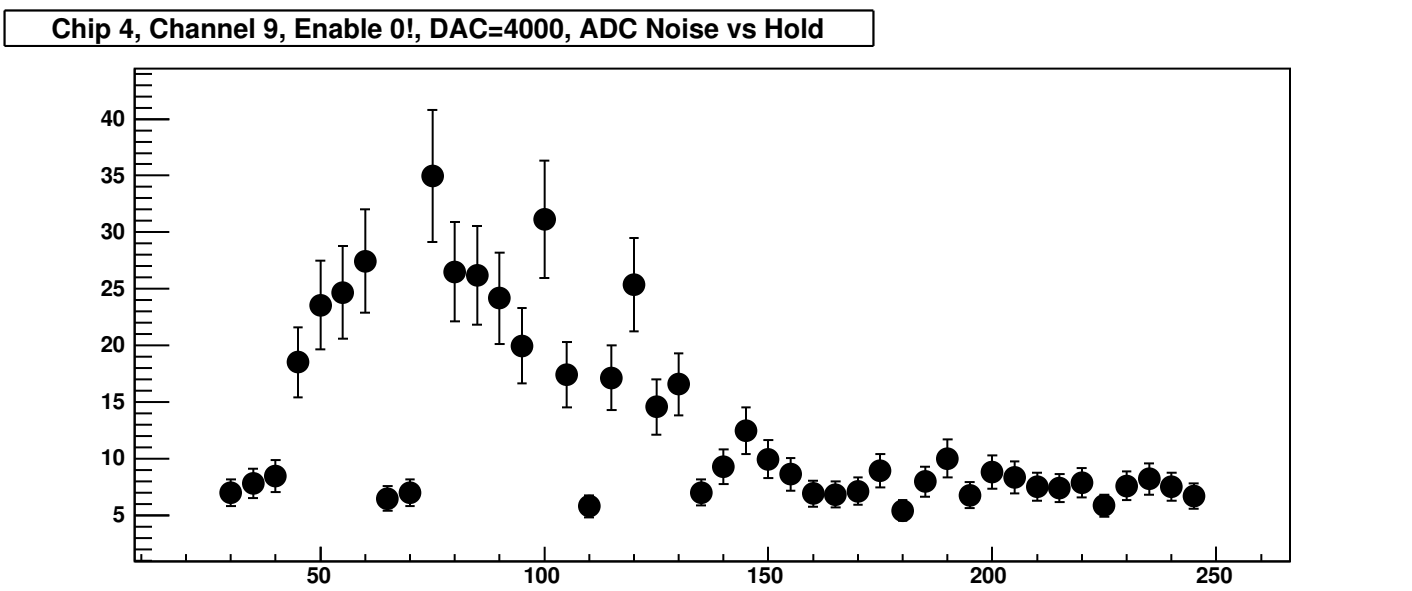

Chip 4, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
-50
50
100
150
200
250

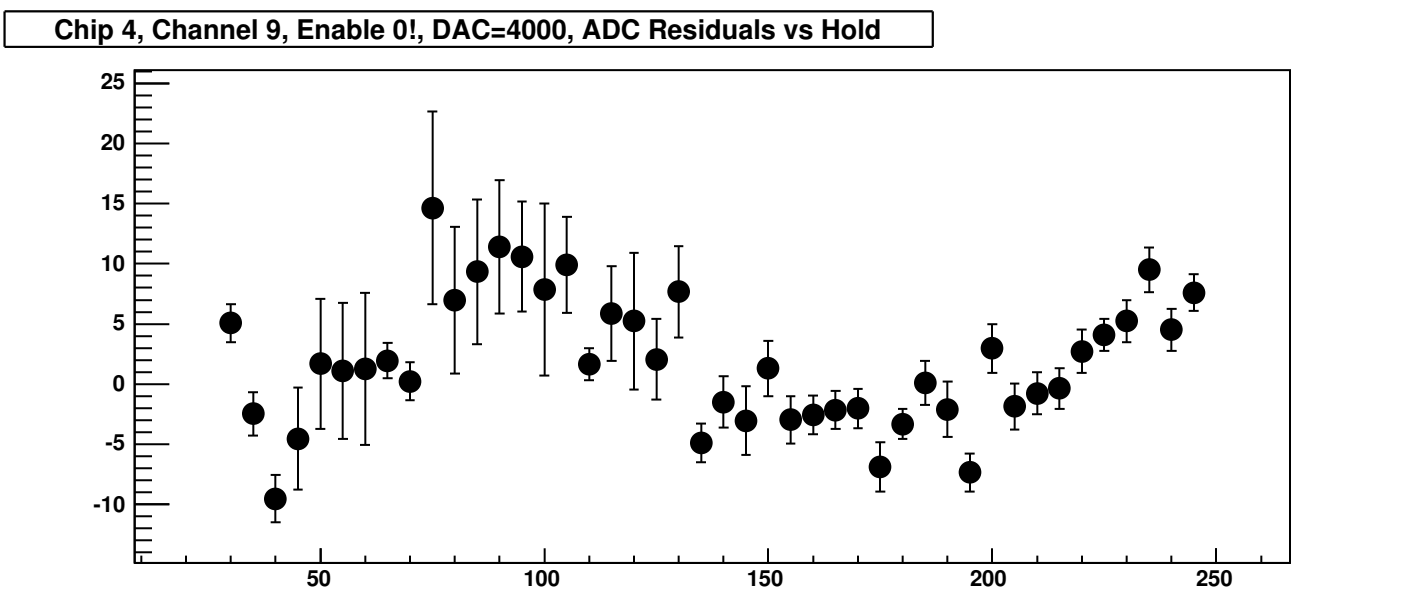

Chip 4, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 134.5 / 31
p0 346.1 ± 0.5849
p1 83.01 ± 0.8299
p2 -2.292e+07 ± 6.438e+05
p3 1.95e+07 ± 2.812e+05
p4 1.084 ± 0.02657

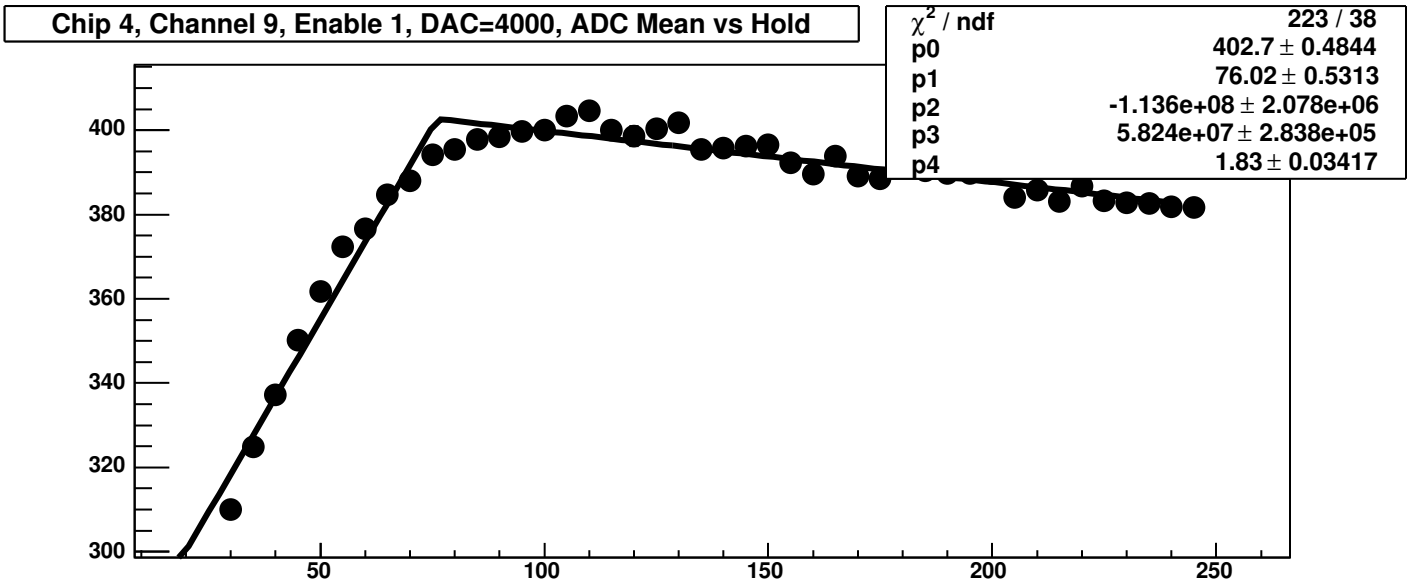

Chip 4, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold

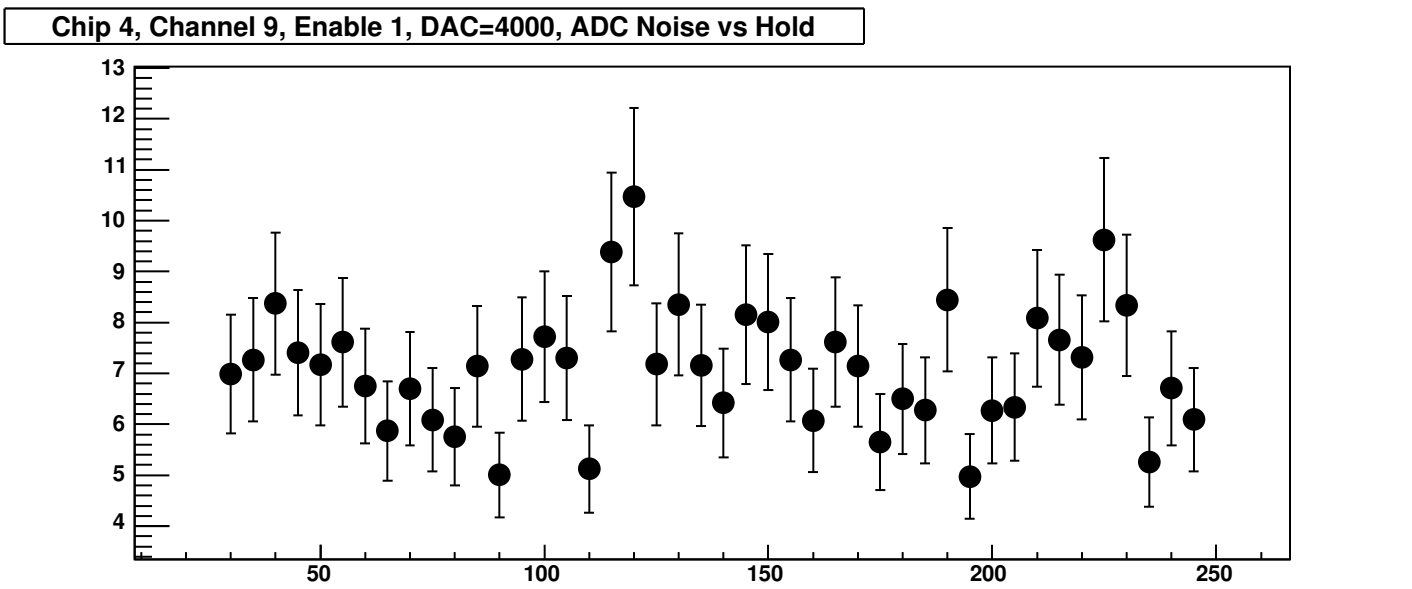

Chip 4, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

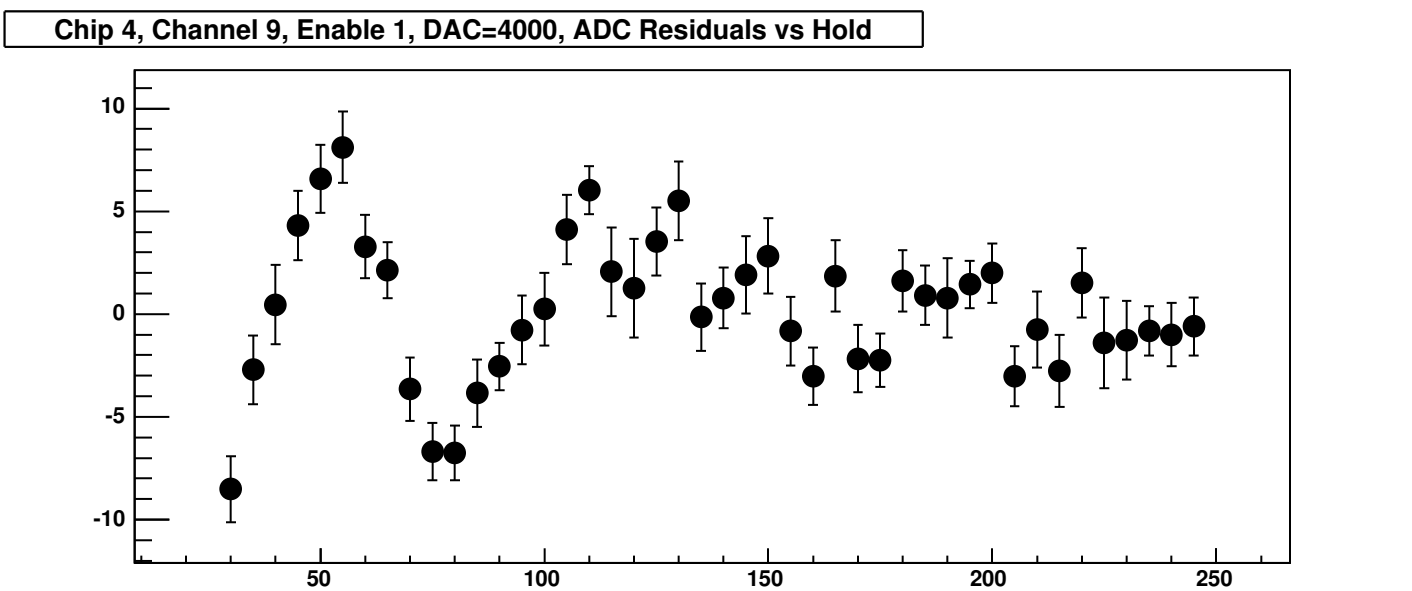

Chip 4, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 189.8 / 39
p0 -456.6 ± 0.6794
p1 4.755 ± 0.01865
p2 1.06e+04 ± 1.013
p3 31.6 ± 0.009003
10000
8000
6000
4000
2000
0
50
100
150
200
250


Chip 4, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold
40
35
30
25
20
15
10
5
50
100
150
200
250

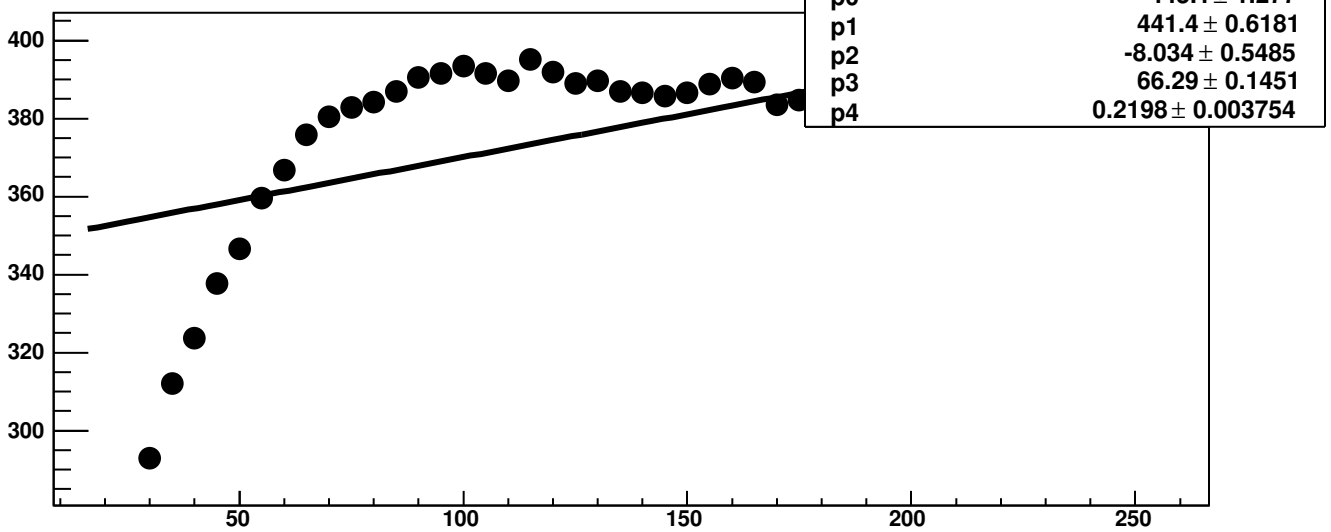

Chip 4, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
50
100
150
200
250


Chip 4, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 223 / 38
p0 402.7 ± 0.4844
p1 76.02 ± 0.5313
p2 -1.136e+08 ± 2.078e+06
p3 5.824e+07 ± 2.838e+05
p4 1.83 ± 0.03417
400
380
360
340
320
300
50
100
150
200
250

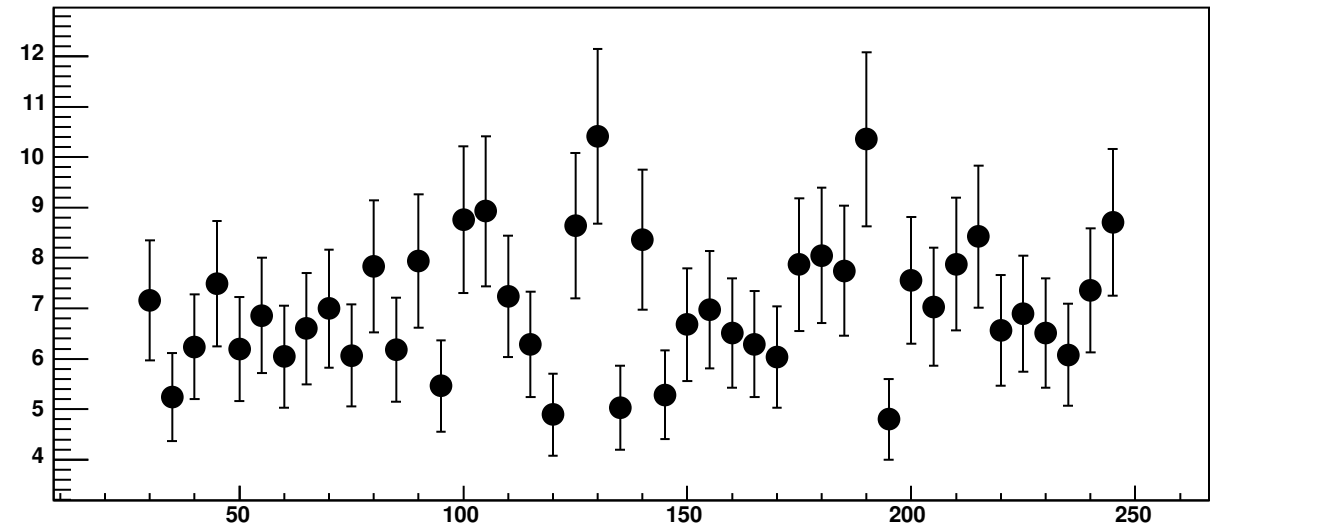

Chip 4, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 4, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

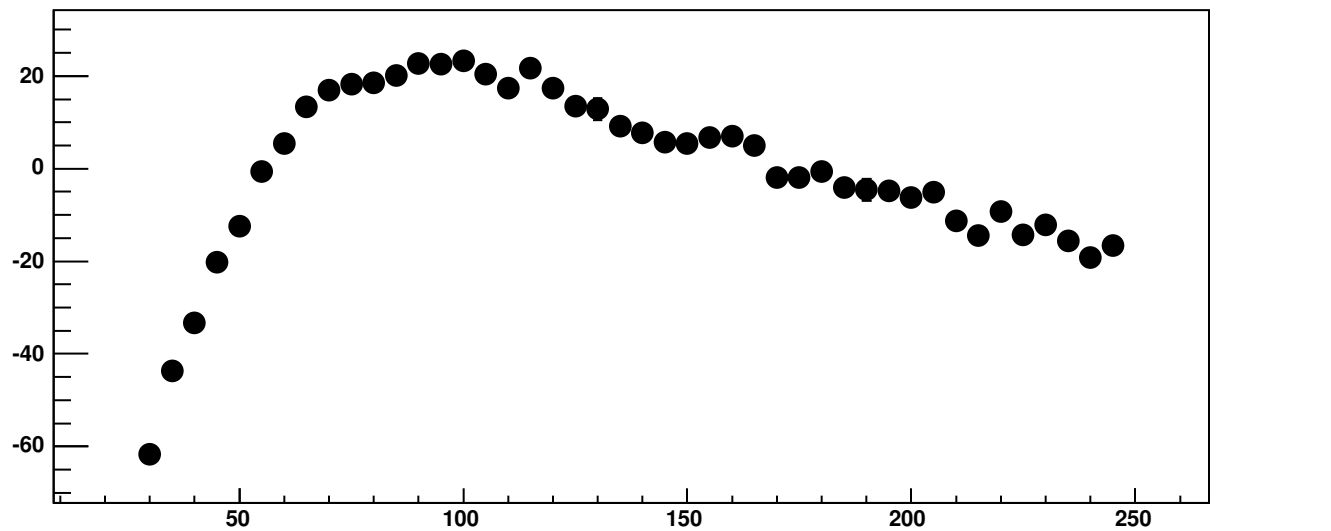
Chip 4, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

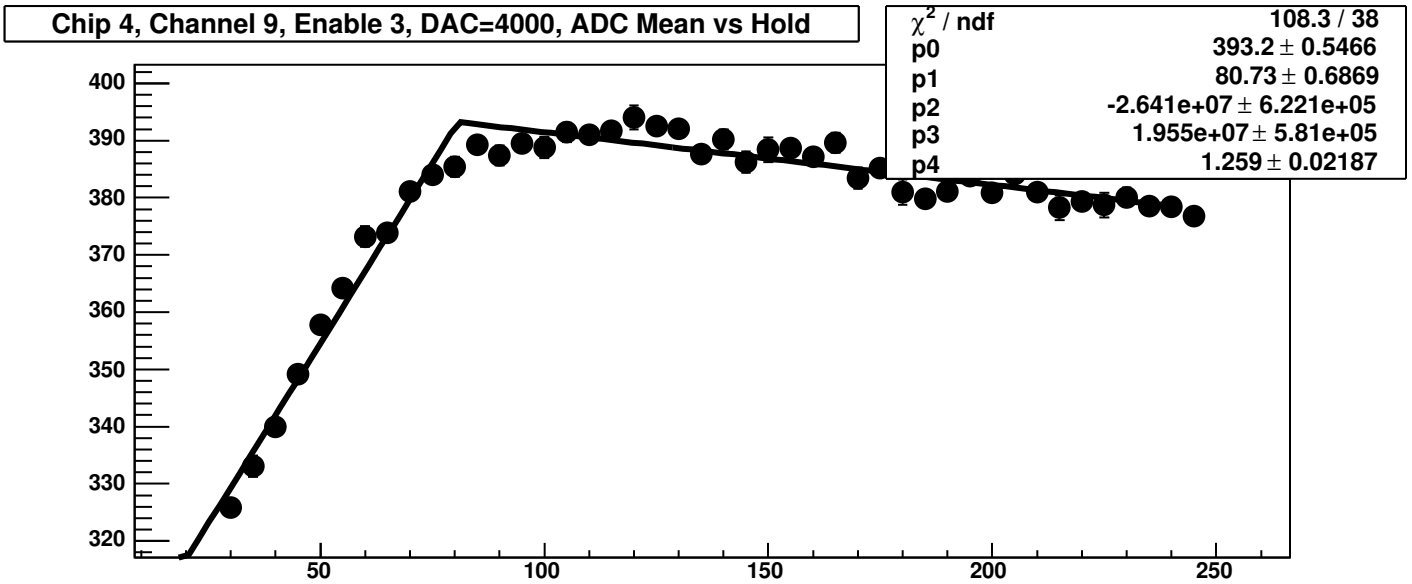


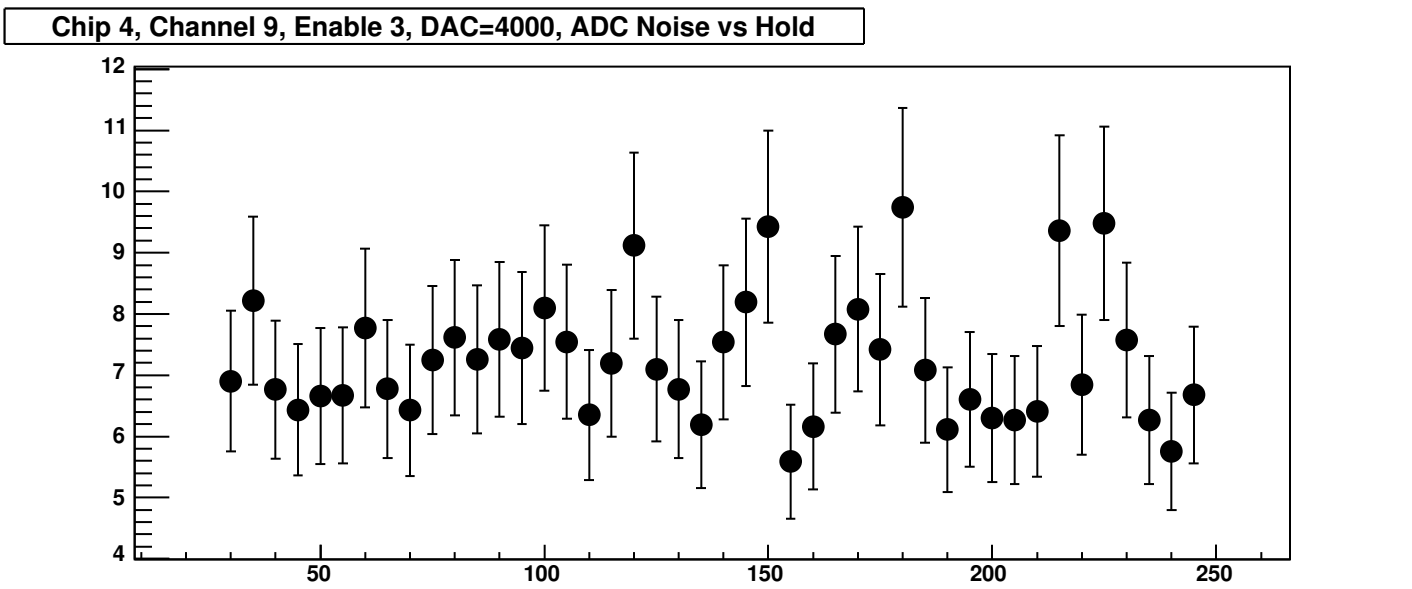
Chip 4, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold

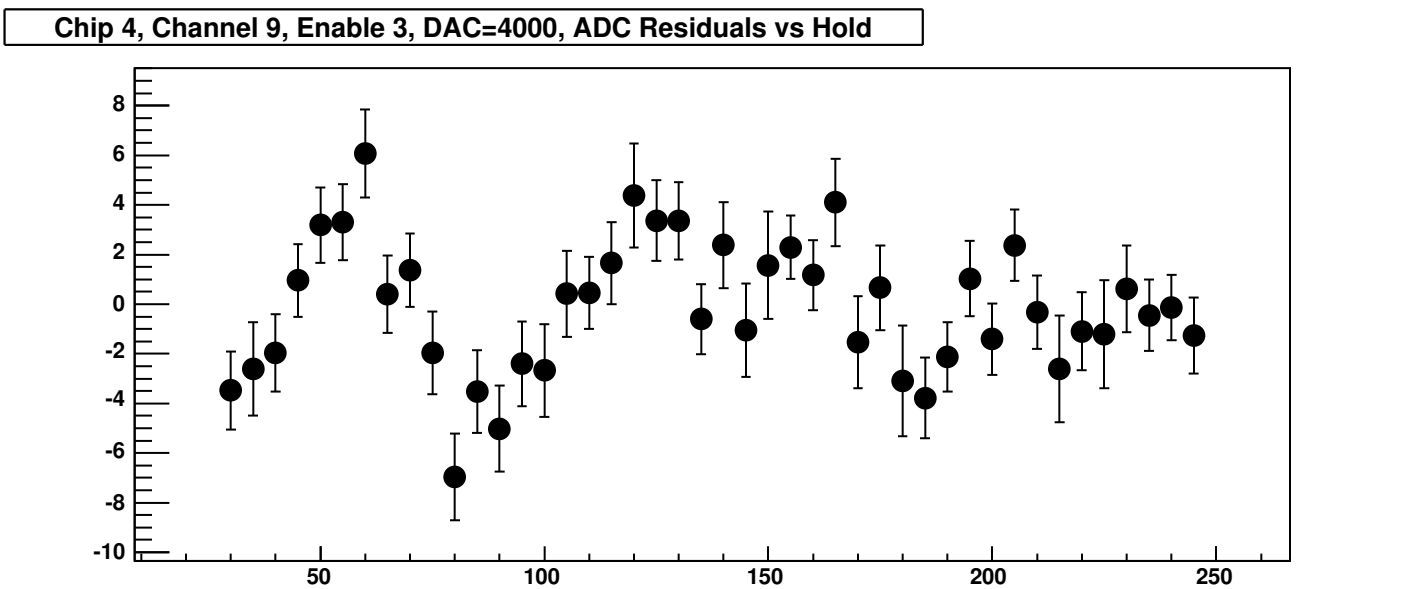


Chip 4, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold

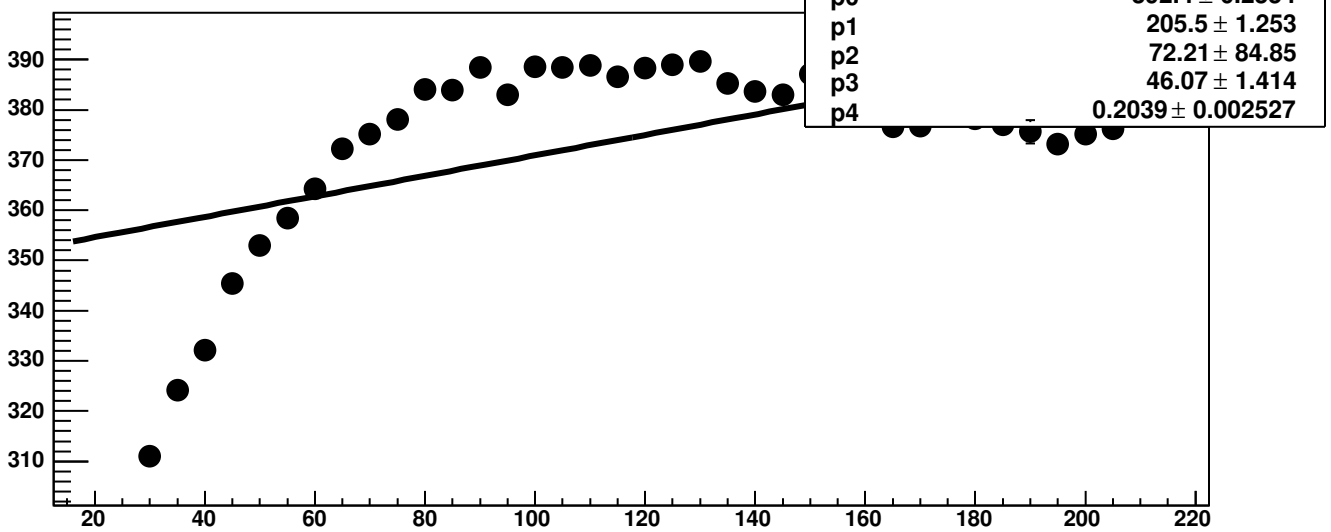



Chip 4, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 108.3 / 38
p0 393.2 ± 0.5466
p1 80.73 ± 0.6869
p2 -2.641e+07 ± 6.221e+05
p3 1.955e+07 ± 5.81e+05
p4 1.259 ± 0.02187
400
390
380
370
360
350
340
330
320
50
100
150
200
250

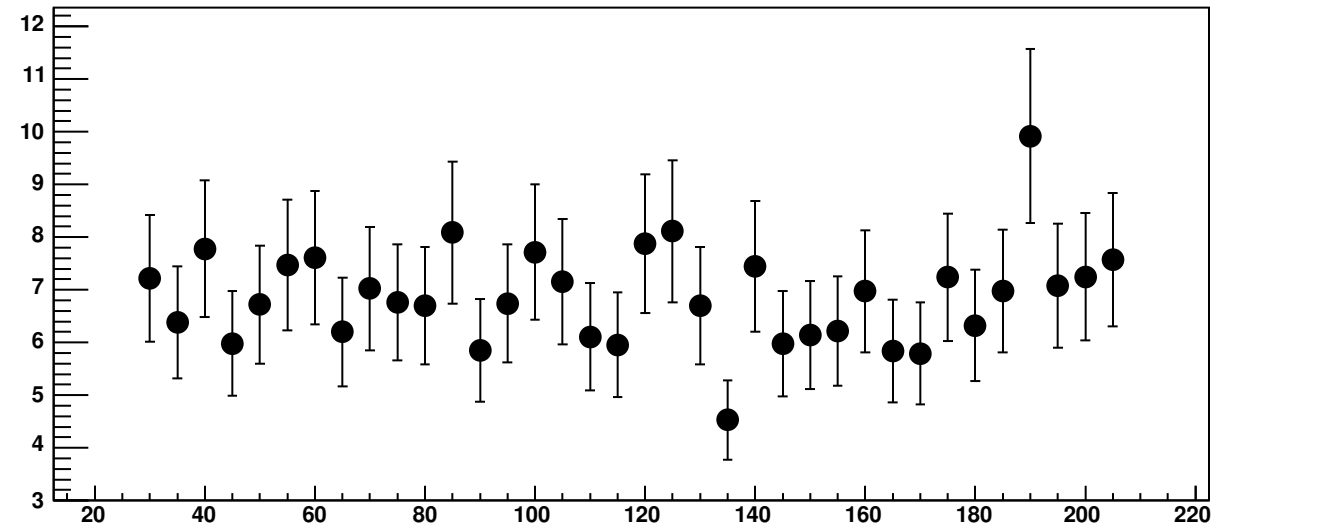

Chip 4, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 4, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
50
100
150
200
250

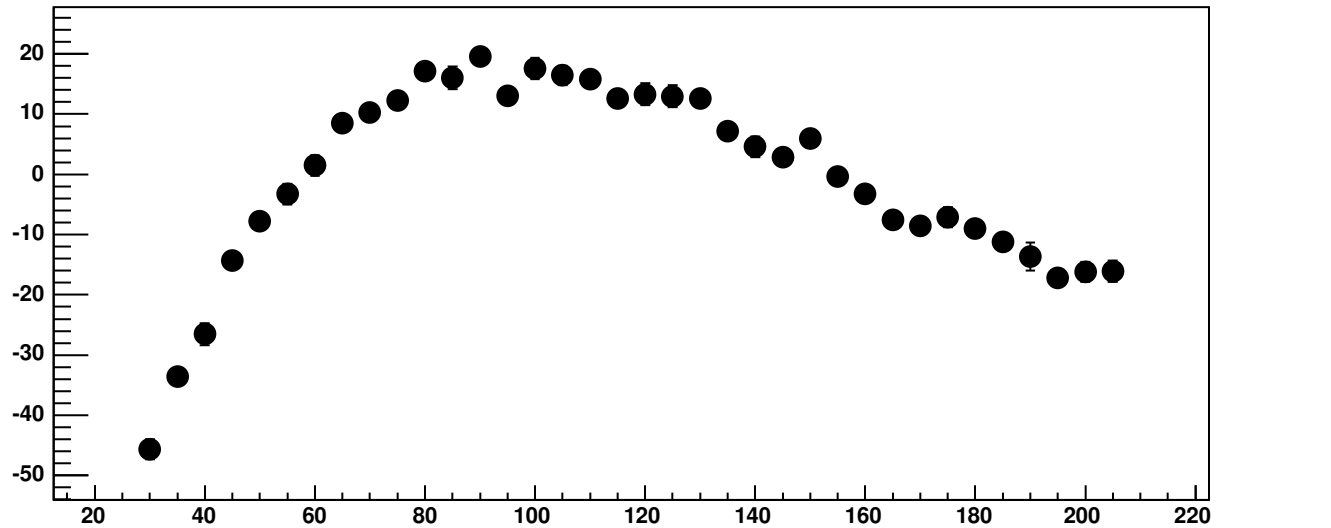
Chip 4, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

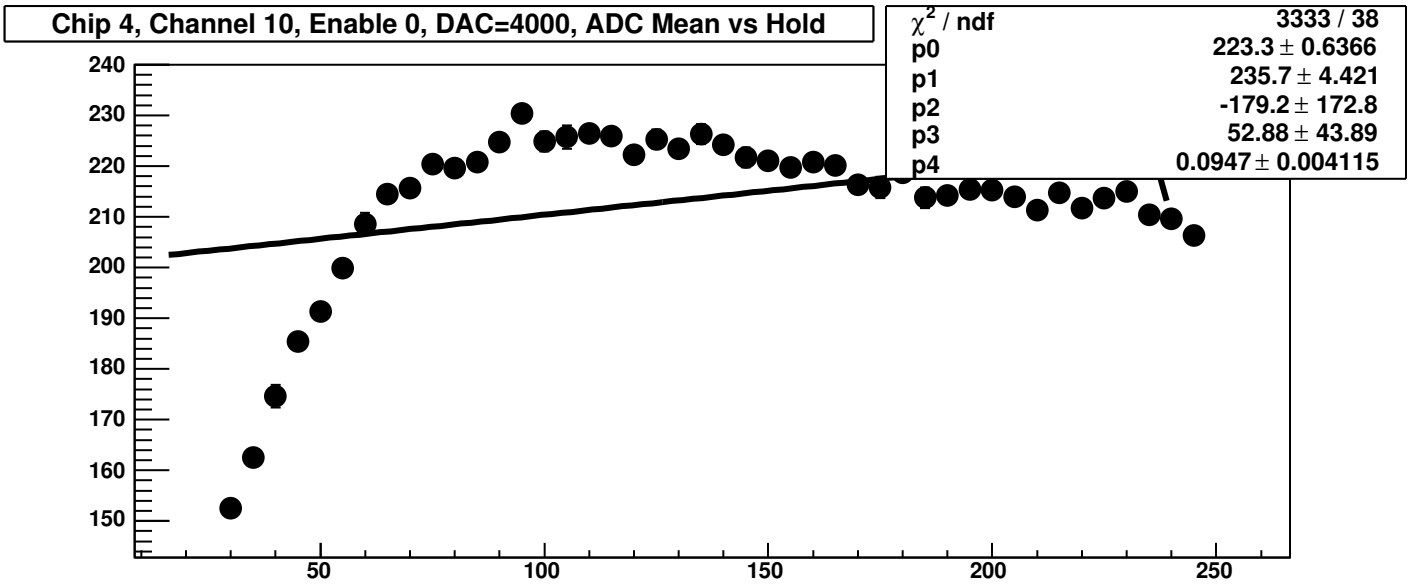


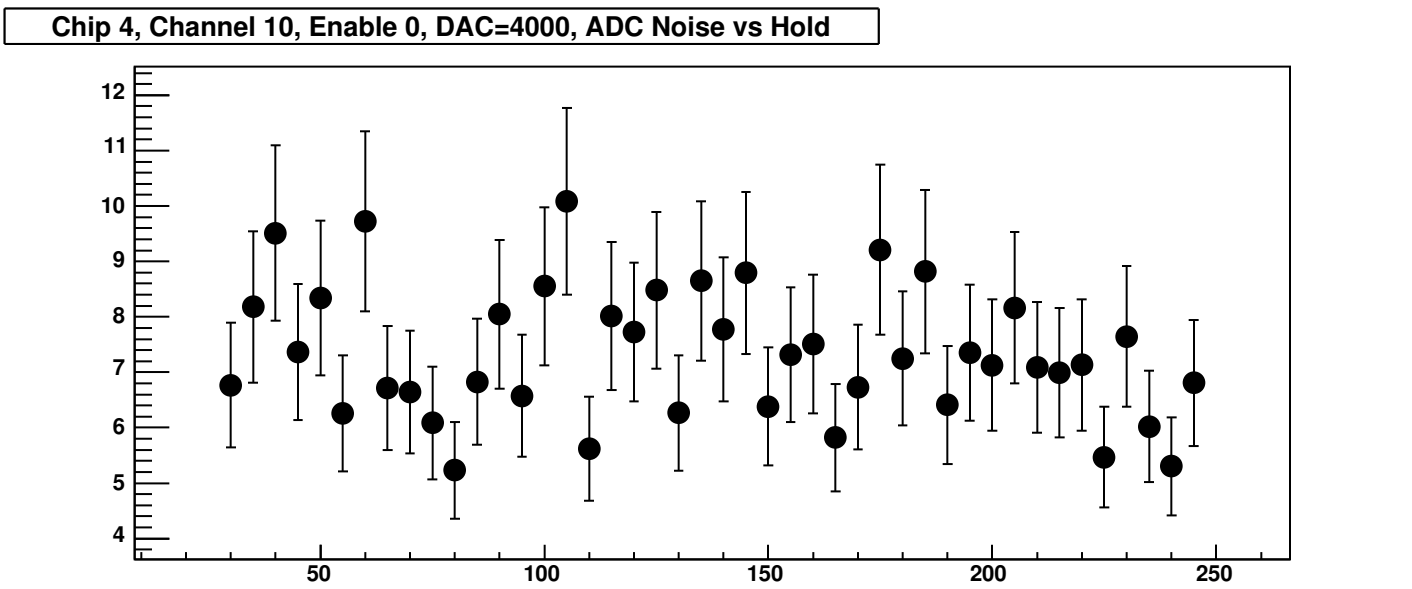
Chip 4, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold

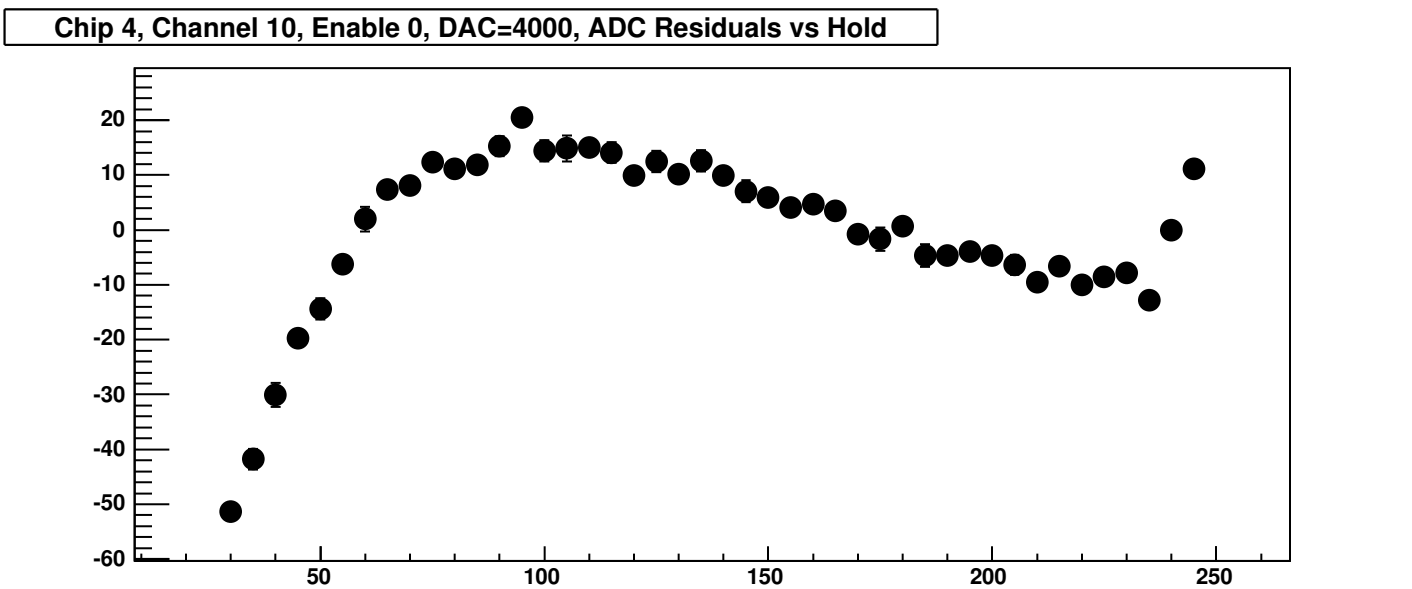


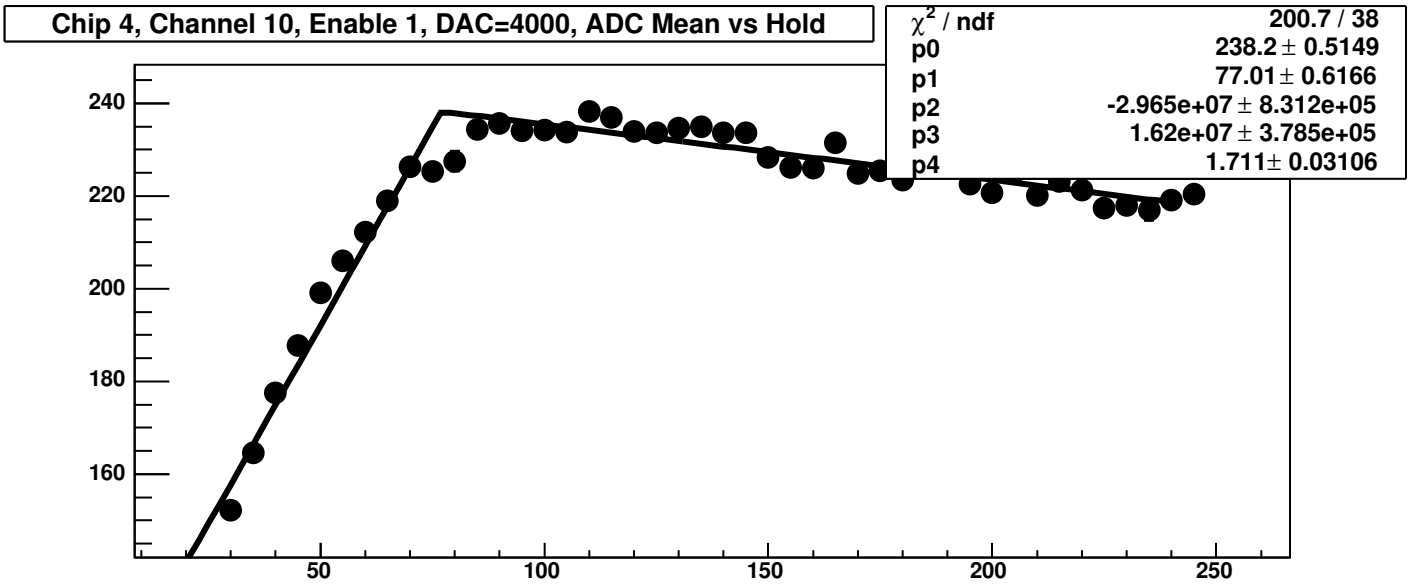
Chip 4, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold

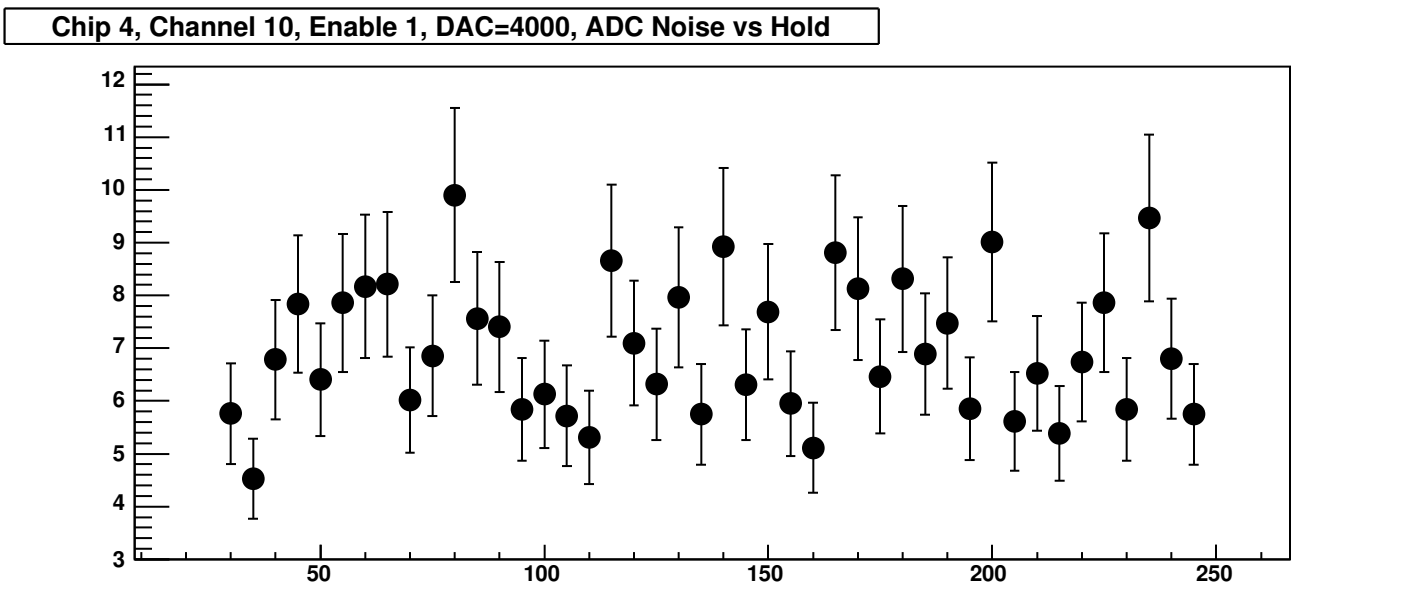


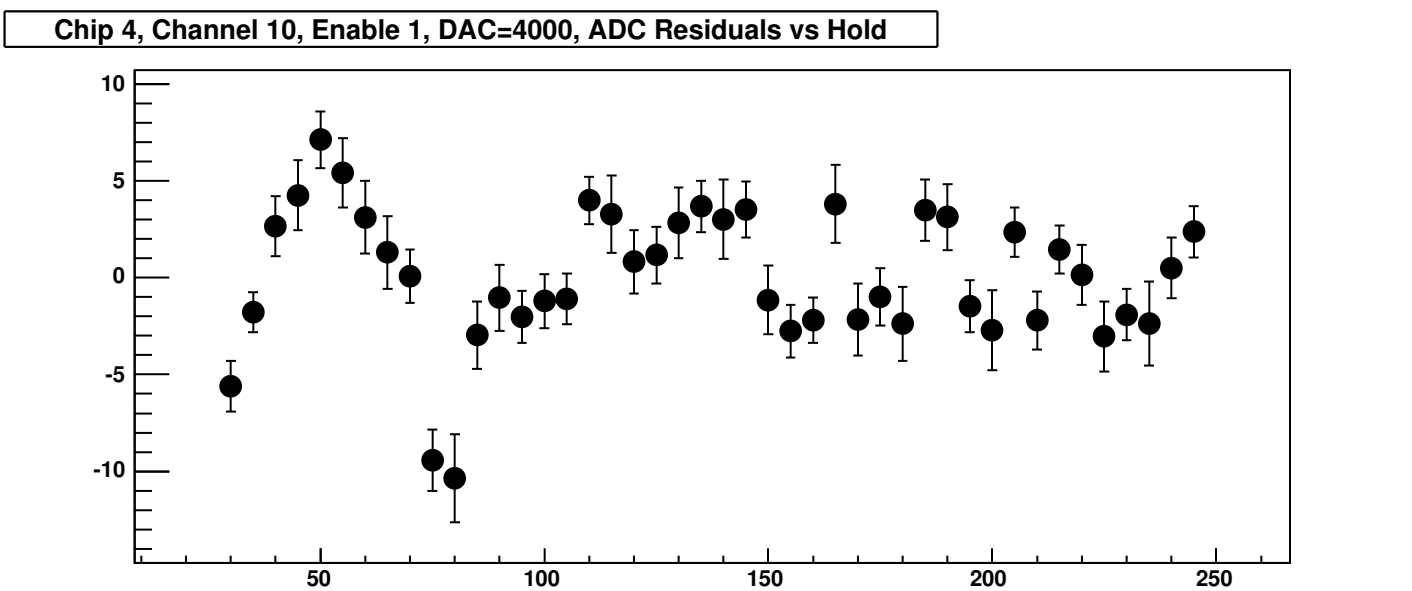

Chip 4, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 3333 / 38
p0 223.3 ± 0.6366
p1 235.7 ± 4.421
p2 -179.2 ± 172.8
p3 52.88 ± 43.89
p4 0.0947 ± 0.004115
240
230
220
210
200
190
180
170
160
150
50
100
150
200
250


Chip 4, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250

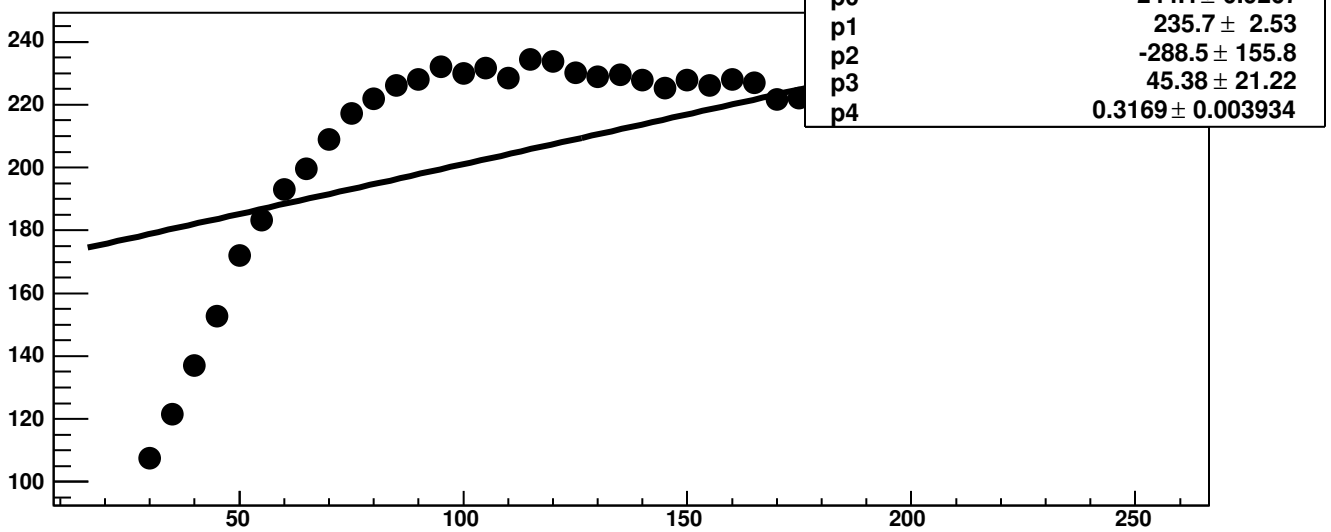

Chip 4, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
-50
-60
50
100
150
200
250


Chip 4, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 200.7 / 38
p0 238.2 ± 0.5149
p1 77.01 ± 0.6166
p2 -2.965e+07 ± 8.312e+05
p3 1.62e+07 ± 3.785e+05
p4 1.711 ± 0.03106
240
220
200
180
160
50
100
150
200
250

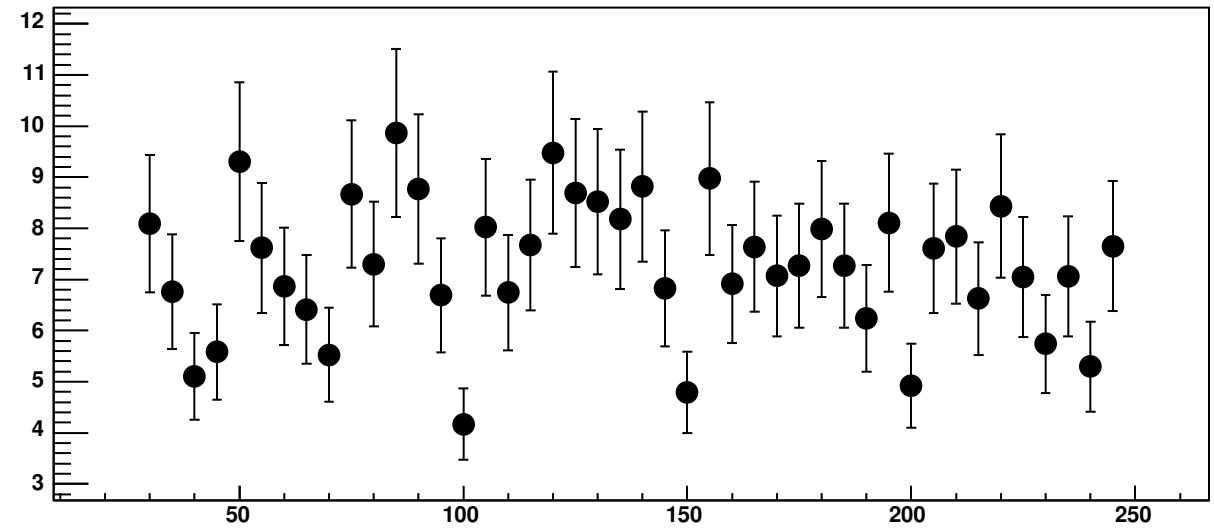

Chip 4, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250


Chip 4, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

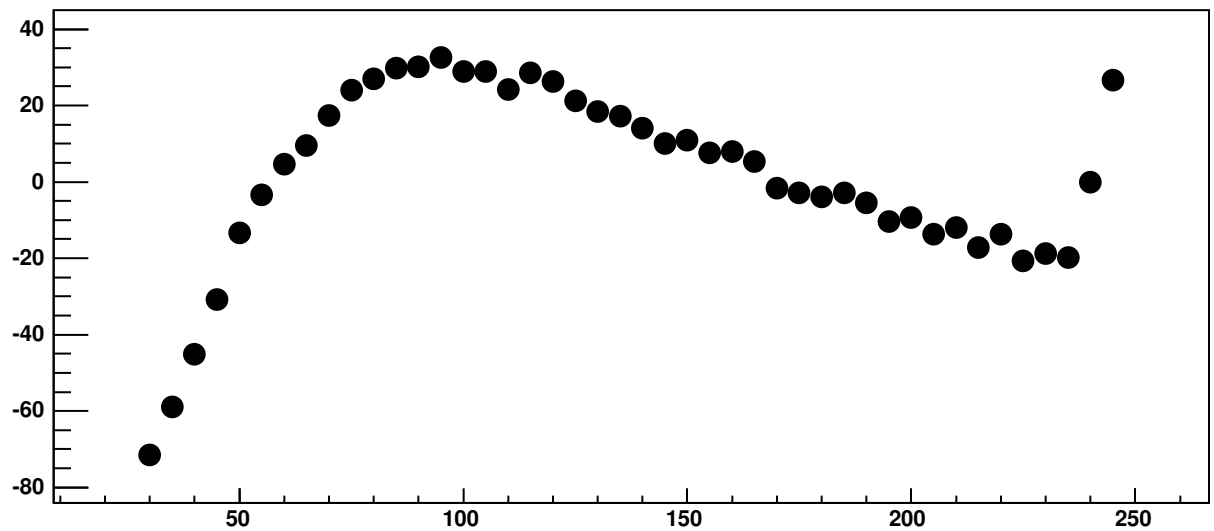
Chip 4, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

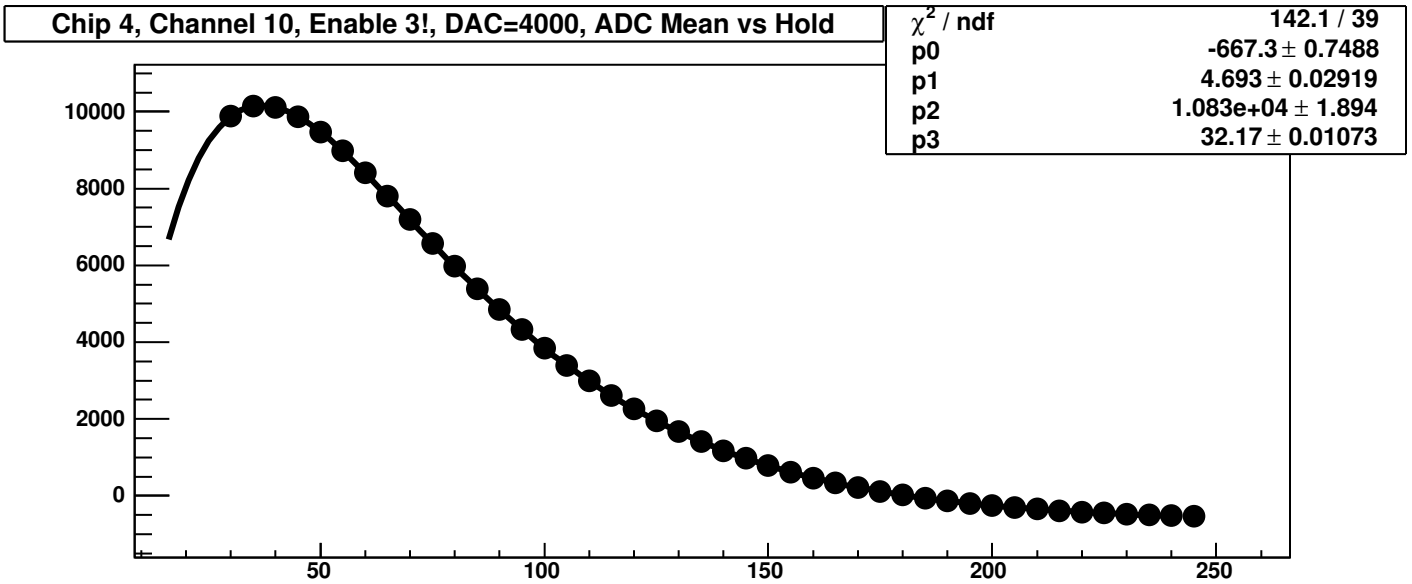


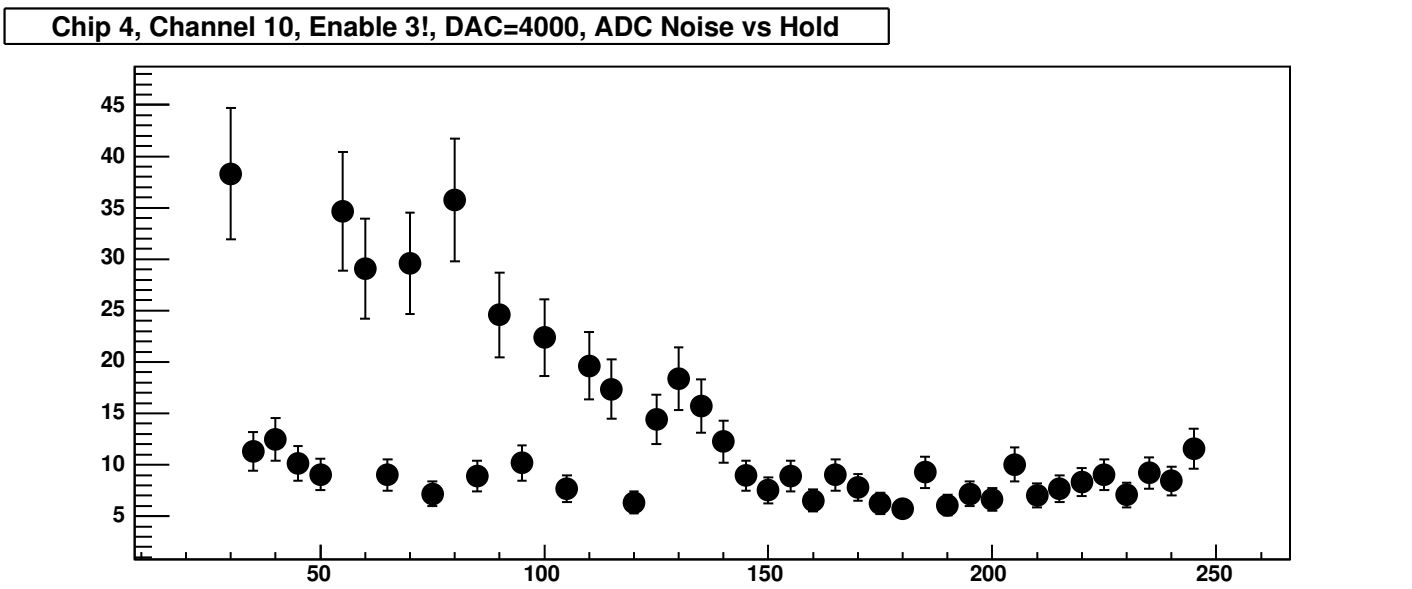
Chip 4, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold

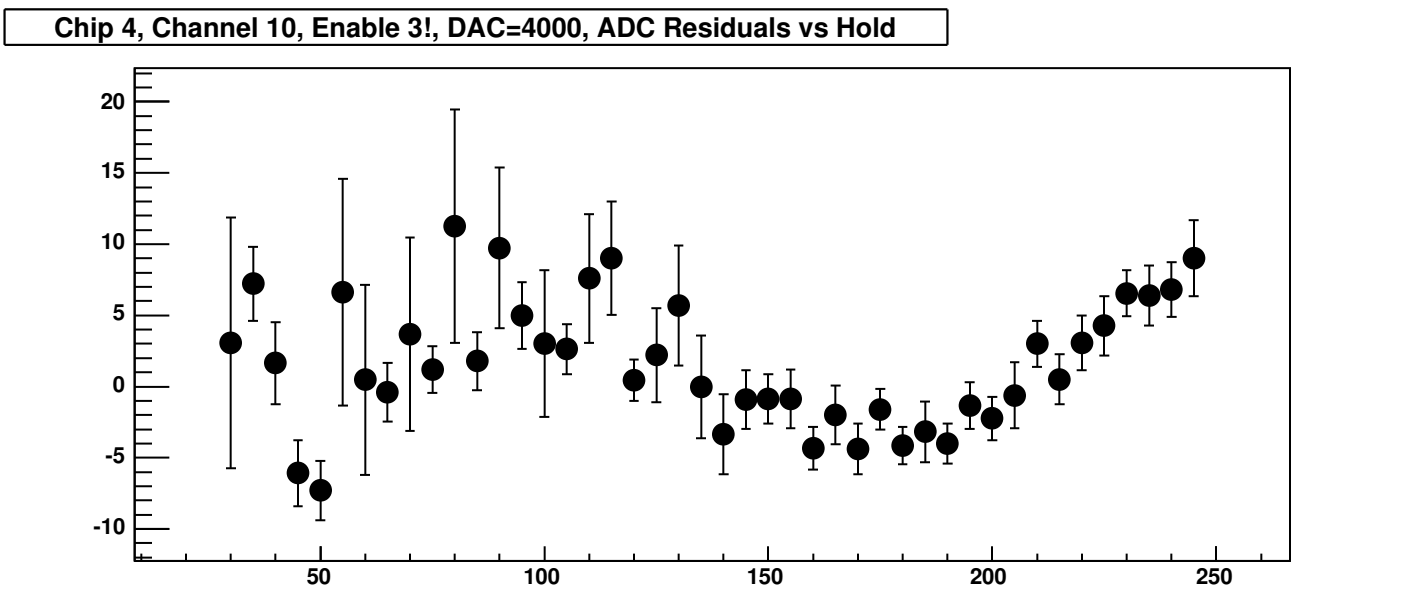


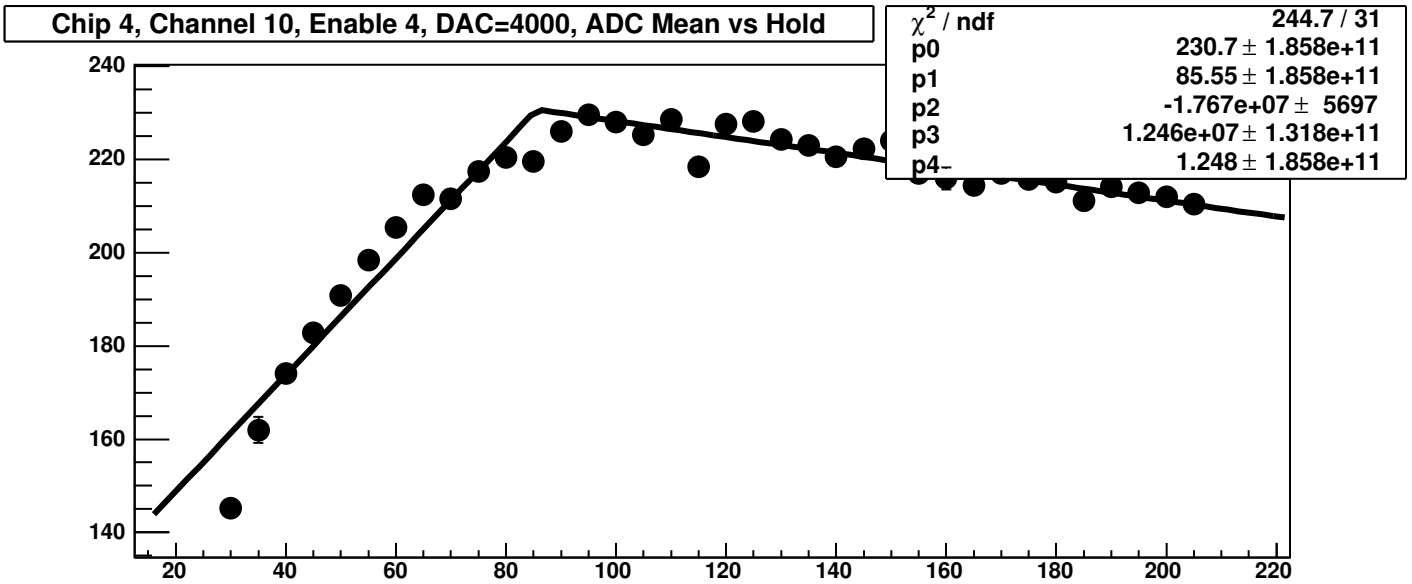
Chip 4, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

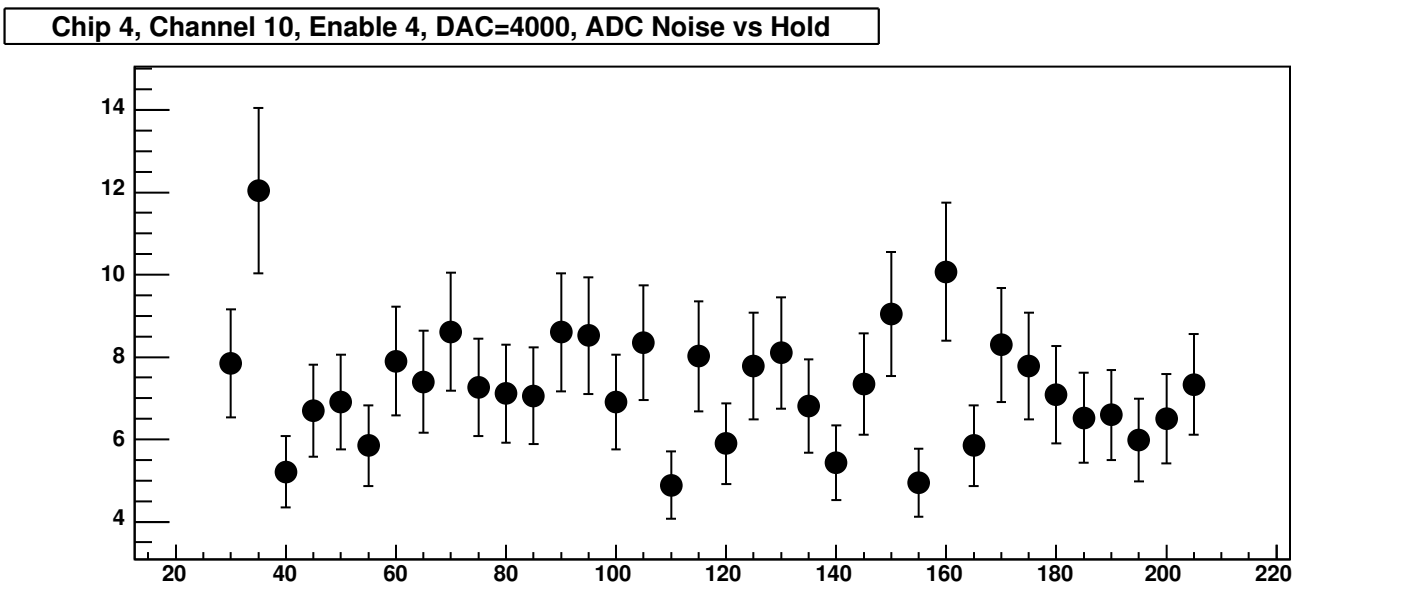


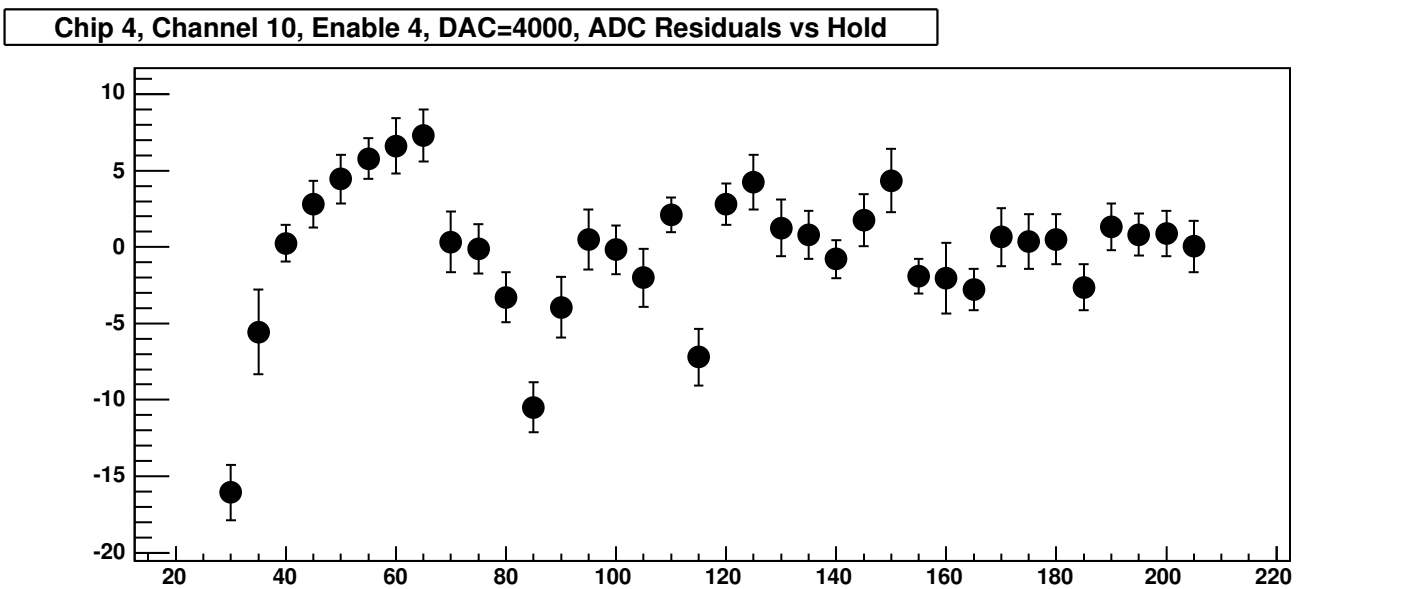

Chip 4, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 142.1 / 39
p0 -667.3 ± 0.7488
p1 4.693 ± 0.02919
p2 1.083e+04 ± 1.894
p3 32.17 ± 0.01073
10000
8000
6000
4000
2000
0
50
100
150
200
250


Chip 4, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold
45
40
35
30
25
20
15
10
5
50
100
150
200
250


Chip 4, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
50
100
150
200
250

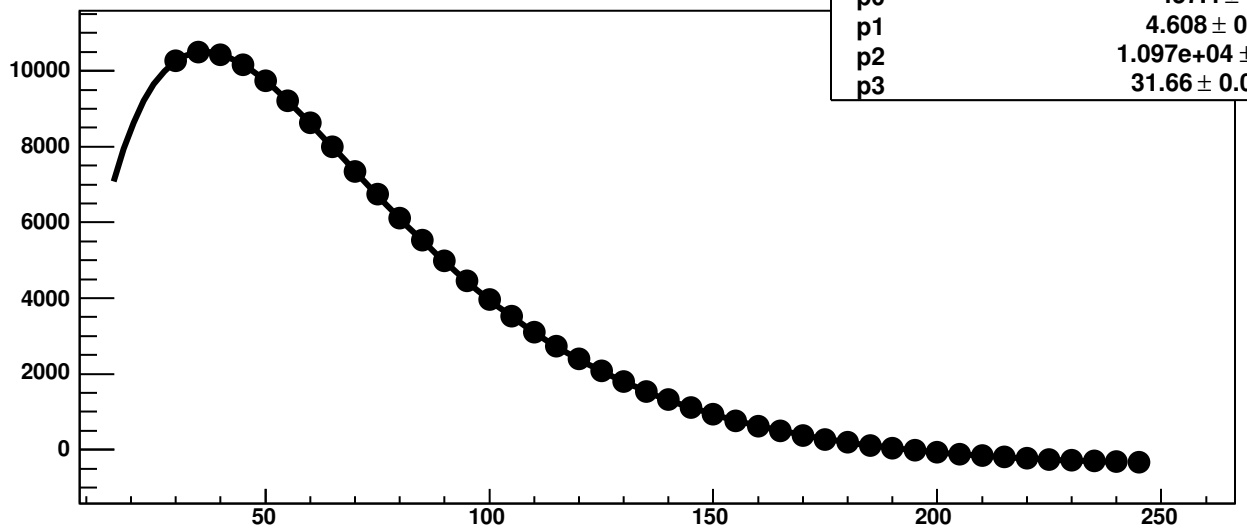

Chip 4, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 244.7 / 31
p0 230.7 ± 1.858e+11
p1 85.55 ± 1.858e+11
p2 -1.767e+07 ± 5697
p3 1.246e+07 ± 1.318e+11
p4 1.248 ± 1.858e+11
240
220
200
180
160
140
20
40
60
80
100
120
140
160
180
200
220


Chip 4, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
20
40
60
80
100
120
140
160
180
200
220

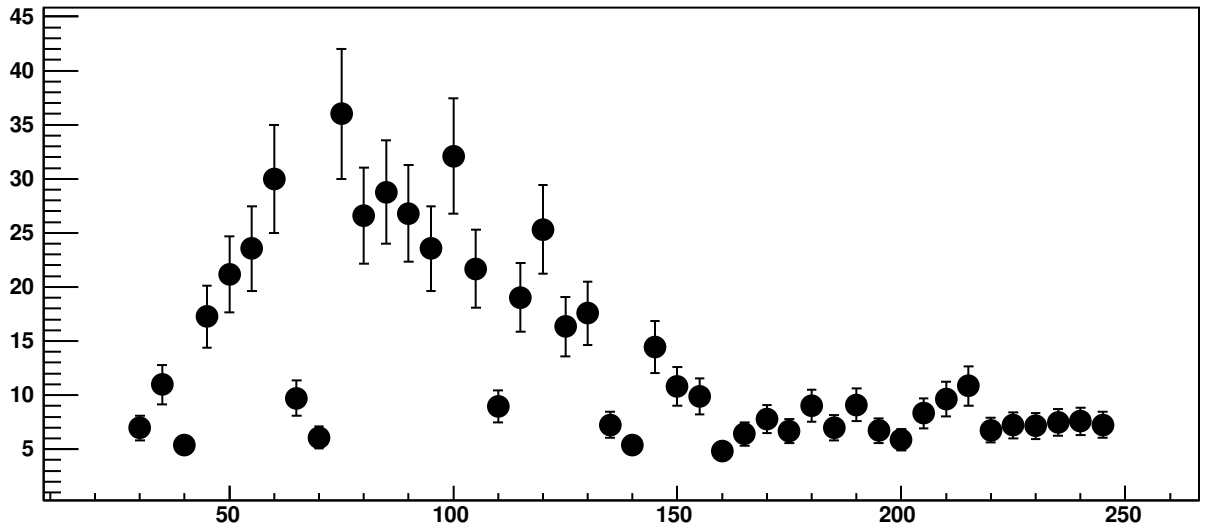

Chip 4, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
-20
20
40
60
80
100
120
140
160
180
200
220

Chip 4, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold

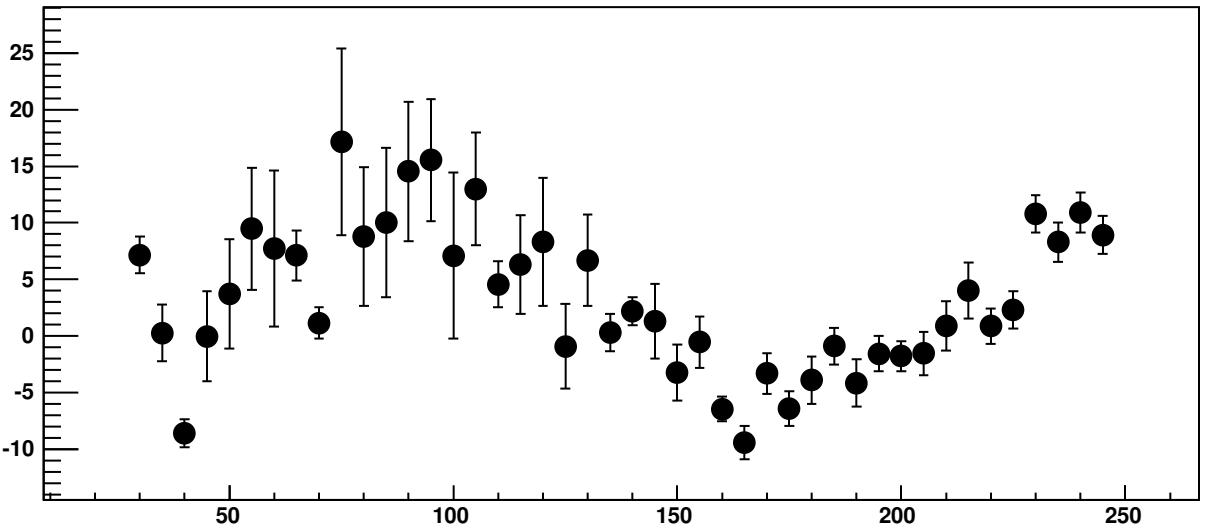
χ^2 / ndf	349.7 / 39
p0	-457.4 ± 0.6945
p1	4.608 ± 0.01812
p2	1.097e+04 ± 1.015
p3	31.66 ± 0.008677



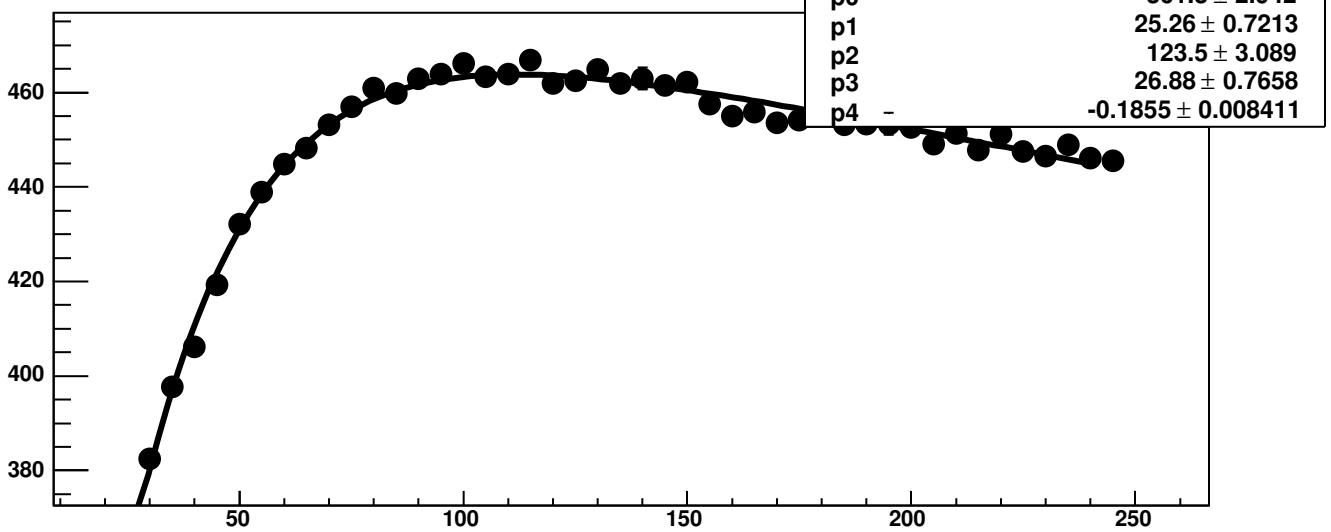
Chip 4, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold



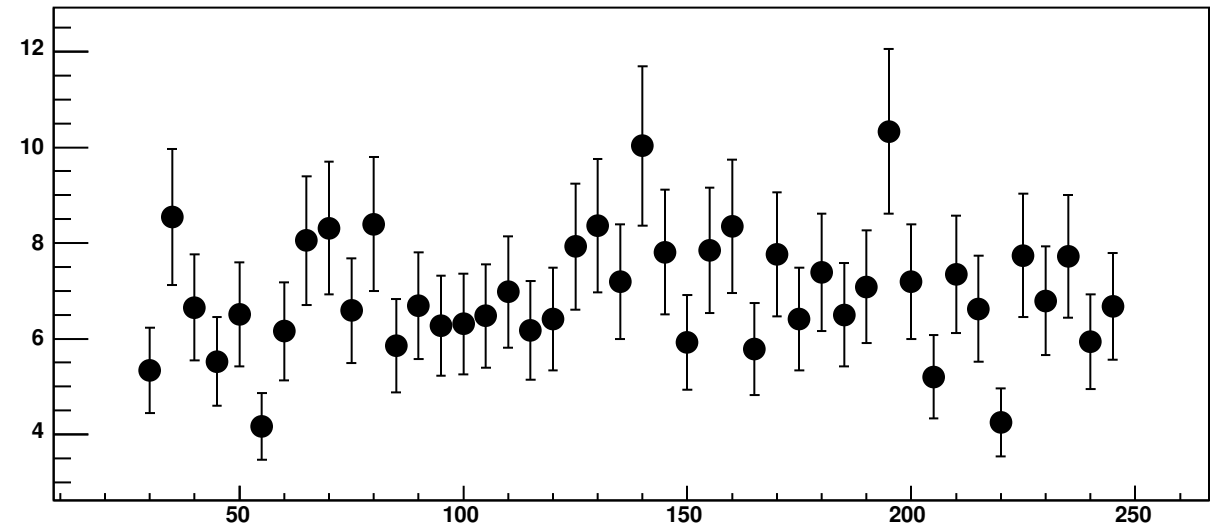
Chip 4, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold



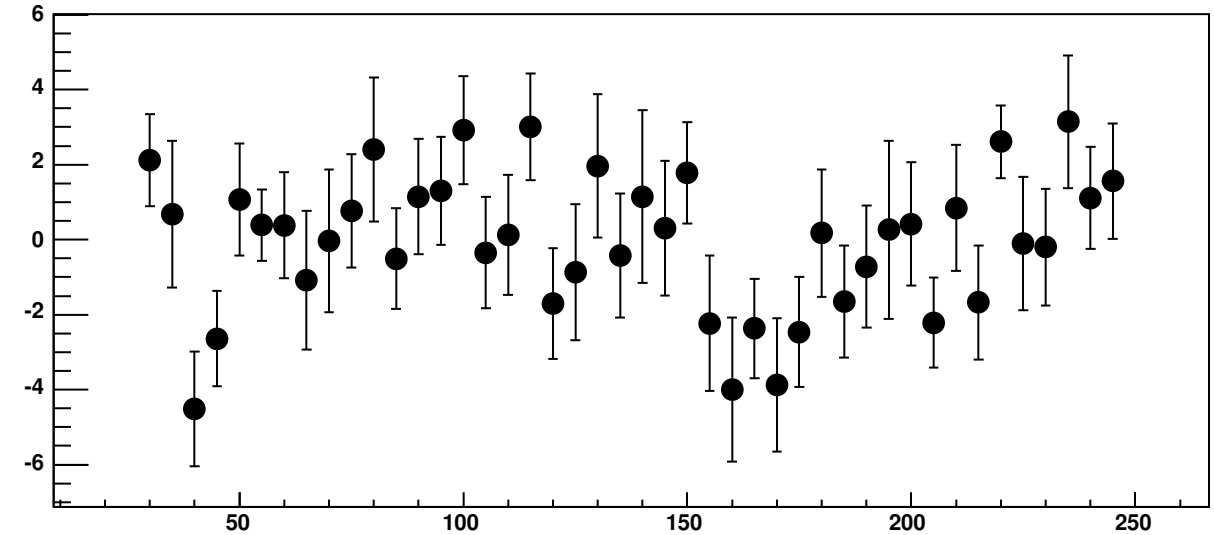
Chip 4, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold



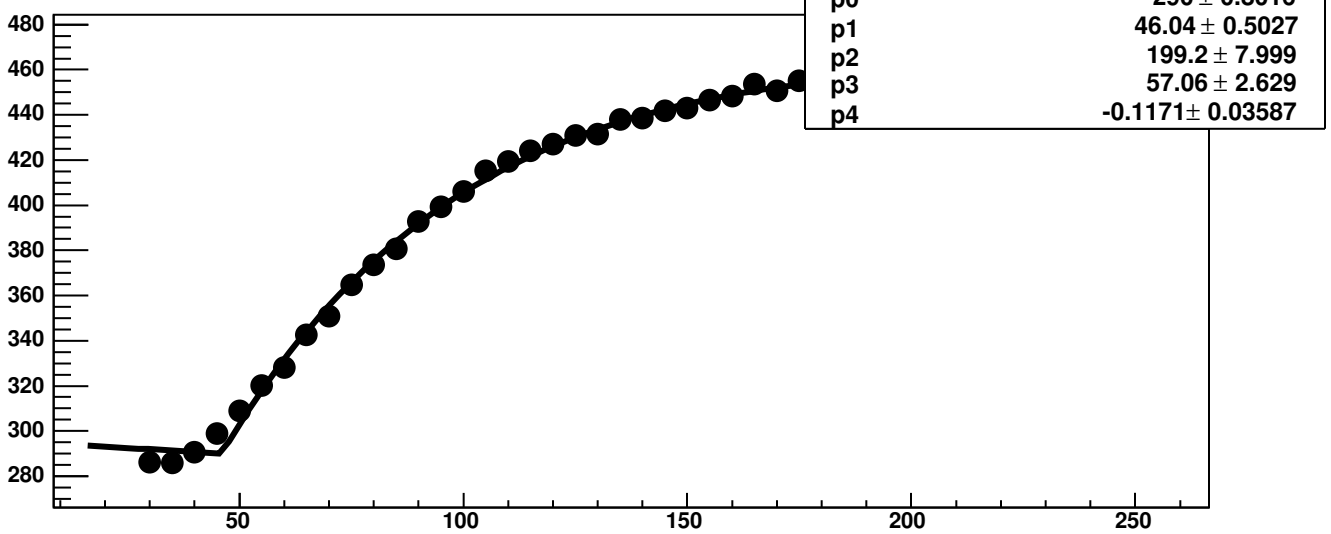
Chip 4, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



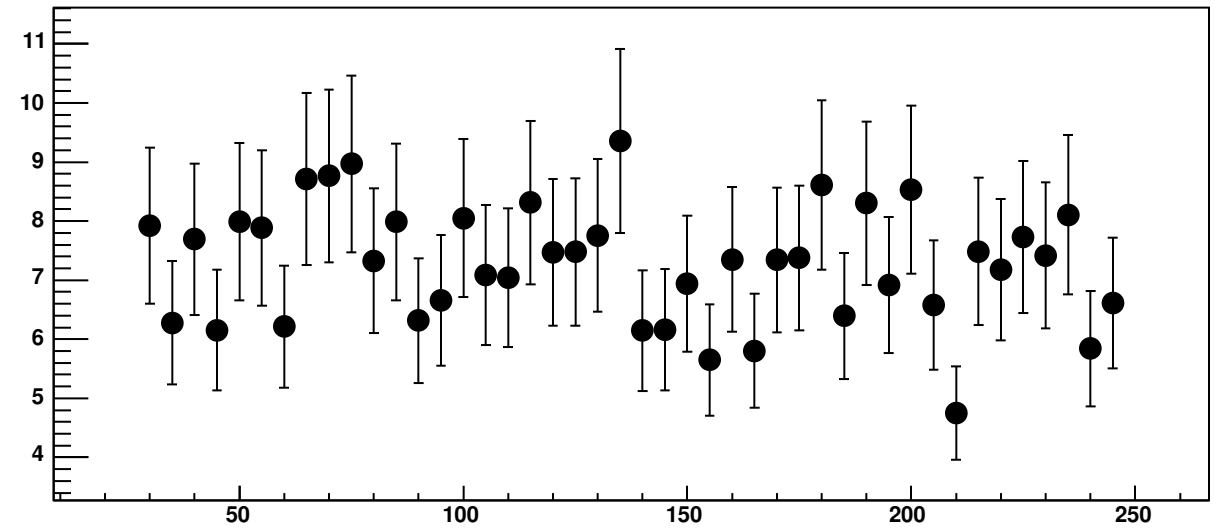
Chip 4, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold



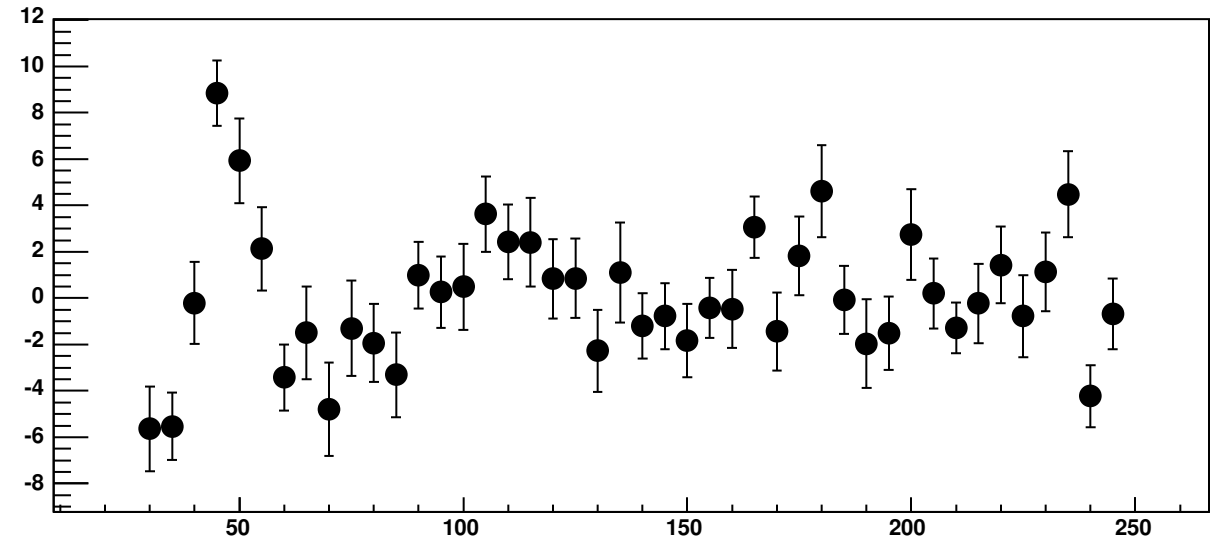
Chip 4, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold



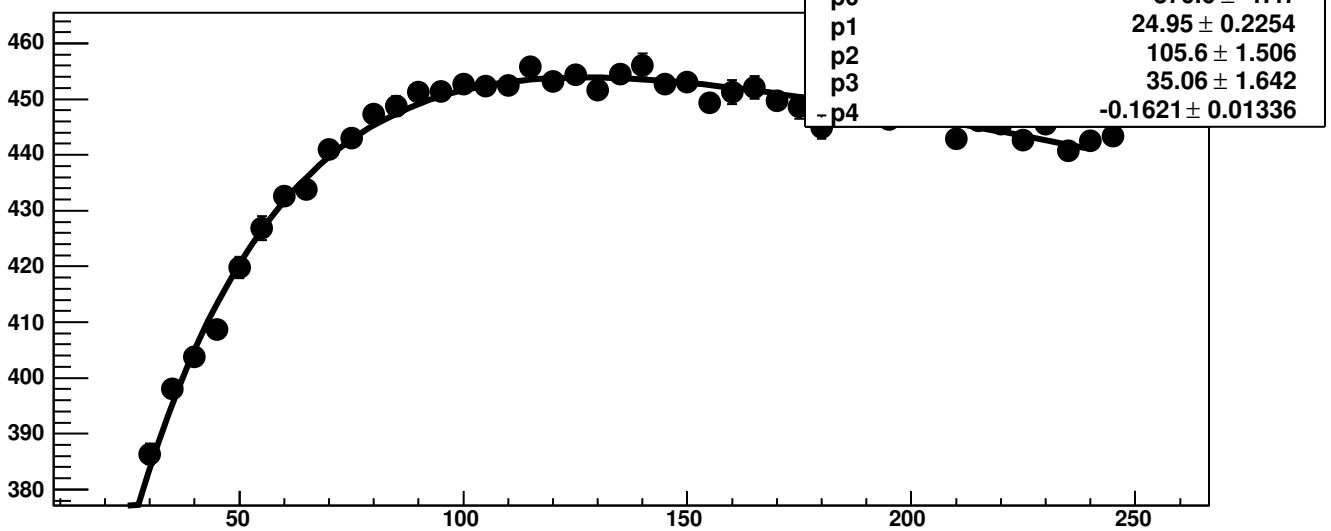
Chip 4, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



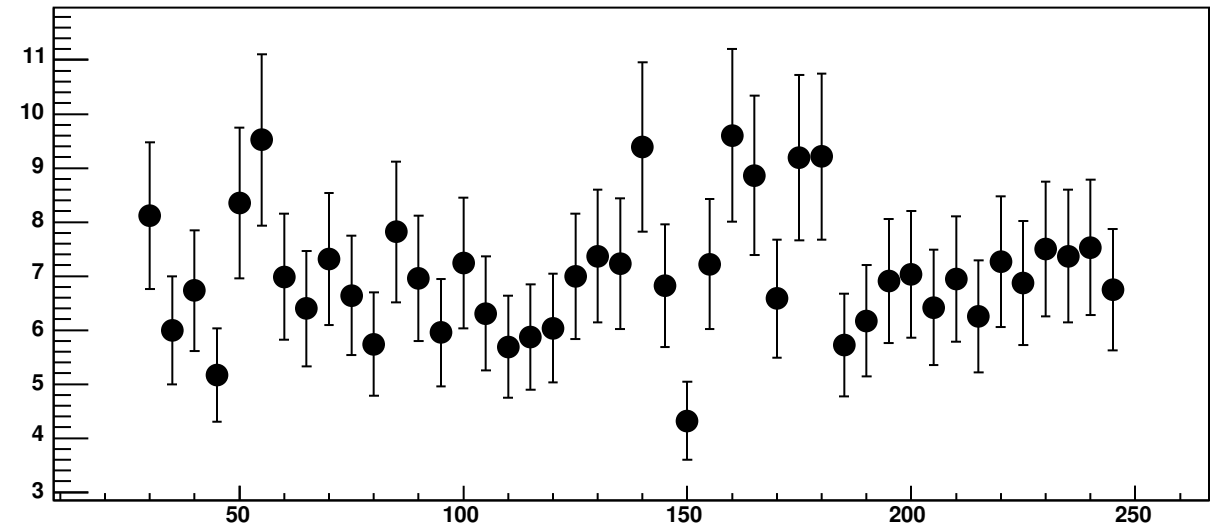
Chip 4, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold



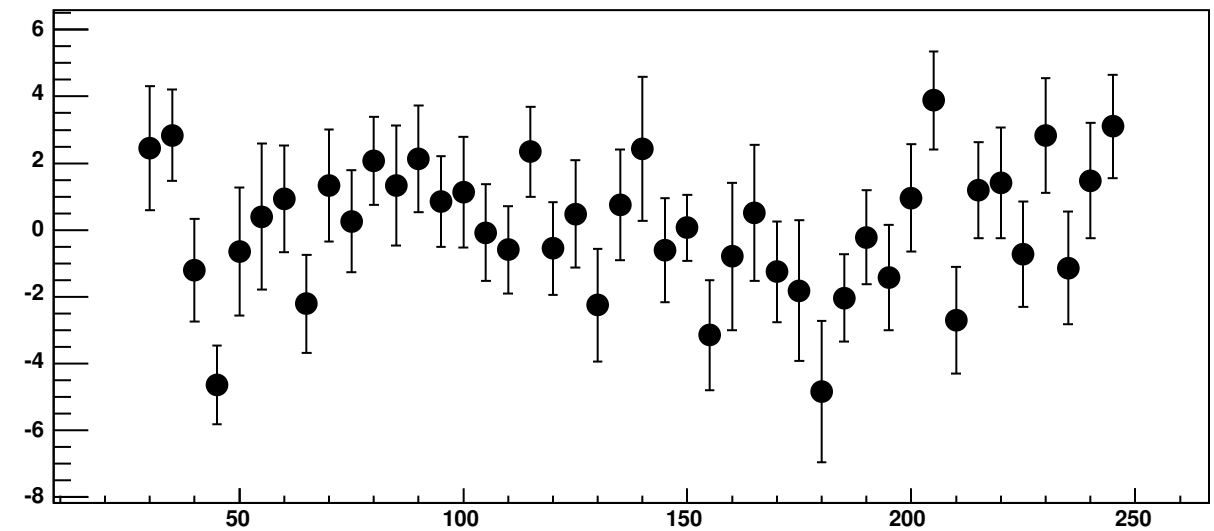
Chip 4, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold

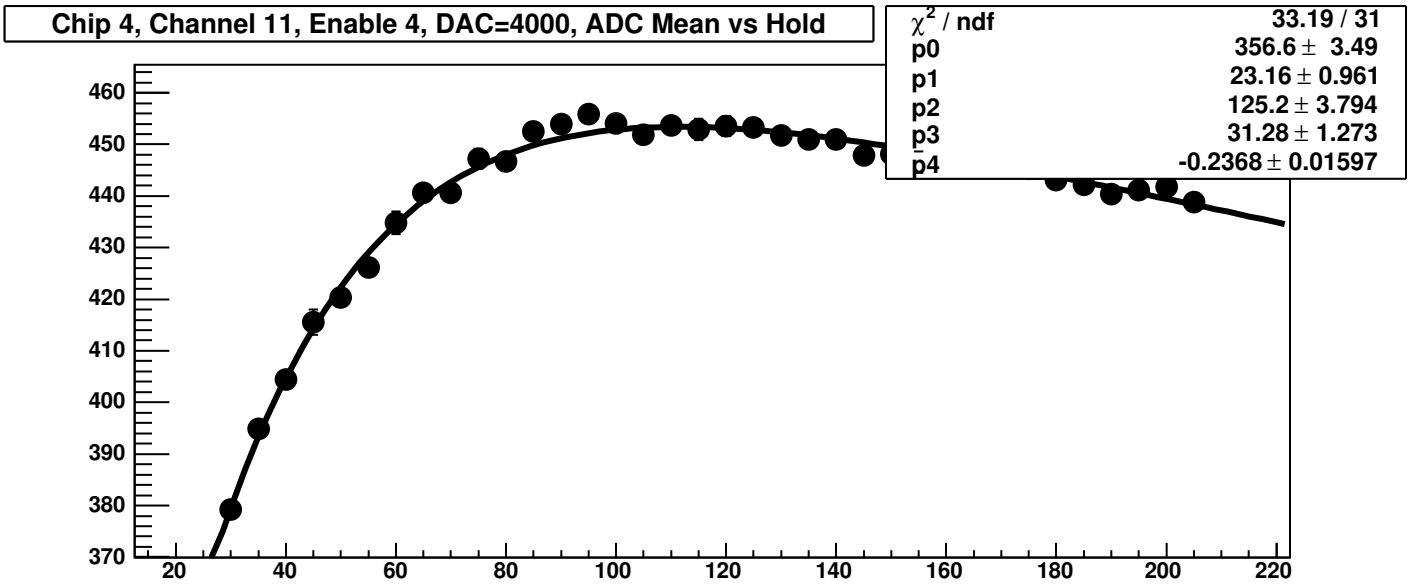


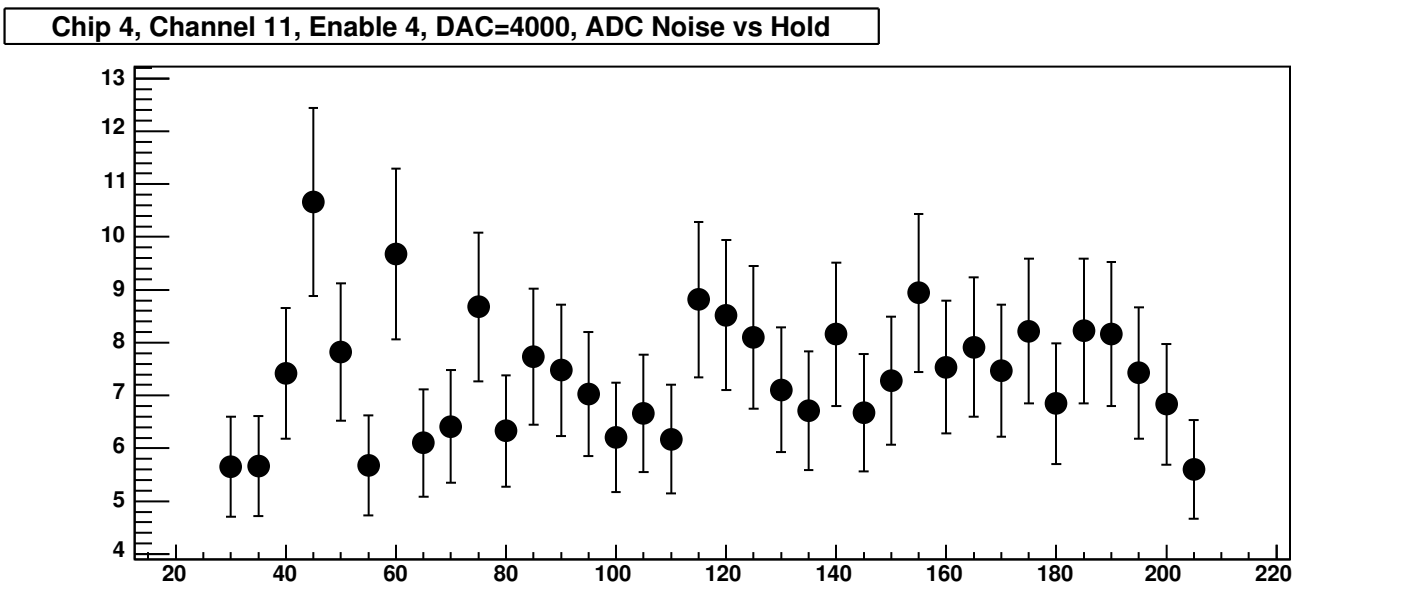
Chip 4, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold

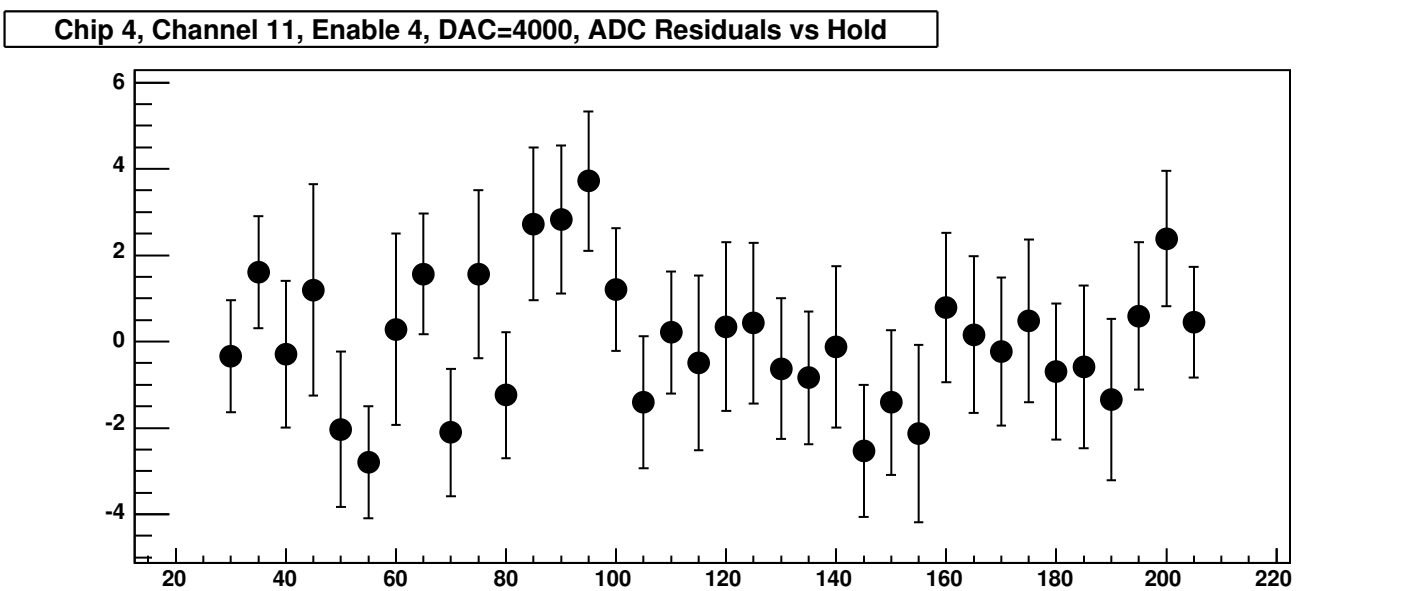


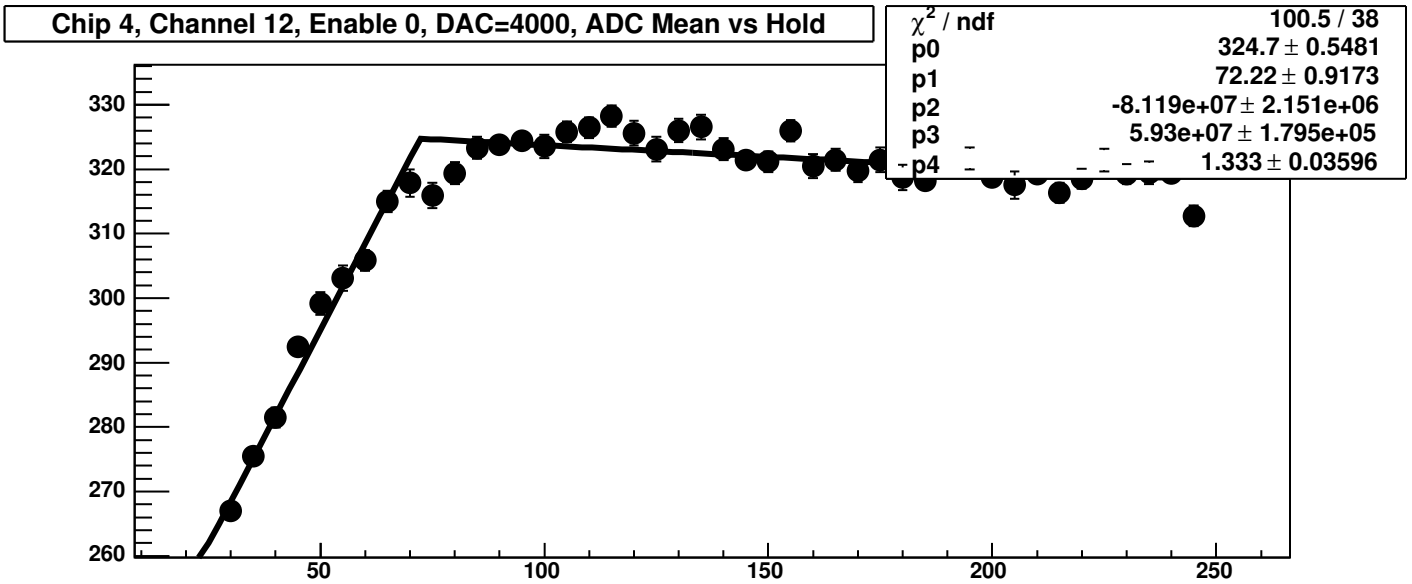
Chip 4, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold

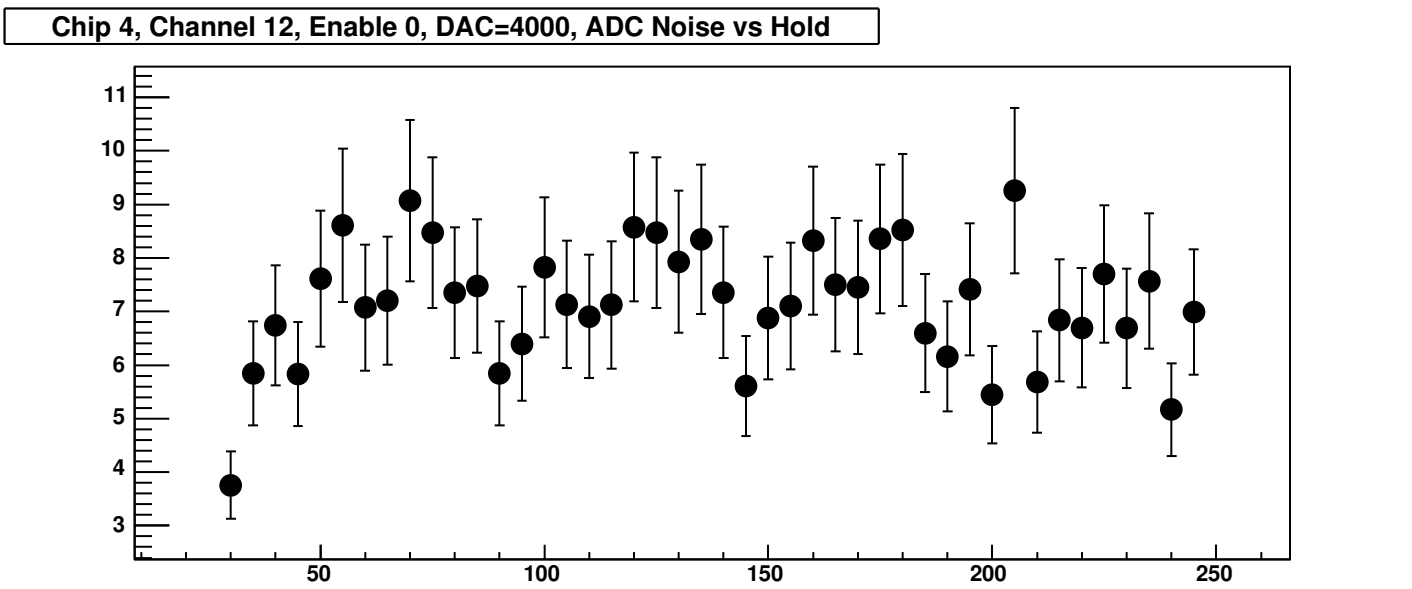


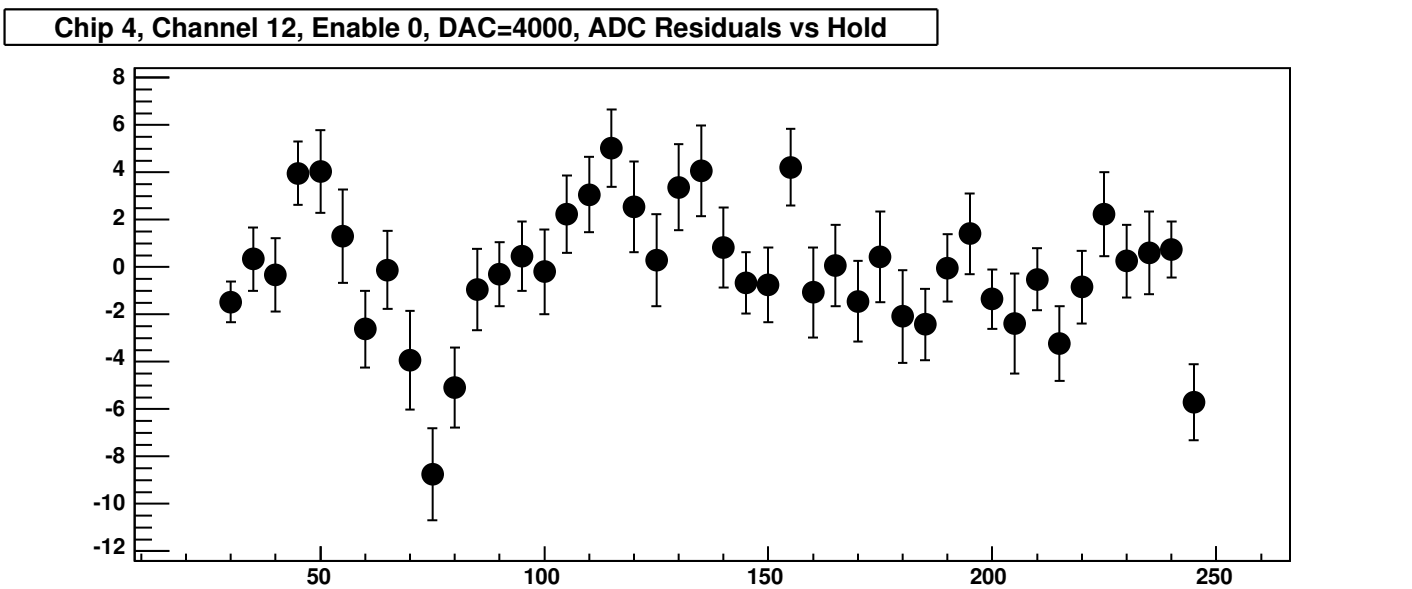

Chip 4, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 33.19 / 31
p0 356.6 ± 3.49
p1 23.16 ± 0.961
p2 125.2 ± 3.794
p3 31.28 ± 1.273
p4 -0.2368 ± 0.01597
460
450
440
430
420
410
400
390
380
370
20
40
60
80
100
120
140
160
180
200
220


Chip 4, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
20
40
60
80
100
120
140
160
180
200
220


Chip 4, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
20
40
60
80
100
120
140
160
180
200
220

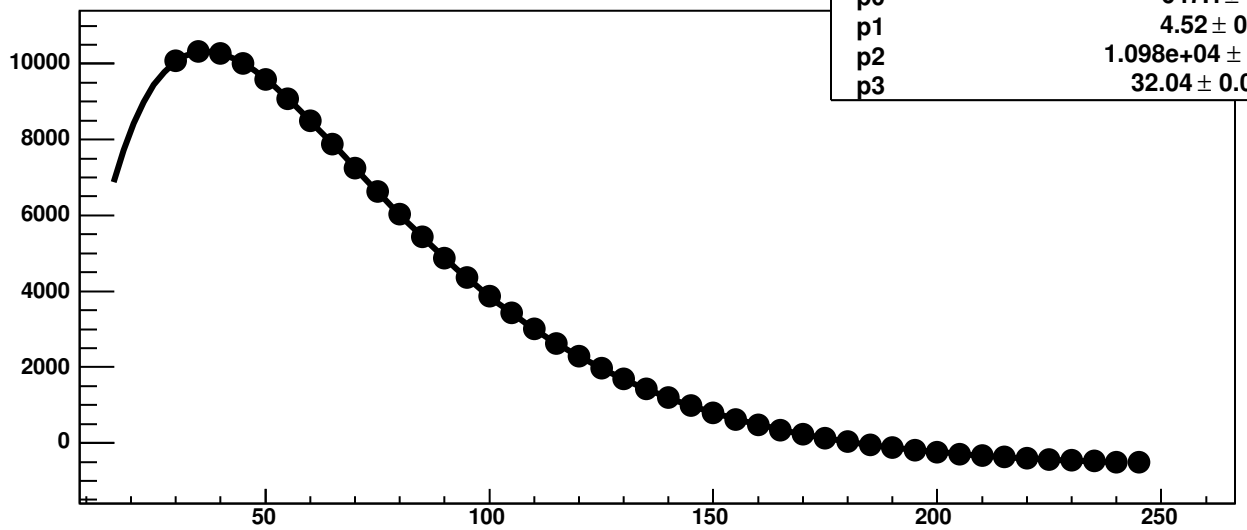

Chip 4, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 100.5 / 38
p0 324.7 ± 0.5481
p1 72.22 ± 0.9173
p2 -8.119e+07 ± 2.151e+06
p3 5.93e+07 ± 1.795e+05
p4 1.333 ± 0.03596
330
320
310
300
290
280
270
260
50
100
150
200
250


Chip 4, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
50
100
150
200
250

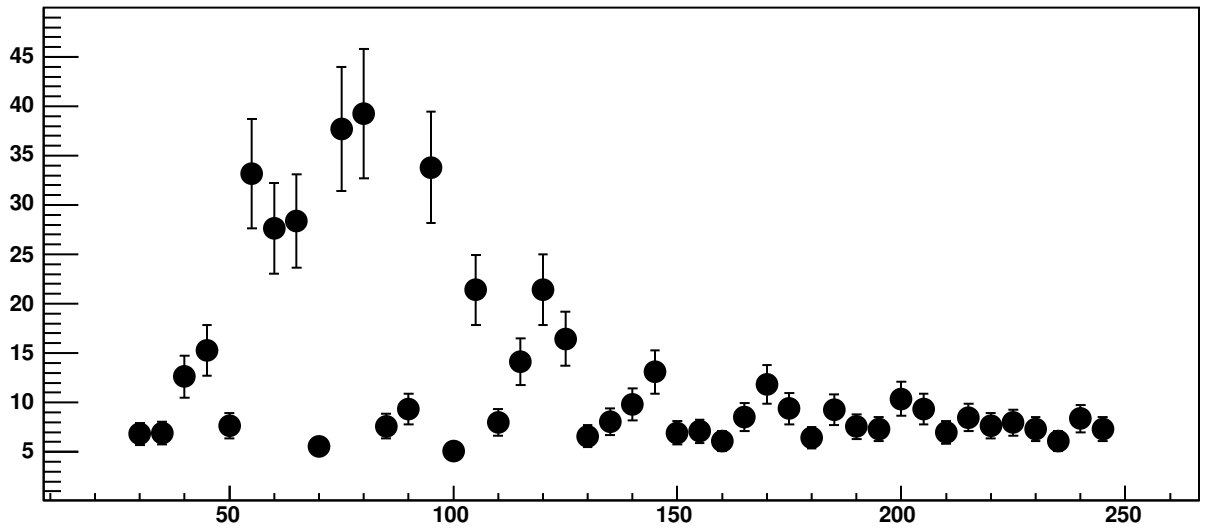

Chip 4, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
-12
50
100
150
200
250

Chip 4, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold

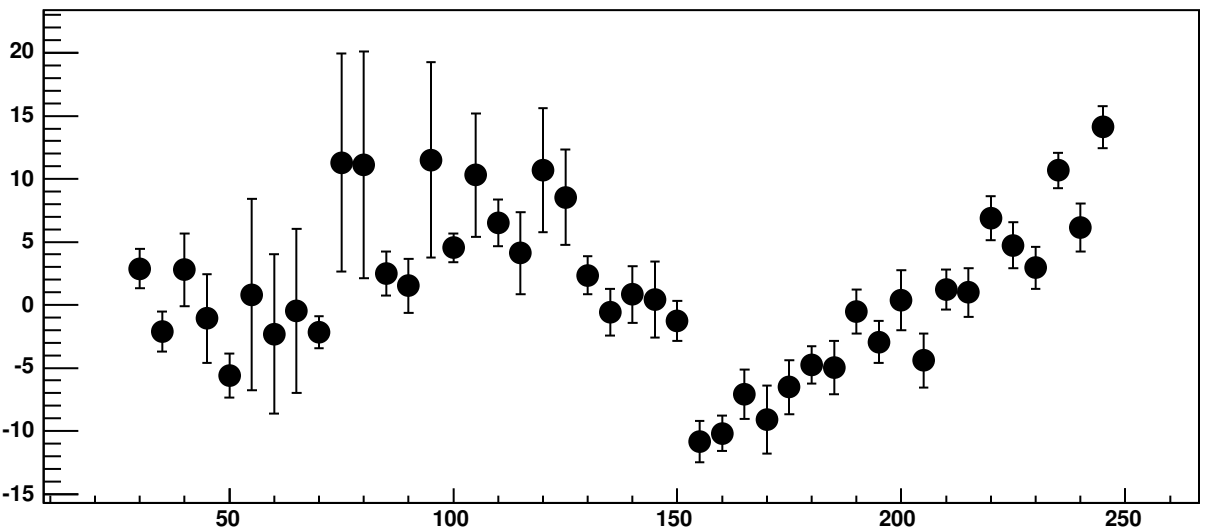
χ^2 / ndf	324 / 39
p0	-647.1 ± 0.6715
p1	4.52 ± 0.01698
p2	1.098e+04 ± 0.9839
p3	32.04 ± 0.007917



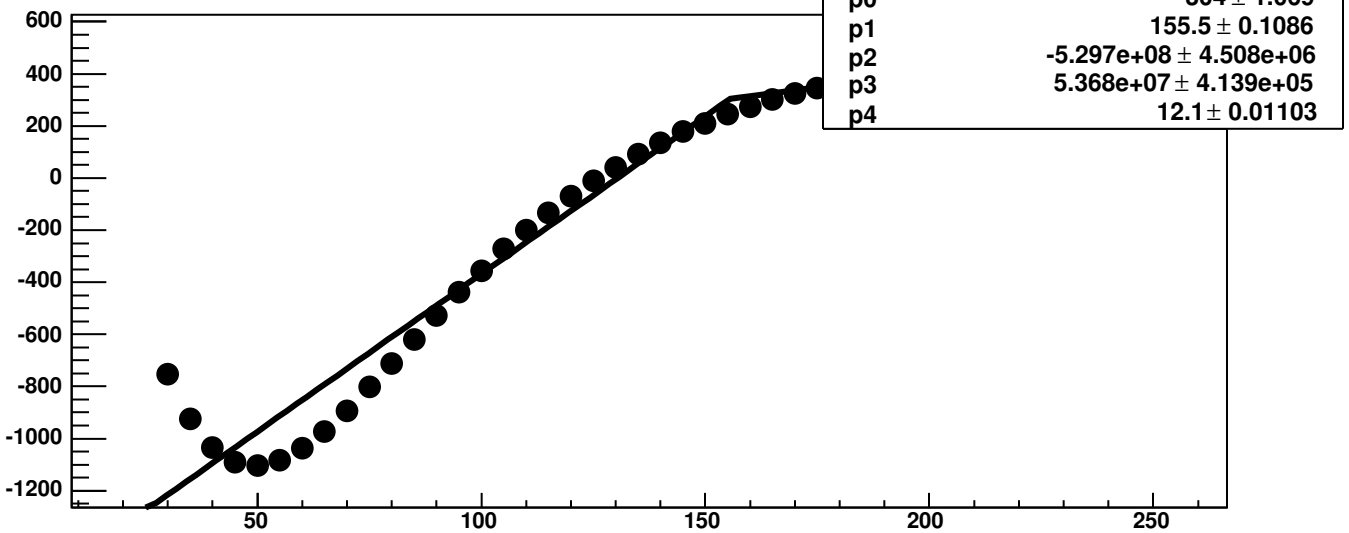
Chip 4, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold



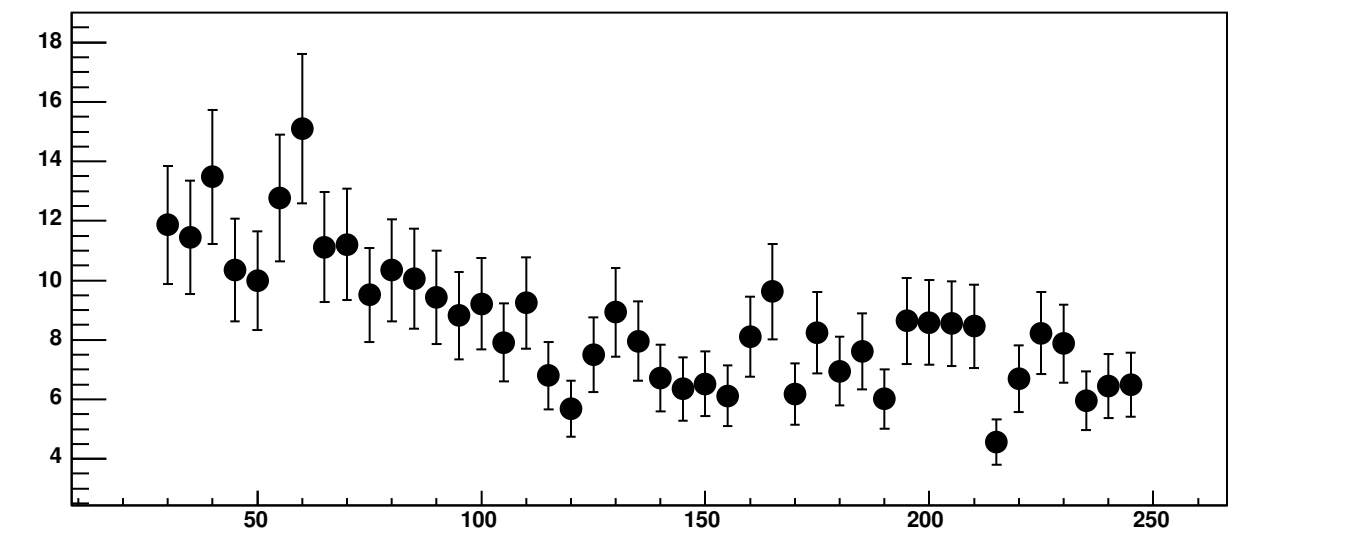
Chip 4, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold



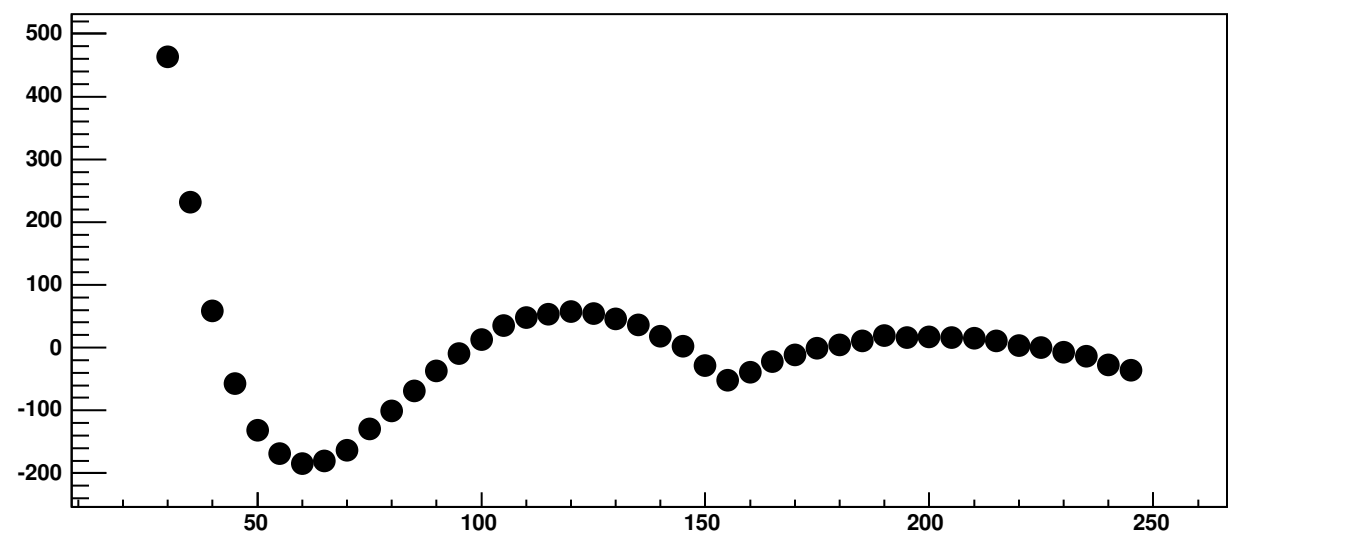
Chip 4, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold

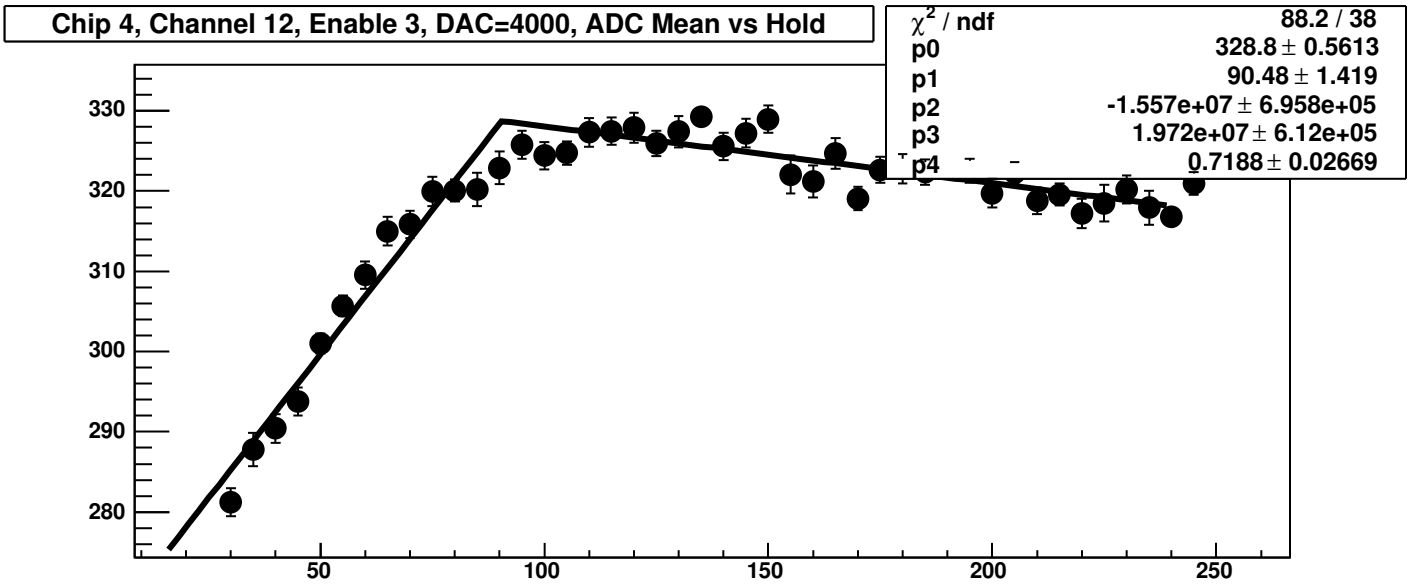


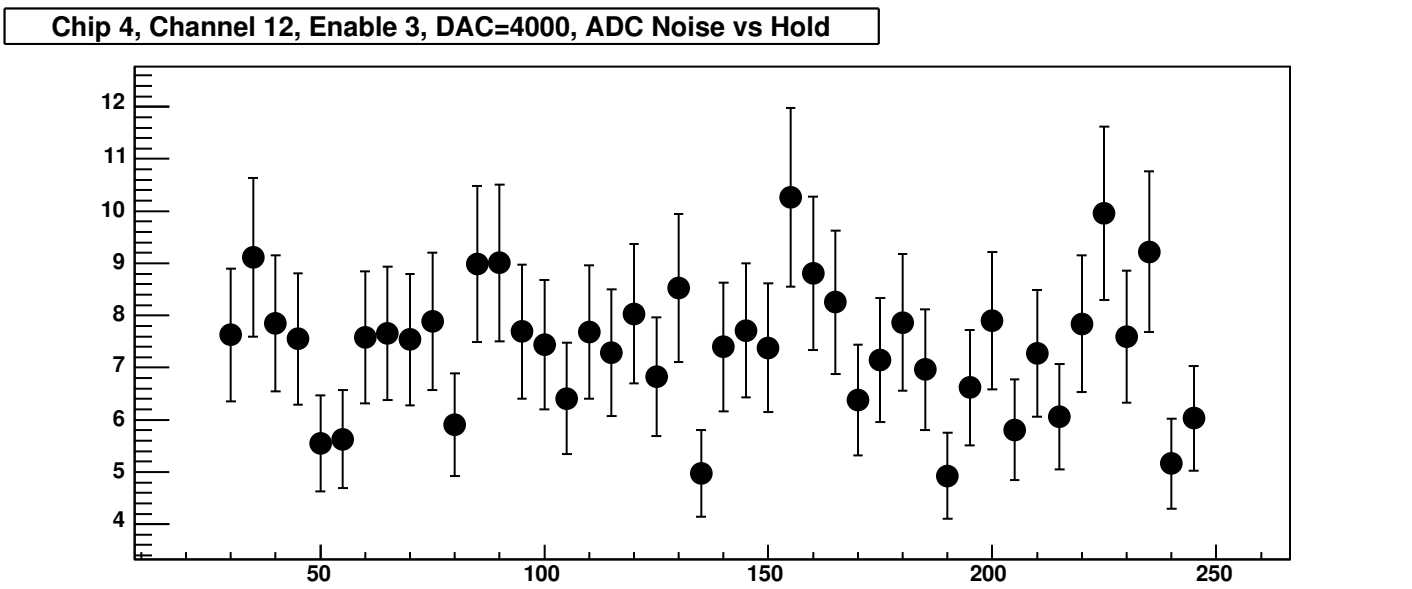
Chip 4, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold

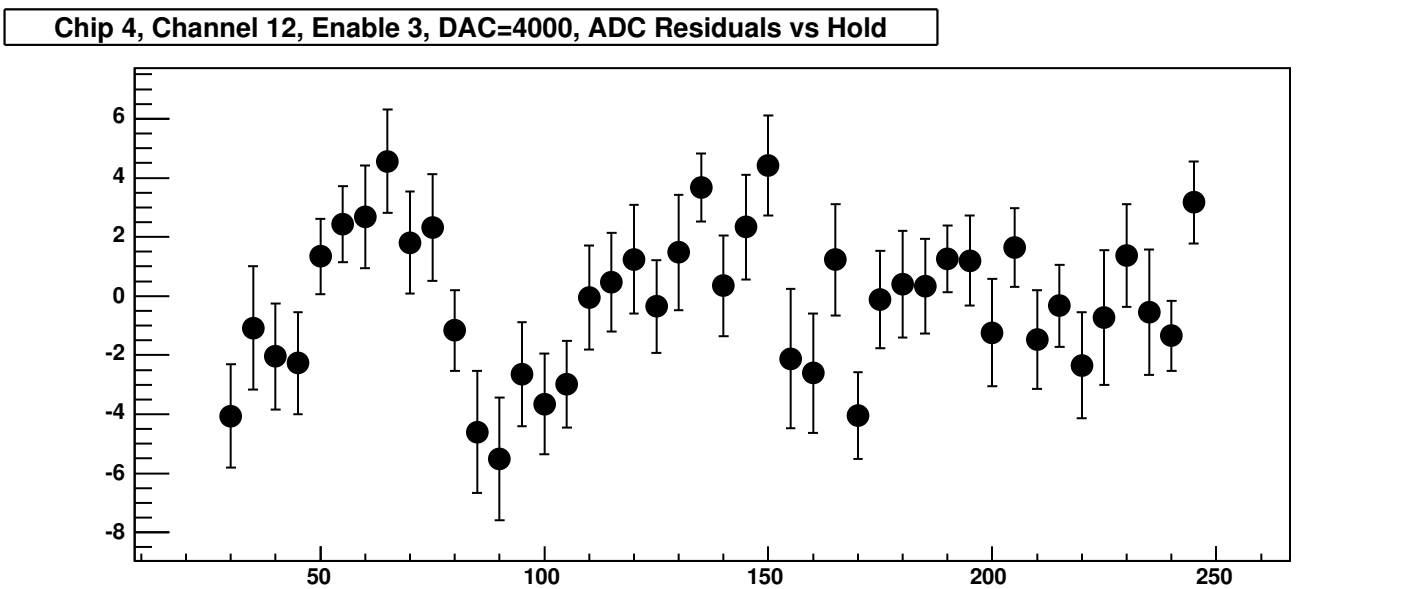


Chip 4, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold

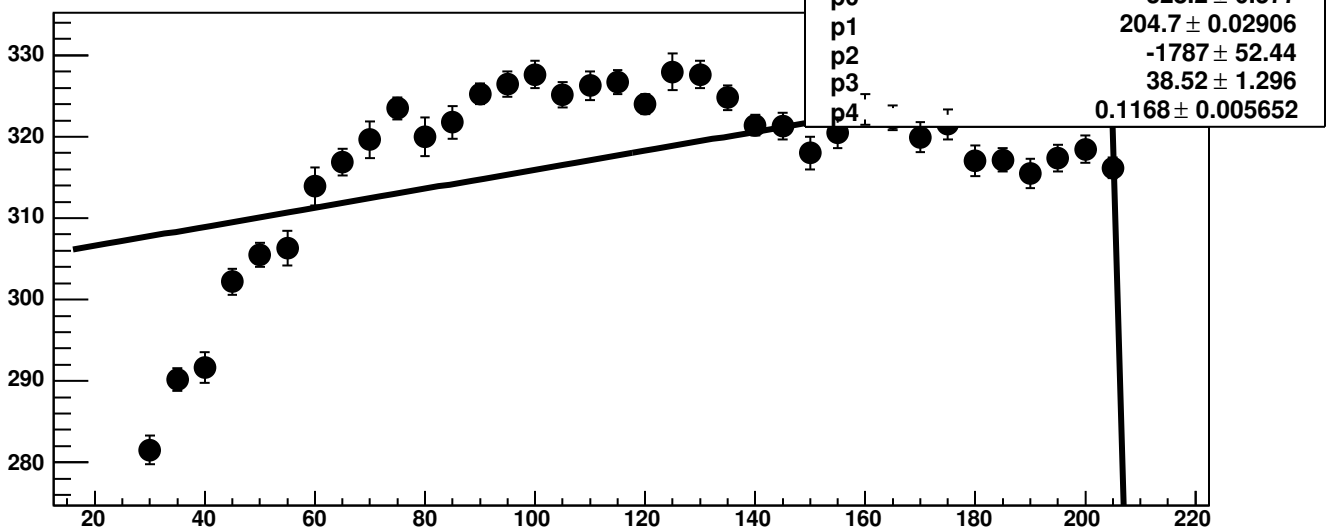



Chip 4, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 88.2 / 38
p0 328.8 ± 0.5613
p1 90.48 ± 1.419
p2 -1.557e+07 ± 6.958e+05
p3 1.972e+07 ± 6.12e+05
p4 0.7188 ± 0.02669
330
320
310
300
290
280
50
100
150
200
250

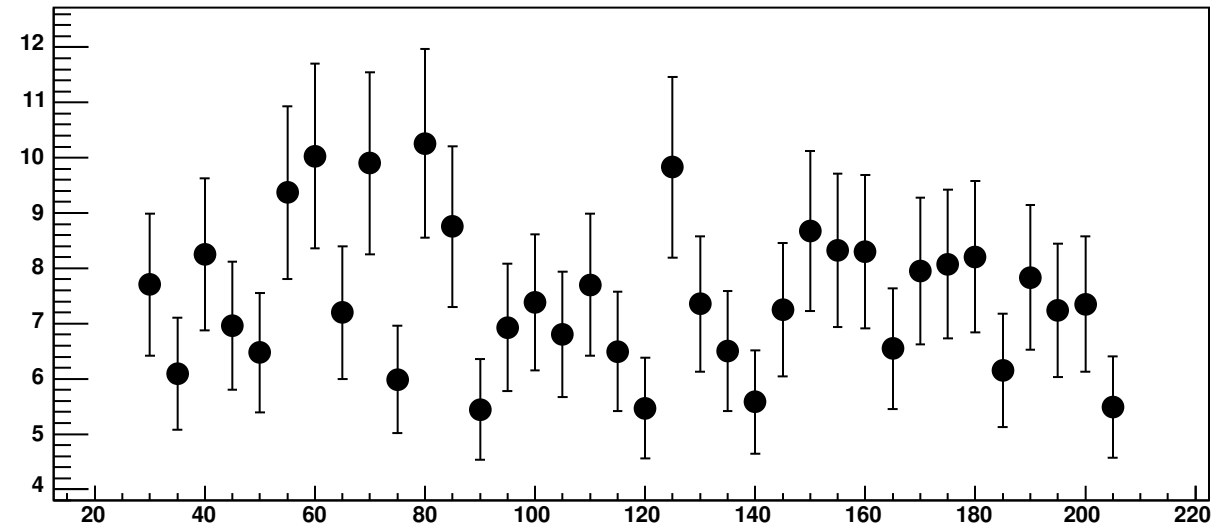

Chip 4, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 4, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
-8
50
100
150
200
250

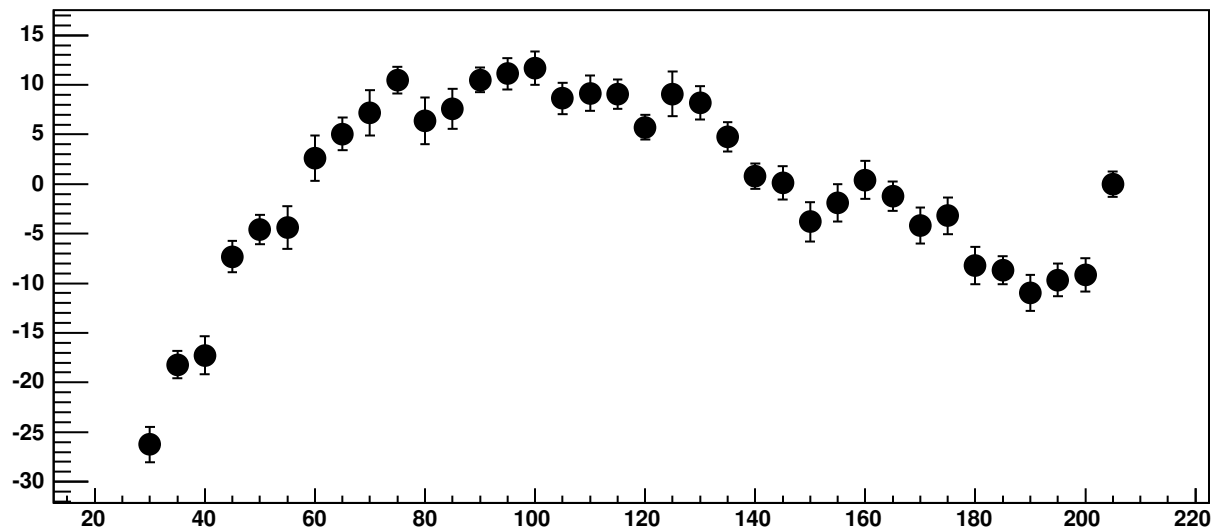
Chip 4, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold

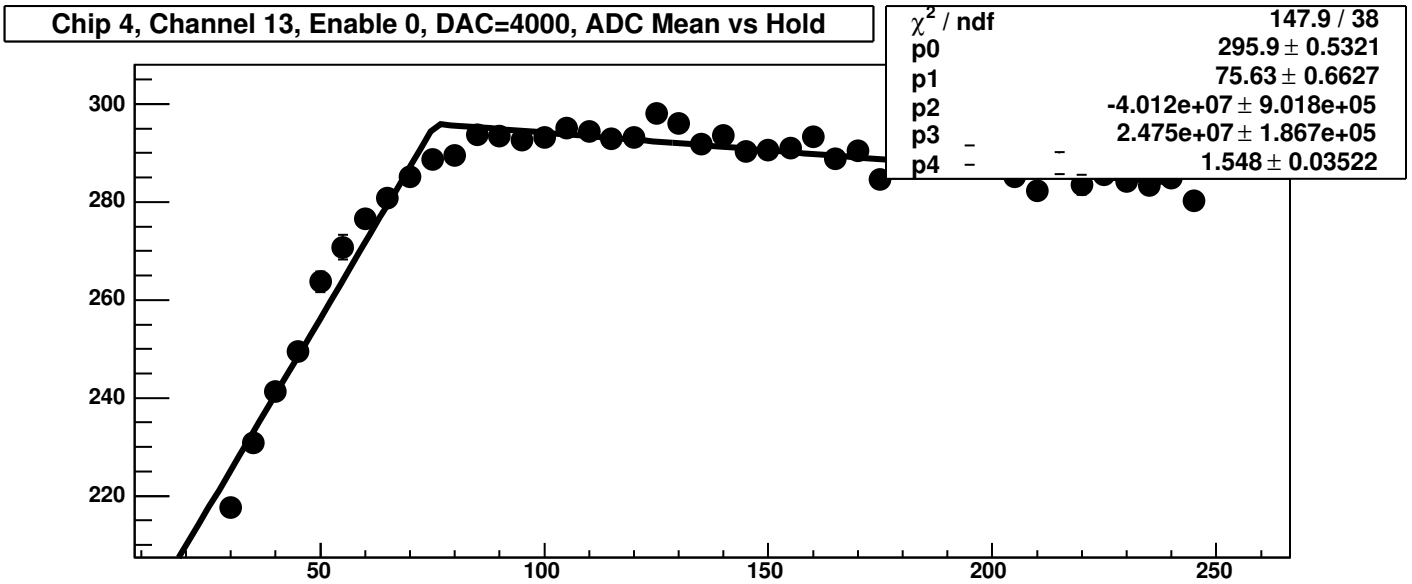


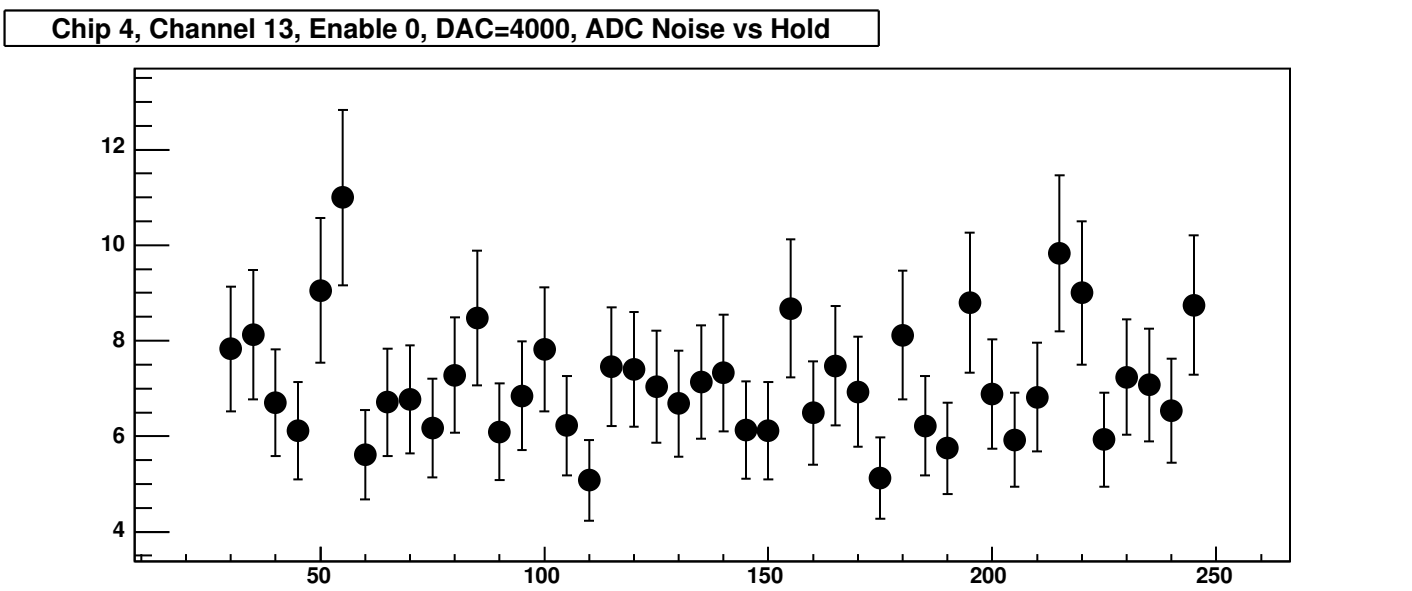
Chip 4, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold

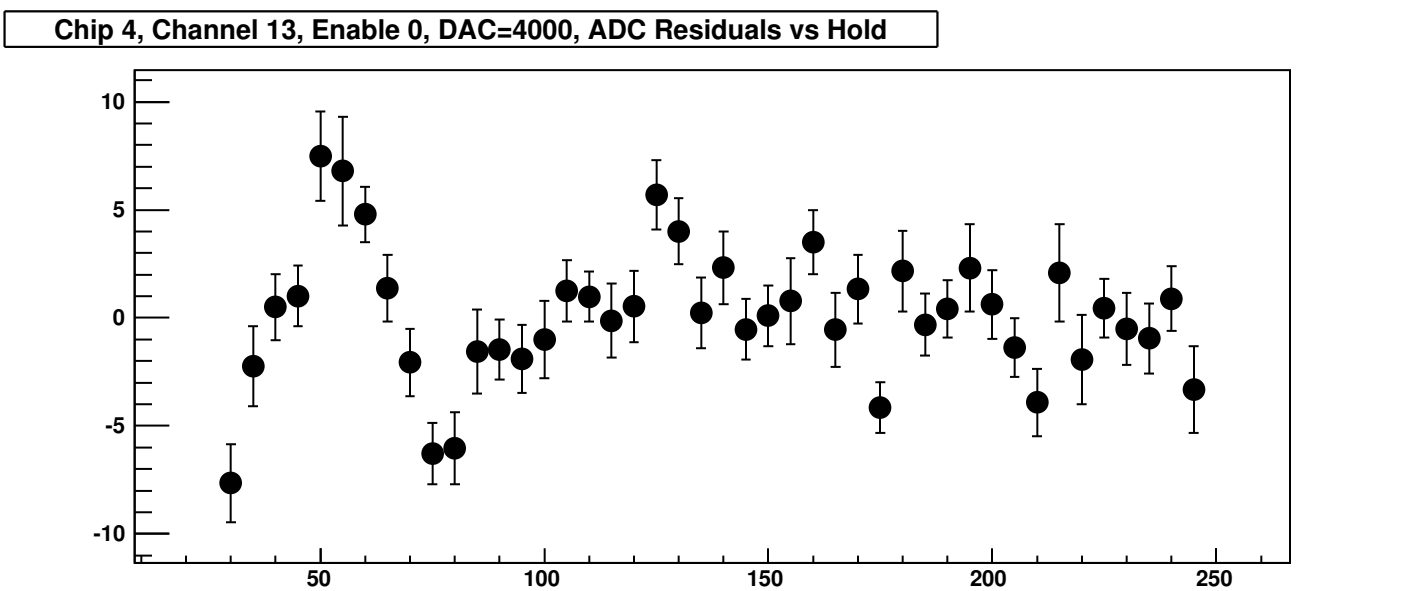


Chip 4, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold

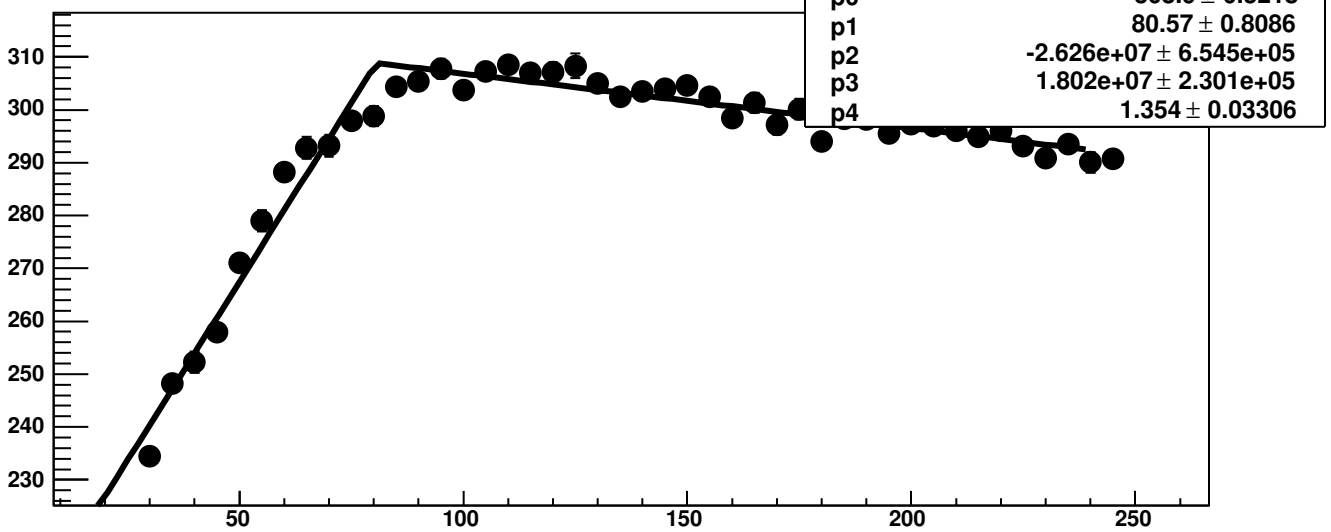



Chip 4, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 147.9 / 38
p0 295.9 ± 0.5321
p1 75.63 ± 0.6627
p2 -4.012e+07 ± 9.018e+05
p3 2.475e+07 ± 1.867e+05
p4 1.548 ± 0.03522
300
280
260
240
220
50
100
150
200
250

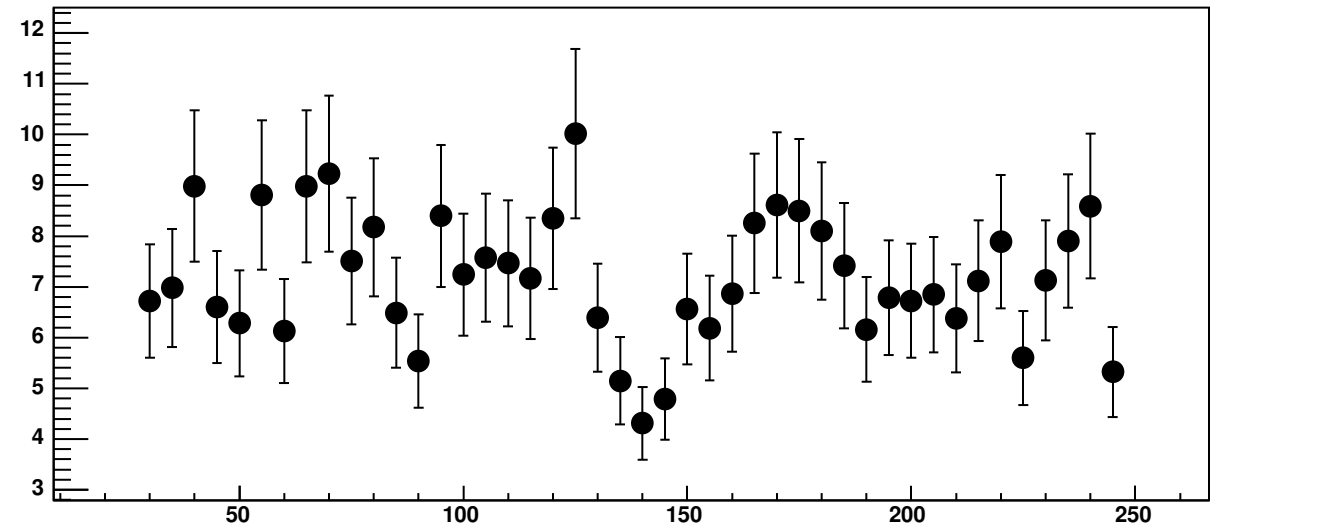

Chip 4, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250


Chip 4, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

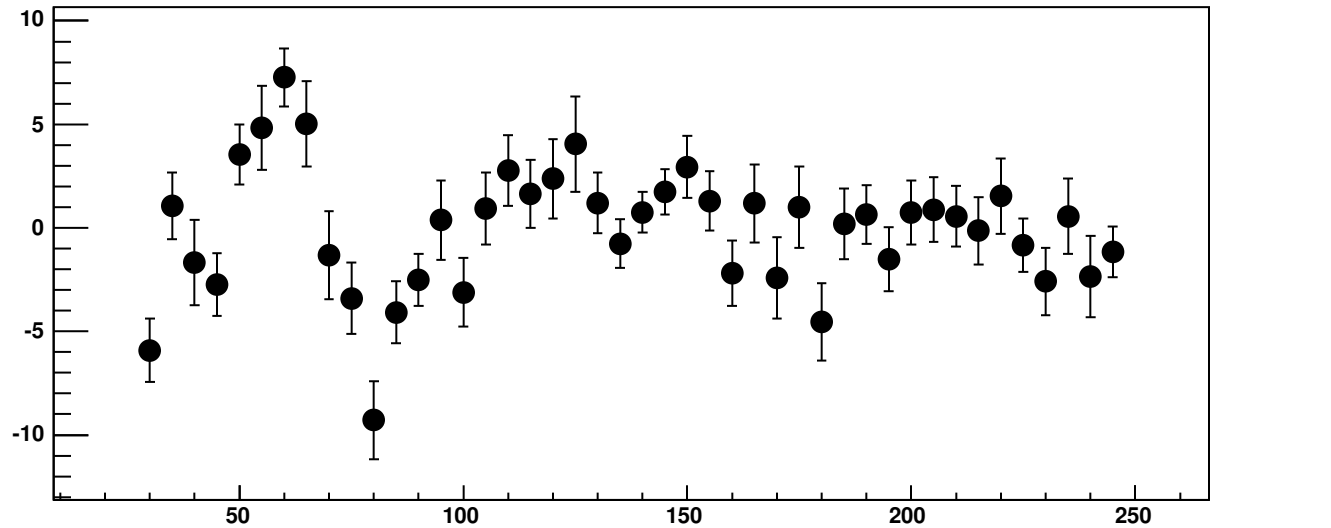
Chip 4, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold



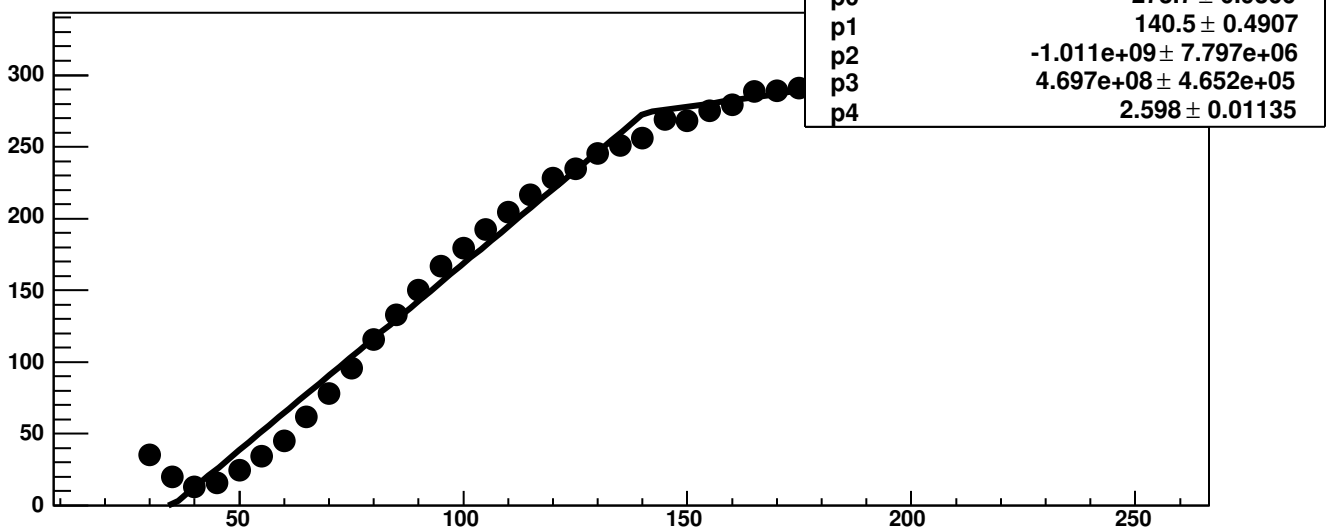
Chip 4, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold



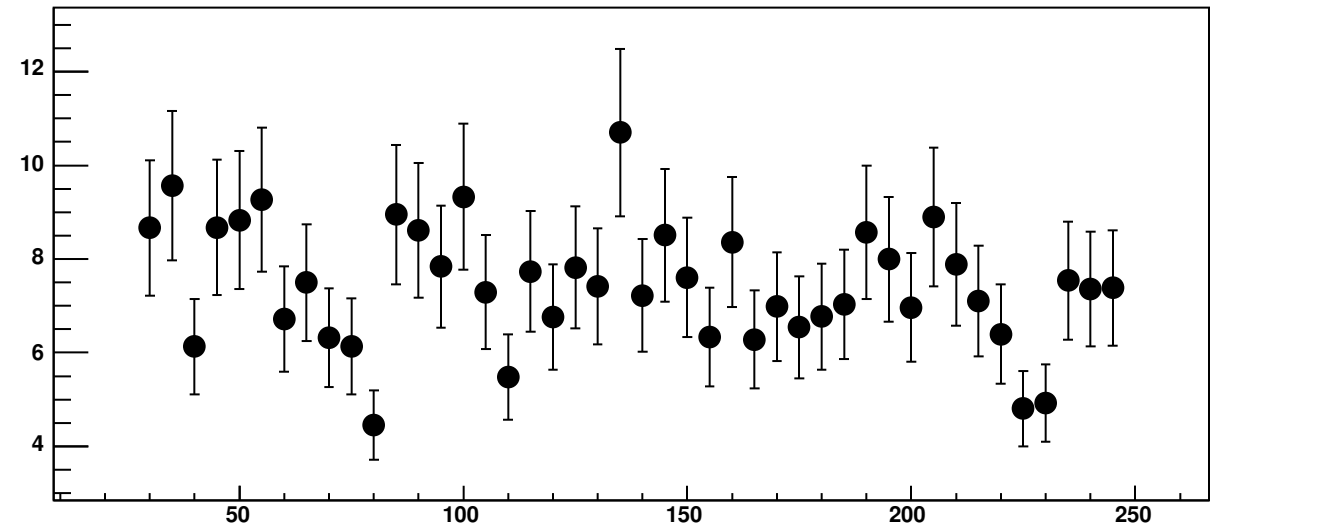
Chip 4, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold



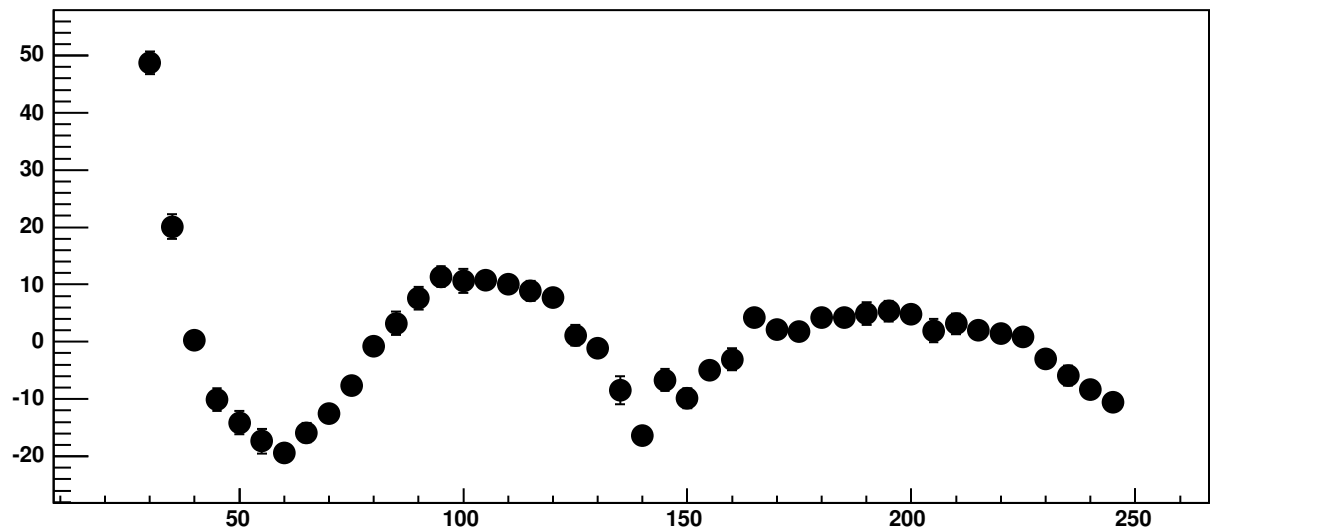
Chip 4, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

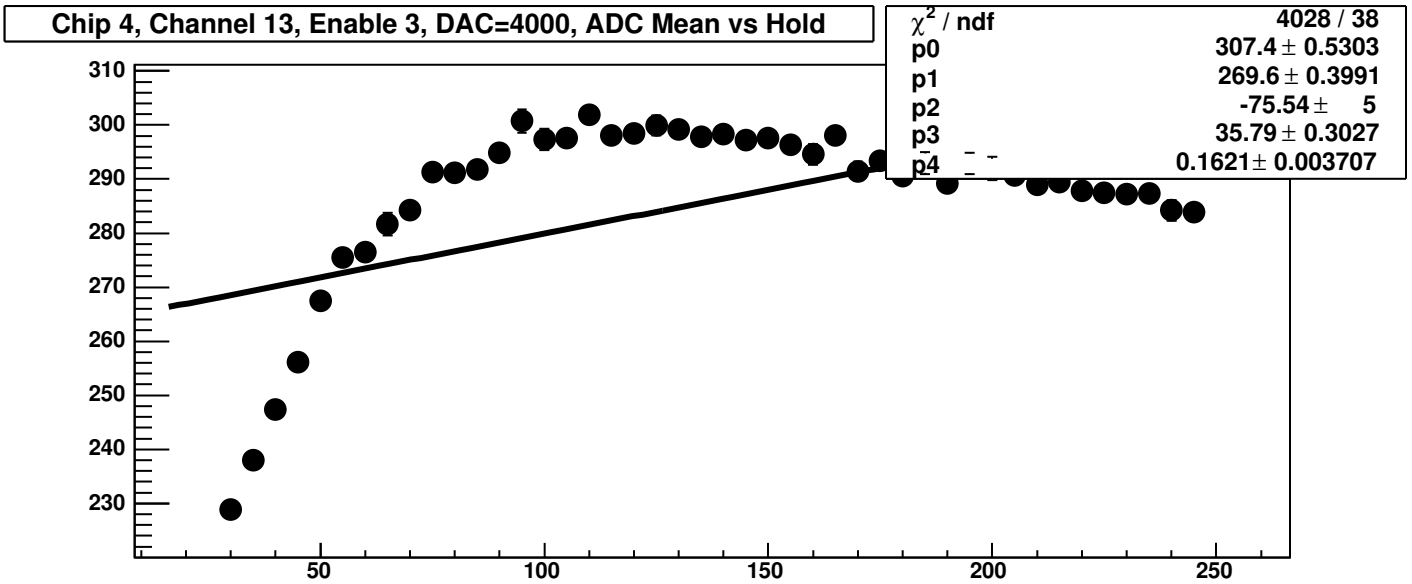


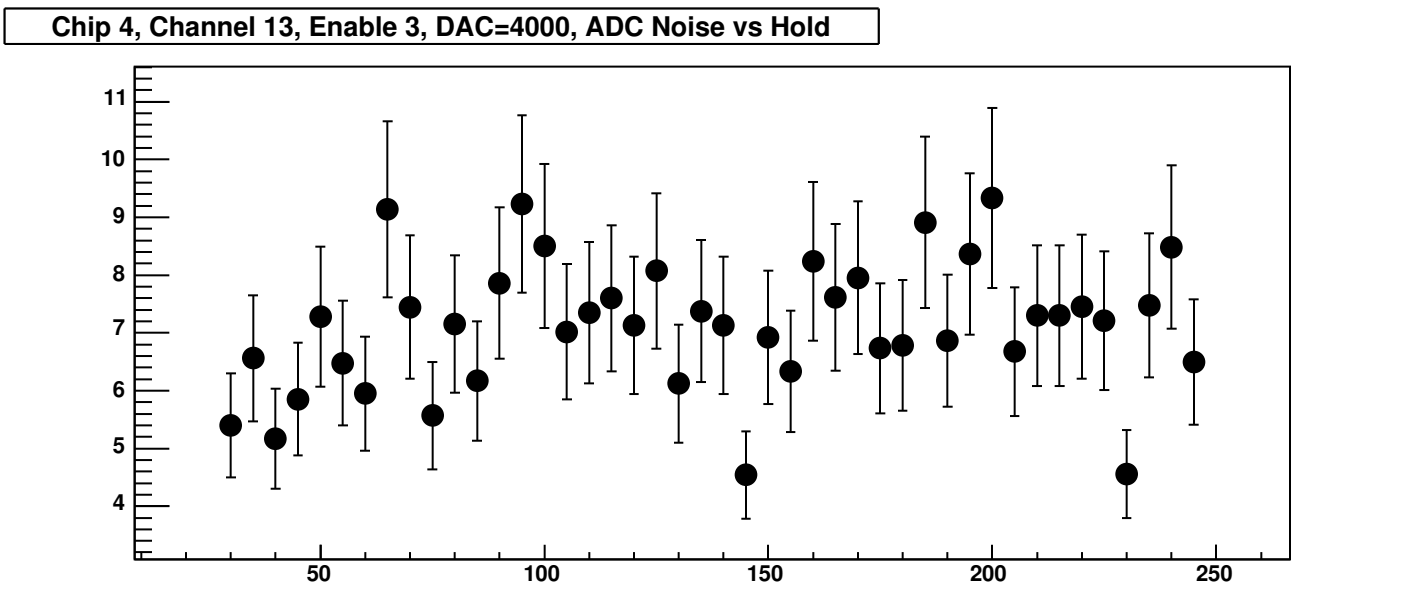
Chip 4, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold

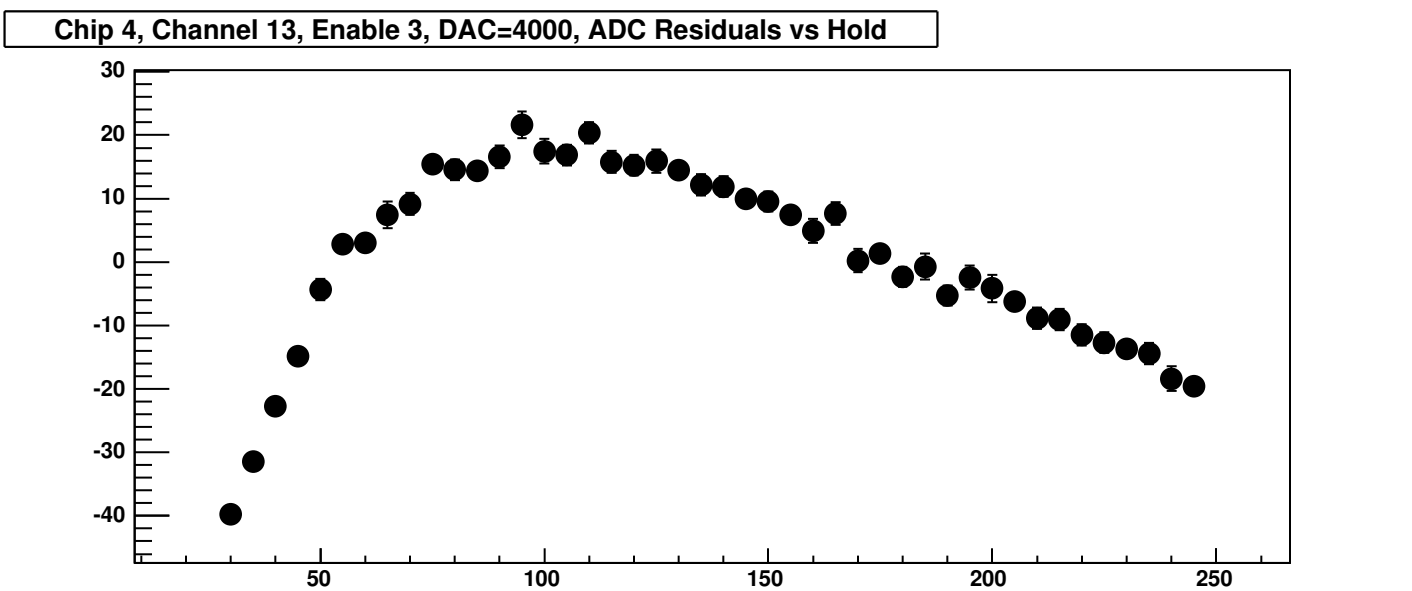


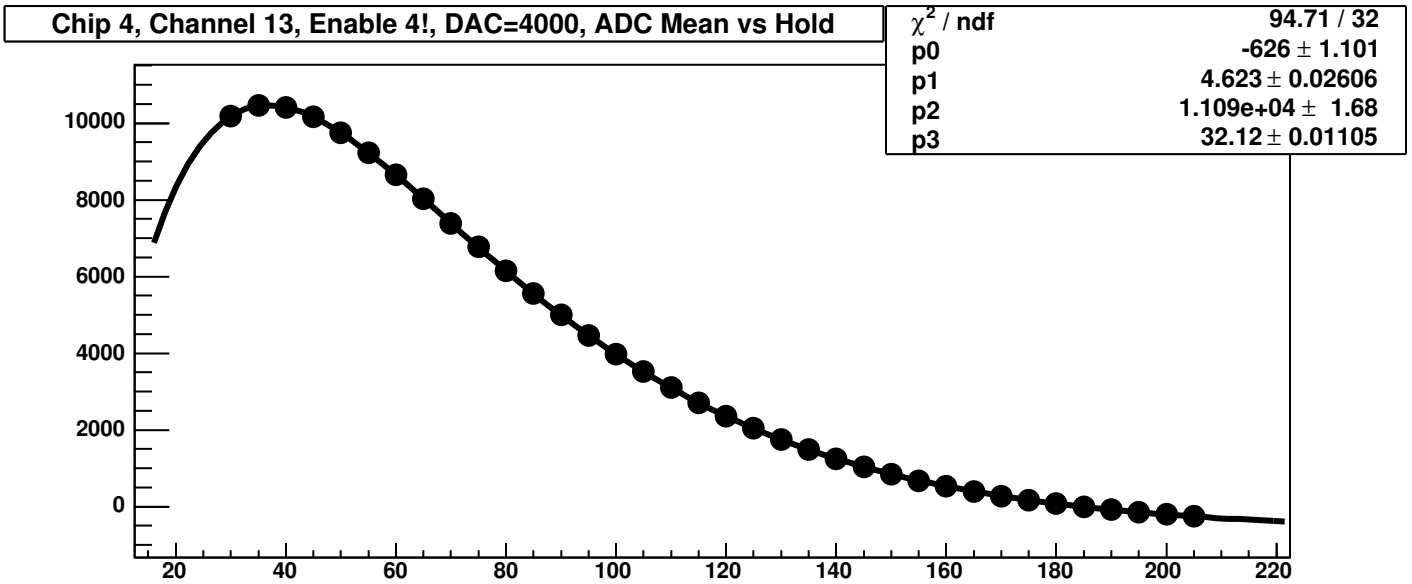
Chip 4, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

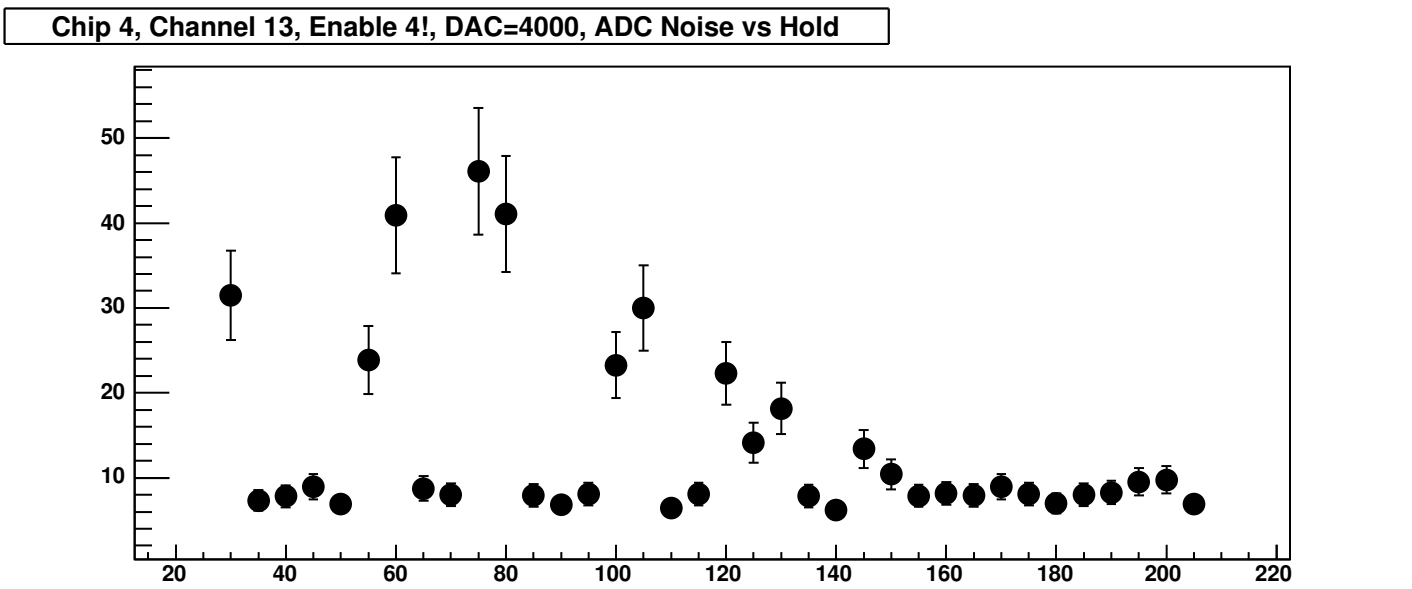


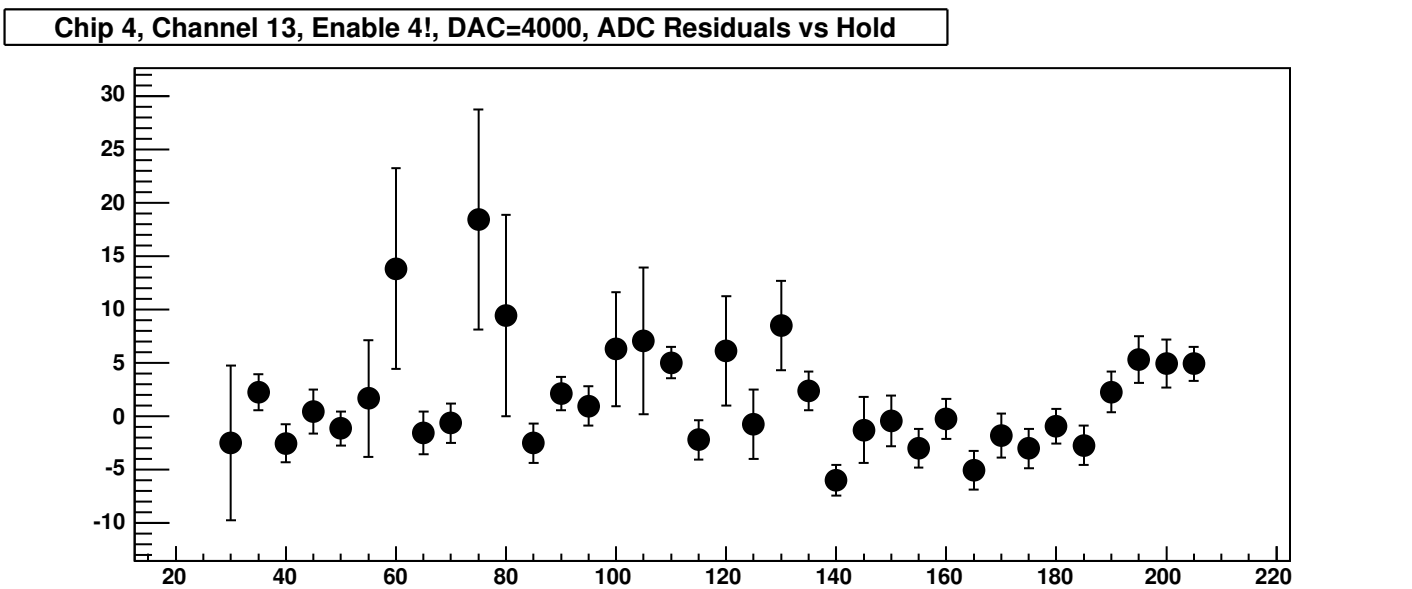

Chip 4, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 4028 / 38
p0 307.4 ± 0.5303
p1 269.6 ± 0.3991
p2 -75.54 ± 5
p3 35.79 ± 0.3027
p4 0.1621 ± 0.003707
310
300
290
280
270
260
250
240
230
50
100
150
200
250

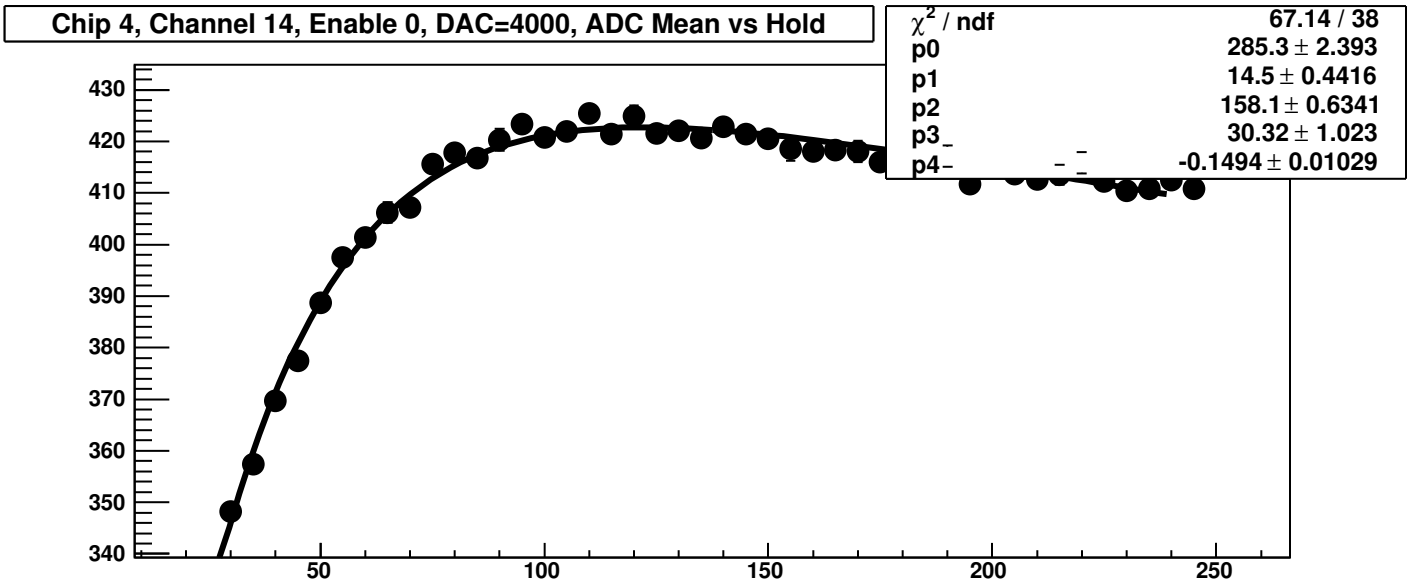

Chip 4, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
50
100
150
200
250

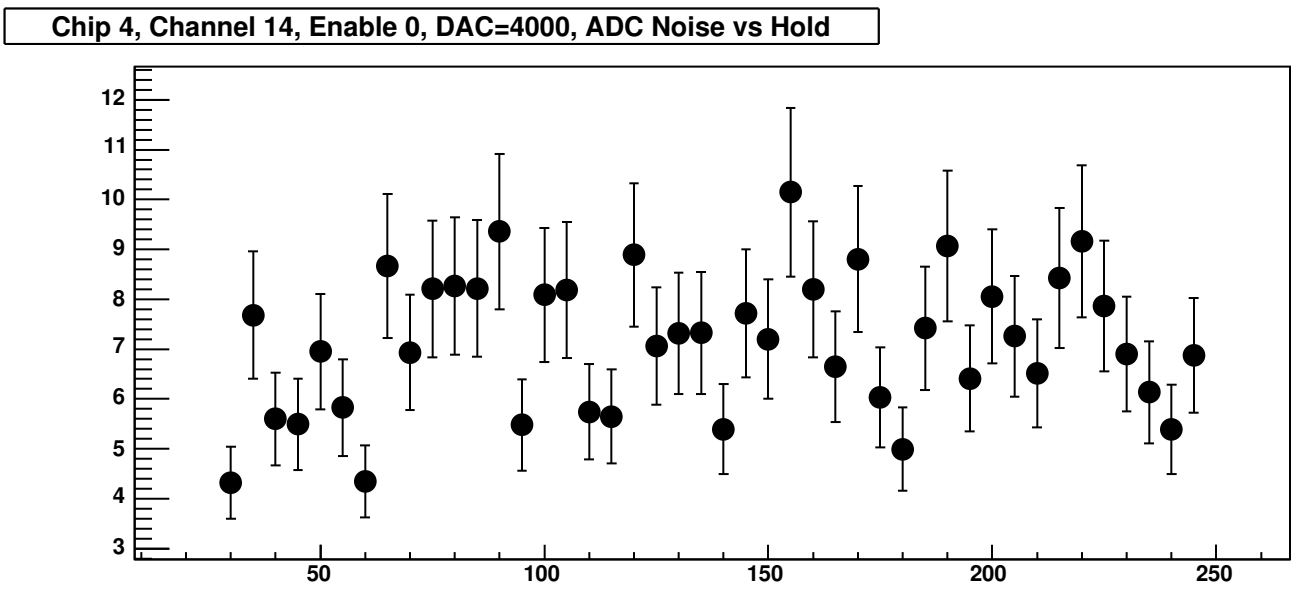

Chip 4, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
-20
-30
-40
50
100
150
200
250

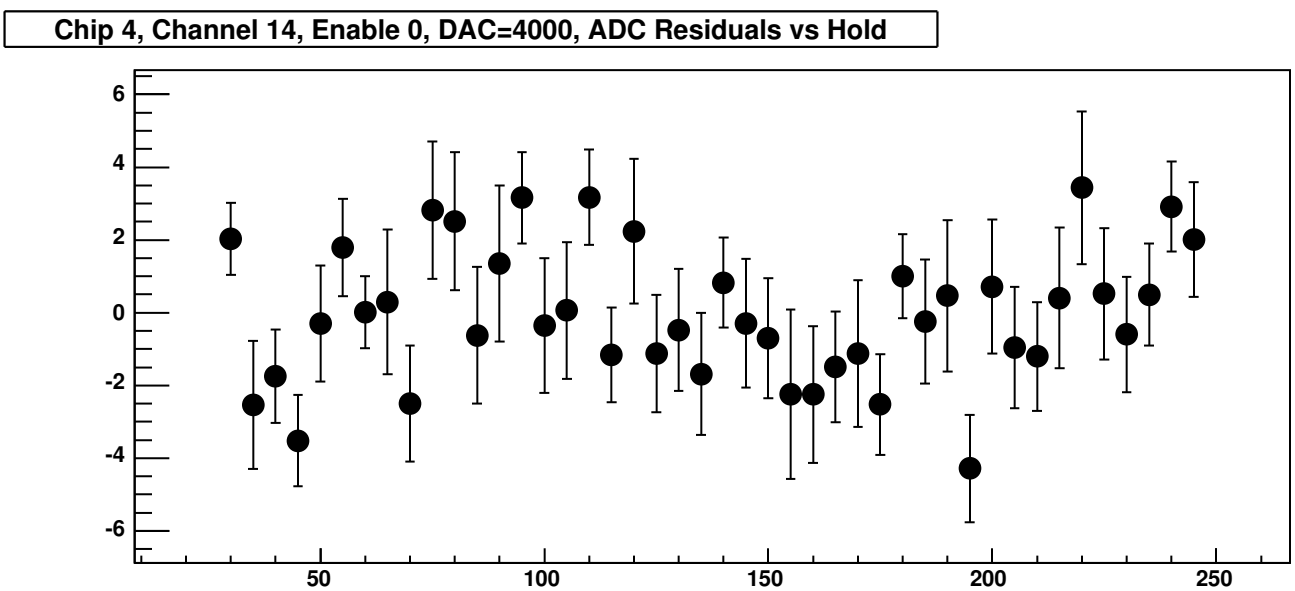

Chip 4, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 94.71 / 32
p0 -626 ± 1.101
p1 4.623 ± 0.02606
p2 1.109e+04 ± 1.68
p3 32.12 ± 0.01105
10000
8000
6000
4000
2000
0
20
40
60
80
100
120
140
160
180
200
220

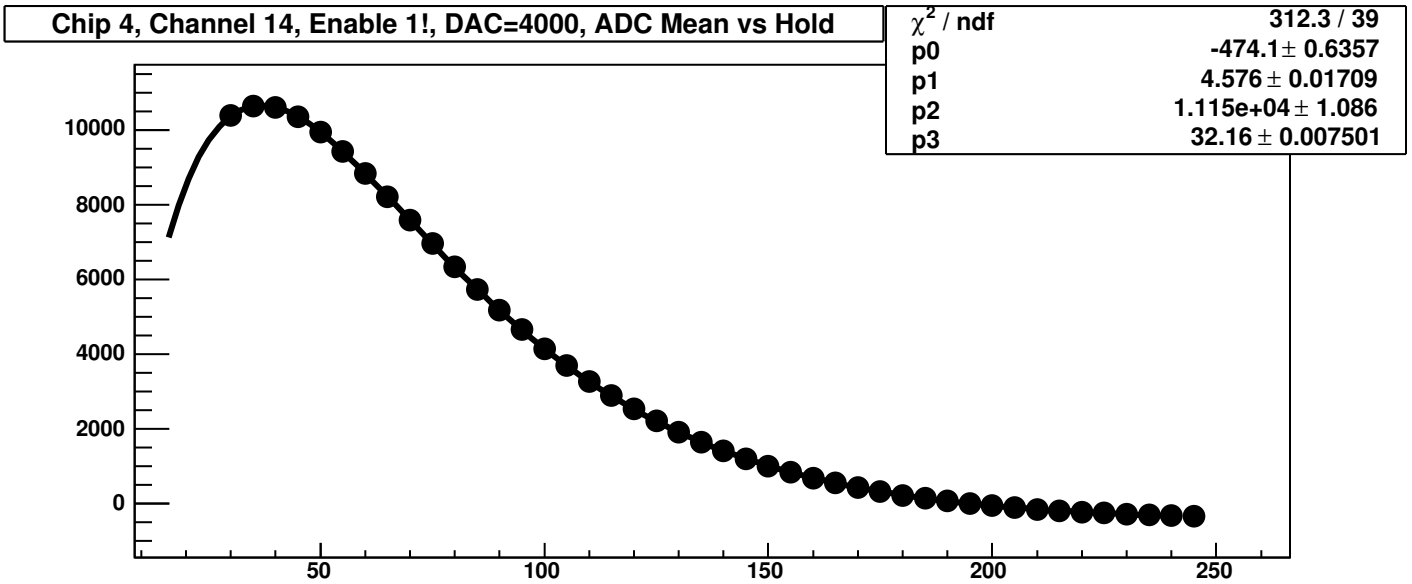

Chip 4, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
20
40
60
80
100
120
140
160
180
200
220

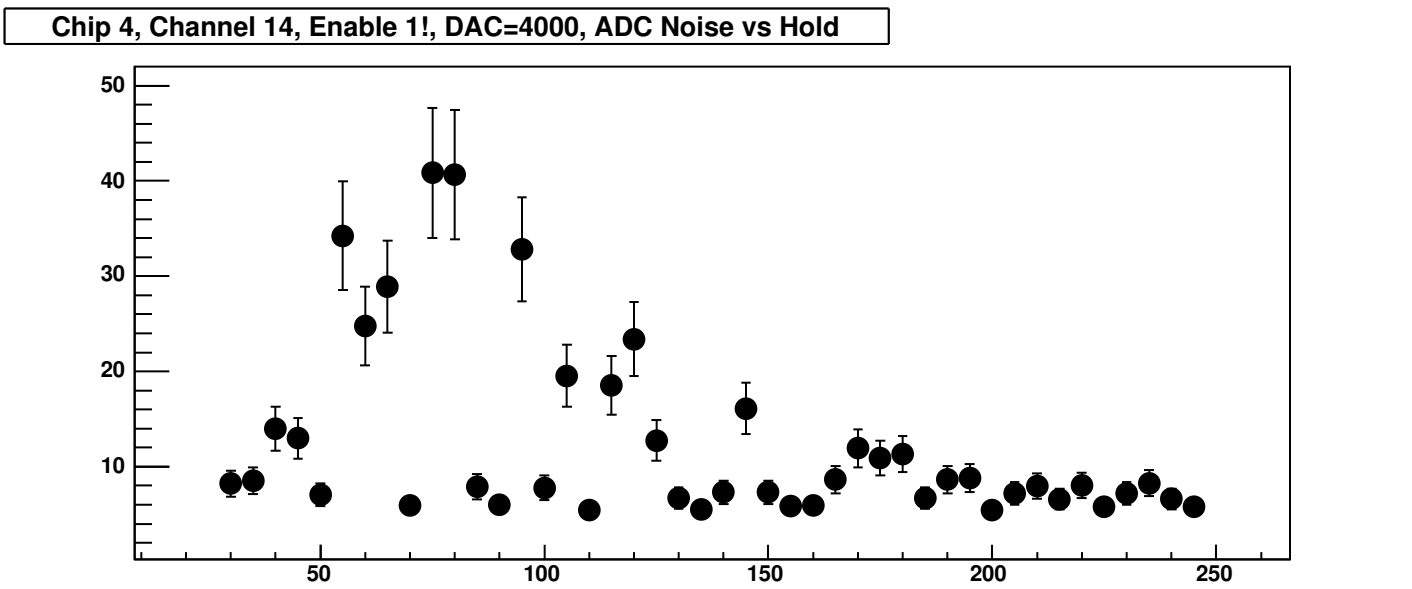

Chip 4, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
20
40
60
80
100
120
140
160
180
200
220

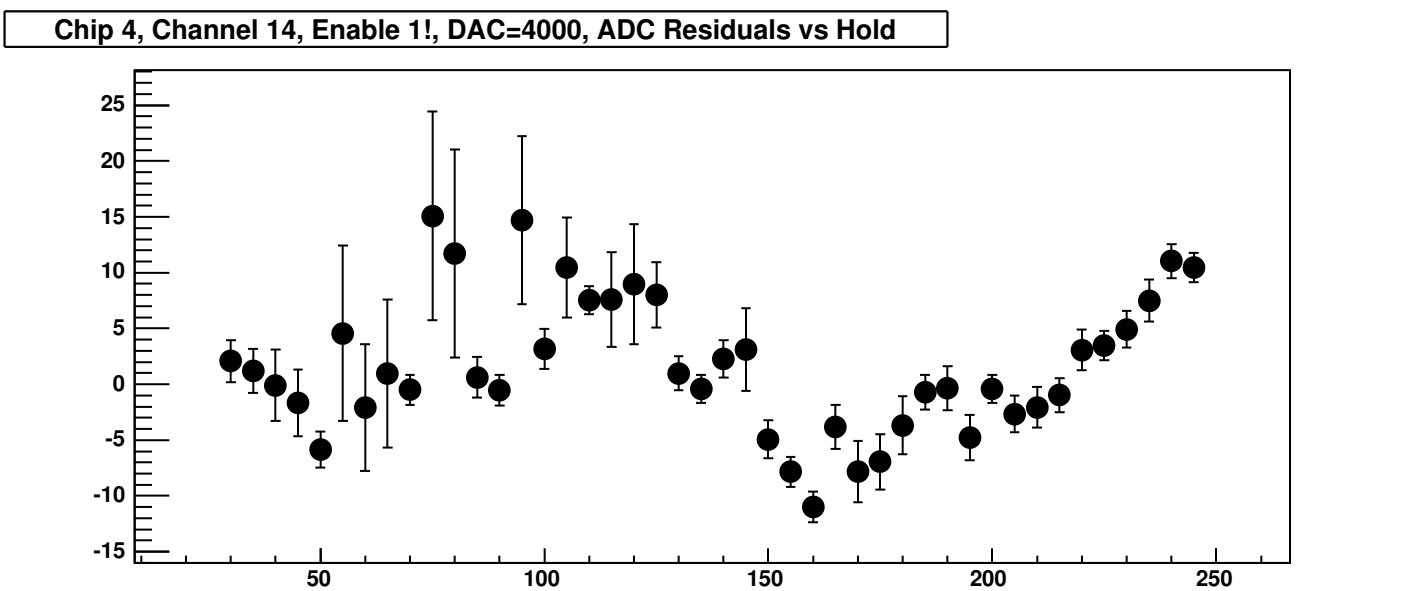

Chip 4, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 67.14 / 38
p0 285.3 ± 2.393
p1 14.5 ± 0.4416
p2 158.1 ± 0.6341
p3 30.32 ± 1.023
p4 -0.1494 ± 0.01029
430
420
410
400
390
380
370
360
350
340
50
100
150
200
250


Chip 4, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250

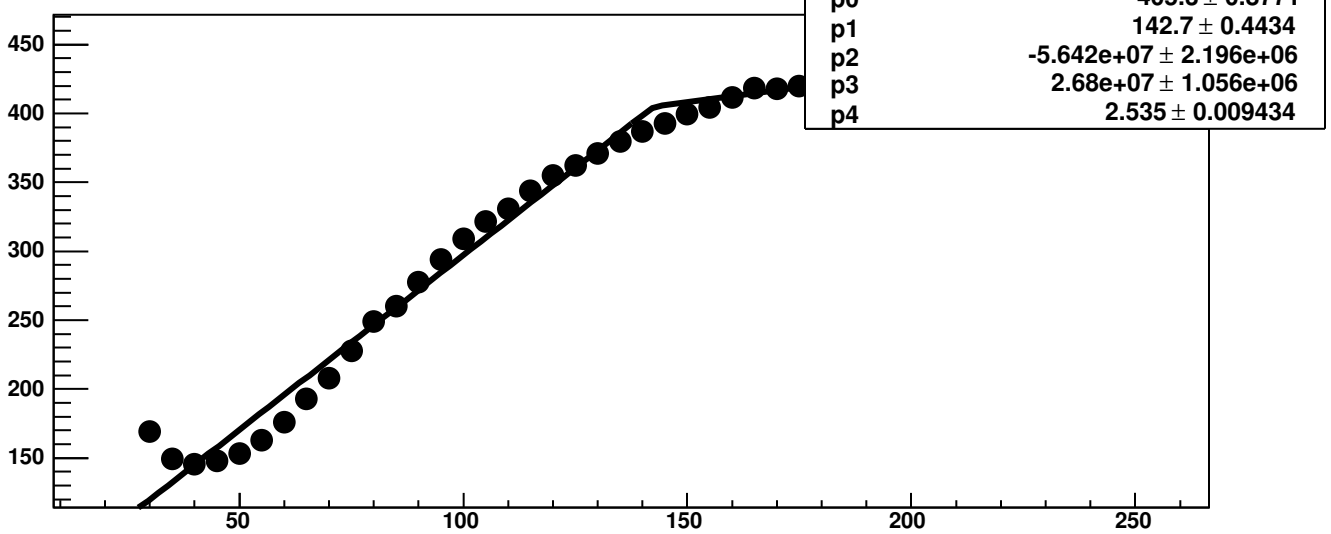

Chip 4, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
50
100
150
200
250


Chip 4, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 312.3 / 39
p0 -474.1 ± 0.6357
p1 4.576 ± 0.01709
p2 1.115e+04 ± 1.086
p3 32.16 ± 0.007501
10000
8000
6000
4000
2000
0
50
100
150
200
250

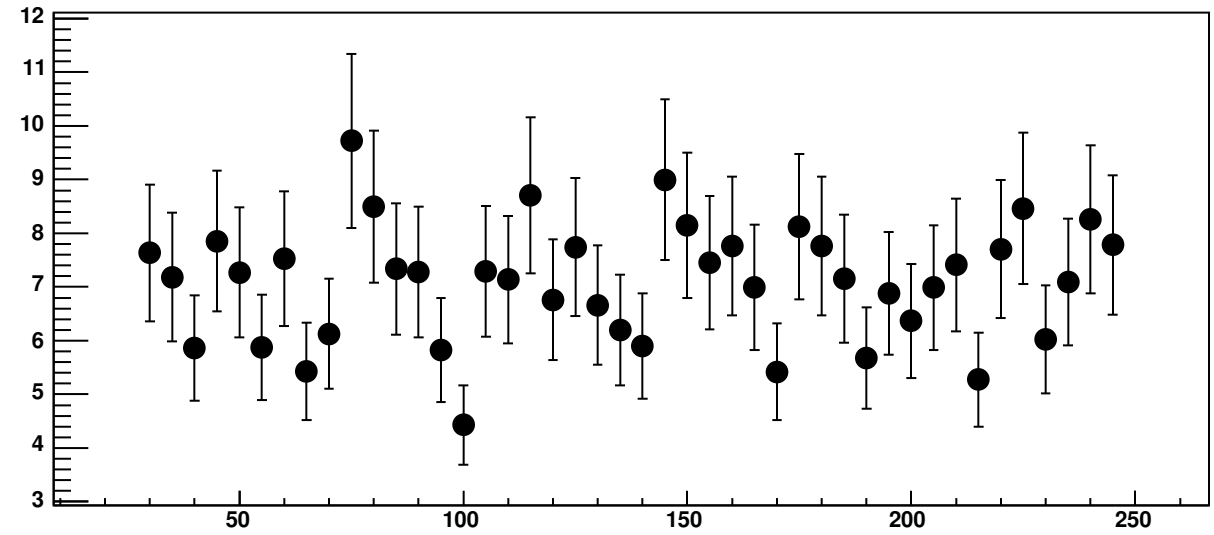

Chip 4, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
50
100
150
200
250


Chip 4, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
-15
50
100
150
200
250

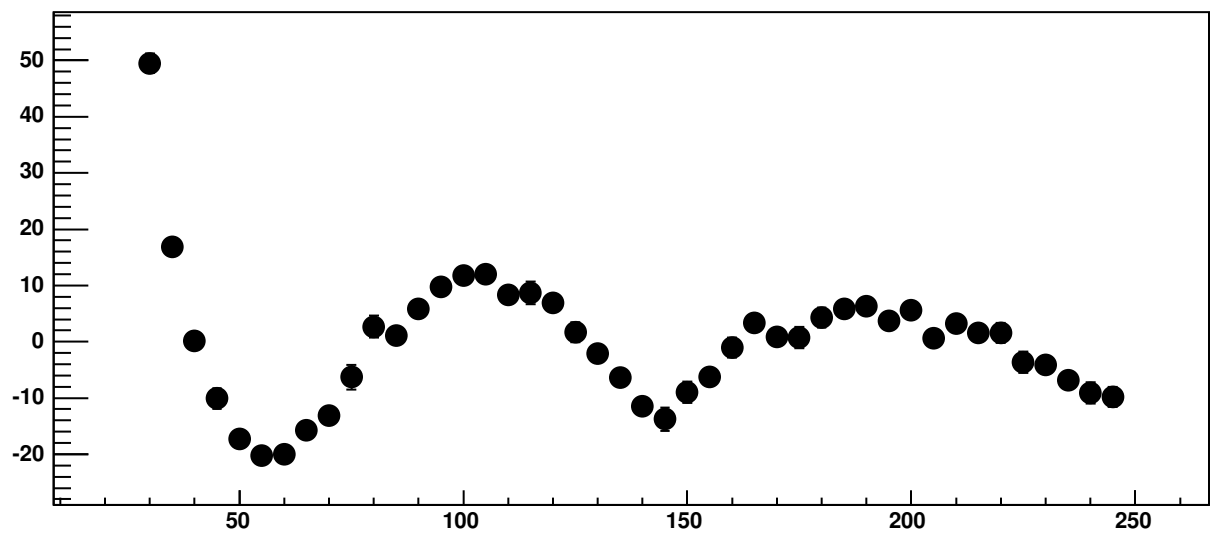
Chip 4, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold



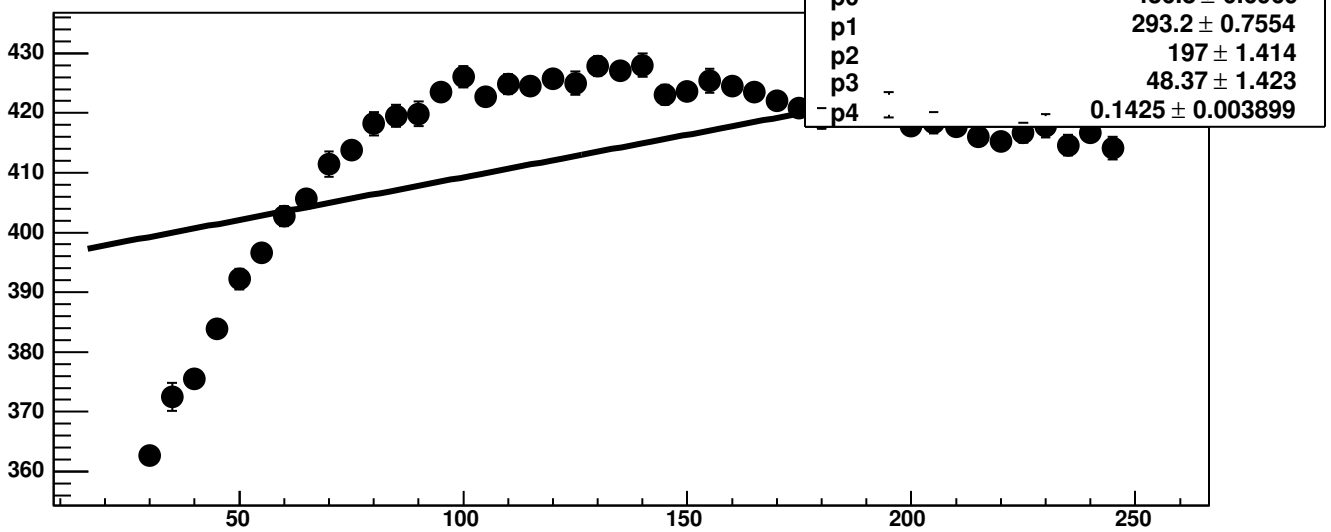
Chip 4, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



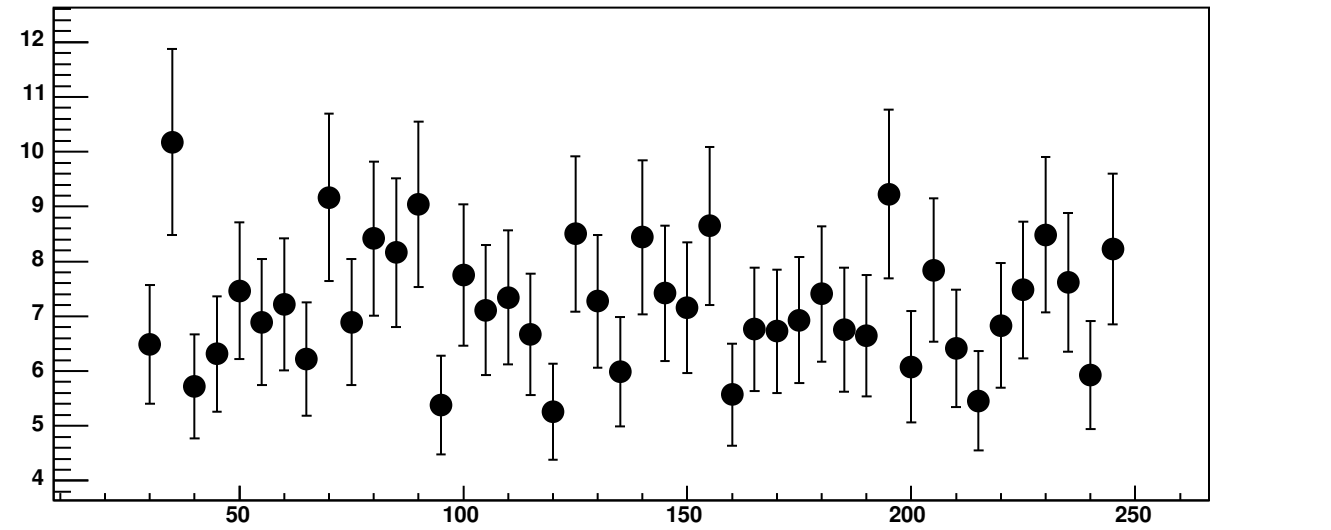
Chip 4, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold



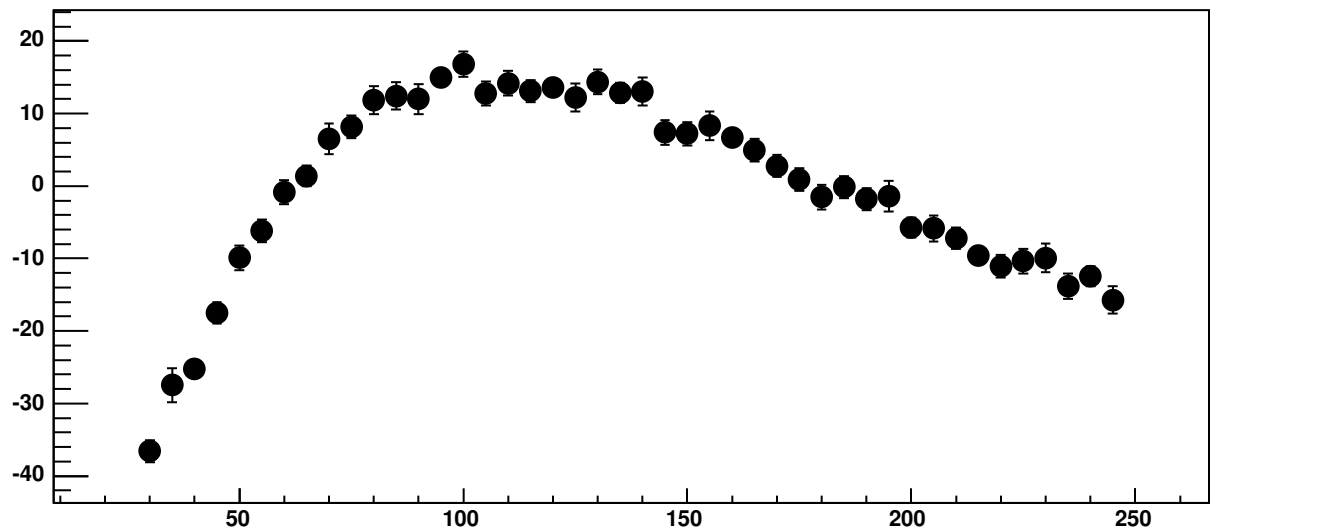
Chip 4, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold



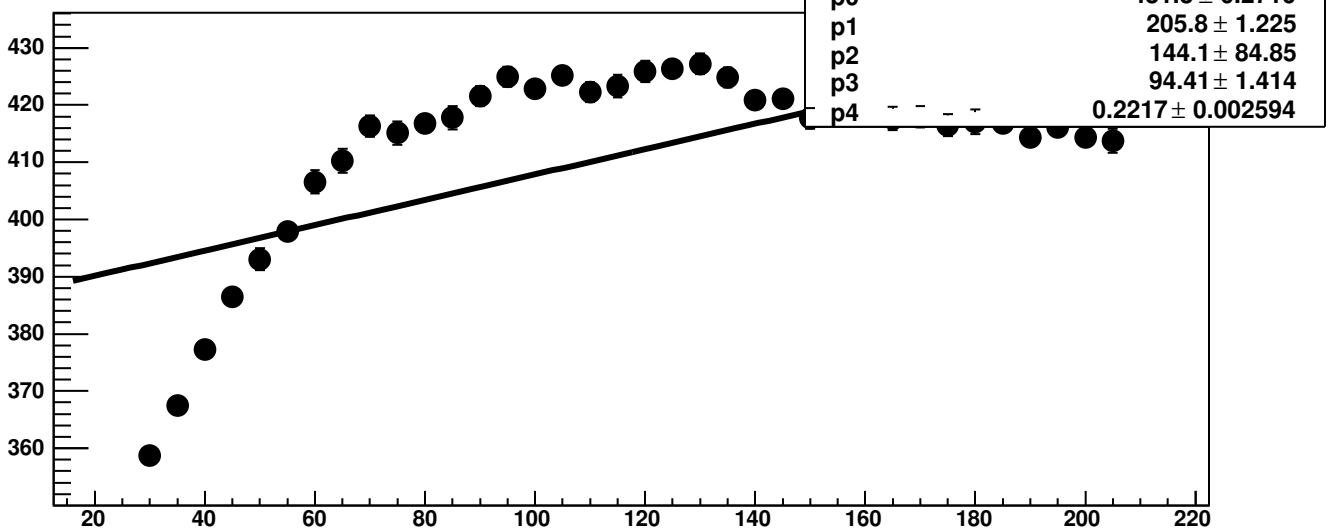
Chip 4, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



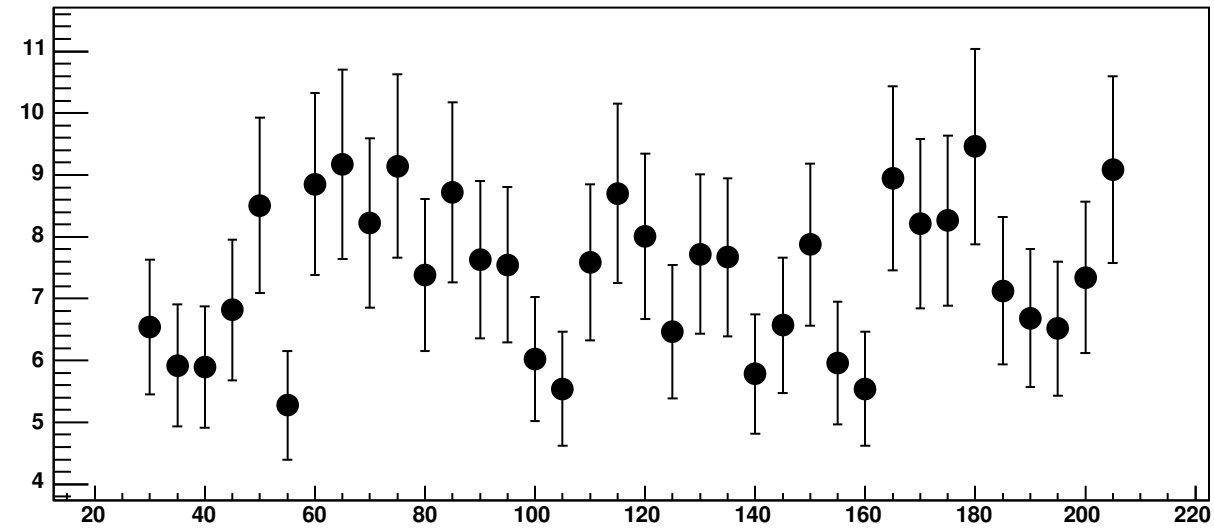
Chip 4, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold



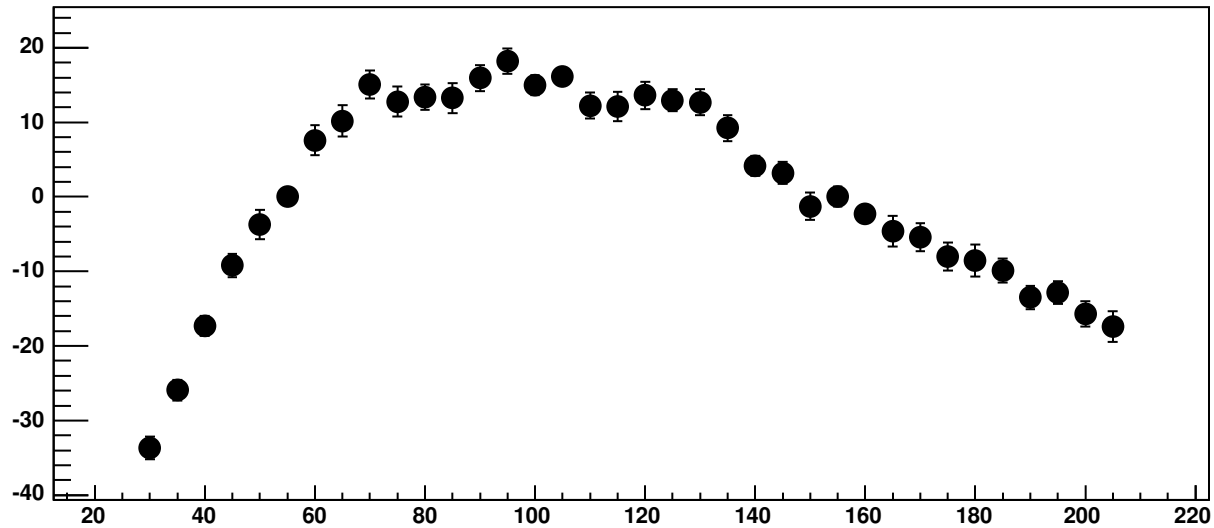
Chip 4, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold



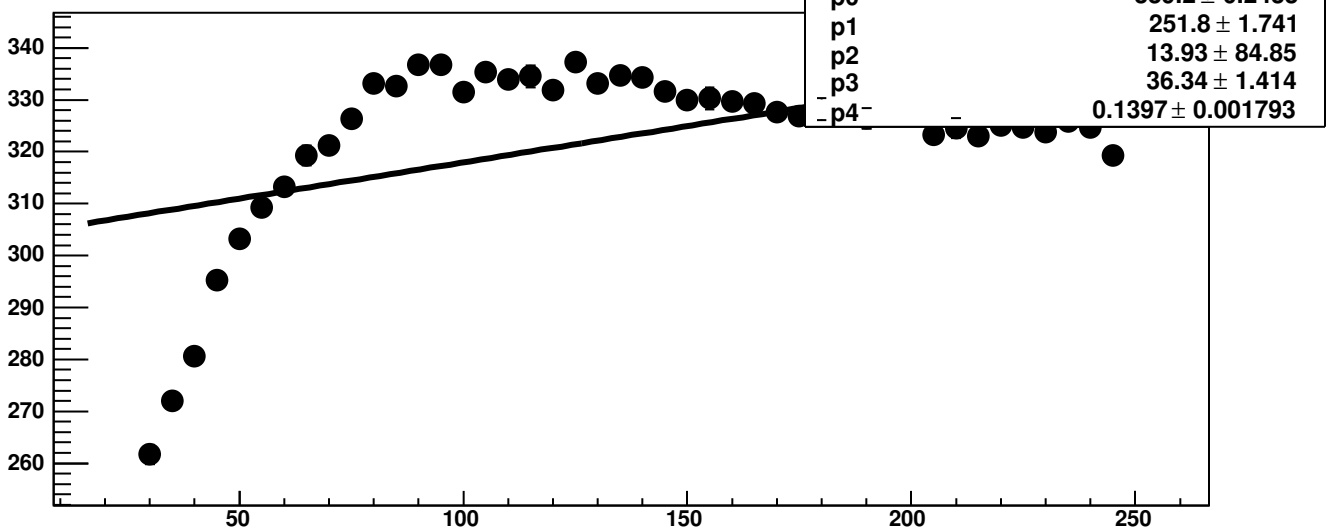
Chip 4, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold



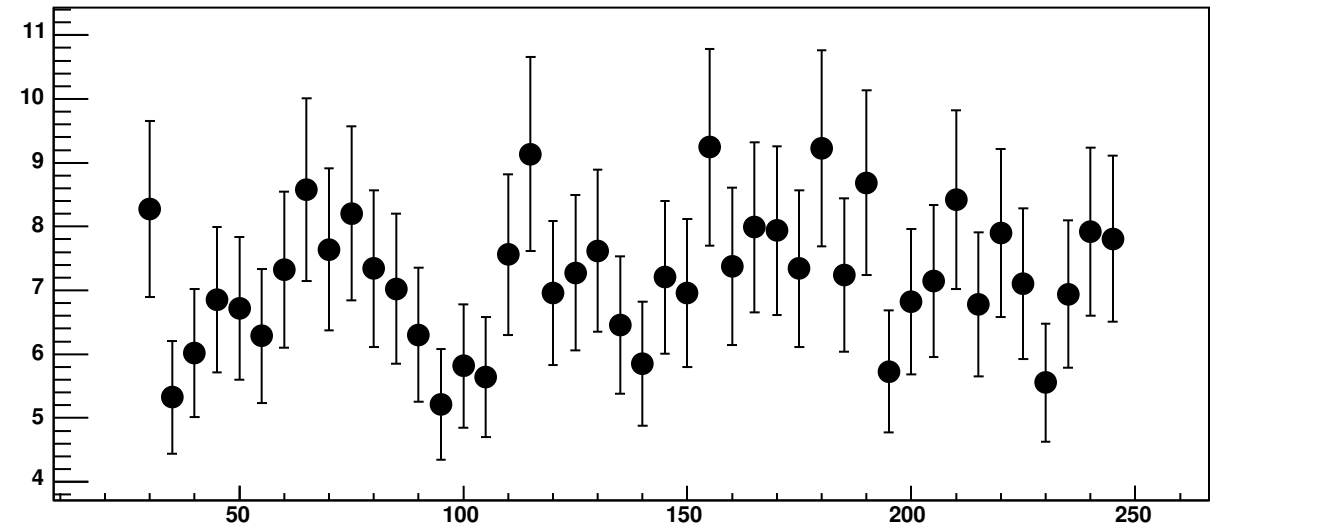
Chip 4, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold



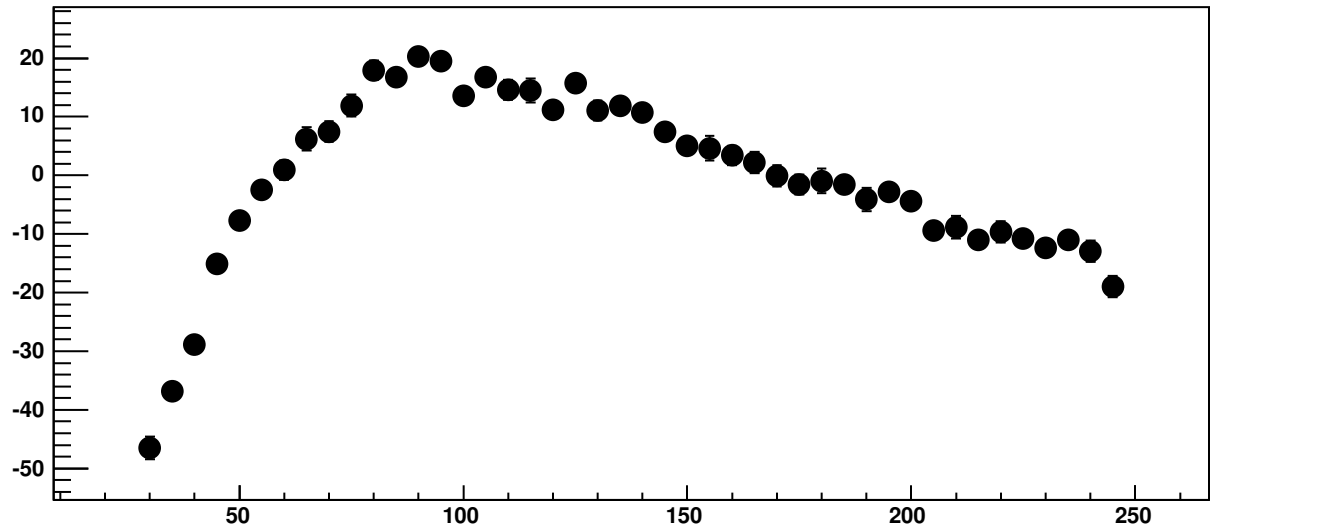
Chip 4, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold

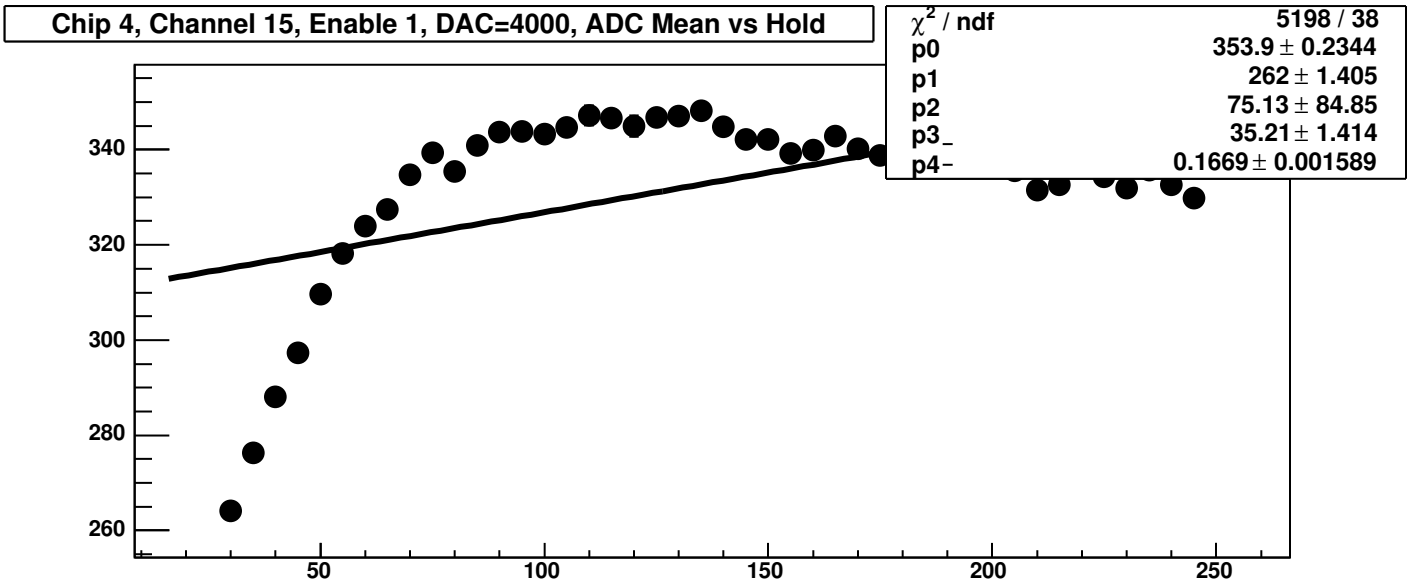


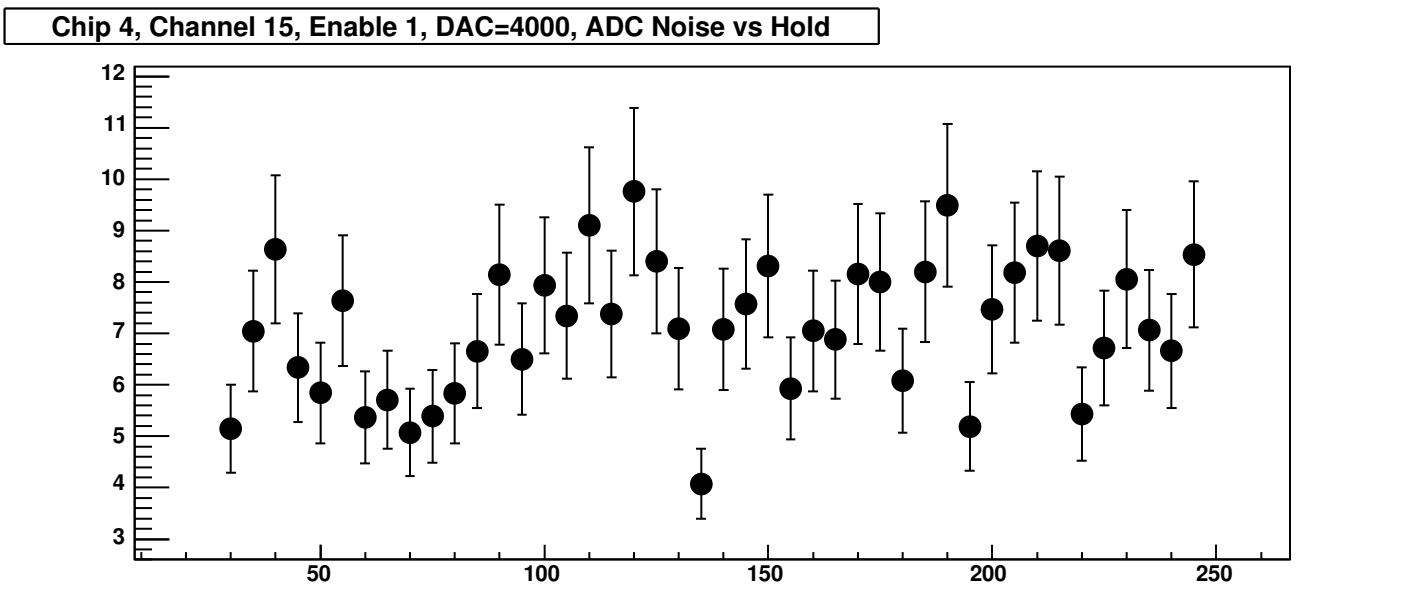
Chip 4, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold

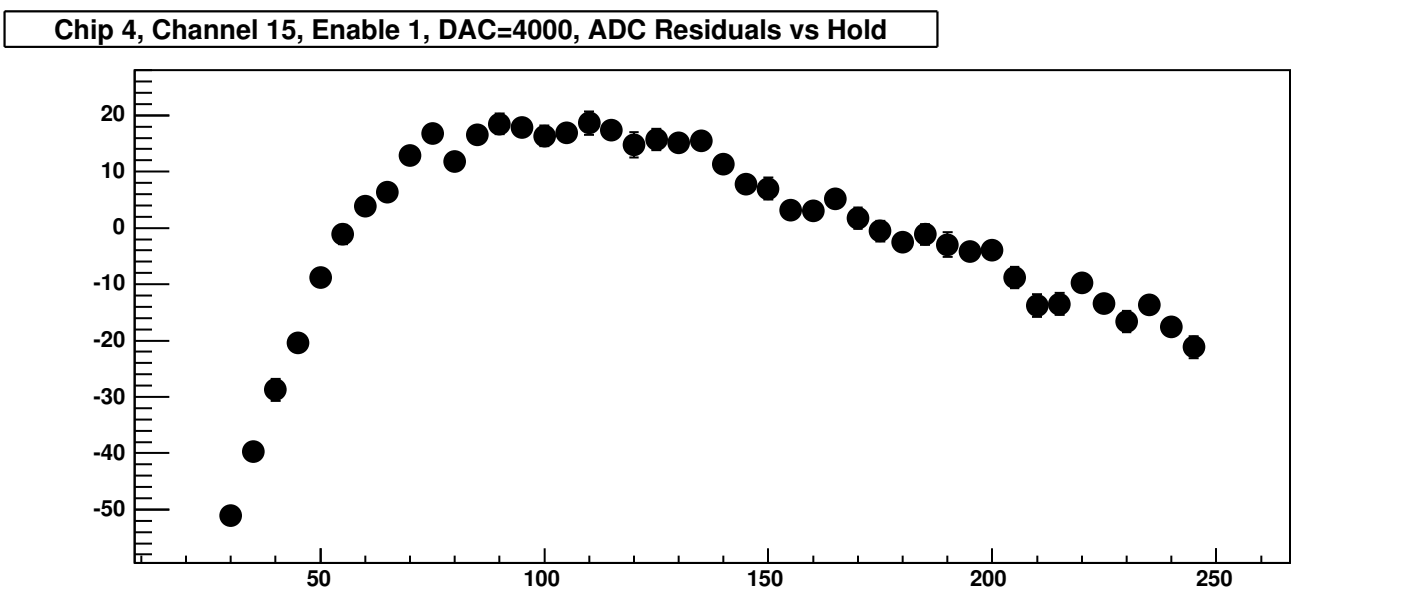


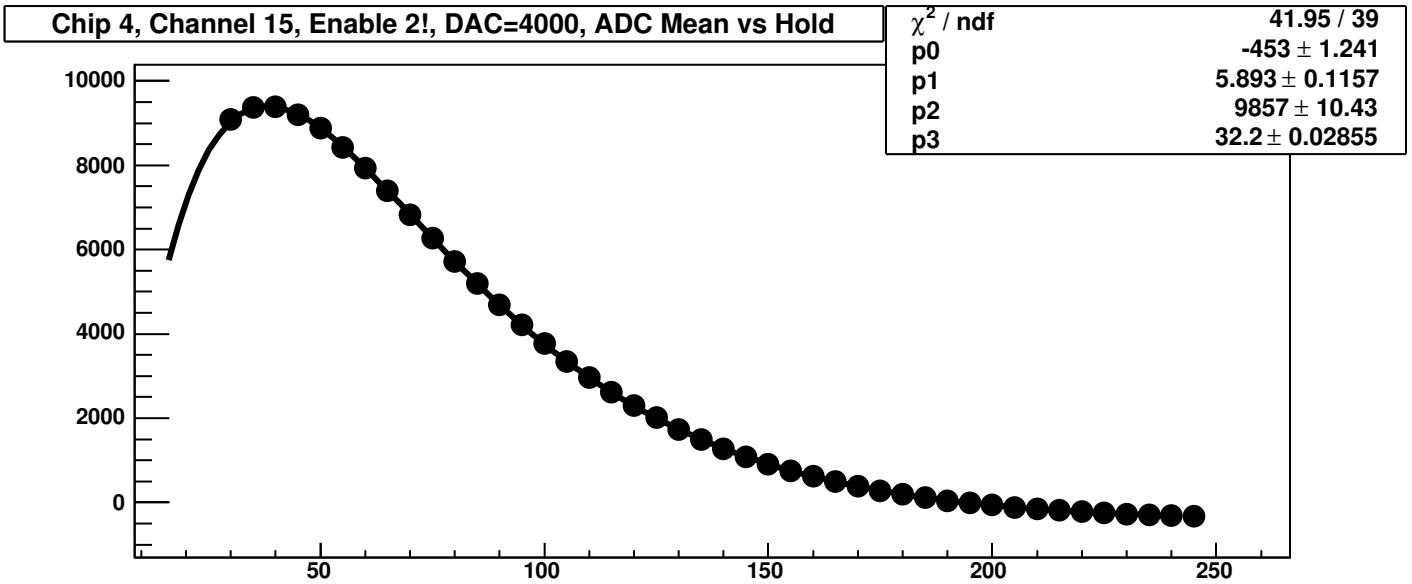
Chip 4, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold

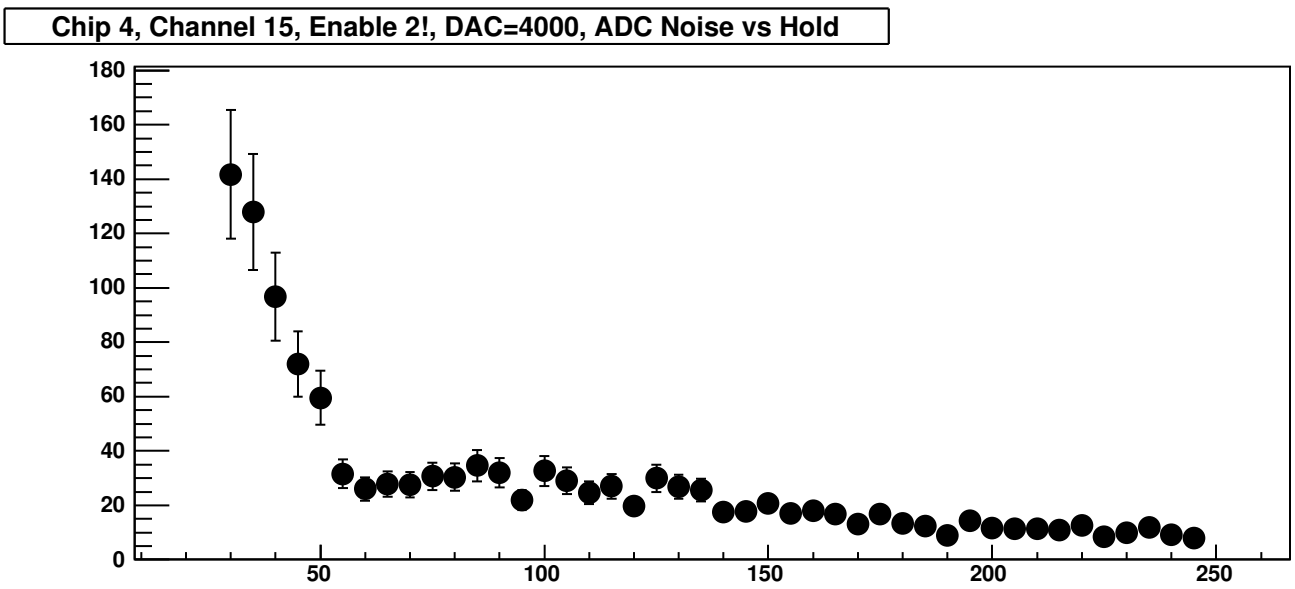


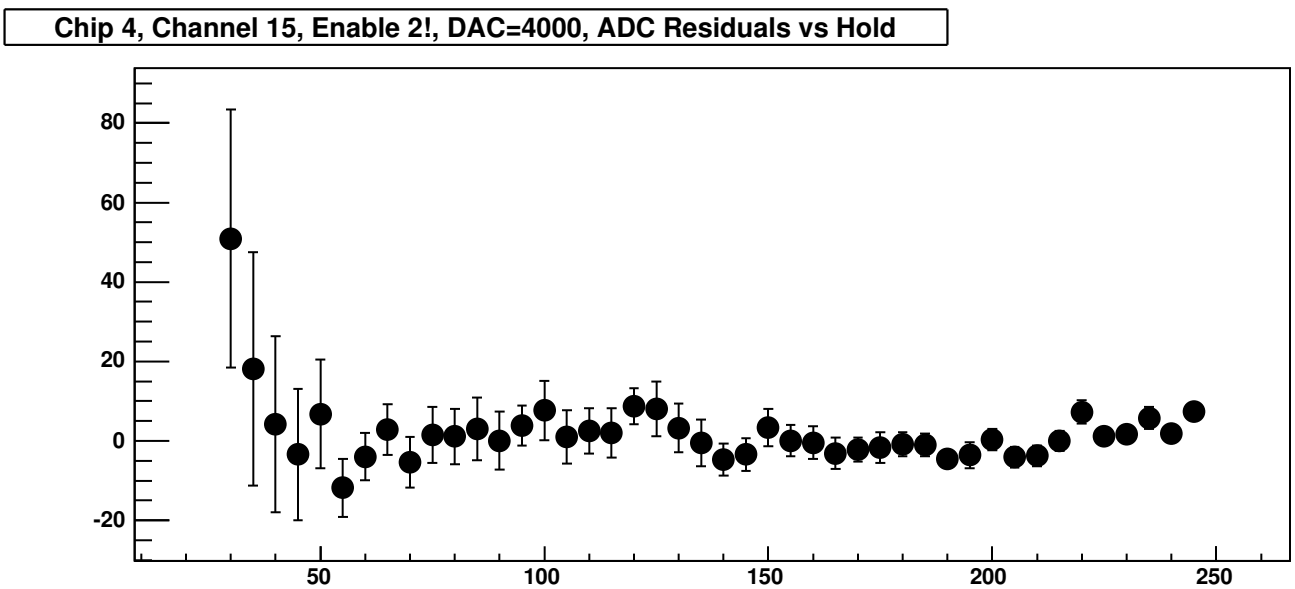

Chip 4, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 5198 / 38
p0 353.9 ± 0.2344
p1 262 ± 1.405
p2 75.13 ± 84.85
p3 35.21 ± 1.414
p4 0.1669 ± 0.001589
340
320
300
280
260
50
100
150
200
250


Chip 4, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
50
100
150
200
250

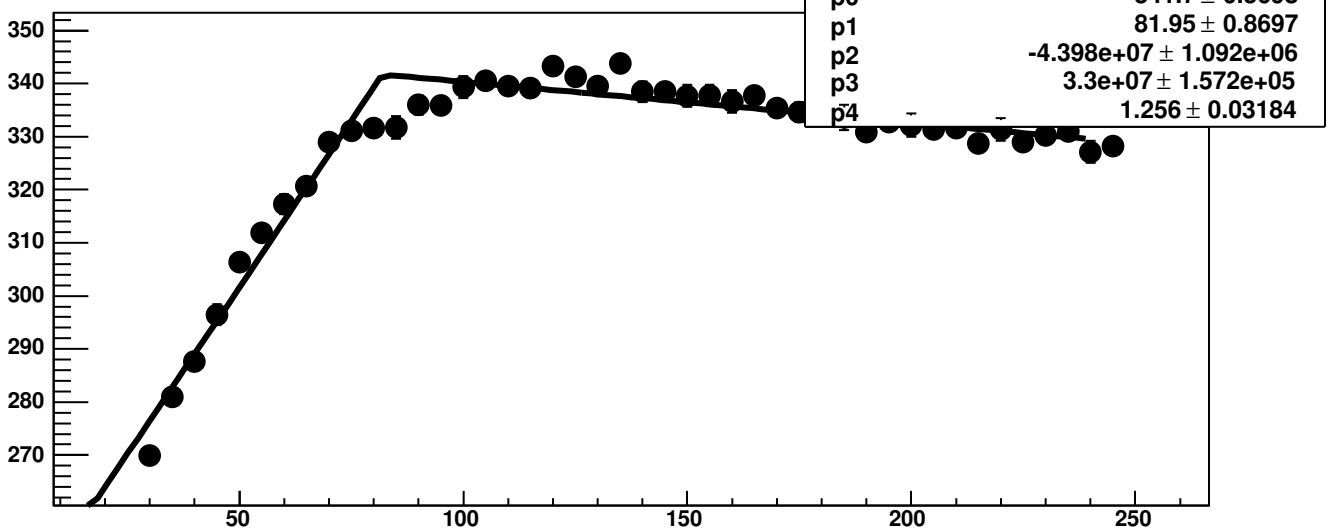

Chip 4, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
-50
50
100
150
200
250


Chip 4, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 41.95 / 39
p0 -453 ± 1.241
p1 5.893 ± 0.1157
p2 9857 ± 10.43
p3 32.2 ± 0.02855
10000
8000
6000
4000
2000
0
50
100
150
200
250

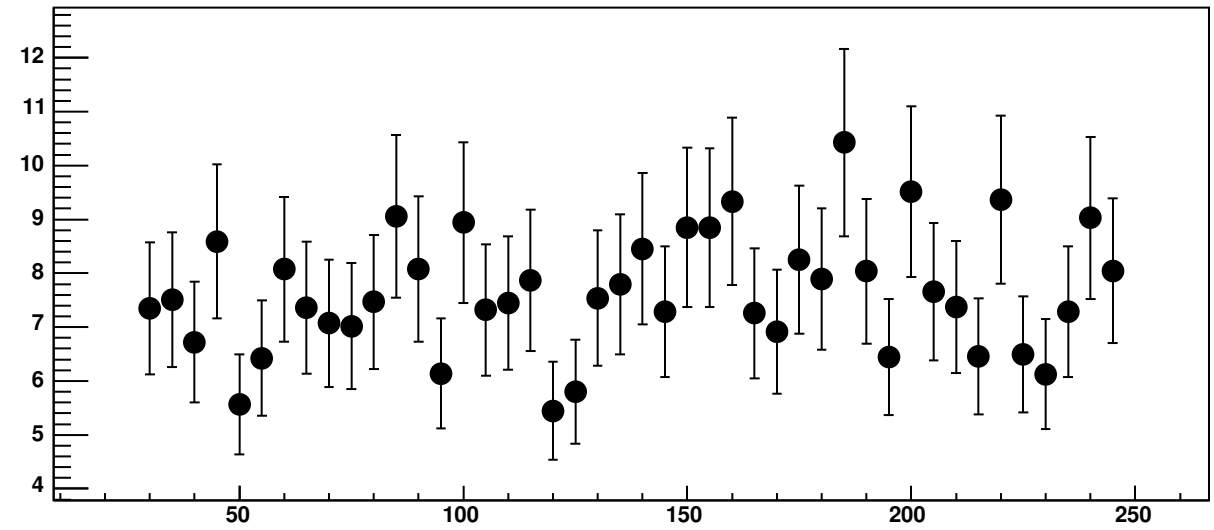

Chip 4, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold
180
160
140
120
100
80
60
40
20
0
50
100
150
200
250


Chip 4, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold
80
60
40
20
0
-20
50
100
150
200
250

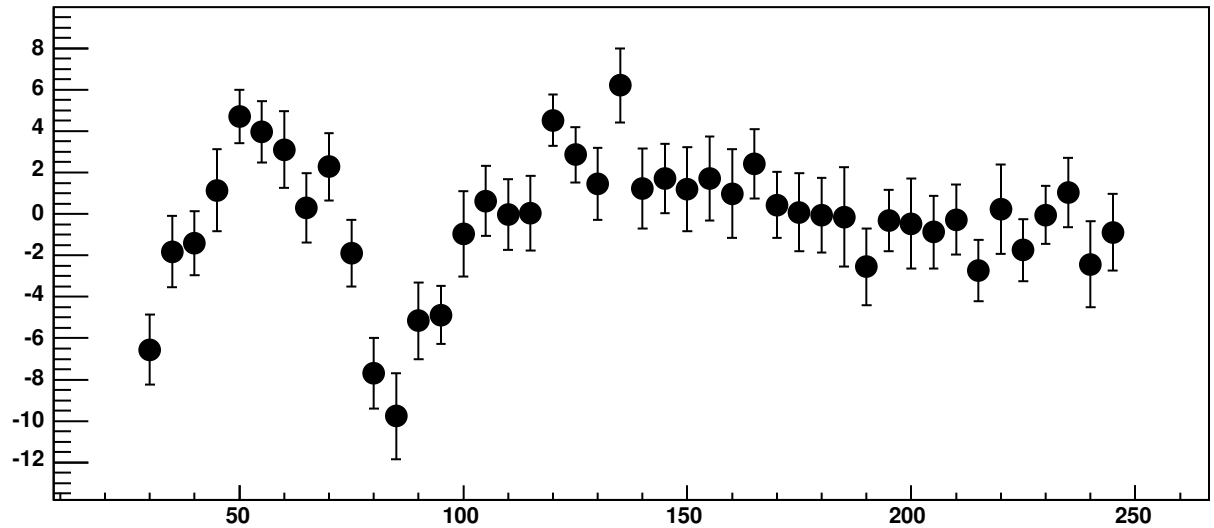
Chip 4, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold



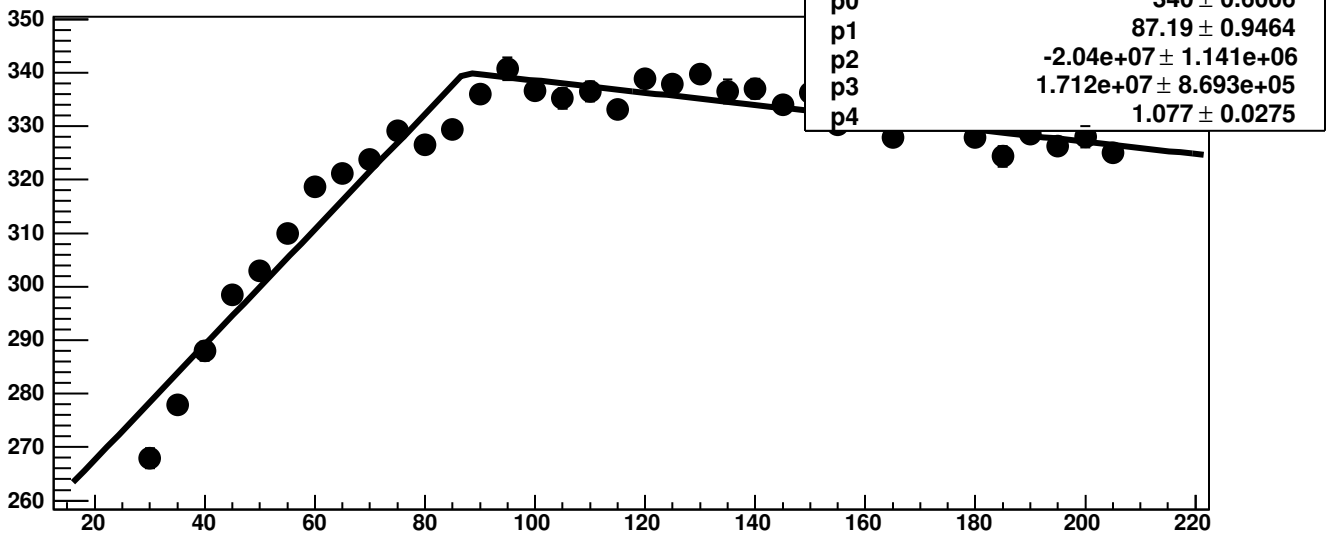
Chip 4, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



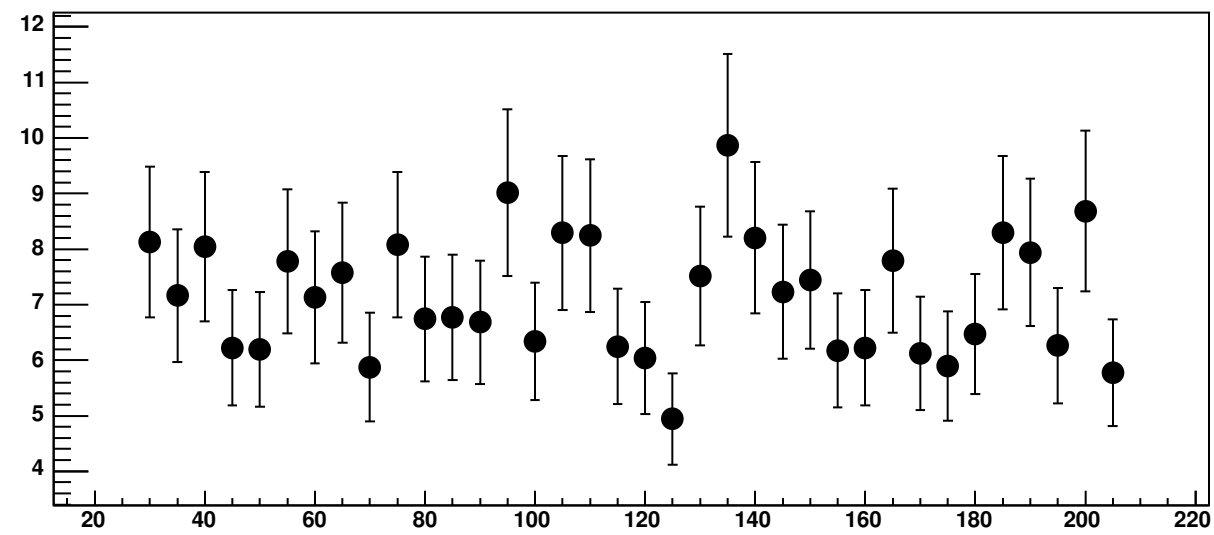
Chip 4, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold



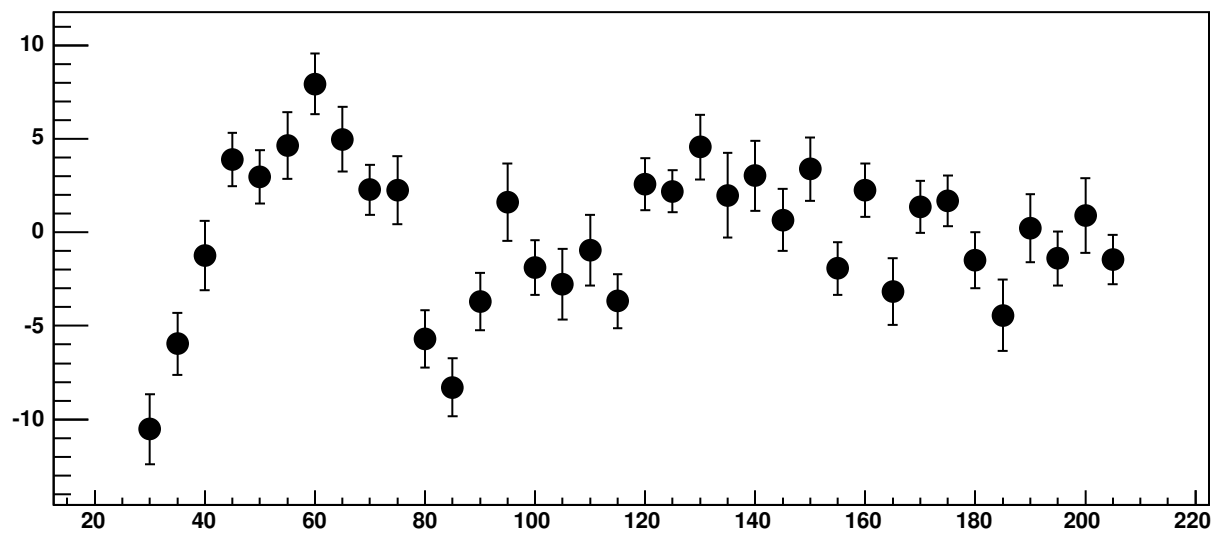
Chip 4, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold



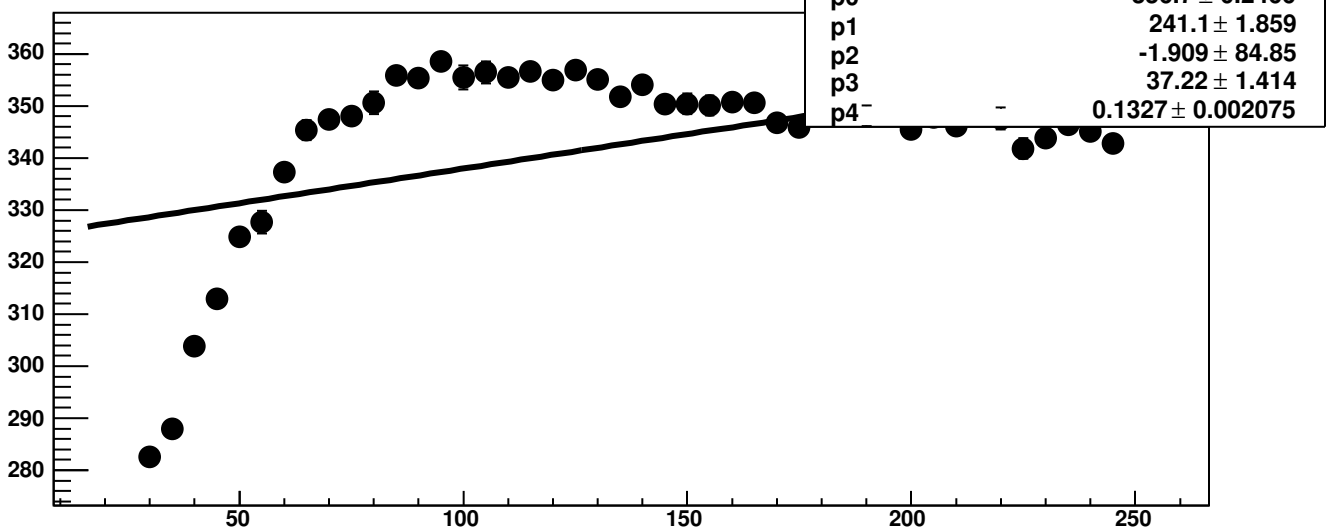
Chip 4, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



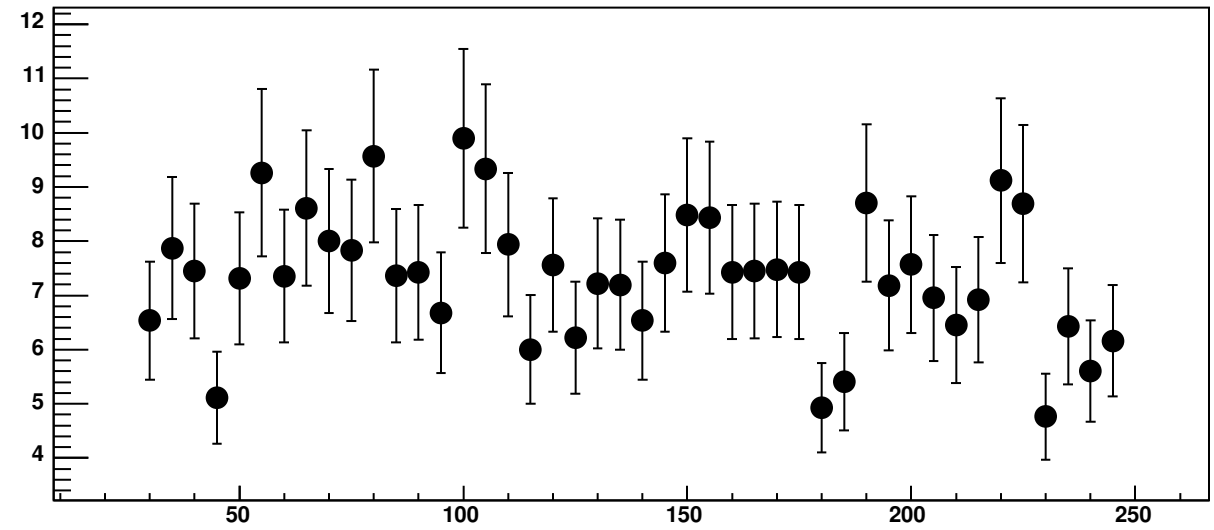
Chip 4, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold



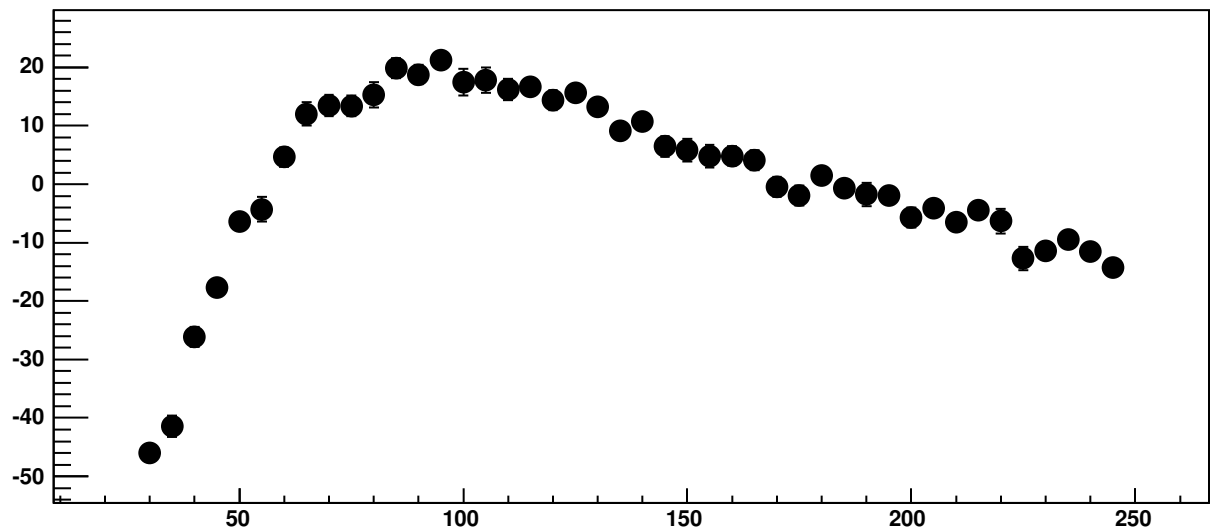
Chip 4, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

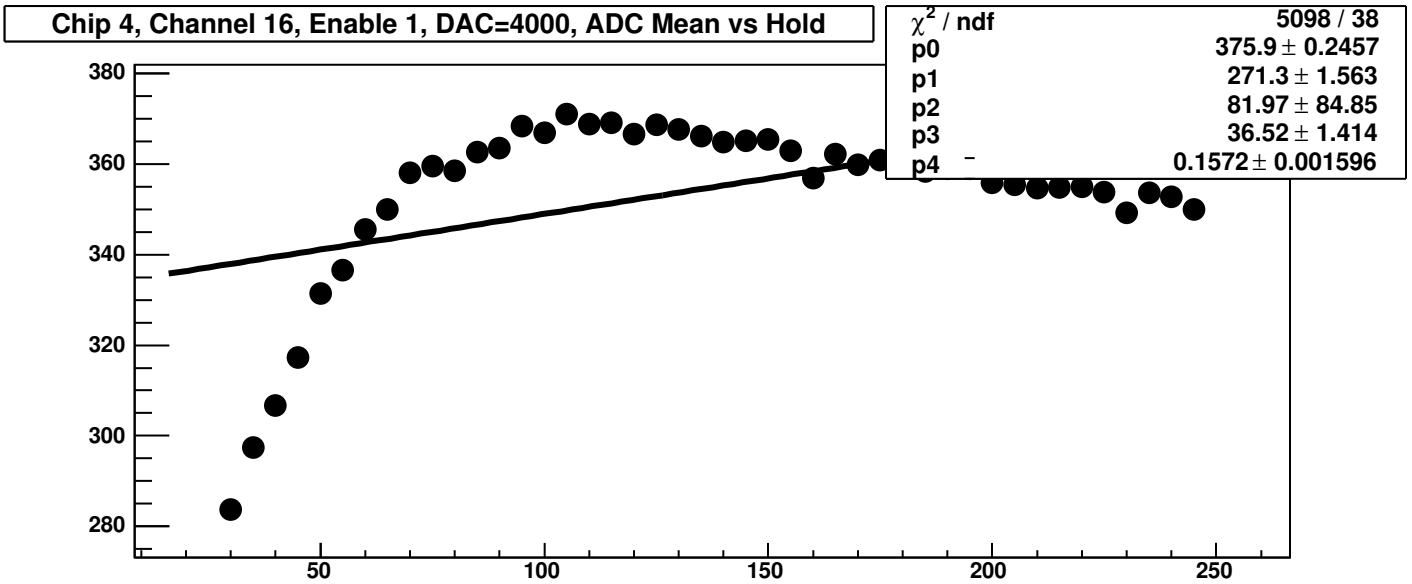


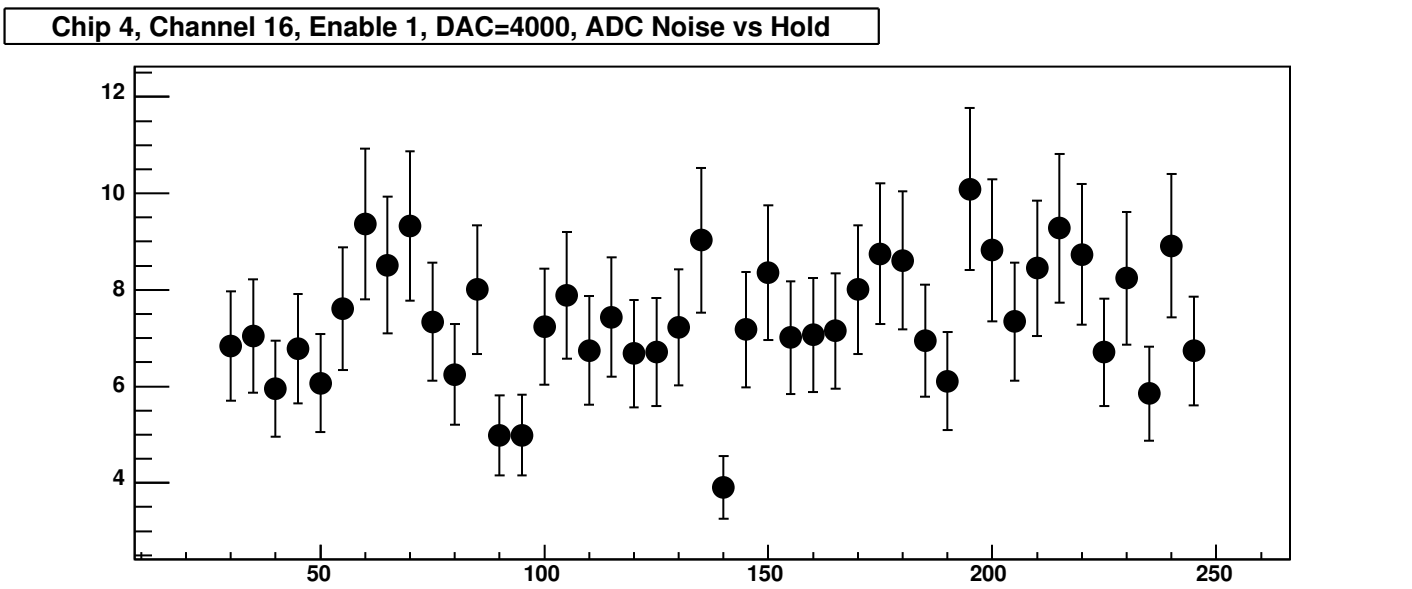
Chip 4, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold

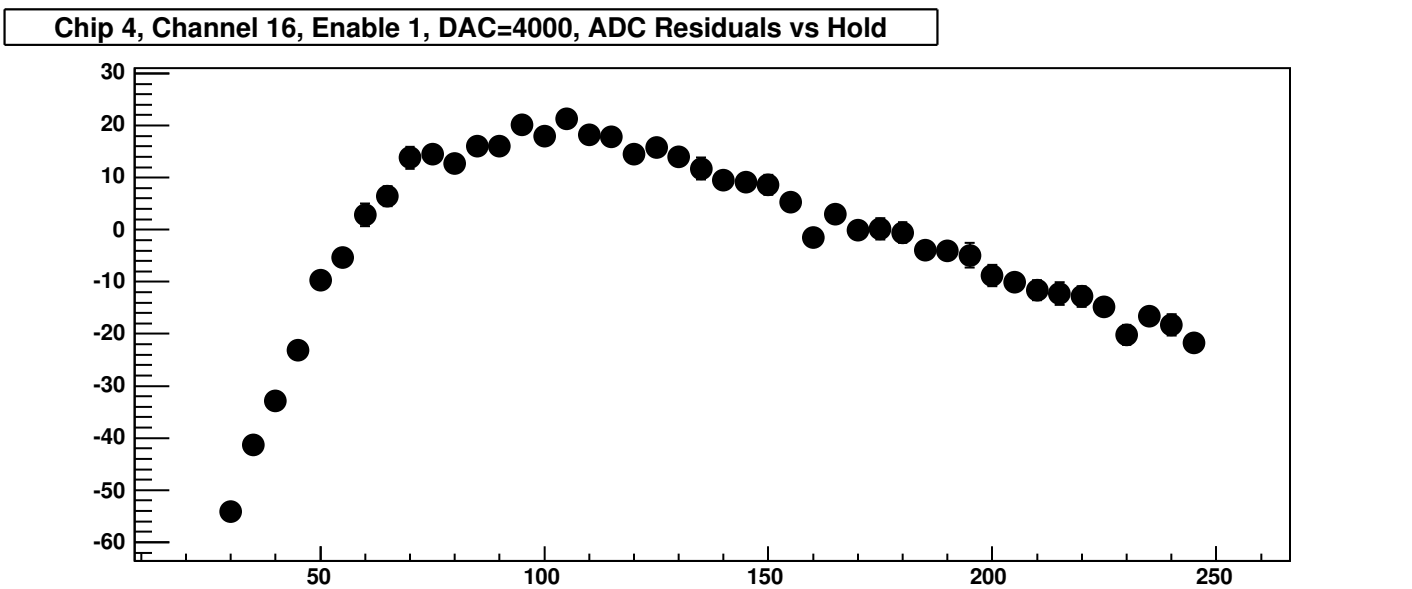


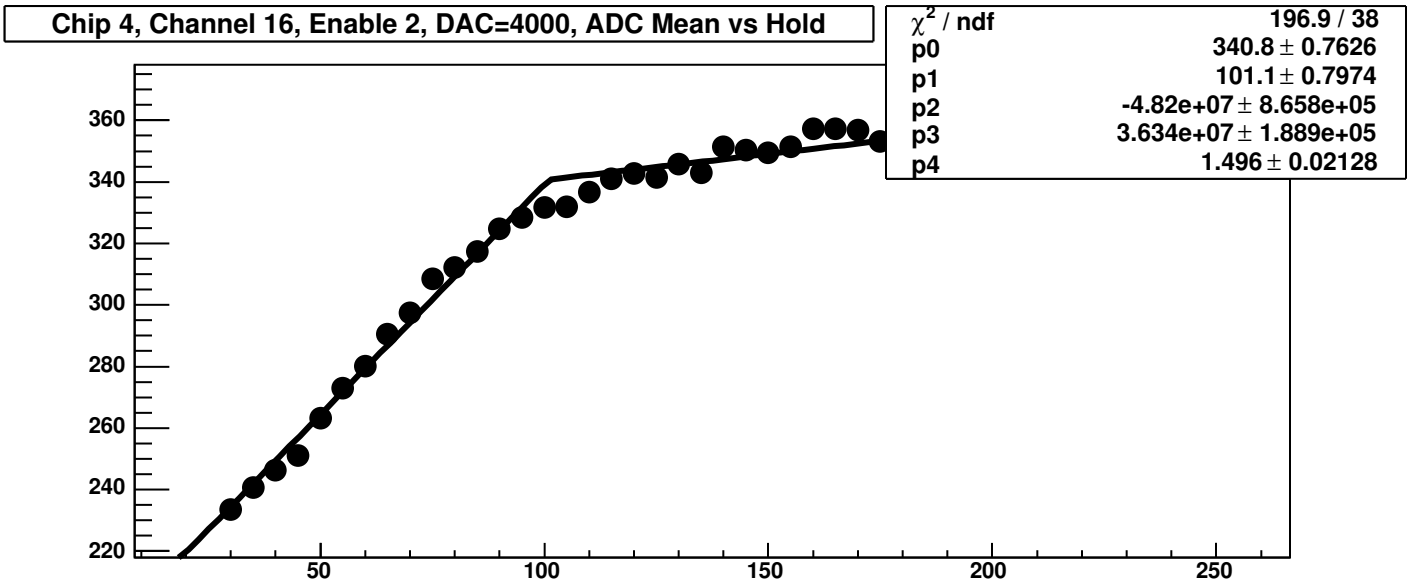
Chip 4, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

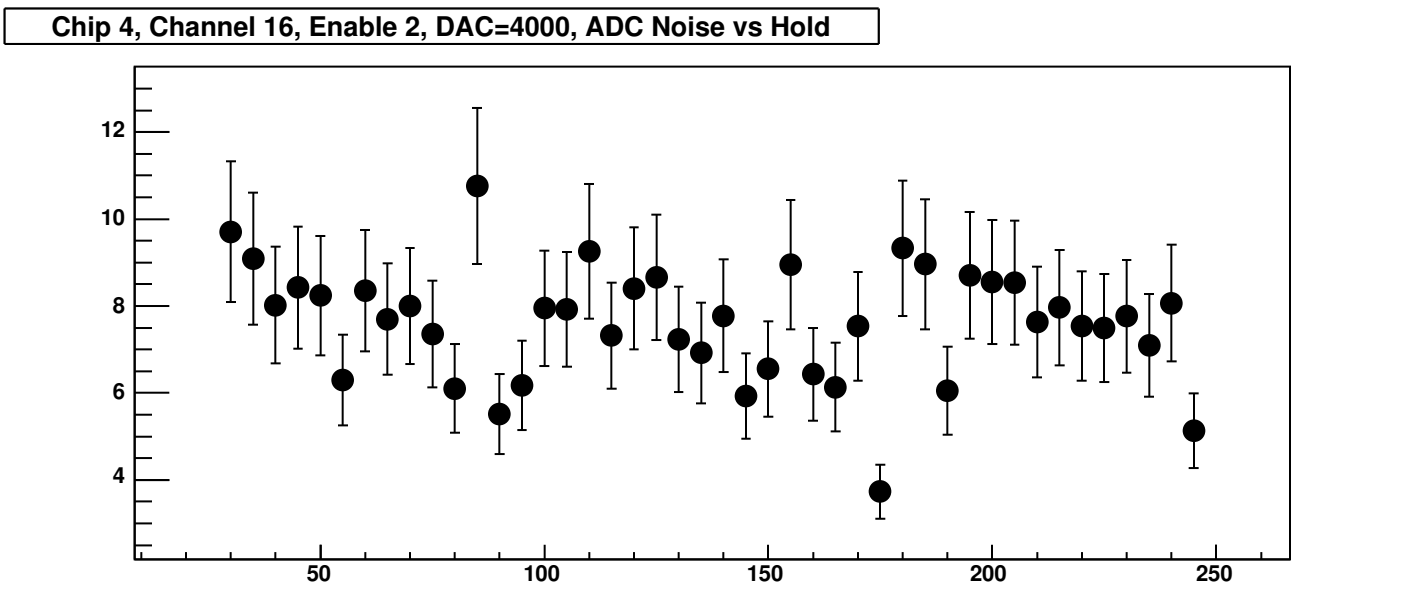


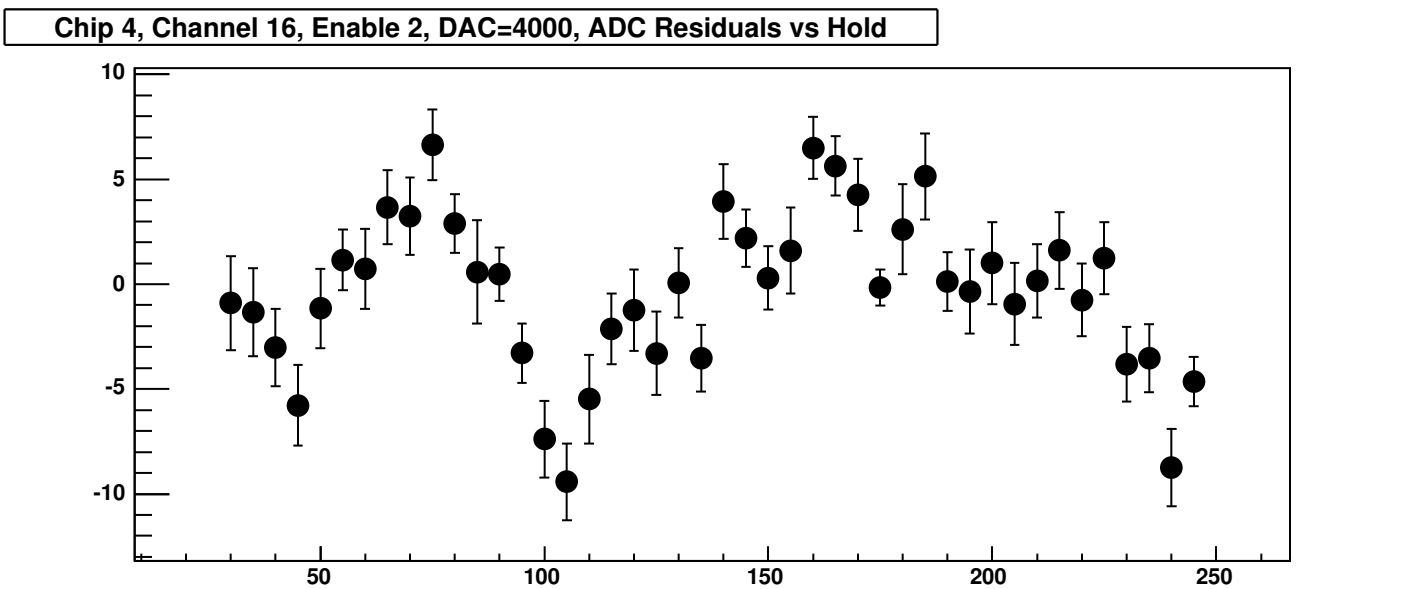

Chip 4, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 5098 / 38
p0 375.9 ± 0.2457
p1 271.3 ± 1.563
p2 81.97 ± 84.85
p3 36.52 ± 1.414
p4 0.1572 ± 0.001596
380
360
340
320
300
280
50
100
150
200
250


Chip 4, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250

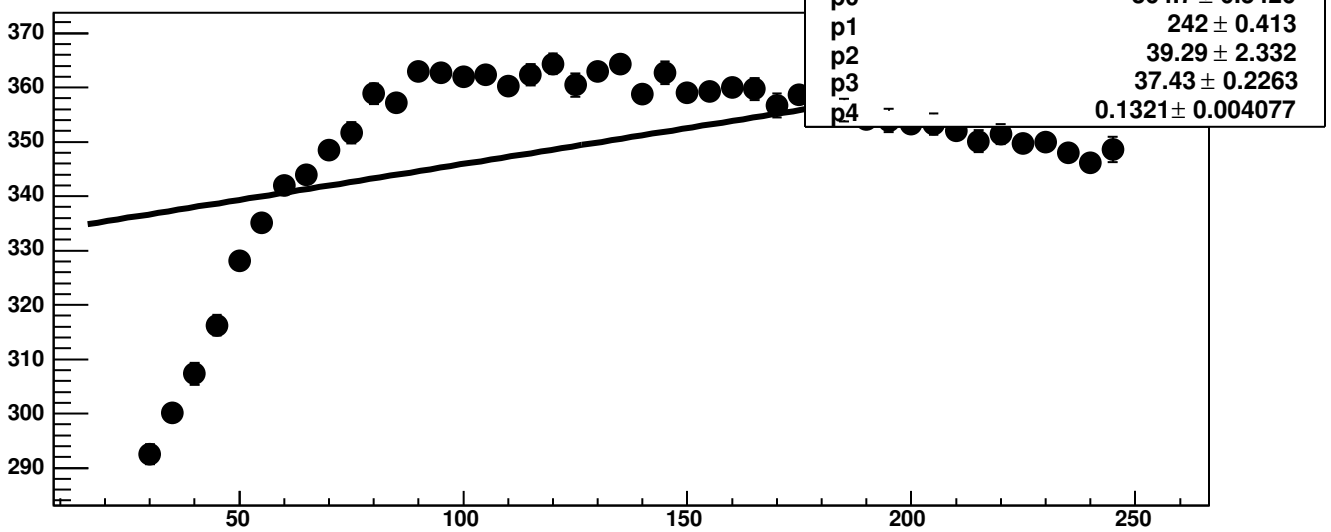

Chip 4, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
-20
-30
-40
-50
-60
50
100
150
200
250


Chip 4, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 196.9 / 38
p0 340.8 ± 0.7626
p1 101.1 ± 0.7974
p2 -4.82e+07 ± 8.658e+05
p3 3.634e+07 ± 1.889e+05
p4 1.496 ± 0.02128
360
340
320
300
280
260
240
220
50
100
150
200
250

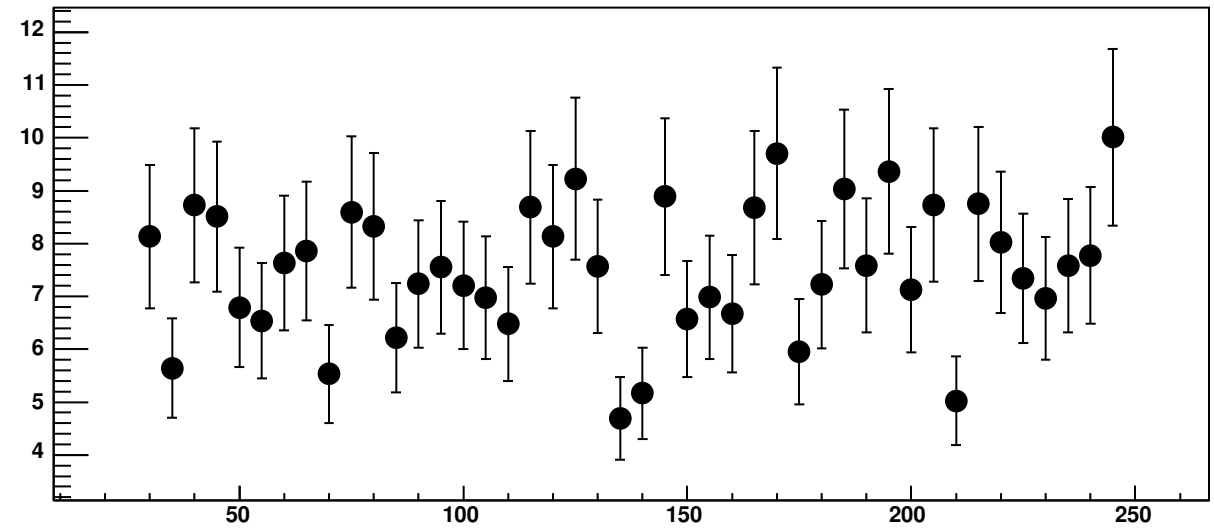

Chip 4, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250


Chip 4, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
50
100
150
200
250

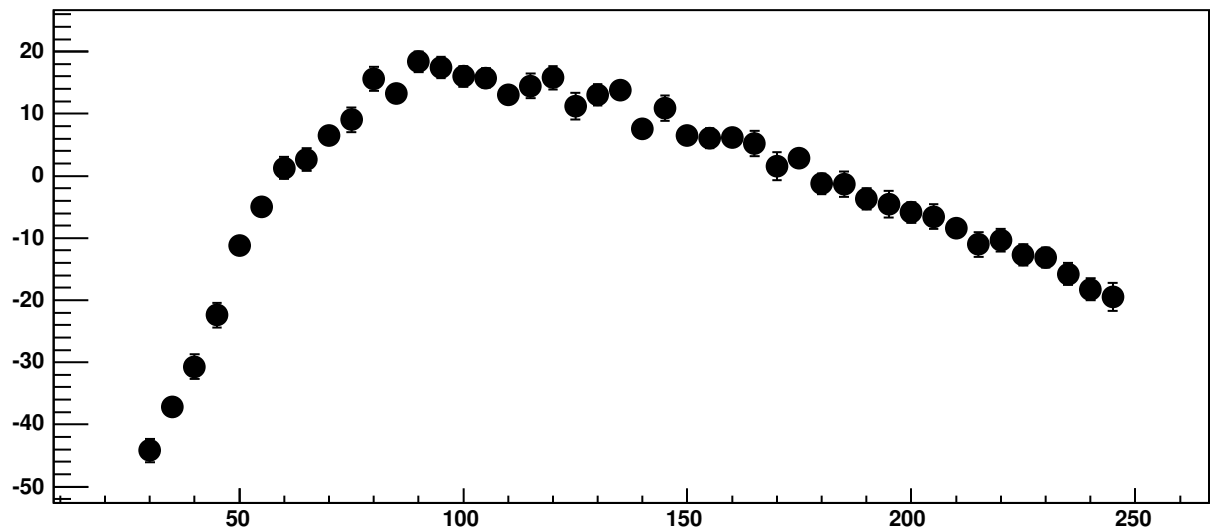
Chip 4, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold

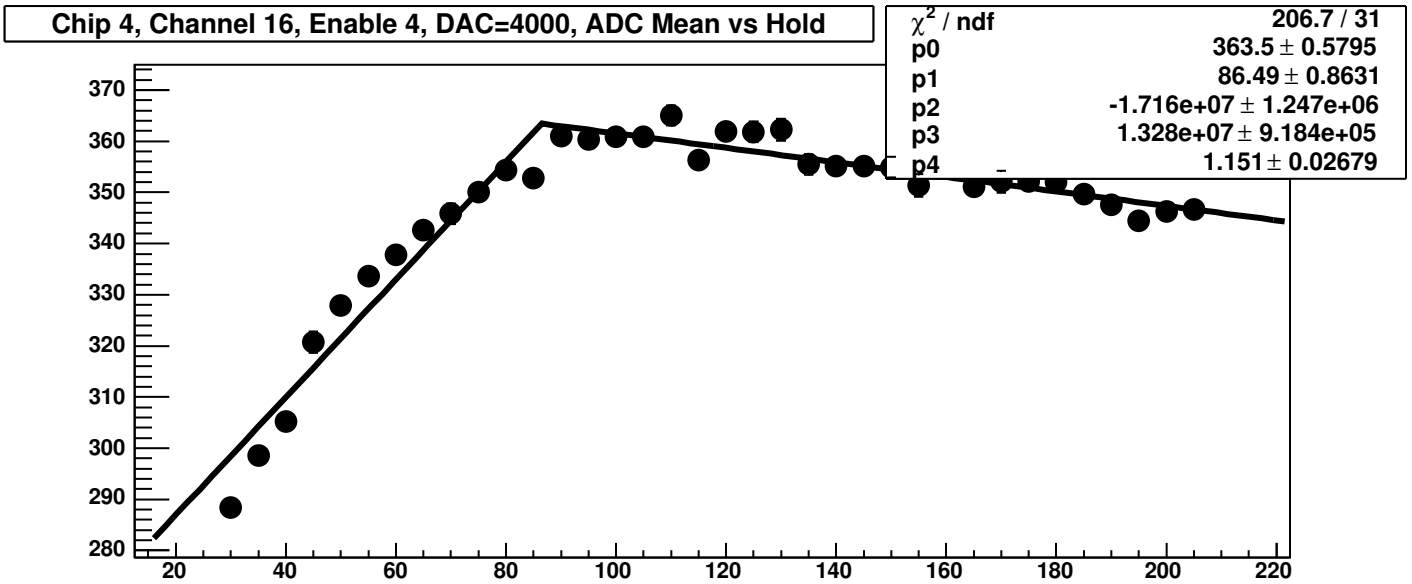


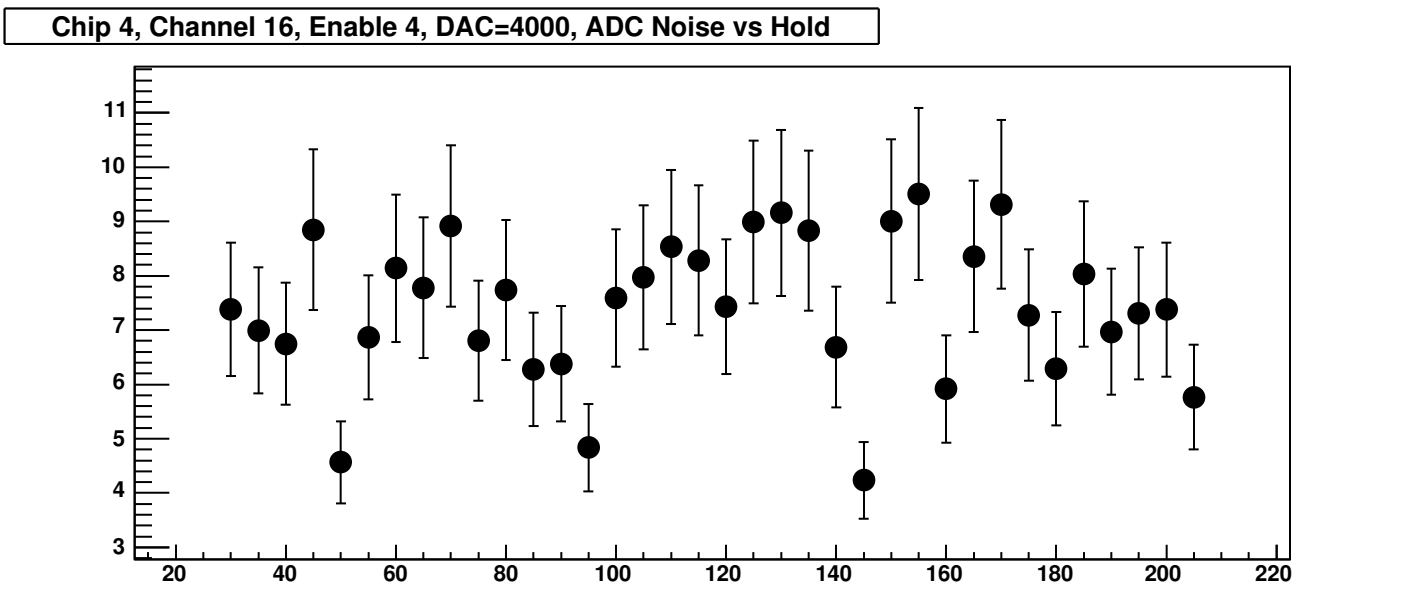
Chip 4, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold

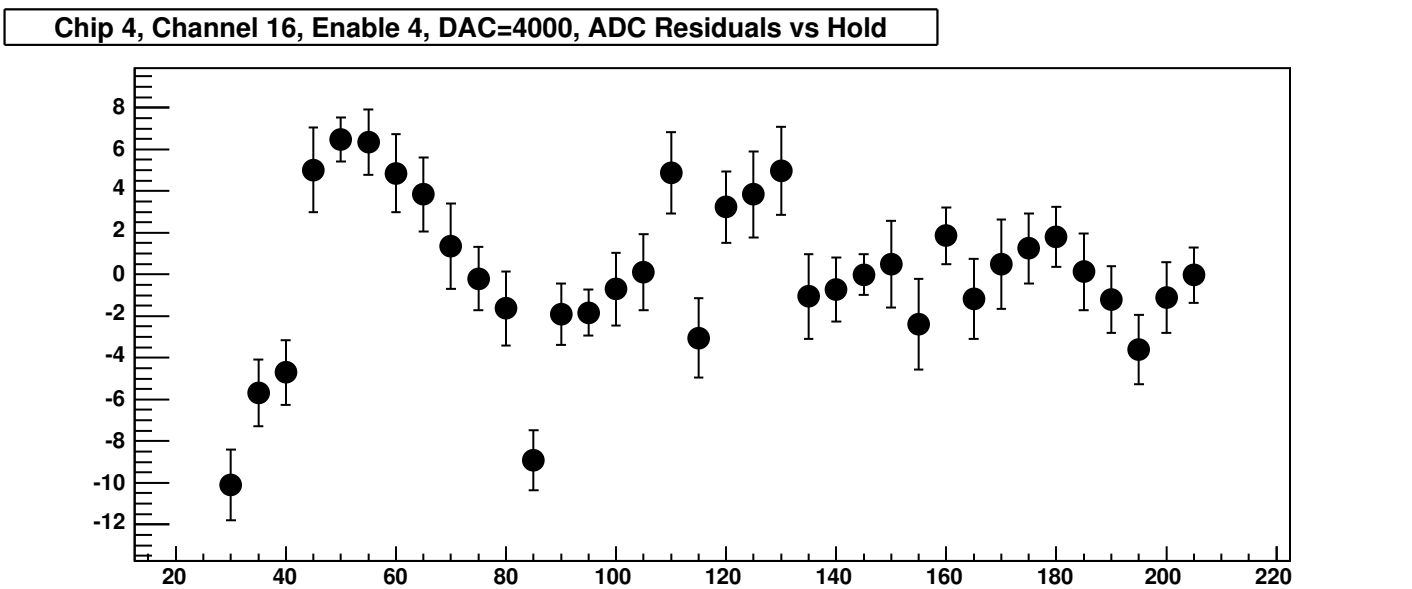


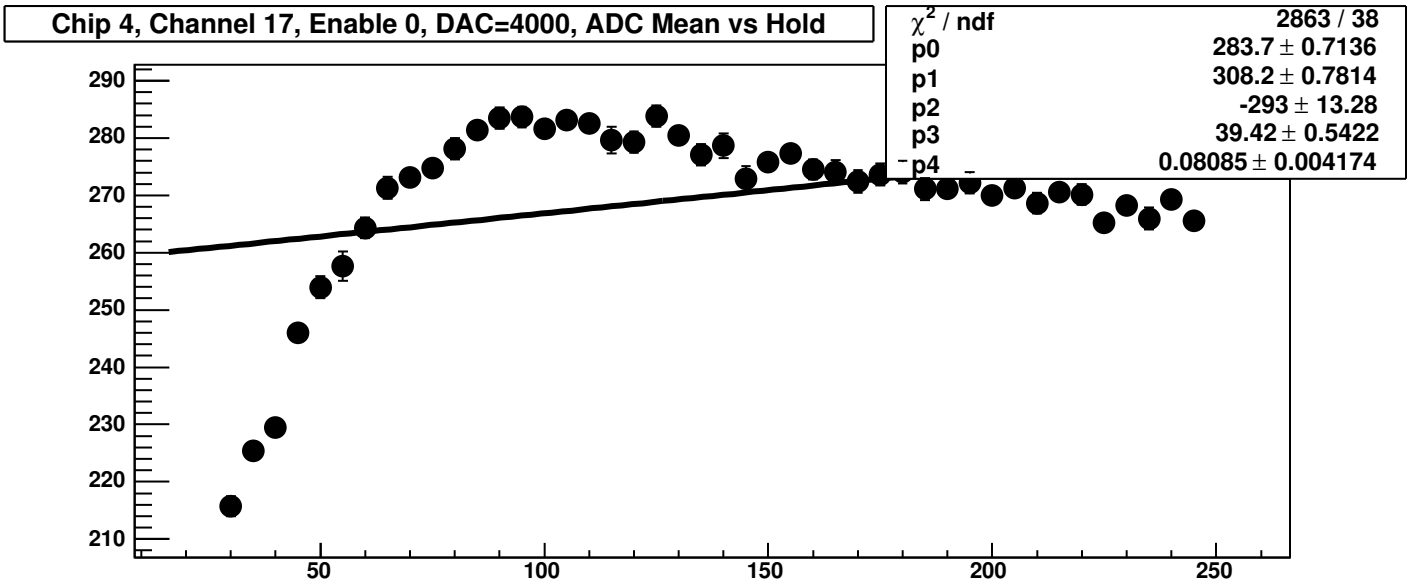
Chip 4, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold

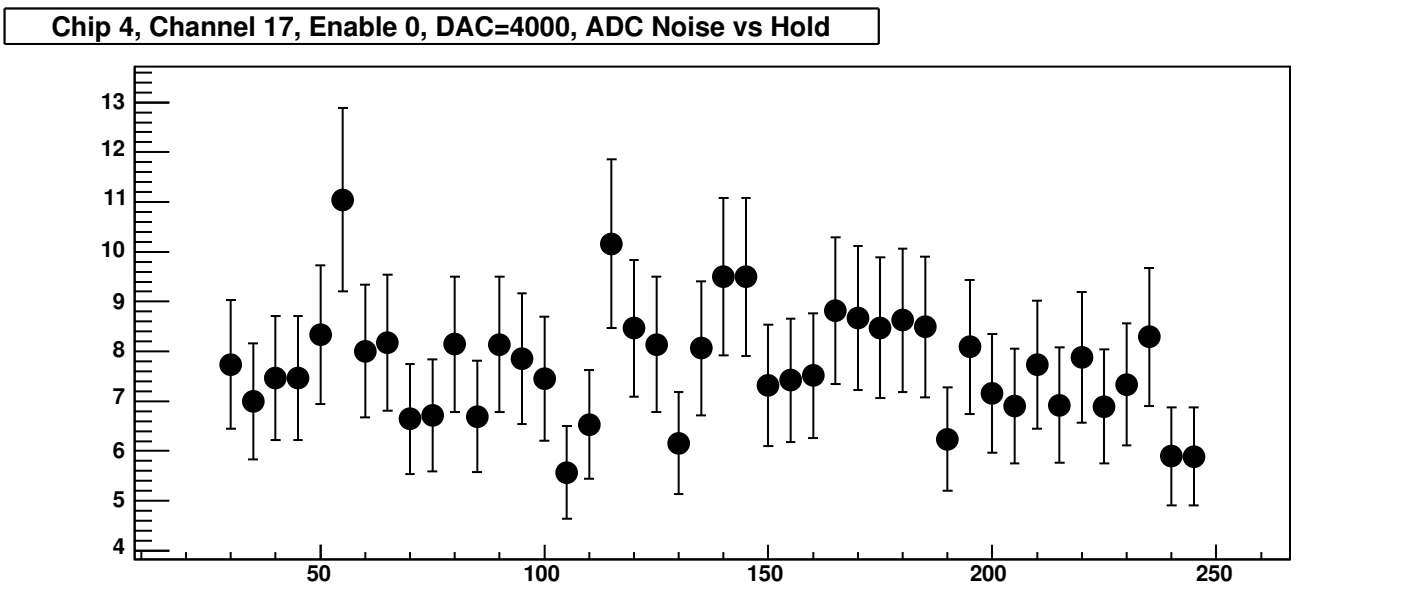


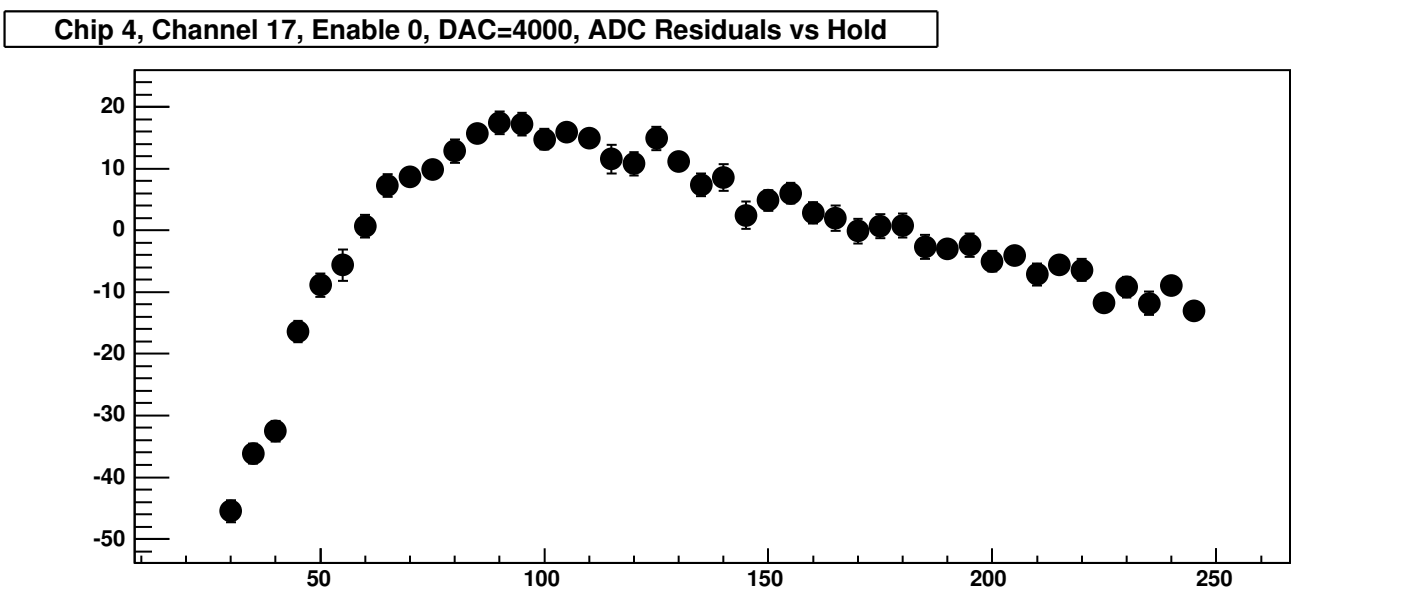

Chip 4, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 206.7 / 31
p0 363.5 ± 0.5795
p1 86.49 ± 0.8631
p2 -1.716e+07 ± 1.247e+06
p3 1.328e+07 ± 9.184e+05
p4 1.151 ± 0.02679

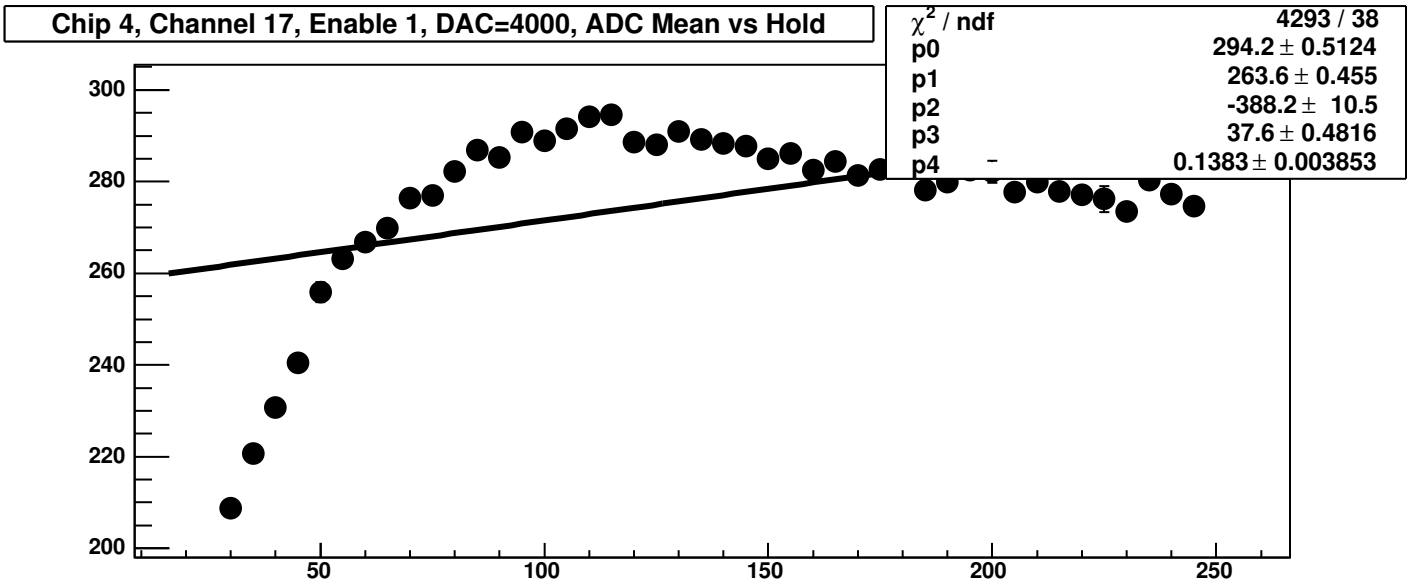

Chip 4, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold

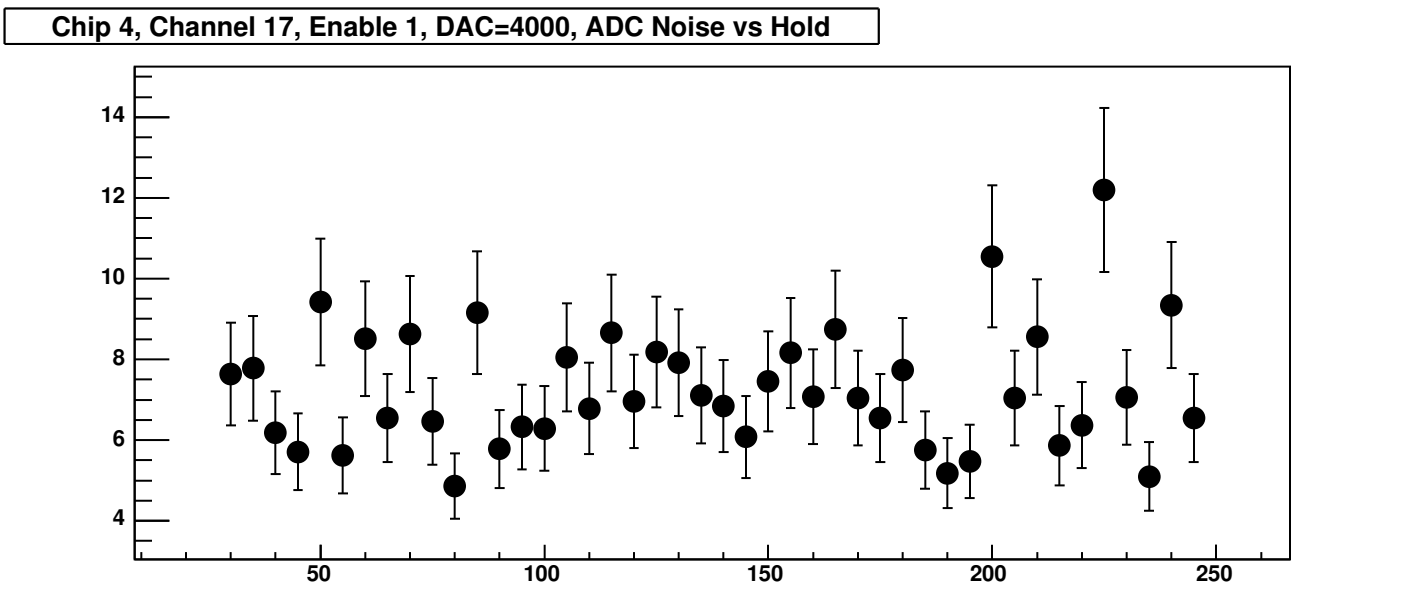

Chip 4, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

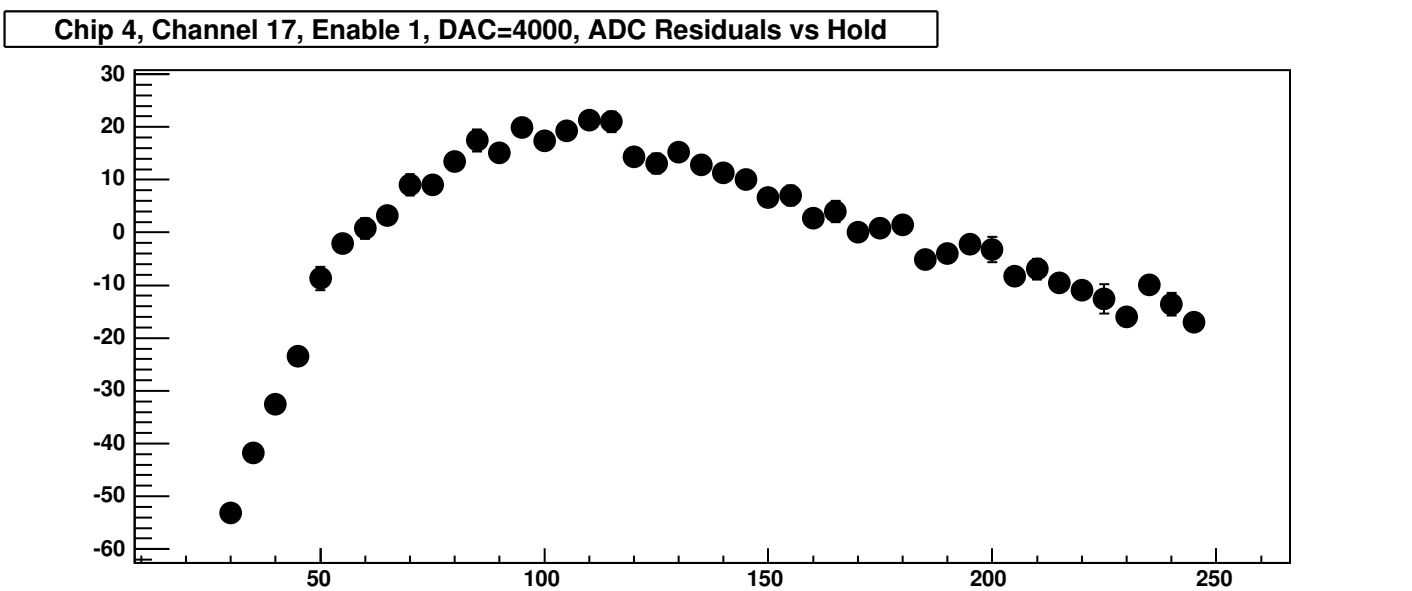

Chip 4, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 2863 / 38
p0 283.7 ± 0.7136
p1 308.2 ± 0.7814
p2 -293 ± 13.28
p3 39.42 ± 0.5422
p4 0.08085 ± 0.004174
290
280
270
260
250
240
230
220
210
50
100
150
200
250

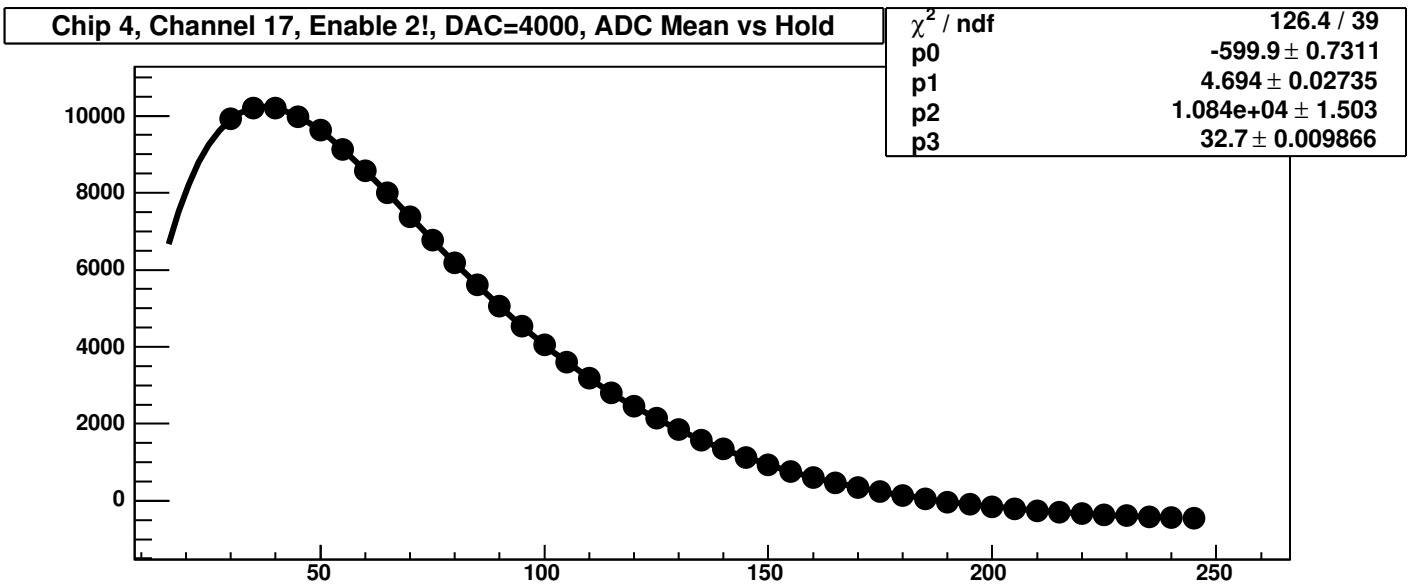

Chip 4, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
50
100
150
200
250

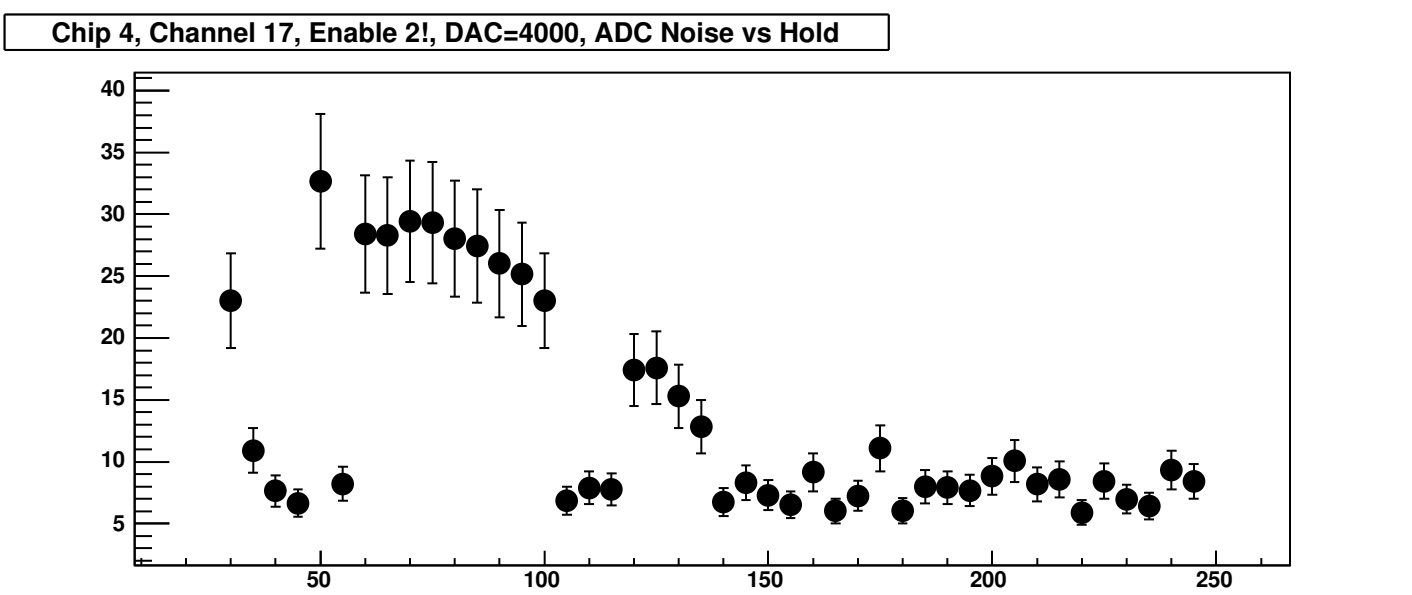

Chip 4, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
-50
50
100
150
200
250

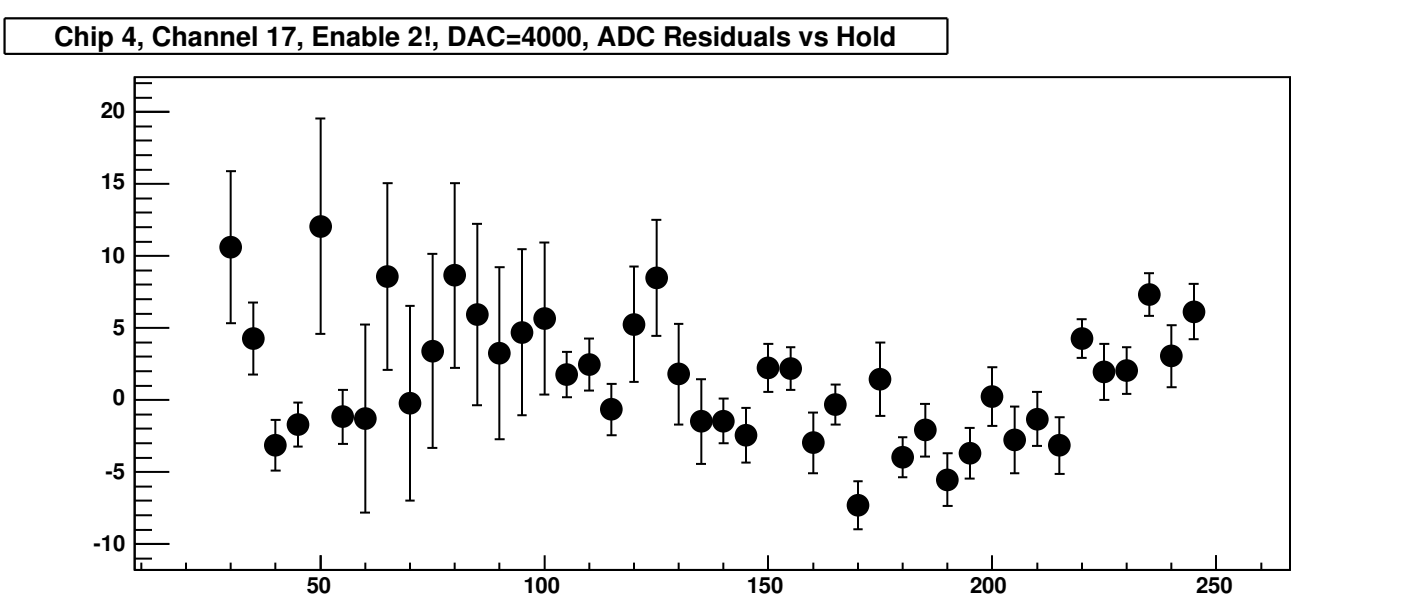

Chip 4, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 4293 / 38
p0 294.2 ± 0.5124
p1 263.6 ± 0.455
p2 -388.2 ± 10.5
p3 37.6 ± 0.4816
p4 0.1383 ± 0.003853
300
280
260
240
220
200
50
100
150
200
250

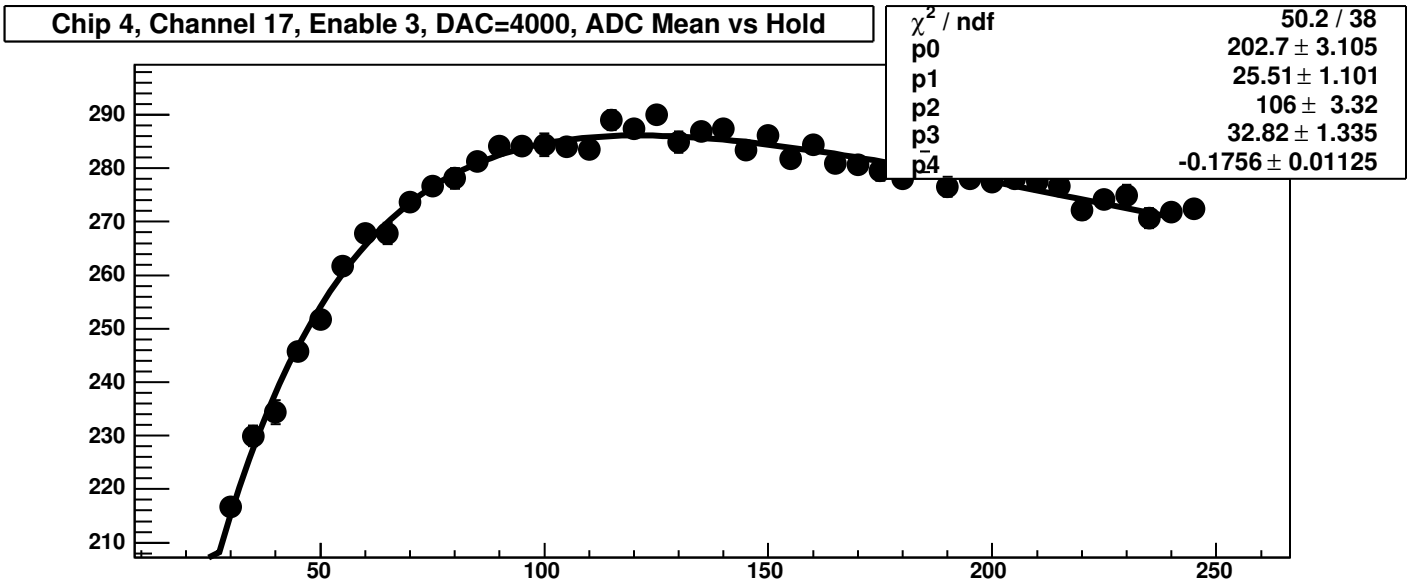

Chip 4, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
50
100
150
200
250

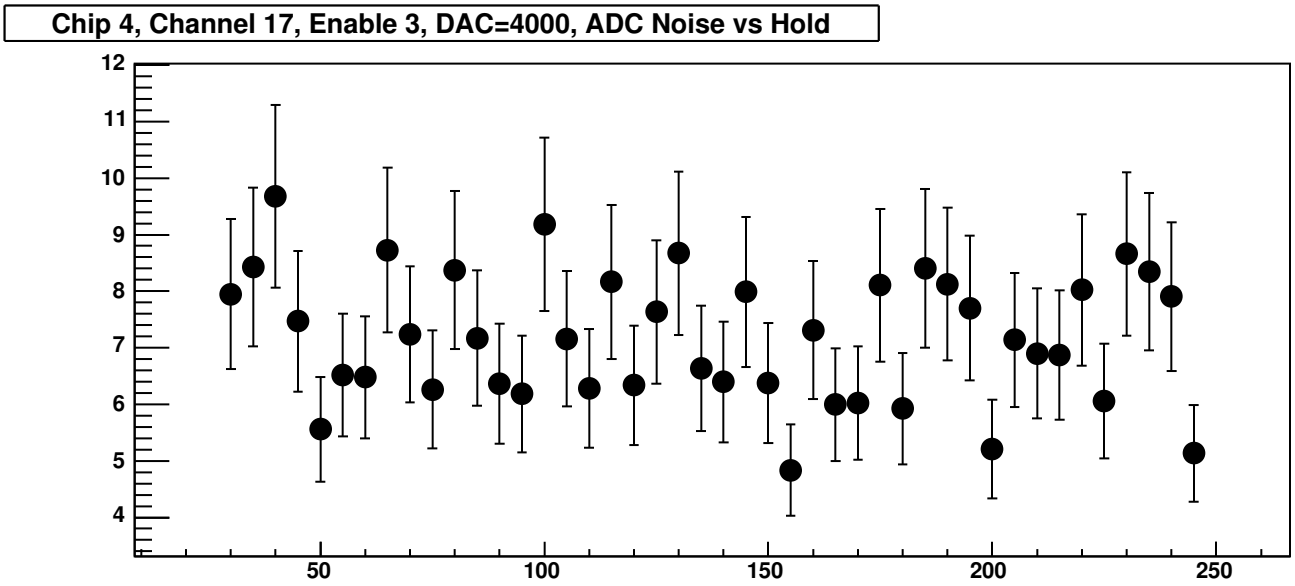

Chip 4, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
-20
-30
-40
-50
-60
50
100
150
200
250

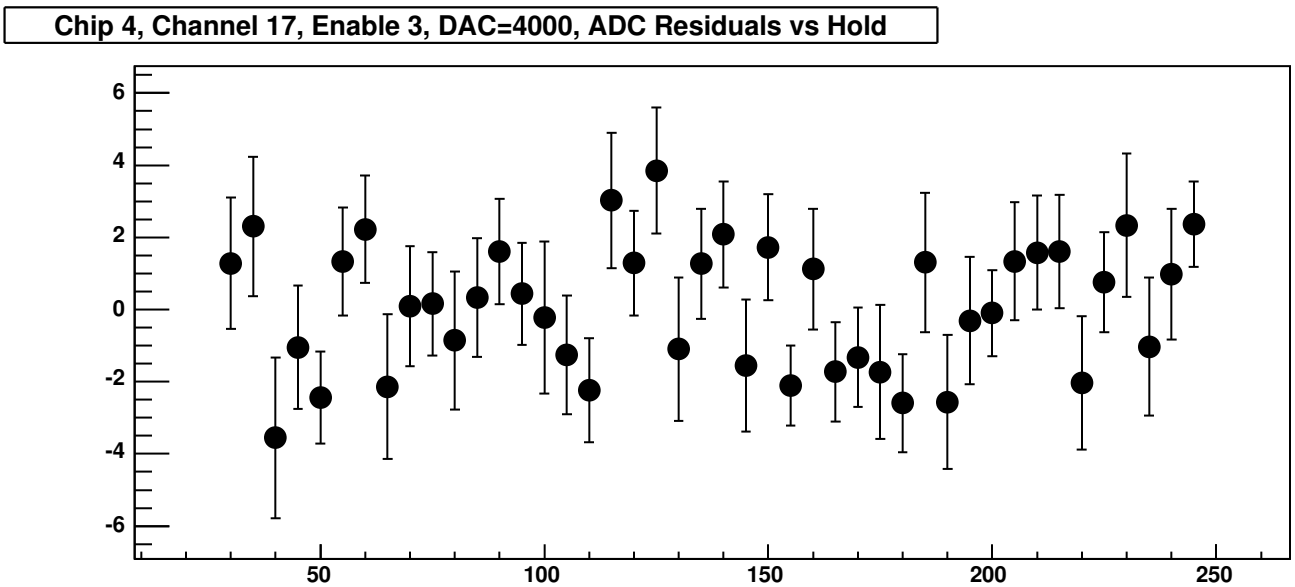

Chip 4, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 126.4 / 39
p0 -599.9 ± 0.7311
p1 4.694 ± 0.02735
p2 1.084e+04 ± 1.503
p3 32.7 ± 0.009866
10000
8000
6000
4000
2000
0
50
100
150
200
250


Chip 4, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold
40
35
30
25
20
15
10
5
50
100
150
200
250

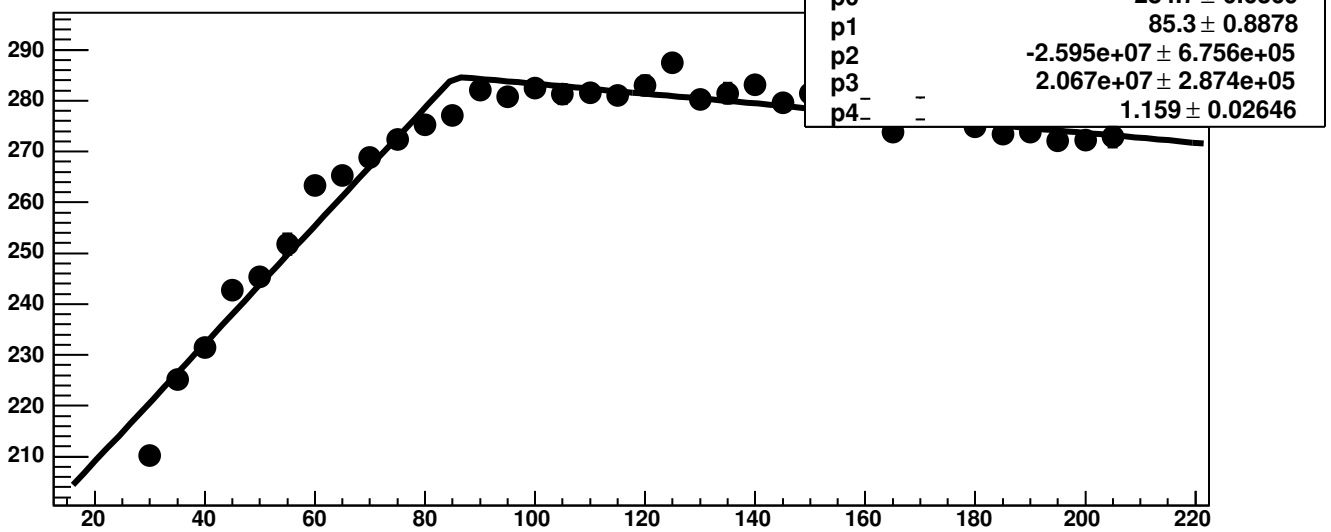

Chip 4, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
50
100
150
200
250


Chip 4, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 50.2 / 38
p0 202.7 ± 3.105
p1 25.51 ± 1.101
p2 106 ± 3.32
p3 32.82 ± 1.335
p4 -0.1756 ± 0.01125
290
280
270
260
250
240
230
220
210
50
100
150
200
250

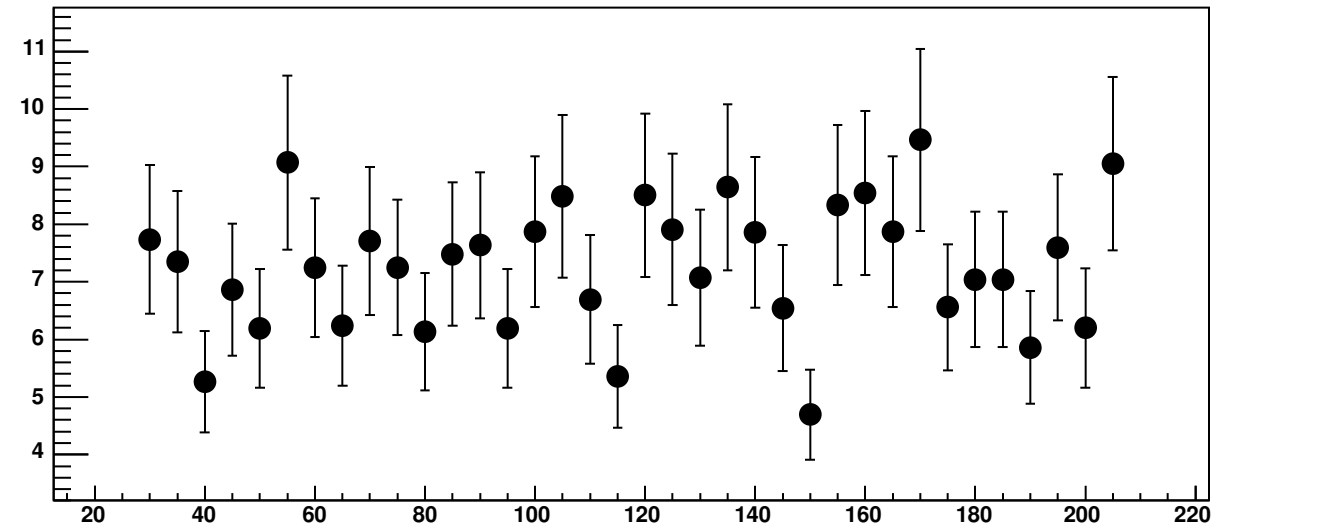

Chip 4, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 4, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold
6
4
2
0
-2
-4
-6
50
100
150
200
250

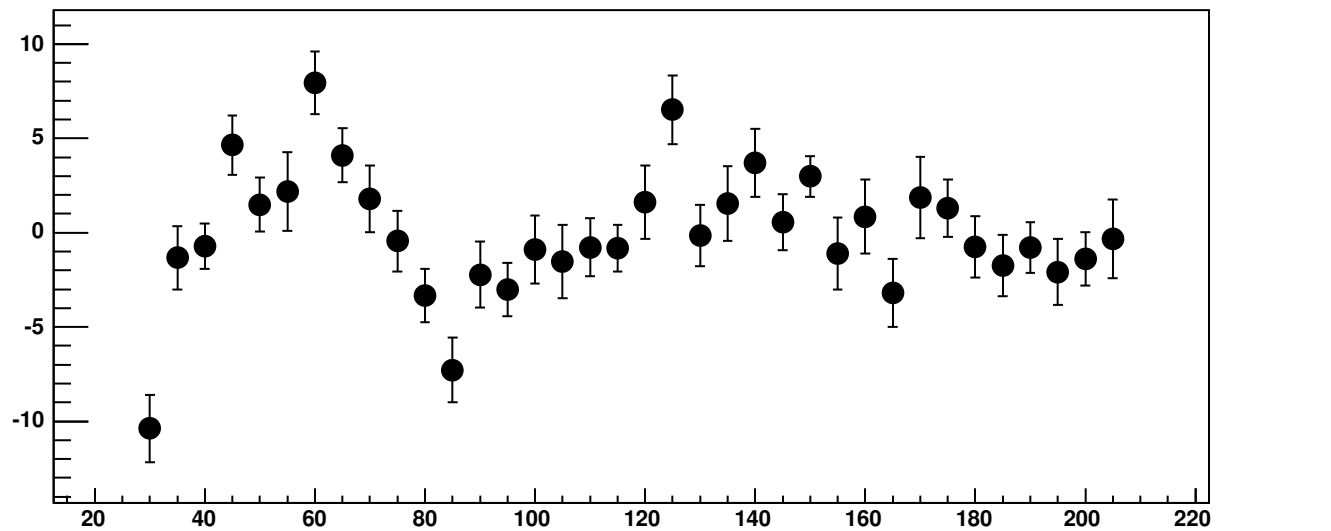
Chip 4, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



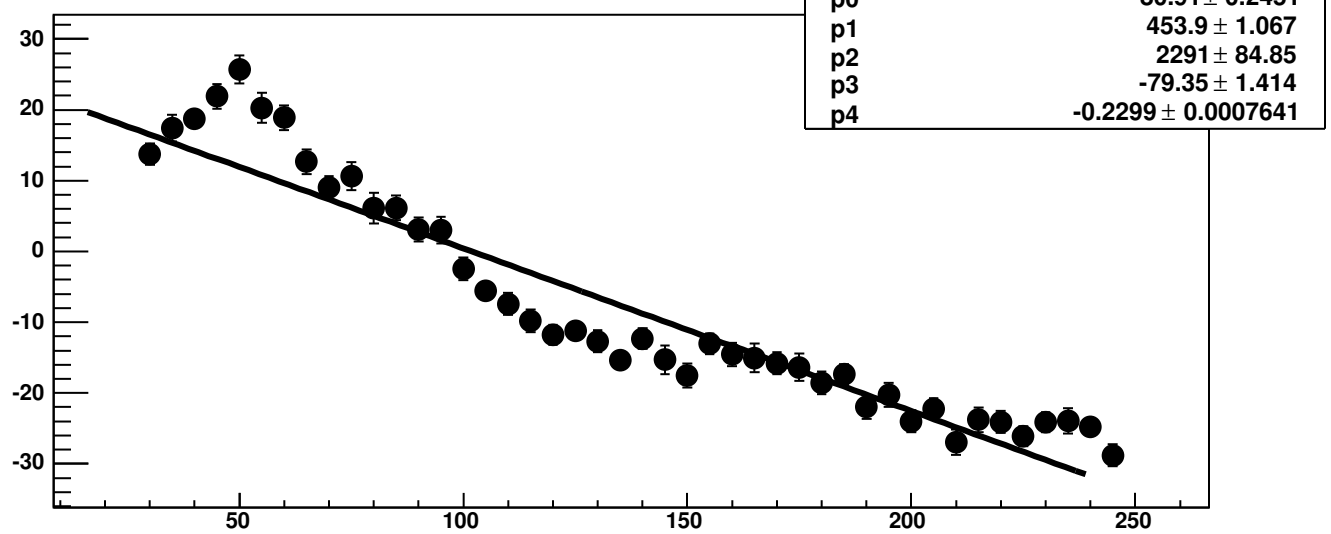
Chip 4, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



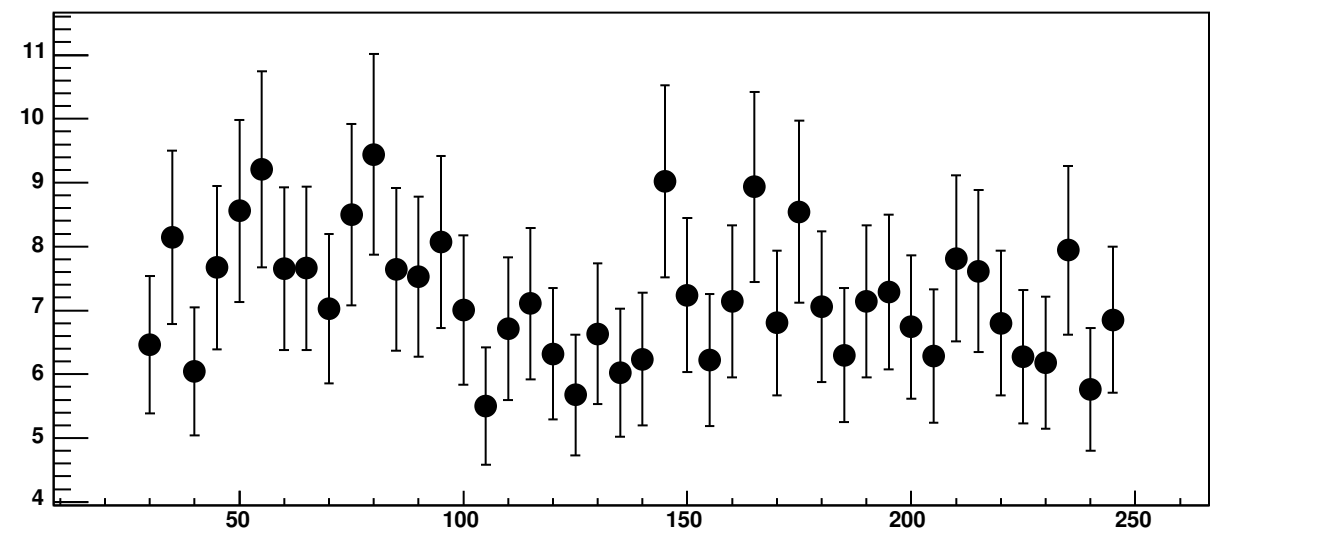
Chip 4, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold



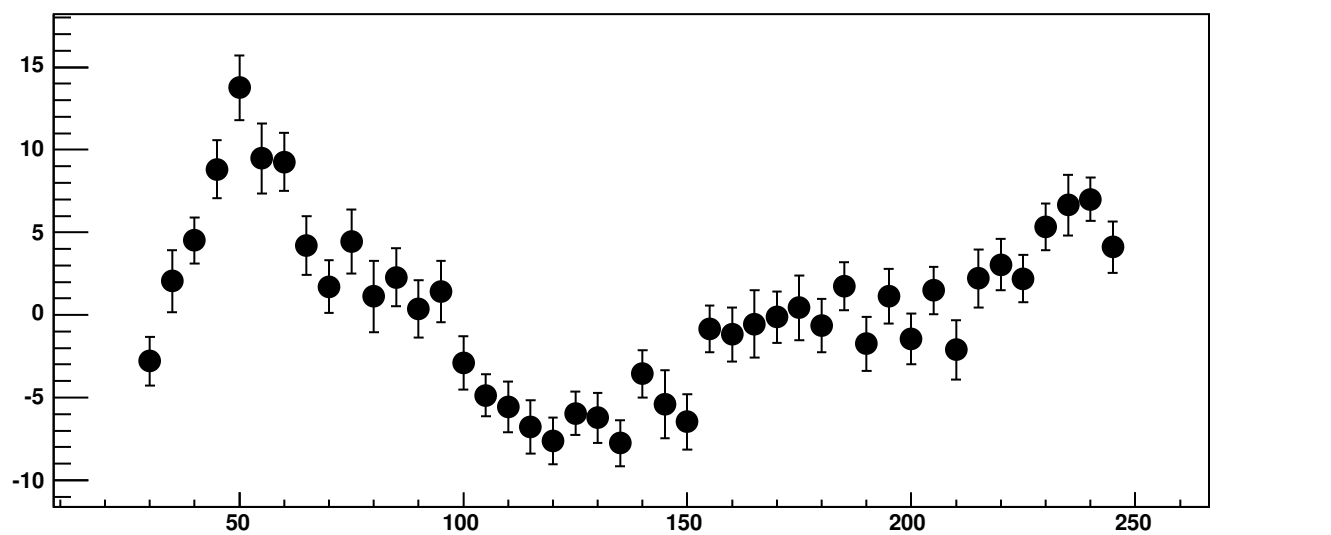
Chip 5, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold



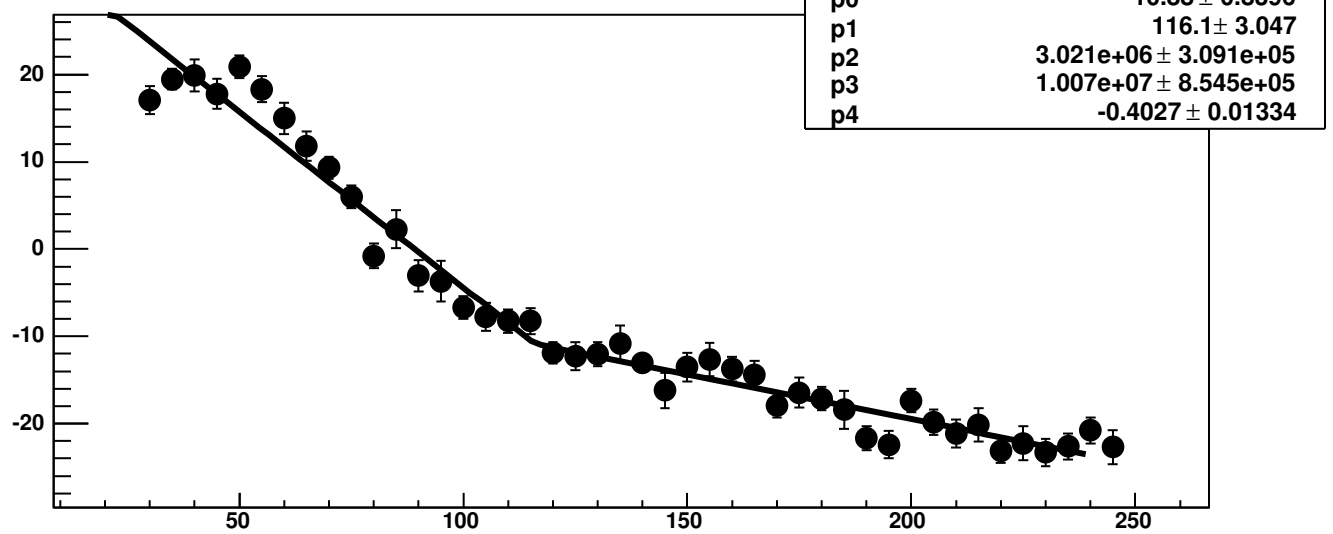
Chip 5, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold



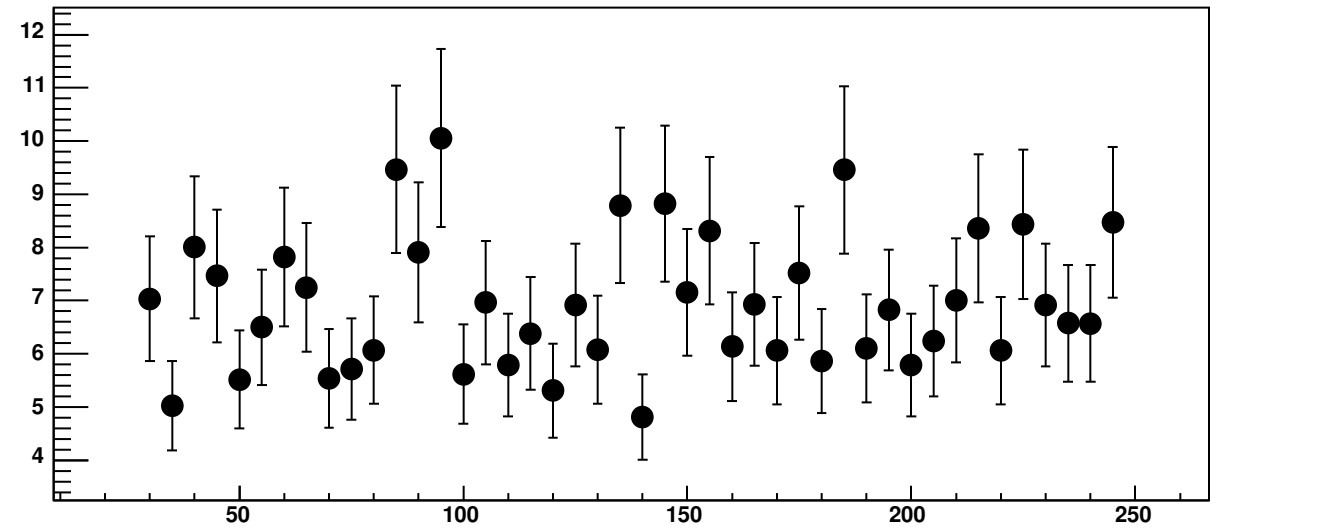
Chip 5, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold



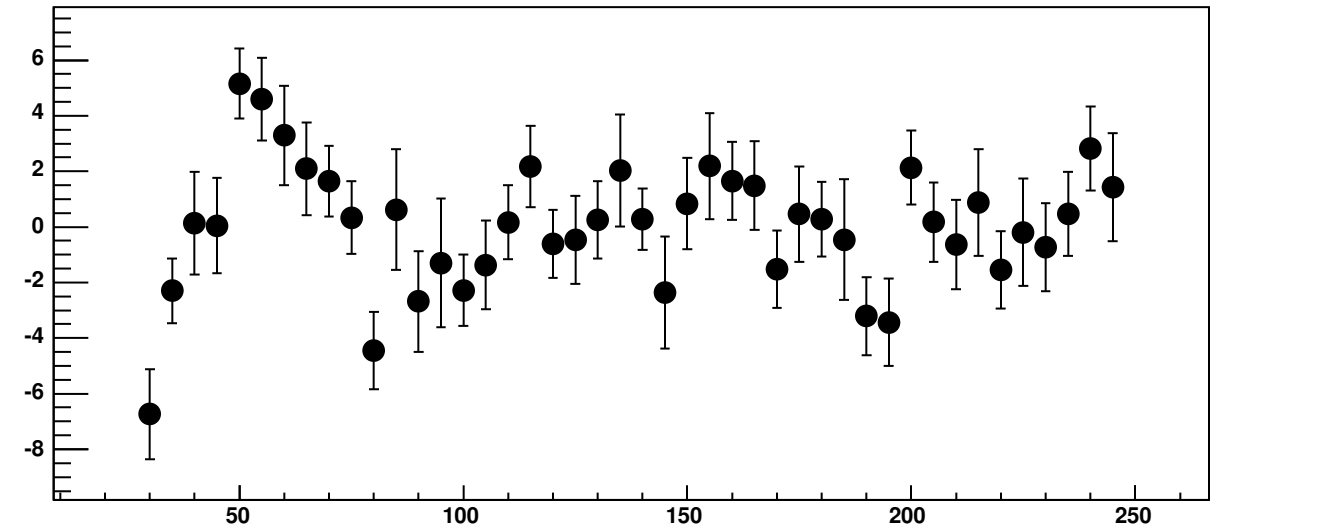
Chip 5, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold



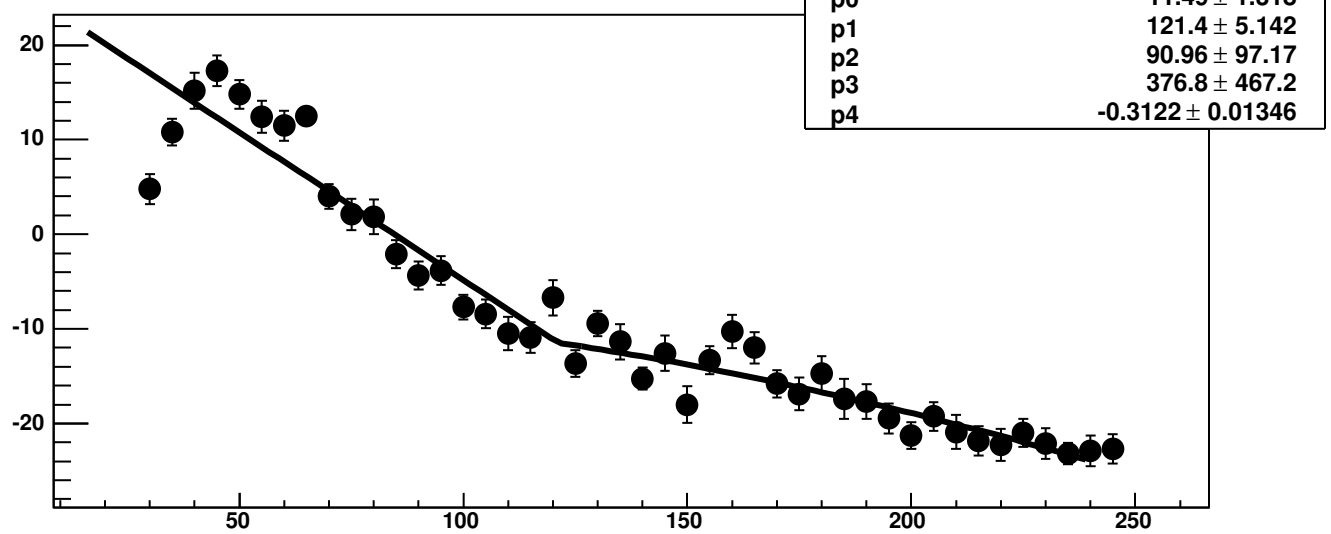
Chip 5, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



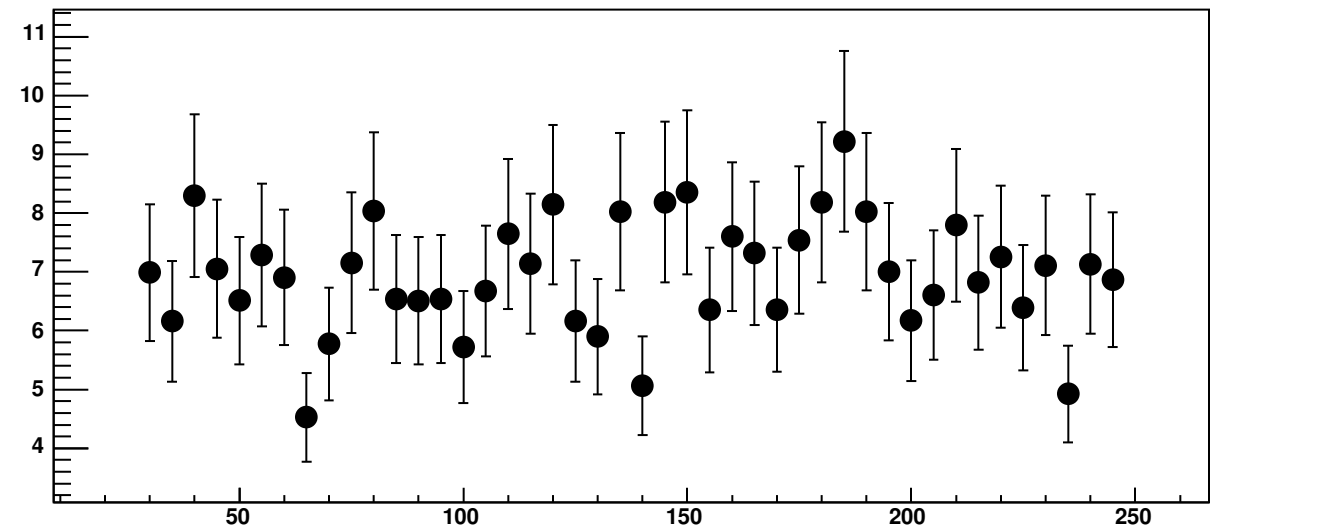
Chip 5, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold



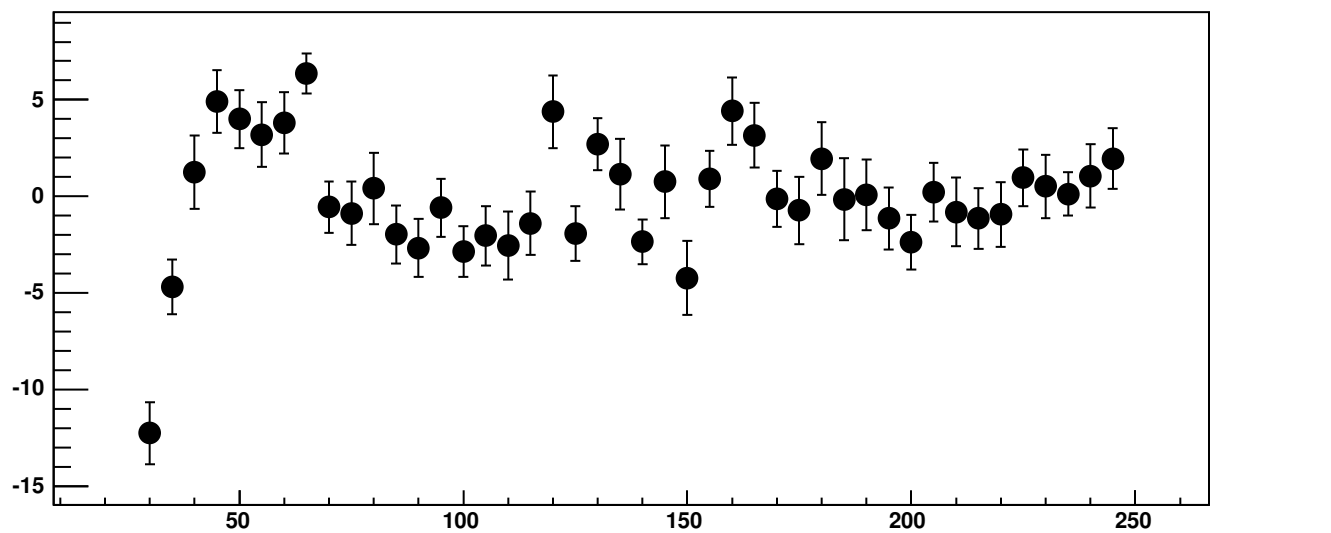
Chip 5, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold



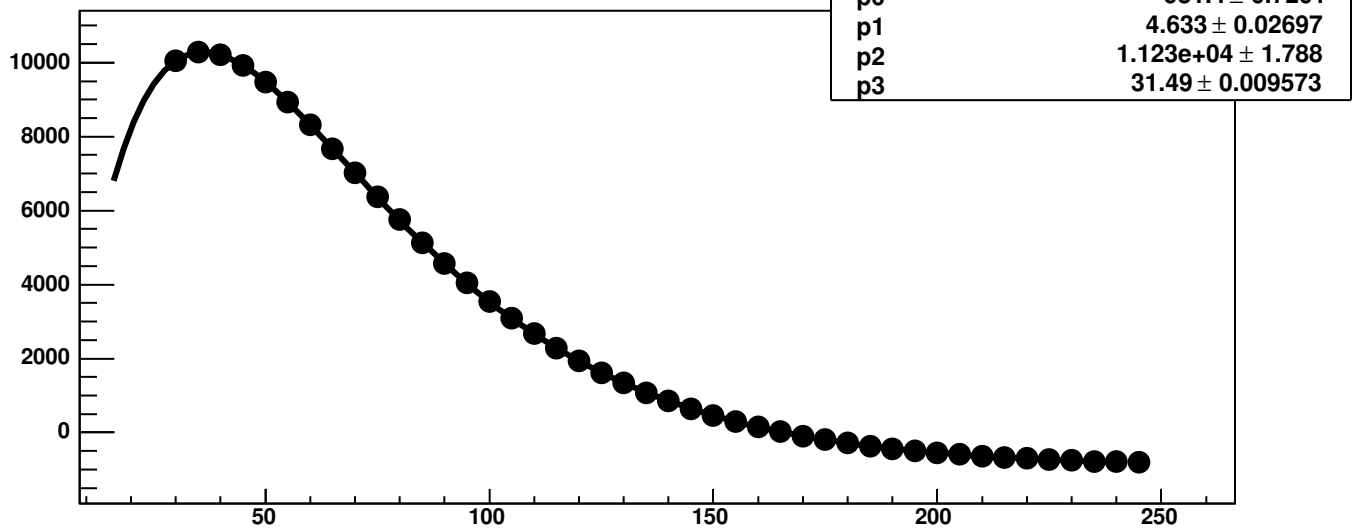
Chip 5, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



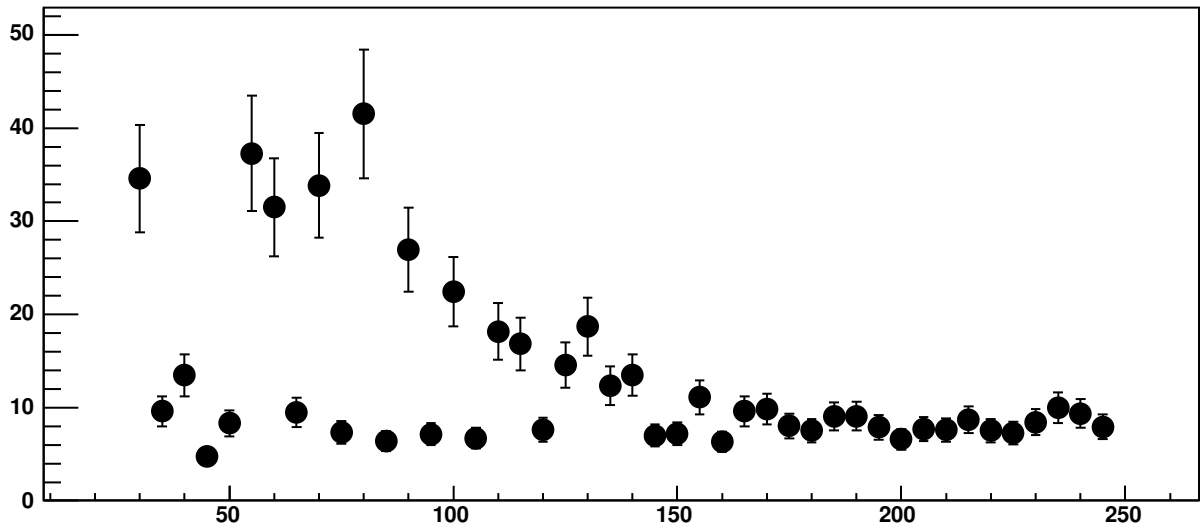
Chip 5, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold



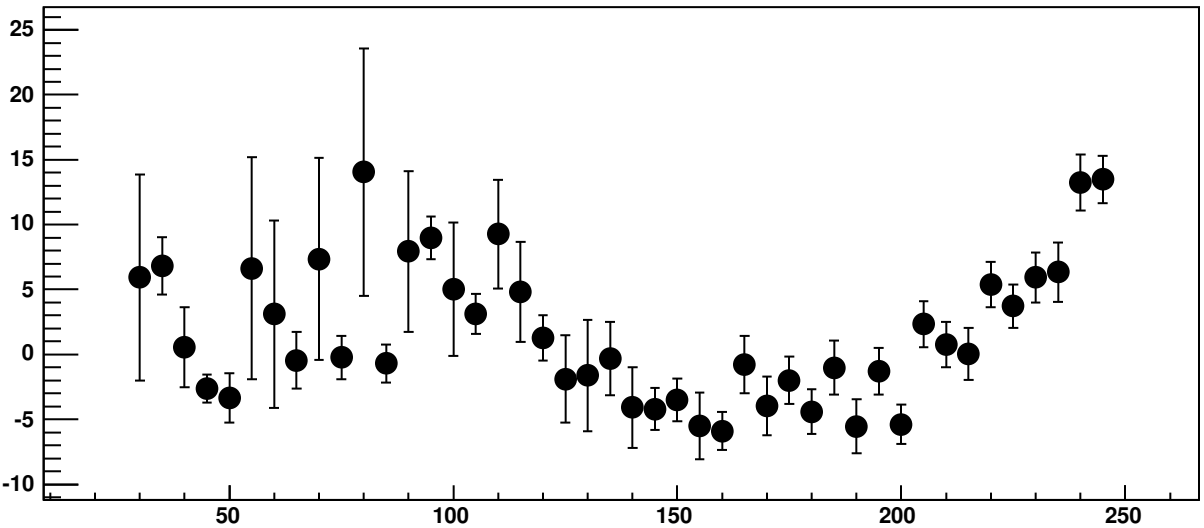
Chip 5, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold

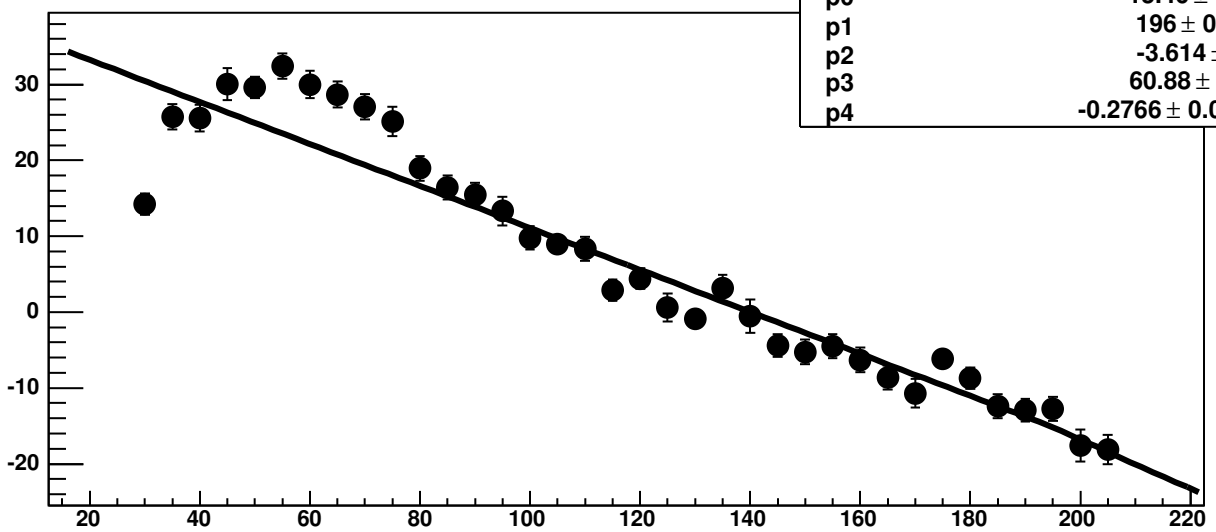


Chip 5, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold

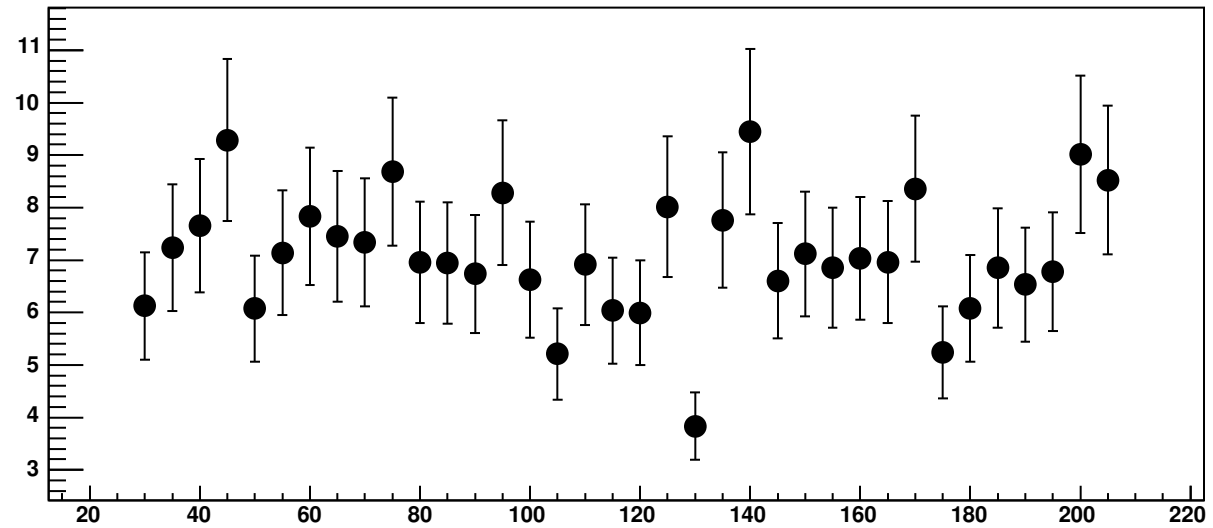


Chip 5, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

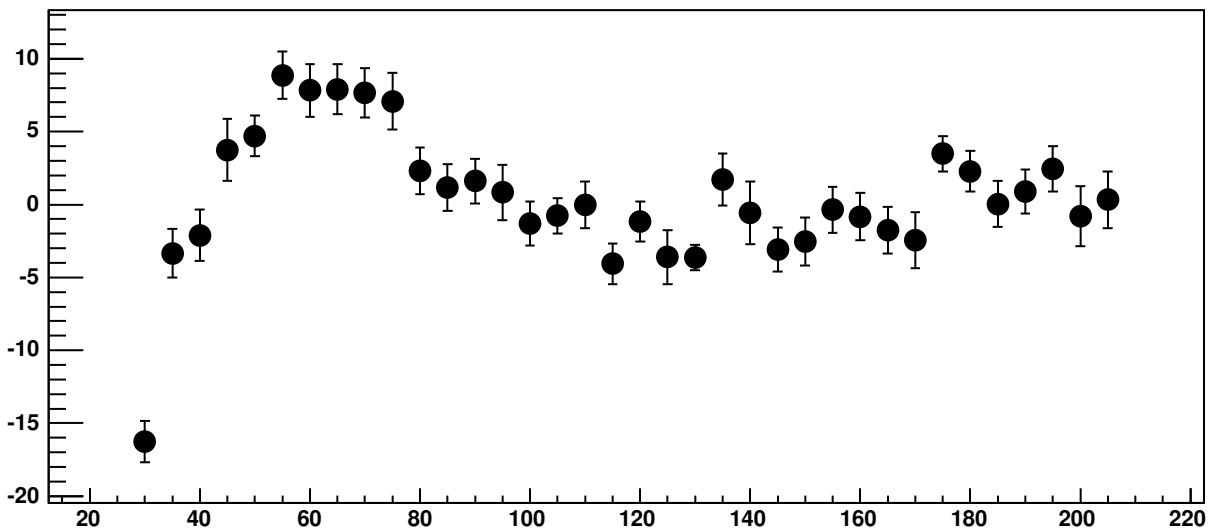
χ^2 / ndf	317.6 / 31
p0	-15.46 ± 0.5084
p1	196 ± 0.01477
p2	-3.614 ± 13.91
p3	60.88 ± 0.1792
p4	-0.2766 ± 0.005457



Chip 5, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold

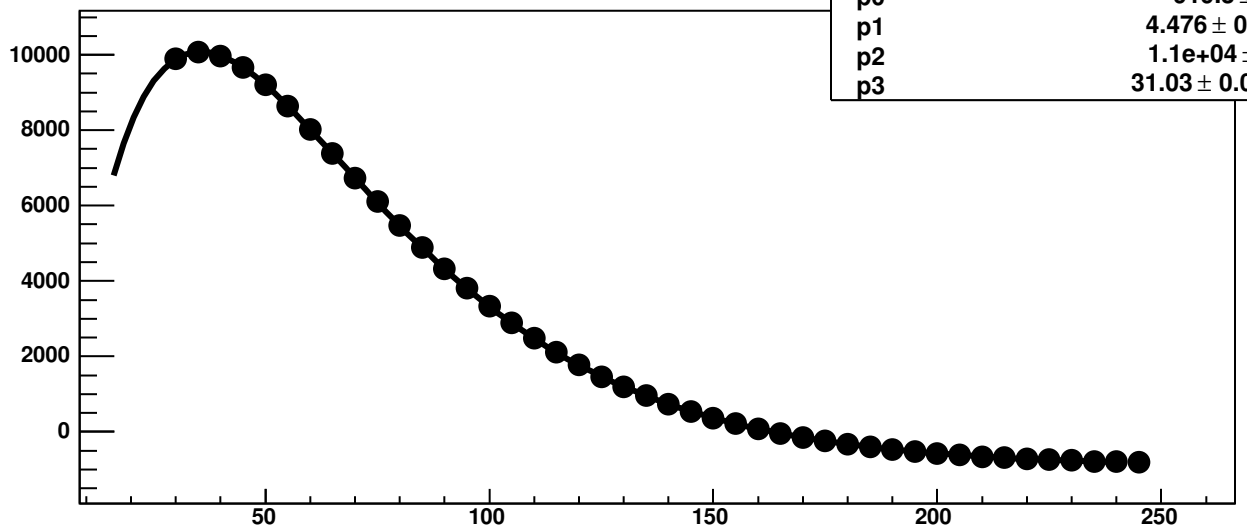


Chip 5, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

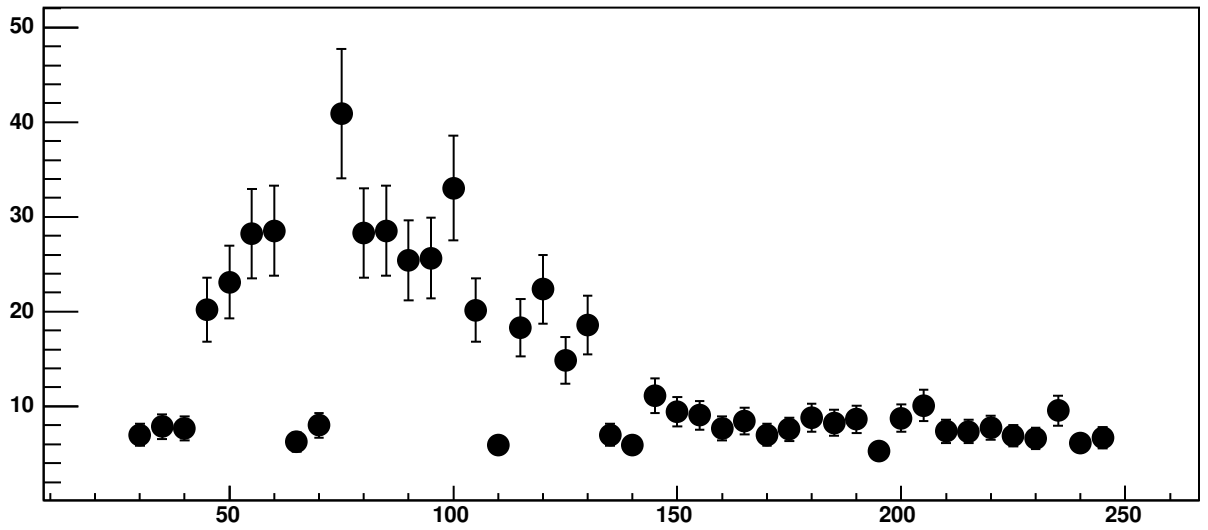


Chip 5, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold

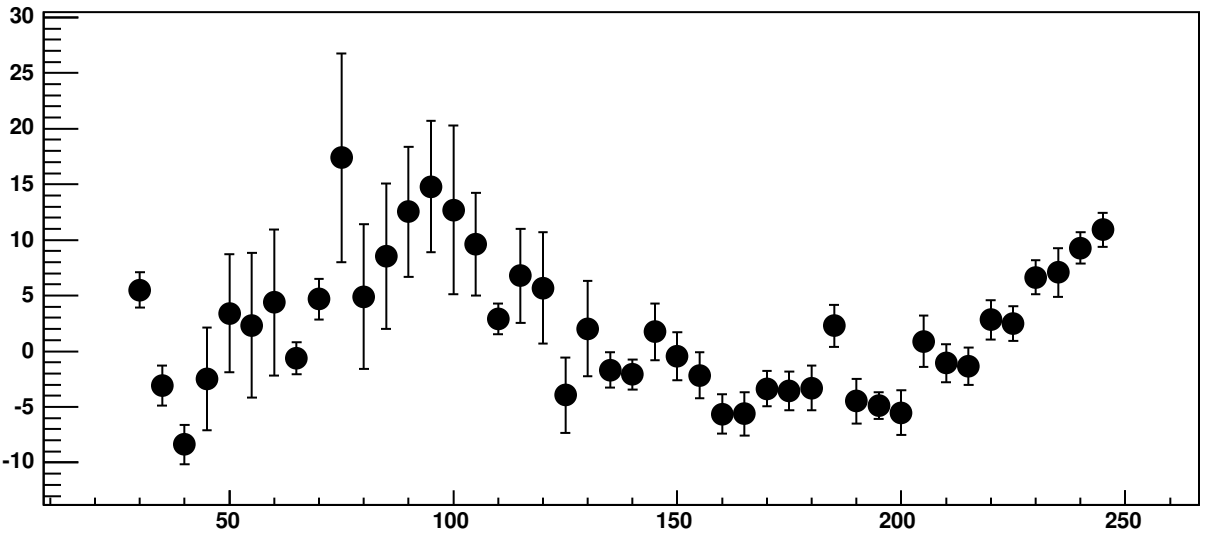
χ^2 / ndf	224.8 / 39
p0	-919.3 ± 0.646
p1	4.476 ± 0.01848
p2	1.1e+04 ± 1.031
p3	31.03 ± 0.008499



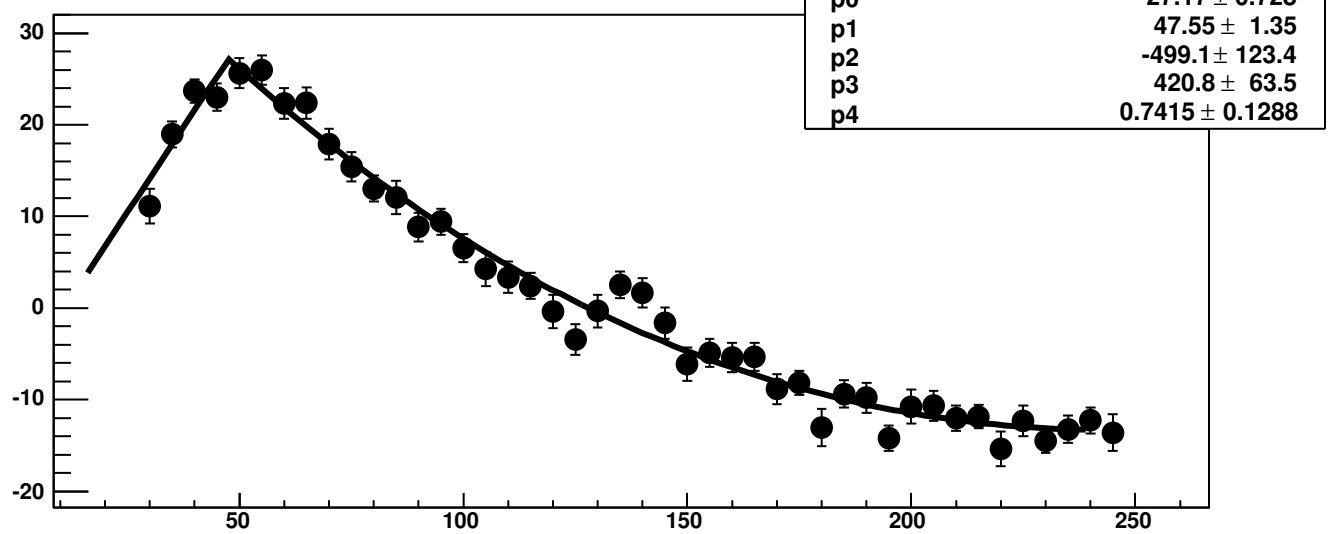
Chip 5, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold



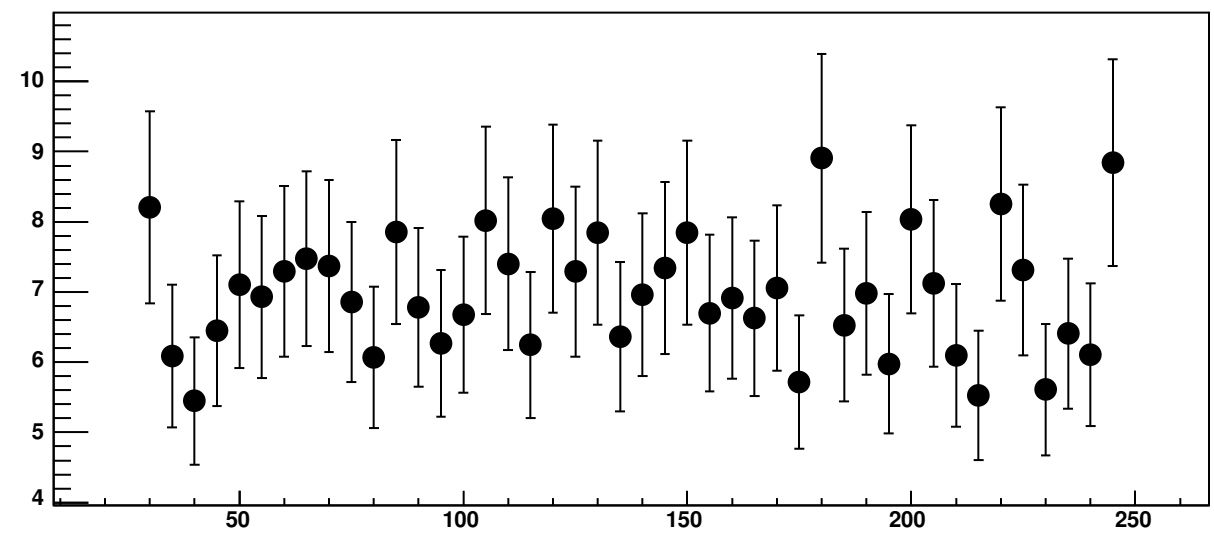
Chip 5, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold



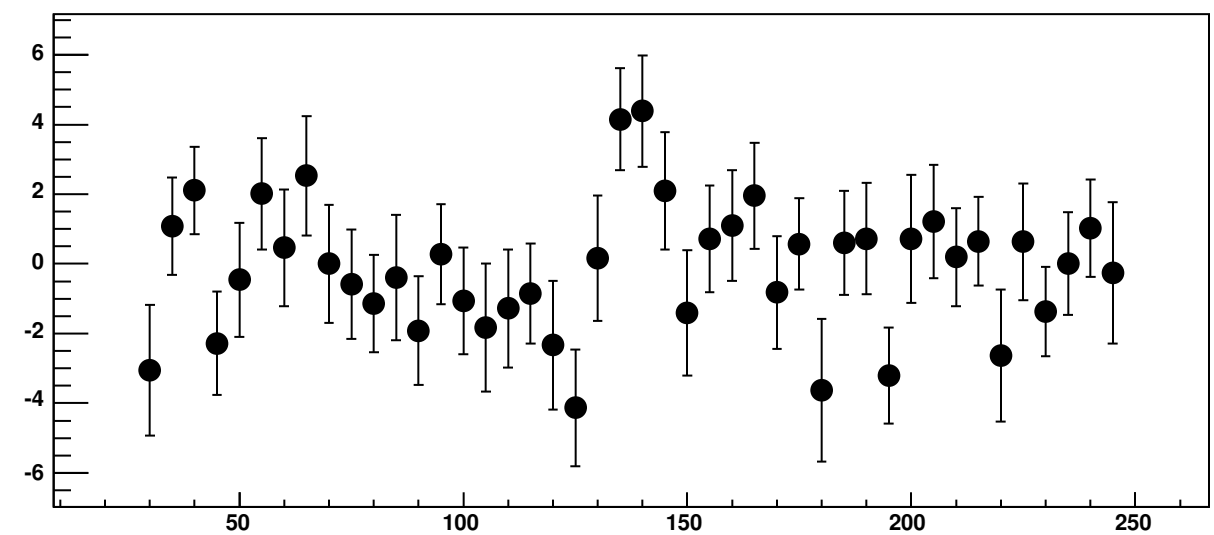
Chip 5, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold

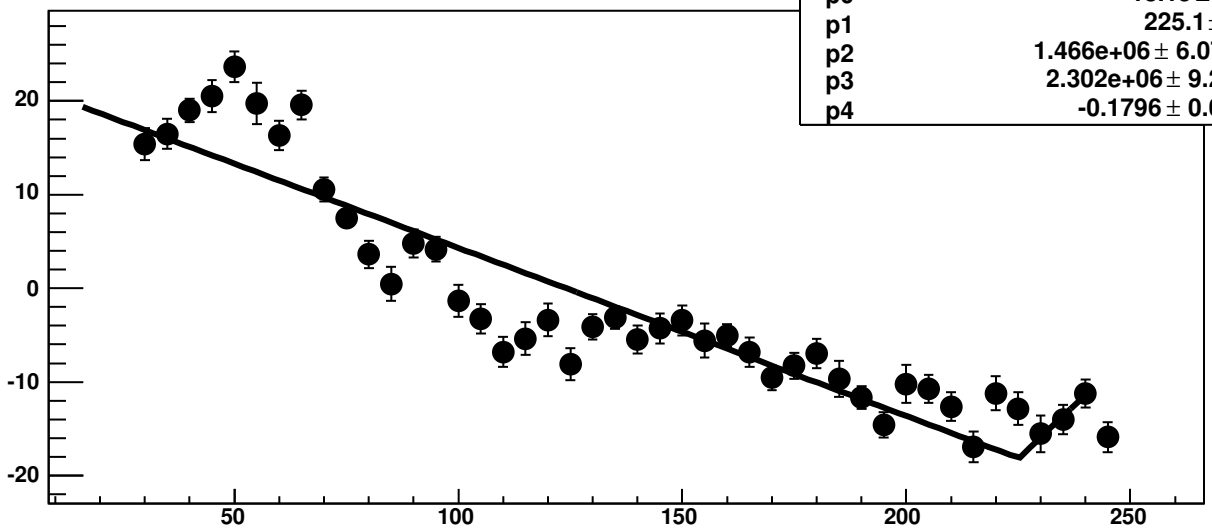


Chip 5, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold

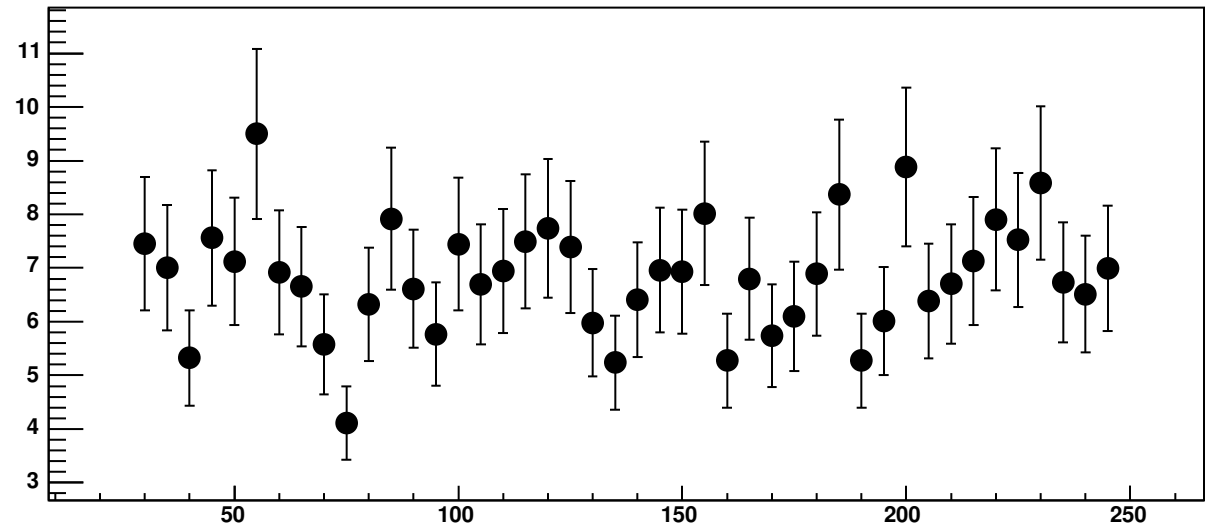


Chip 5, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

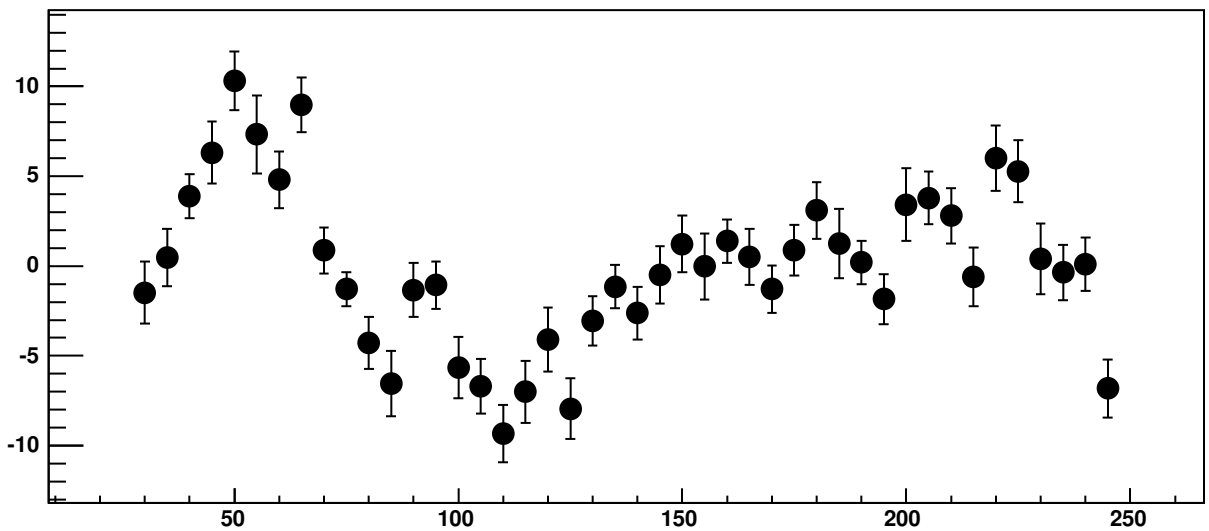
χ^2 / ndf	304.7 / 38
p0	-18.15 ± 0.8809
p1	225.1 ± 4.569
p2	1.466e+06 ± 6.078e+06
p3	2.302e+06 ± 9.24e+06
p4	-0.1796 ± 0.004219



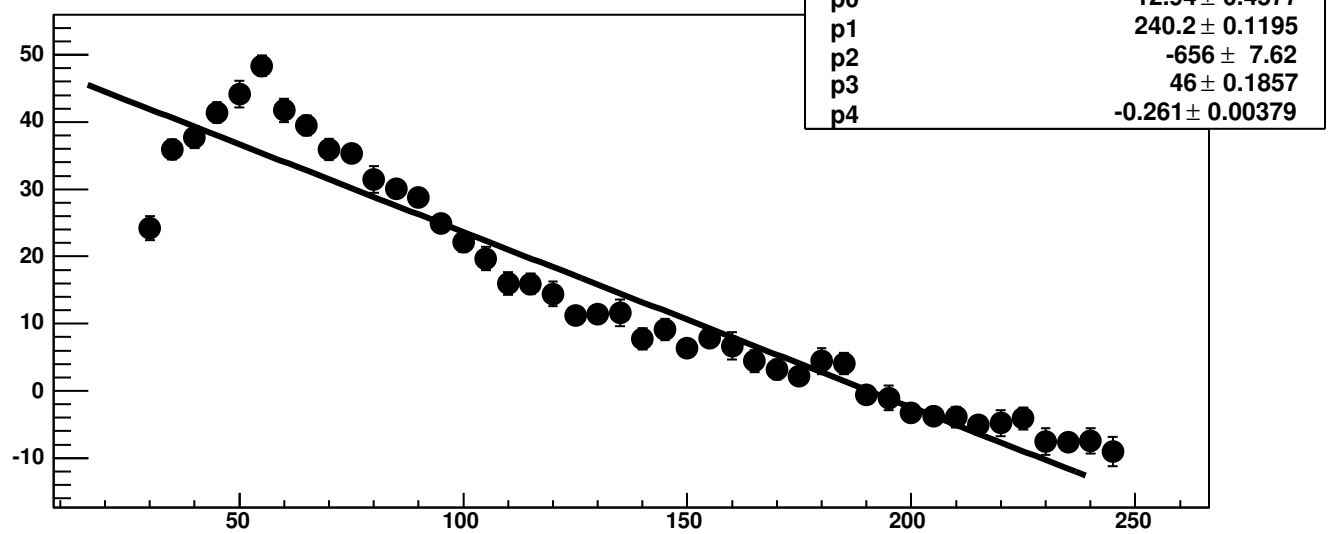
Chip 5, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



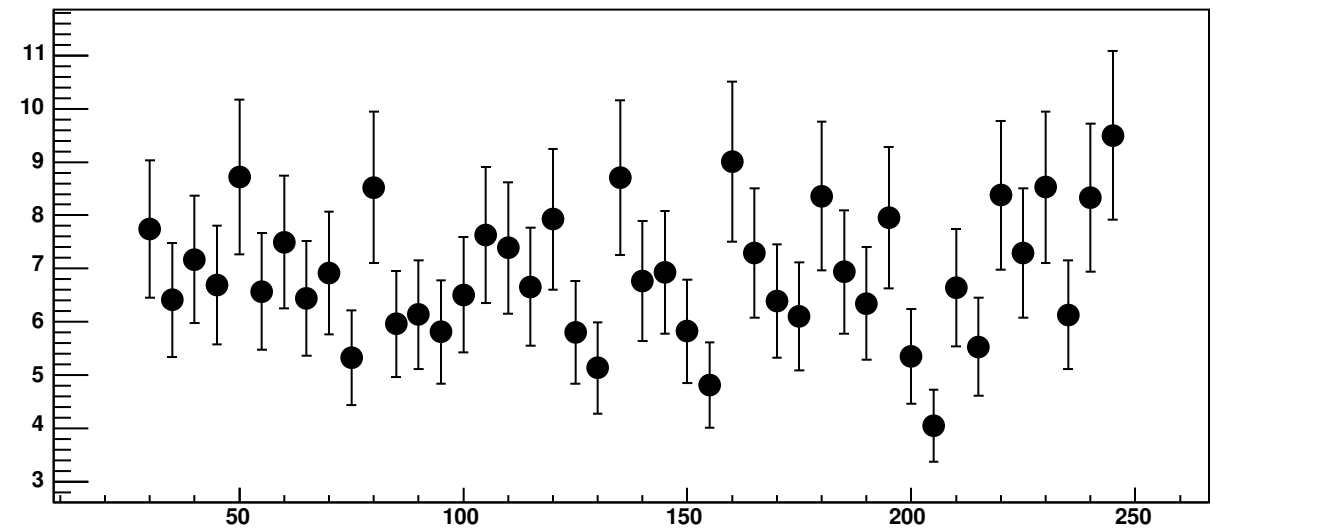
Chip 5, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold



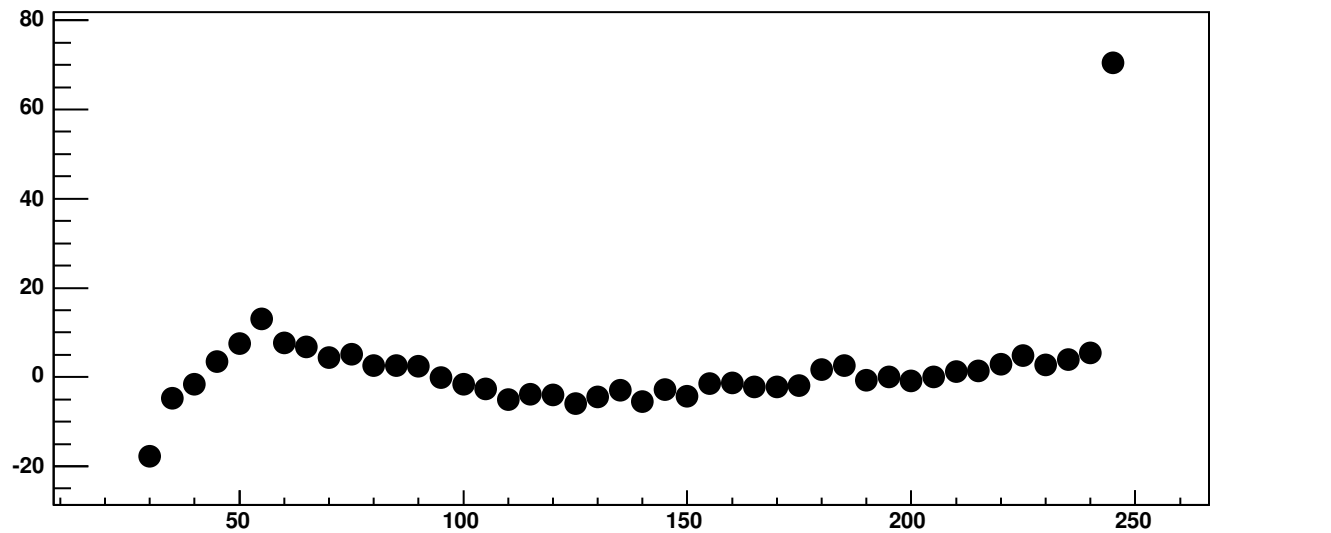
Chip 5, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold



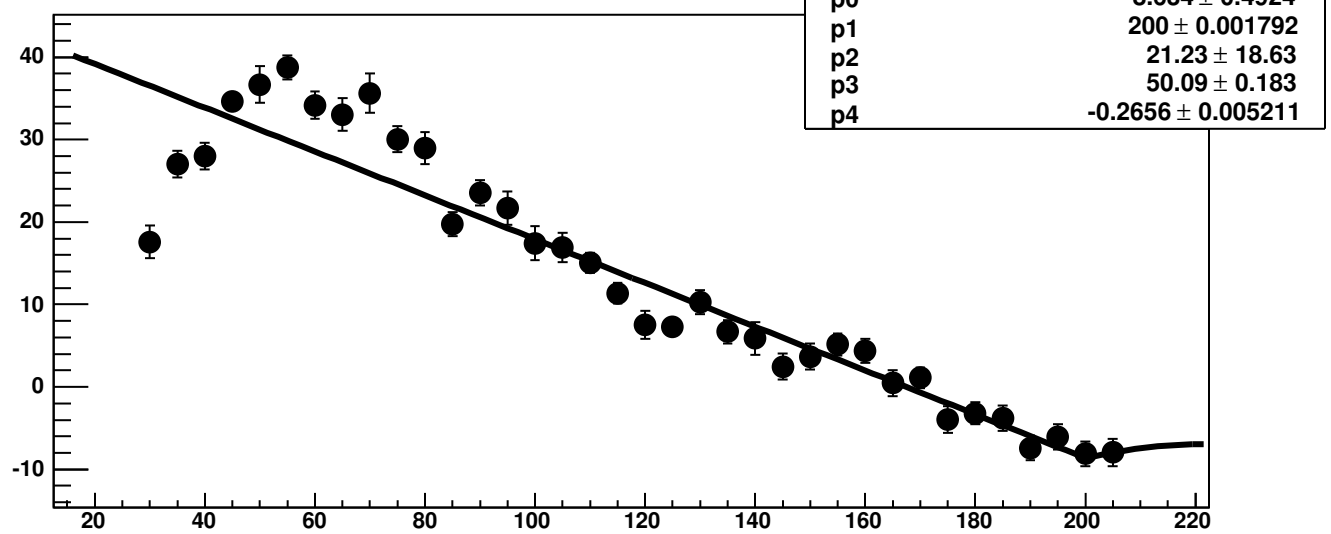
Chip 5, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold



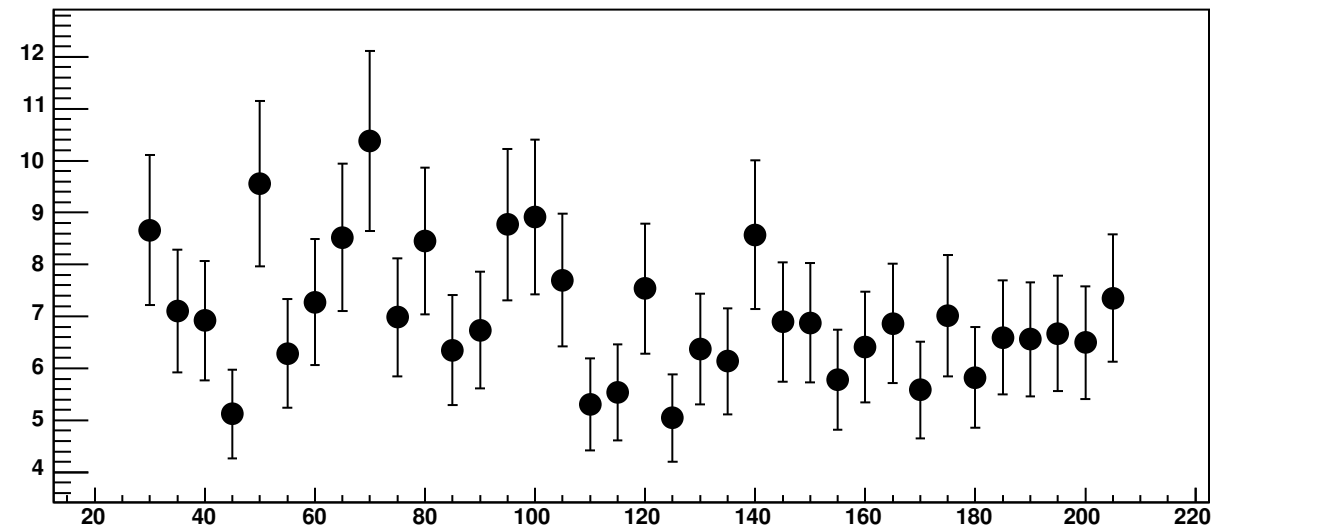
Chip 5, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold



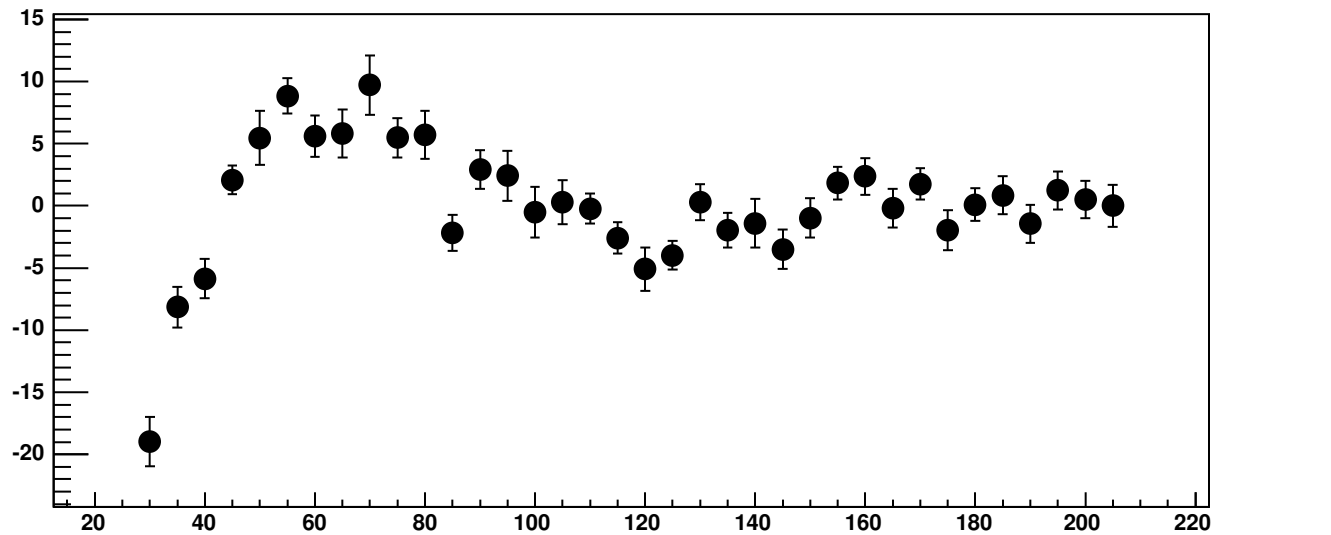
Chip 5, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold



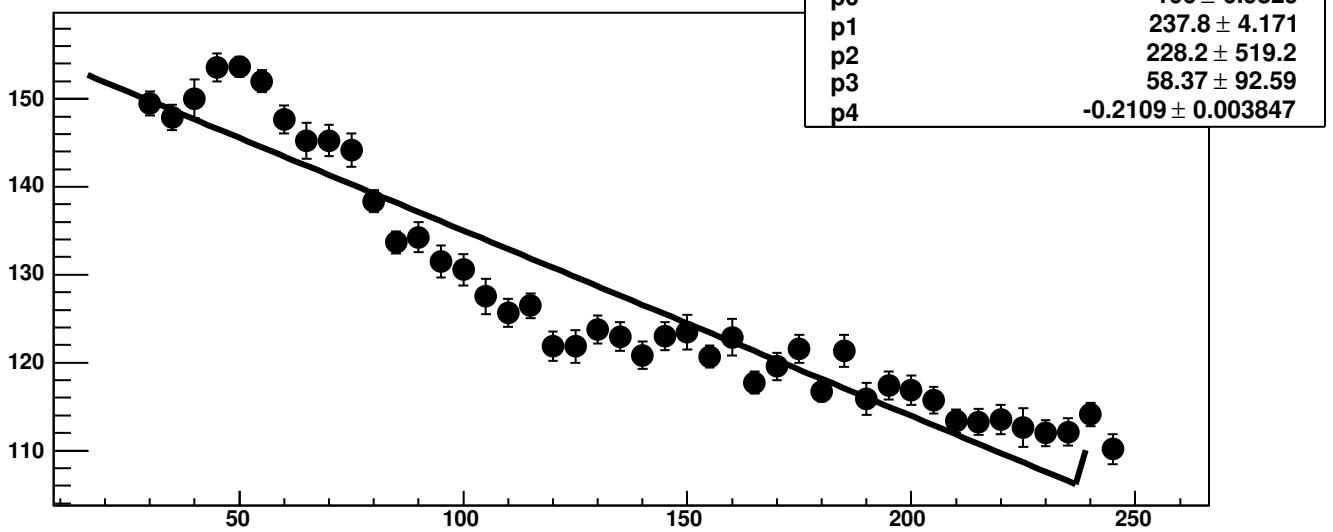
Chip 5, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



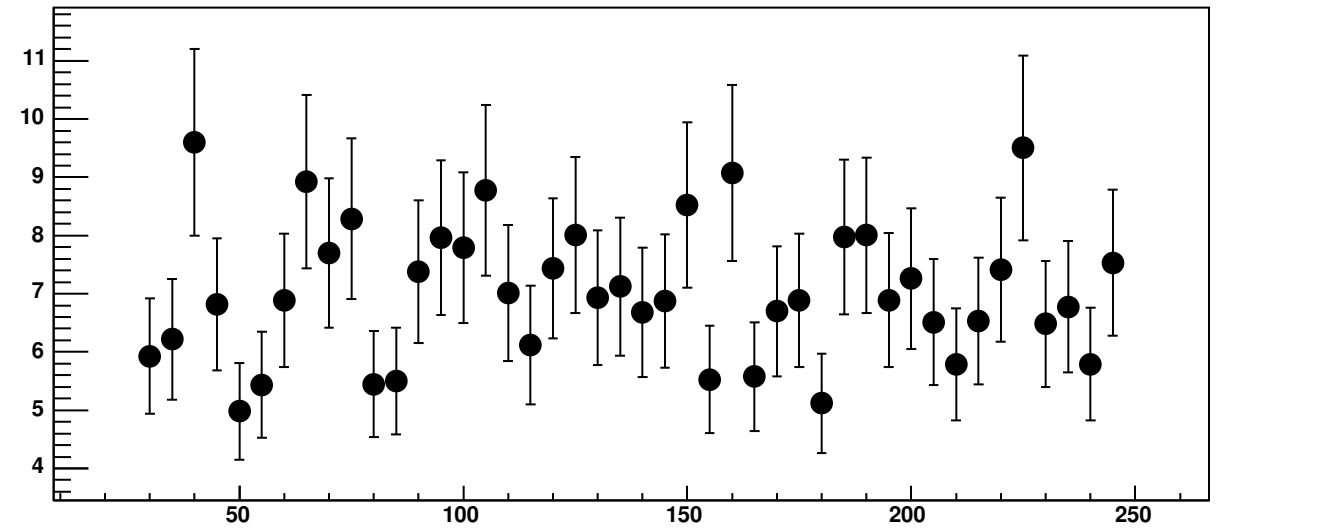
Chip 5, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold



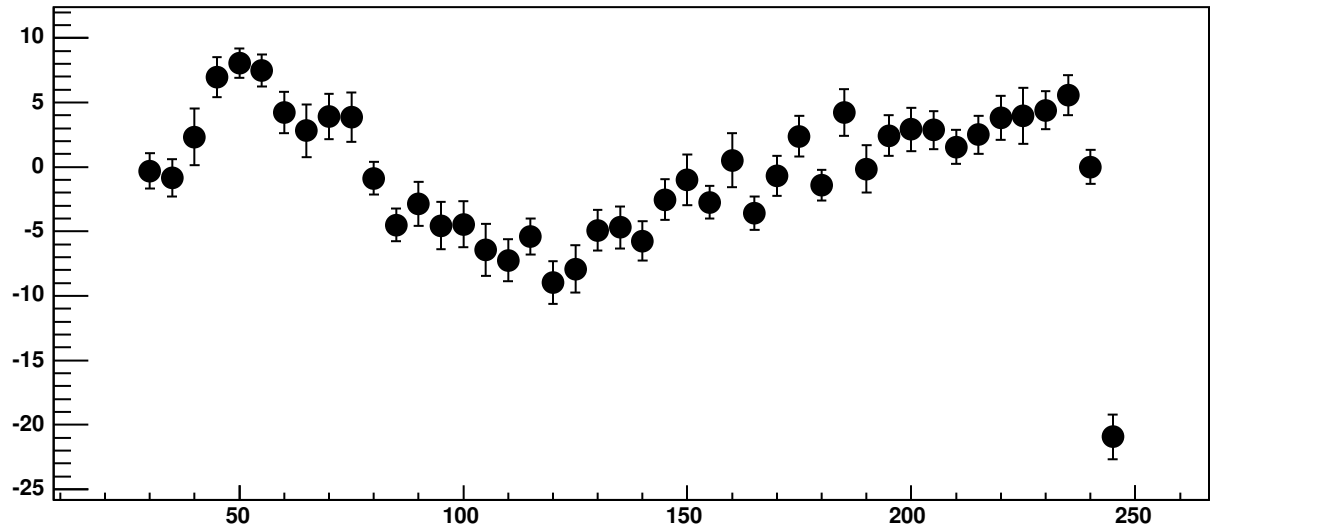
Chip 5, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold



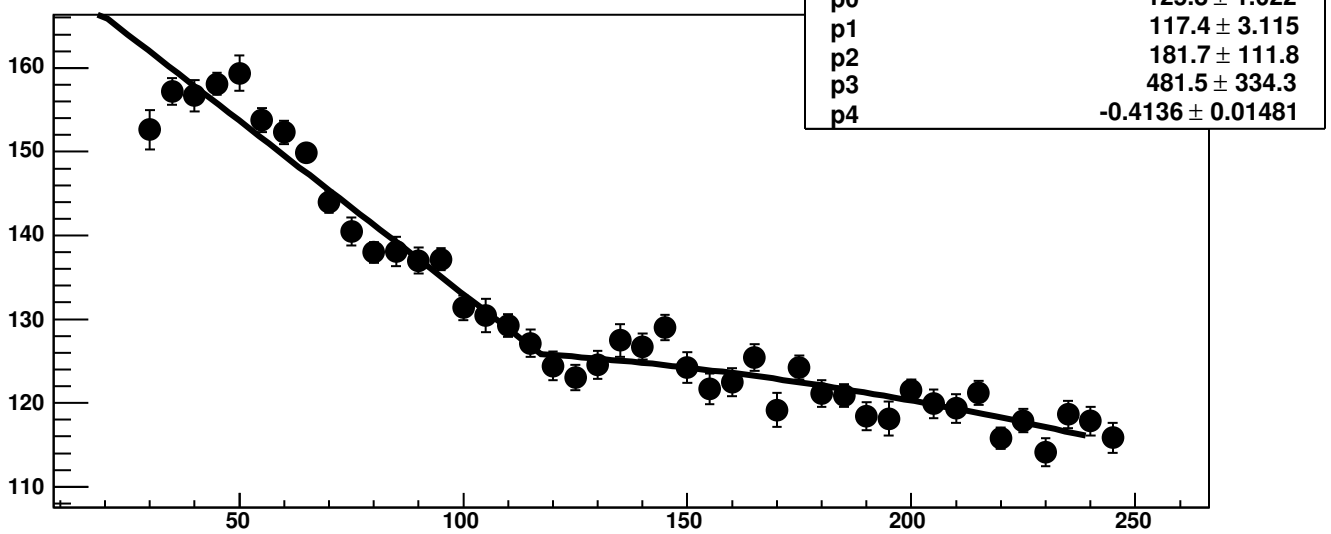
Chip 5, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold



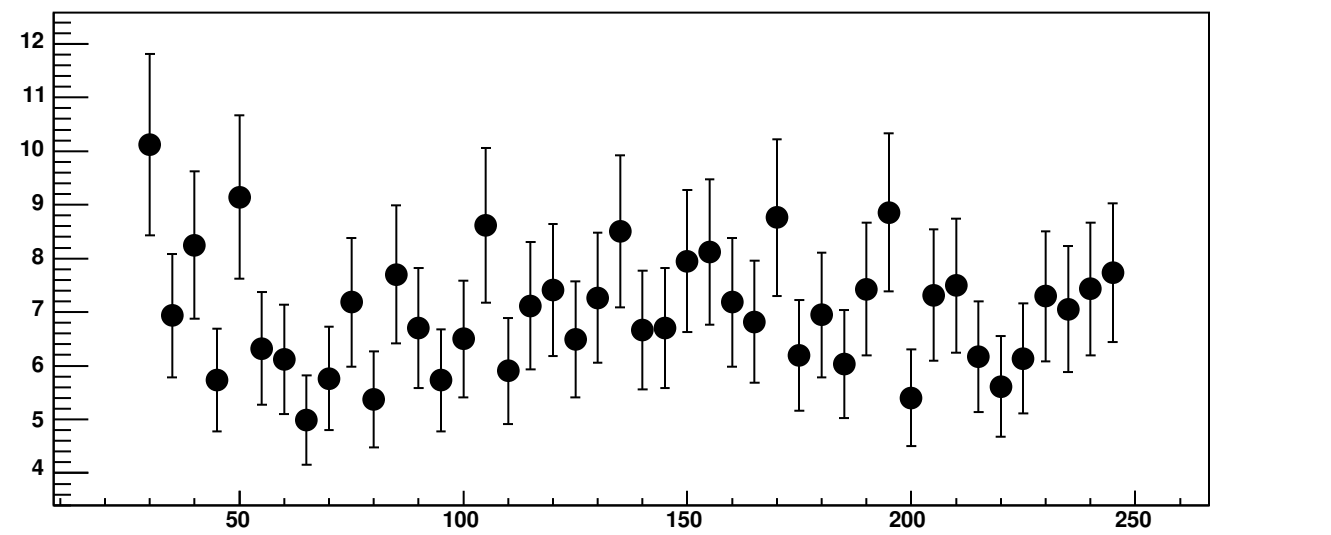
Chip 5, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold



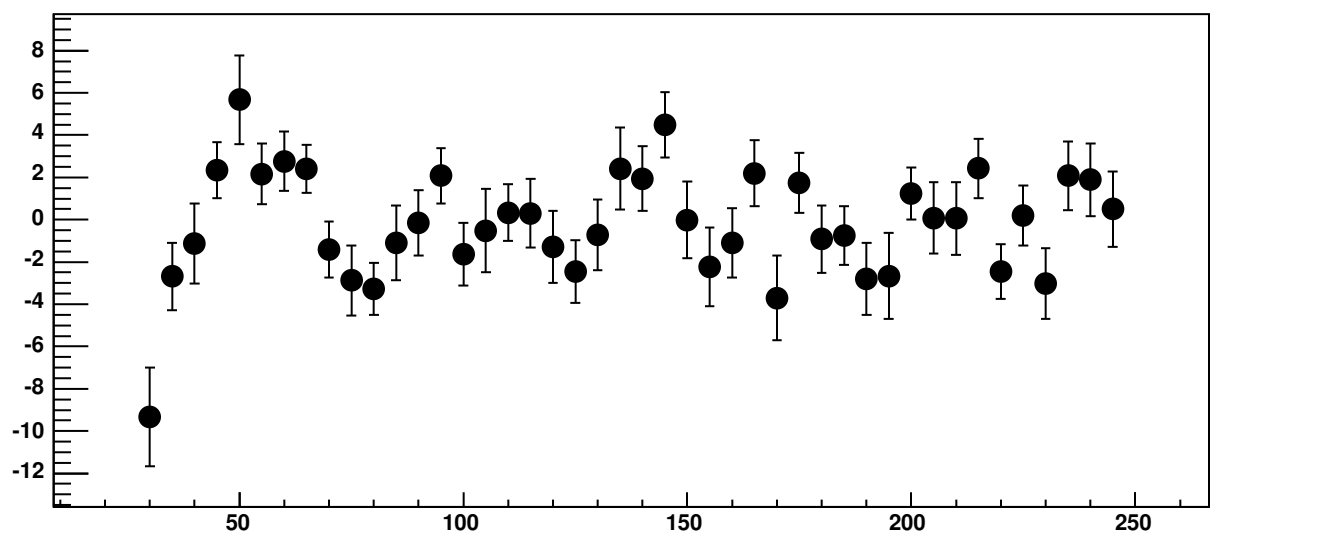
Chip 5, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold



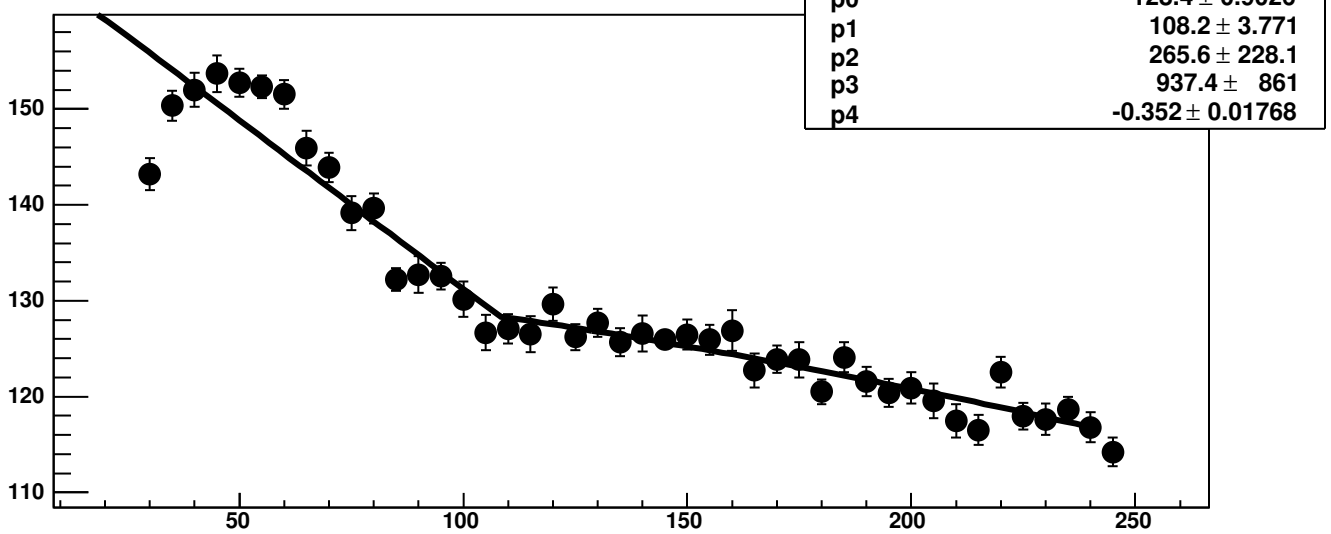
Chip 5, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



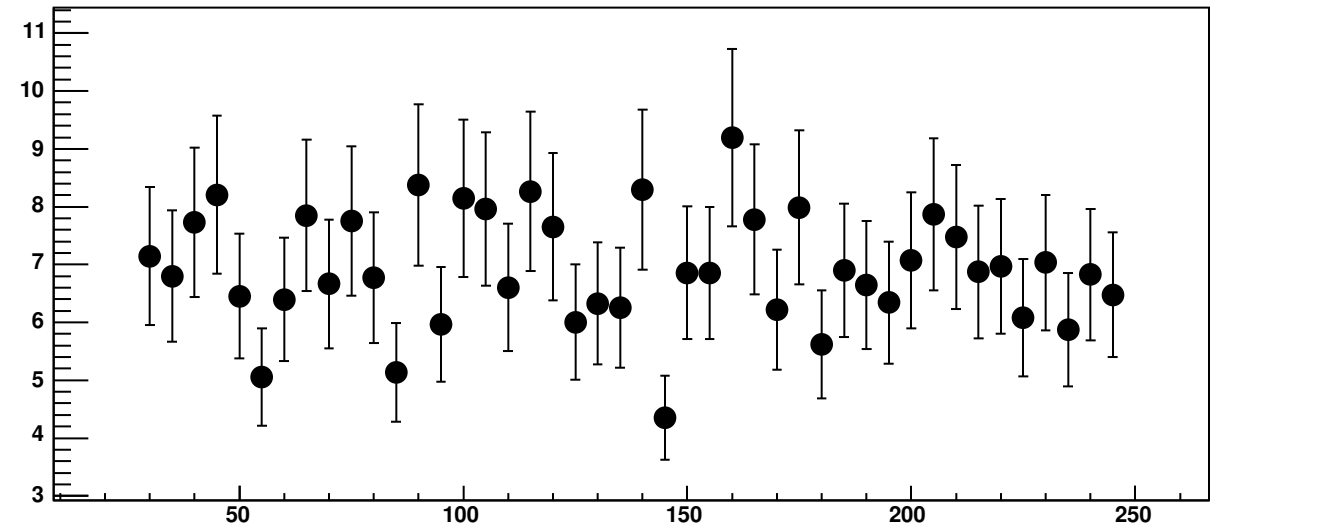
Chip 5, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold



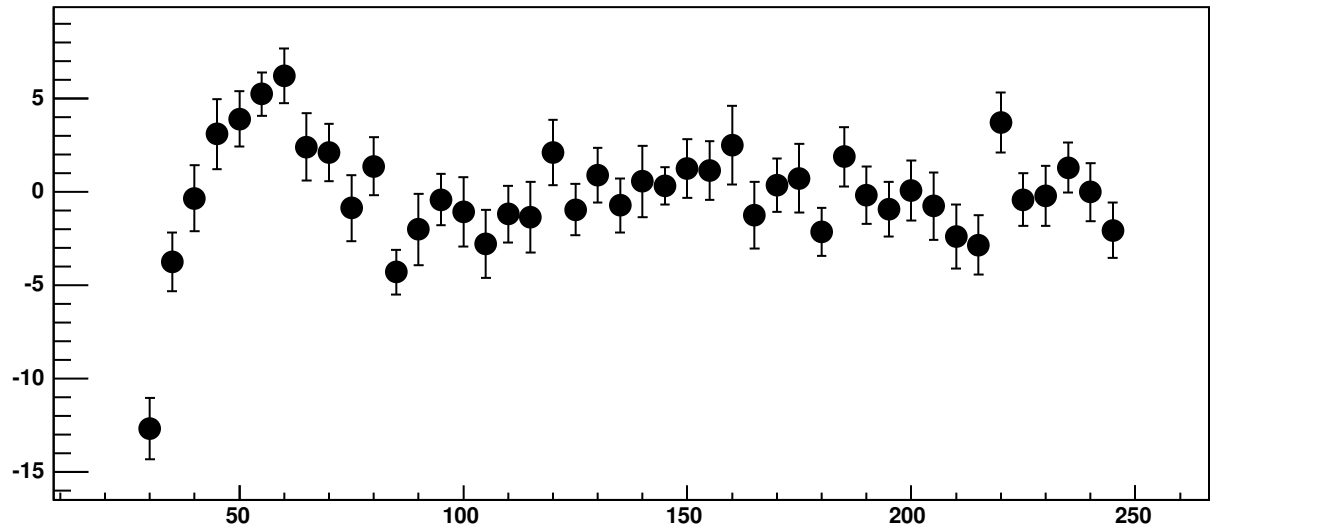
Chip 5, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold

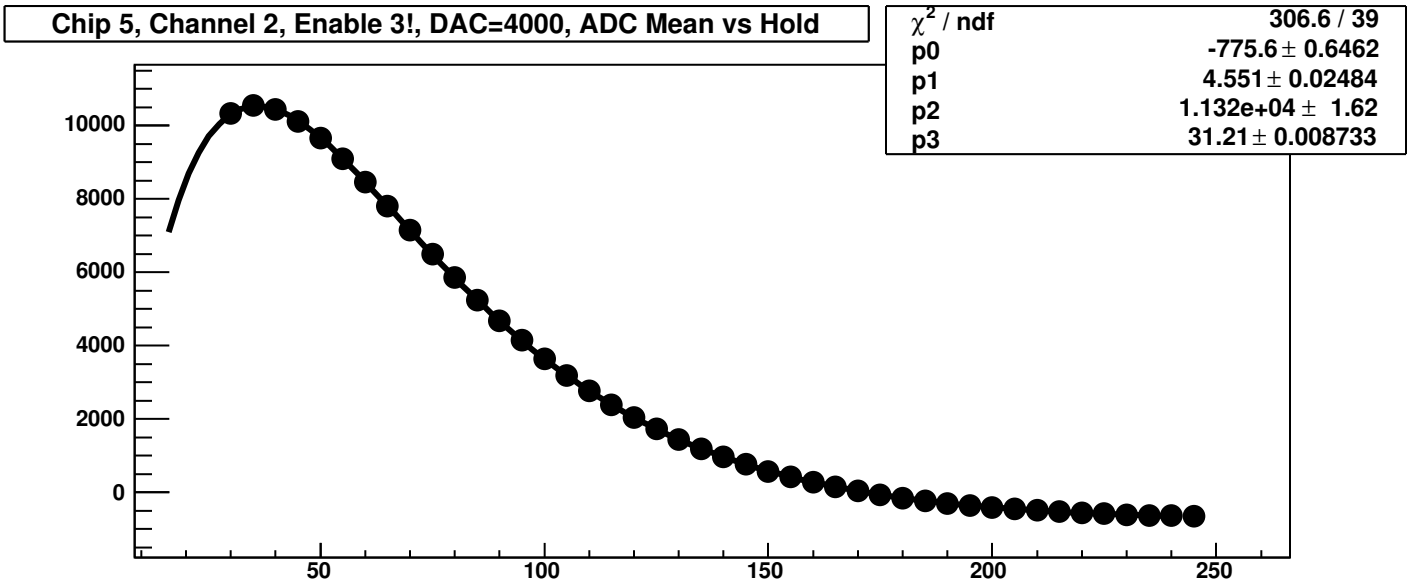


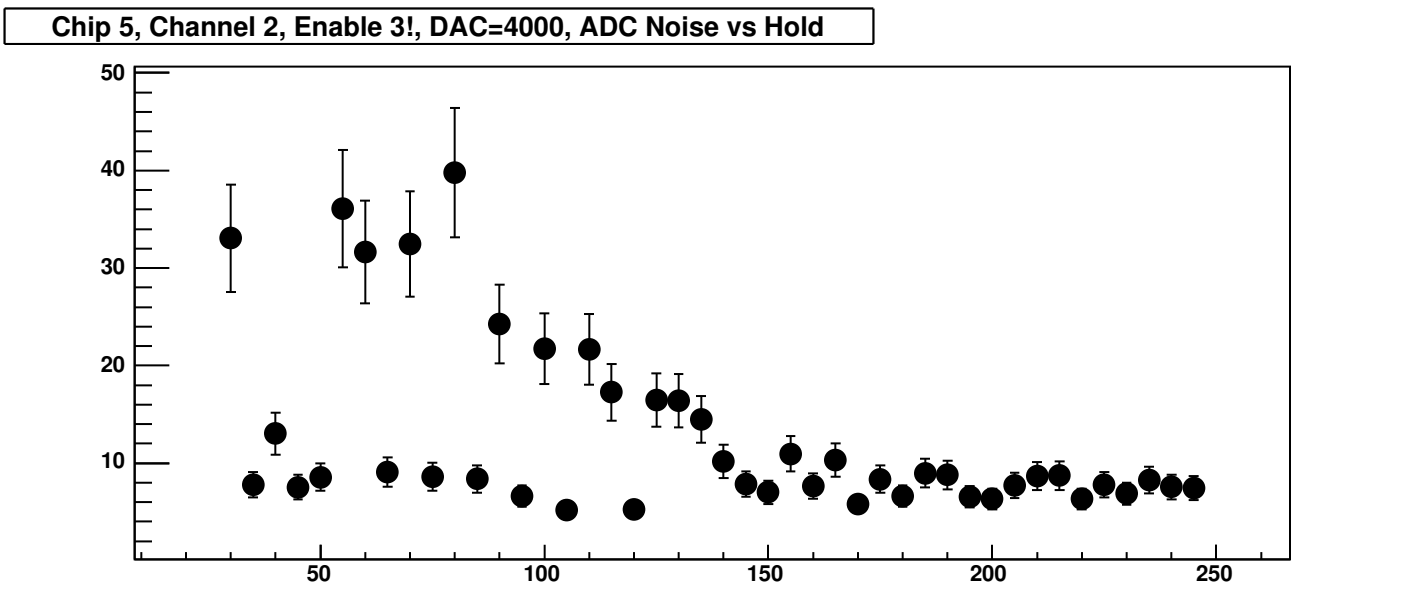
Chip 5, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold

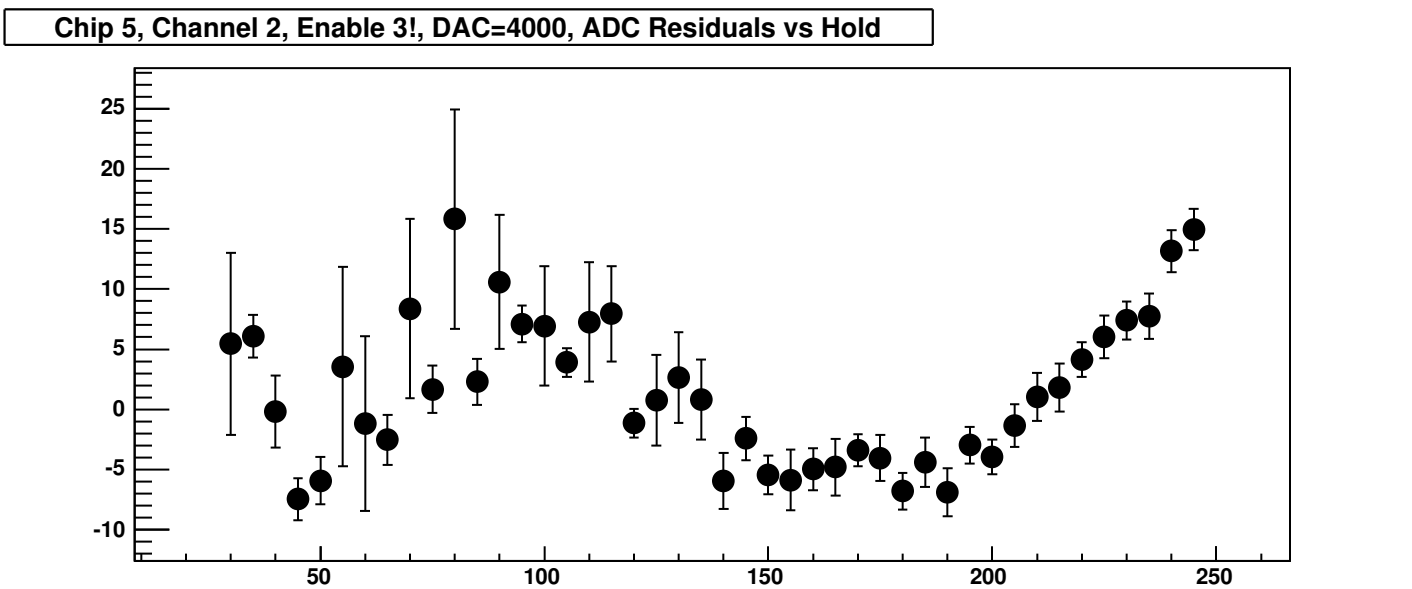


Chip 5, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

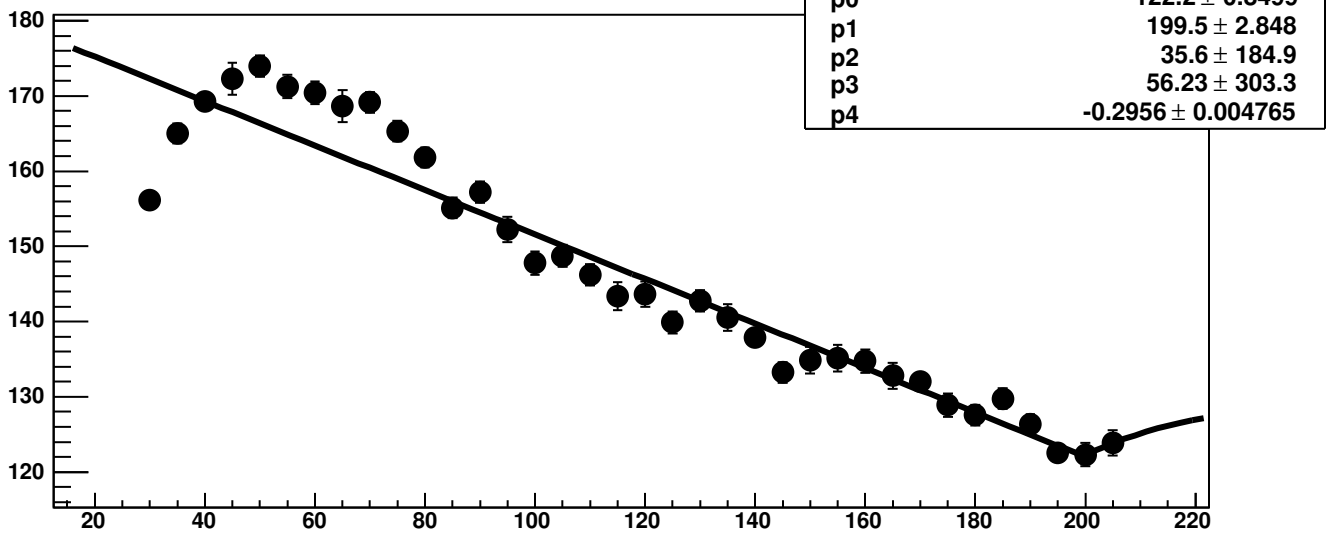



Chip 5, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 306.6 / 39
p0 -775.6 ± 0.6462
p1 4.551 ± 0.02484
p2 1.132e+04 ± 1.62
p3 31.21 ± 0.008733
10000
8000
6000
4000
2000
0
50
100
150
200
250

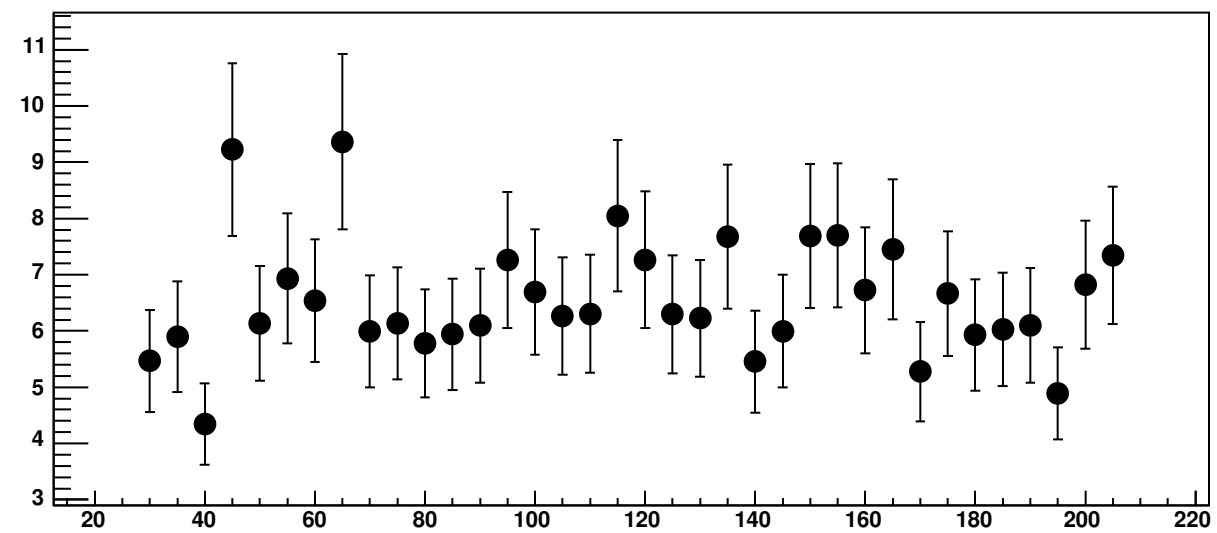

Chip 5, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
50
100
150
200
250


Chip 5, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
50
100
150
200
250

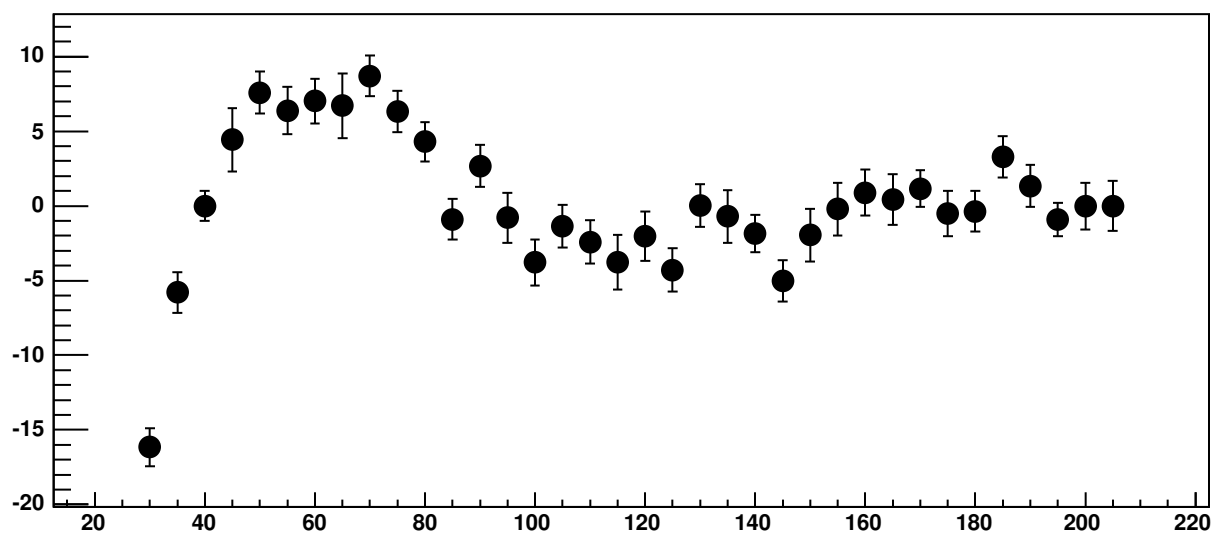
Chip 5, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold



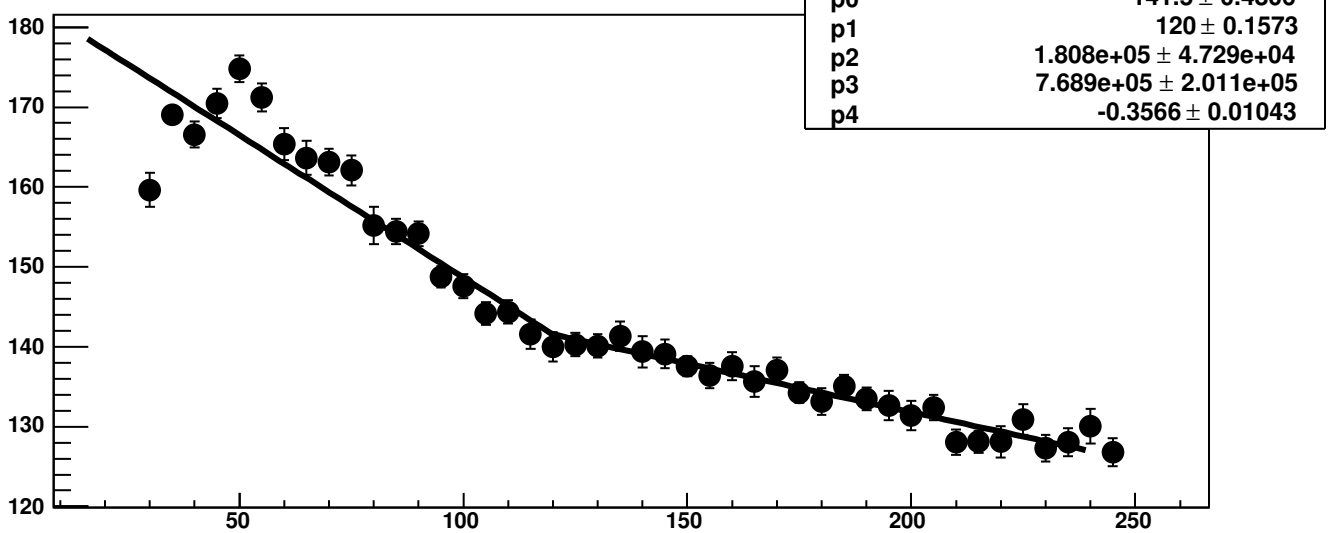
Chip 5, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



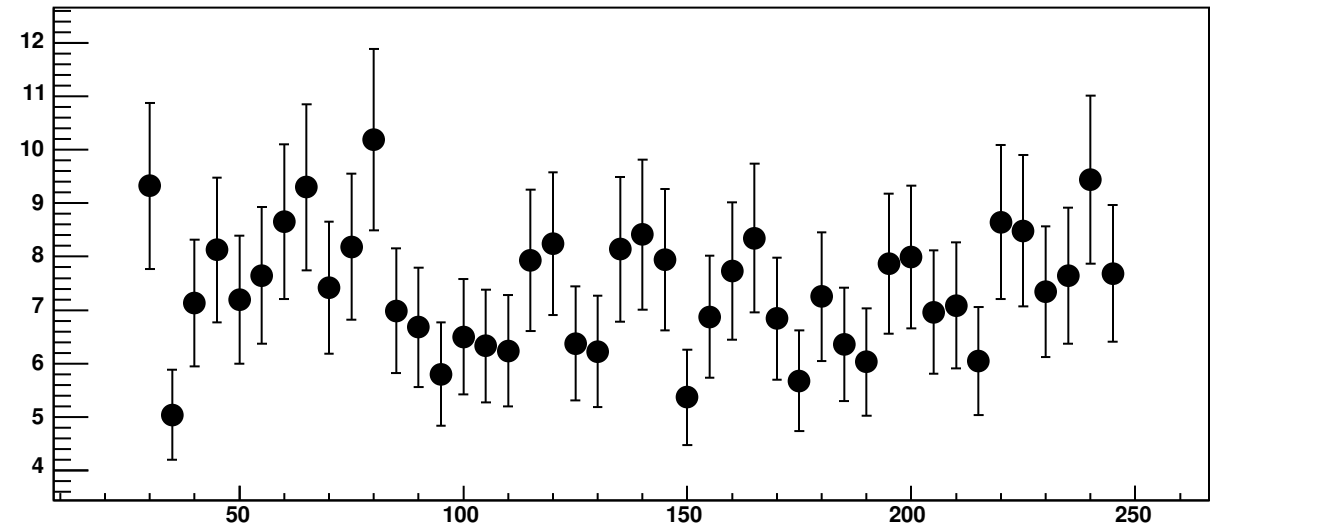
Chip 5, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold



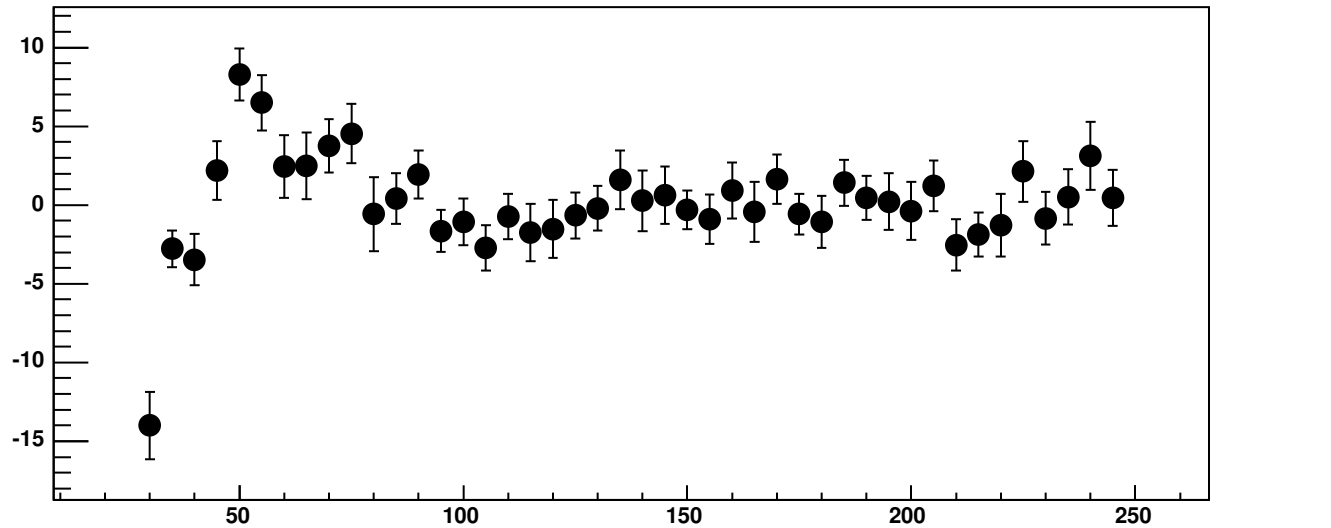
Chip 5, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold



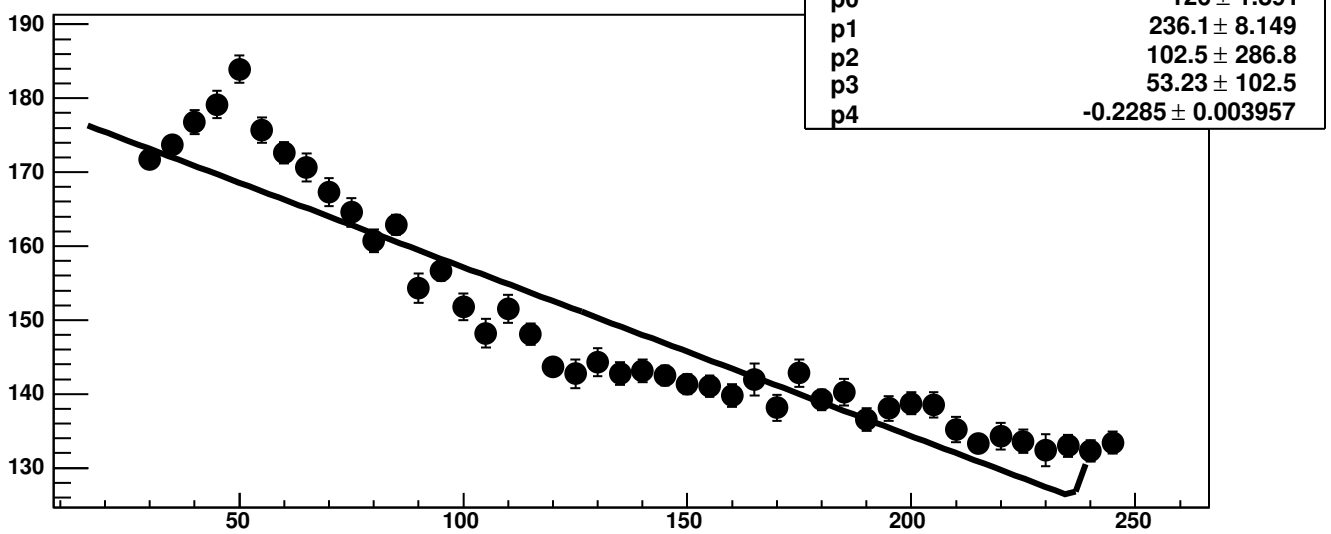
Chip 5, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



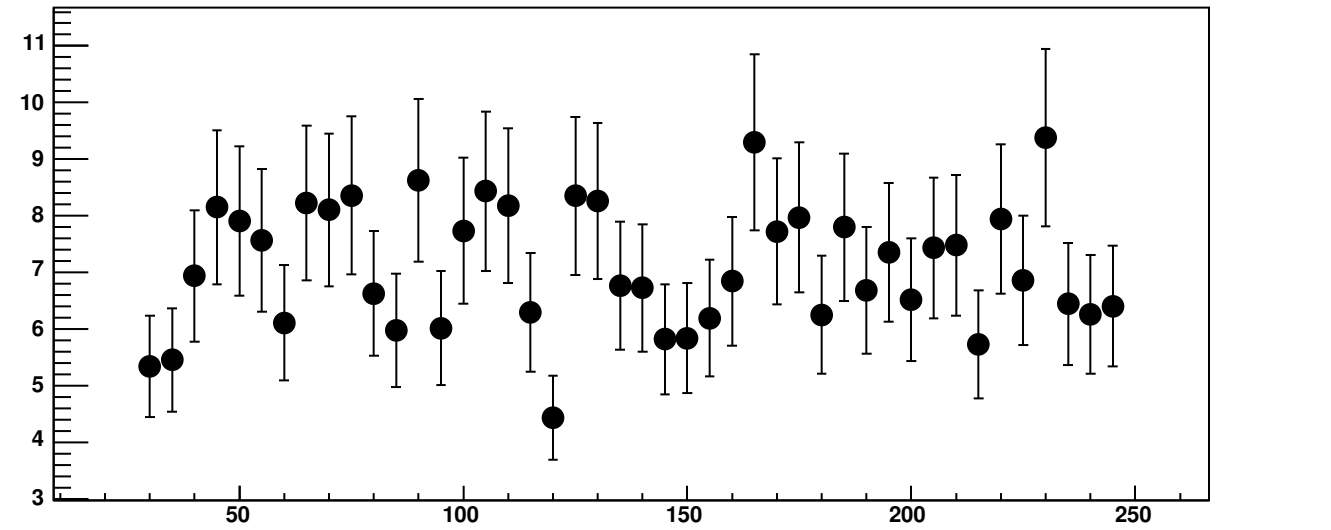
Chip 5, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold



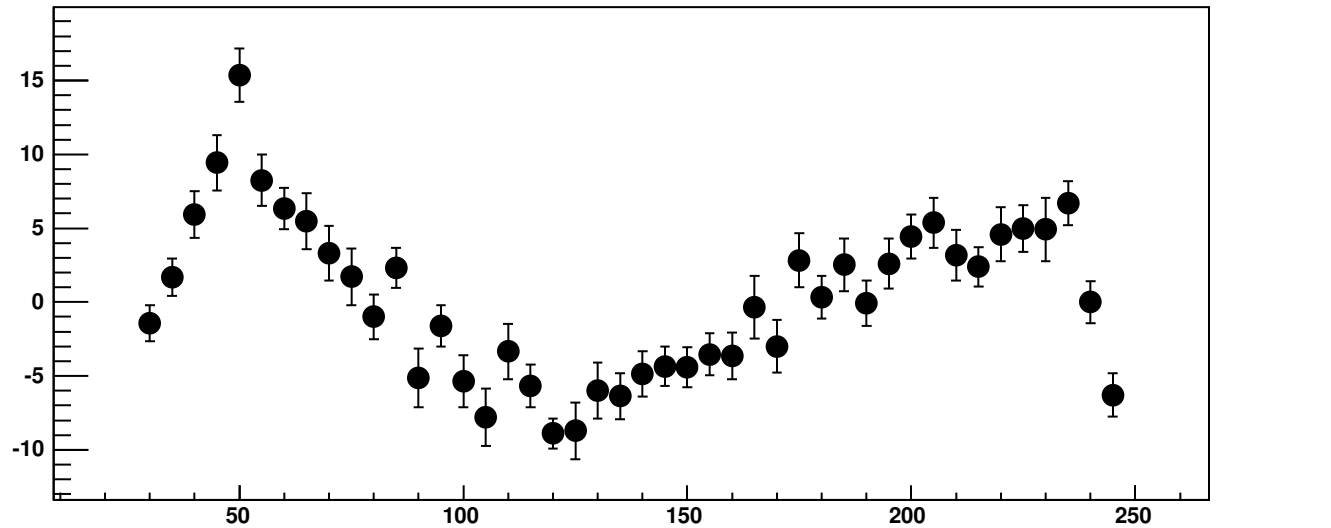
Chip 5, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold



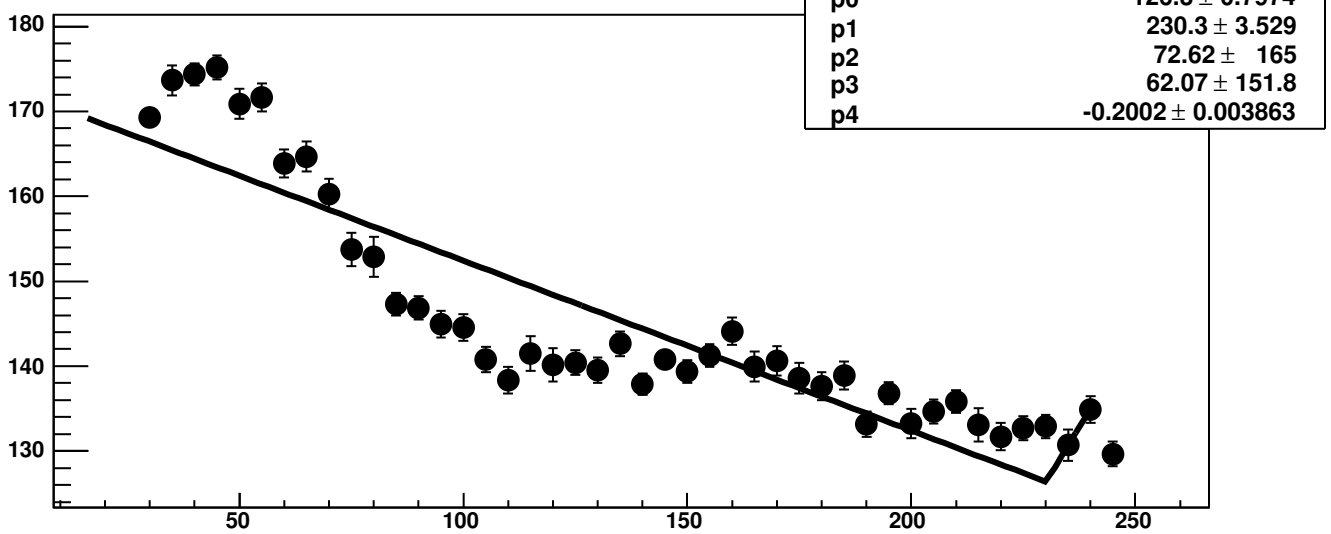
Chip 5, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold



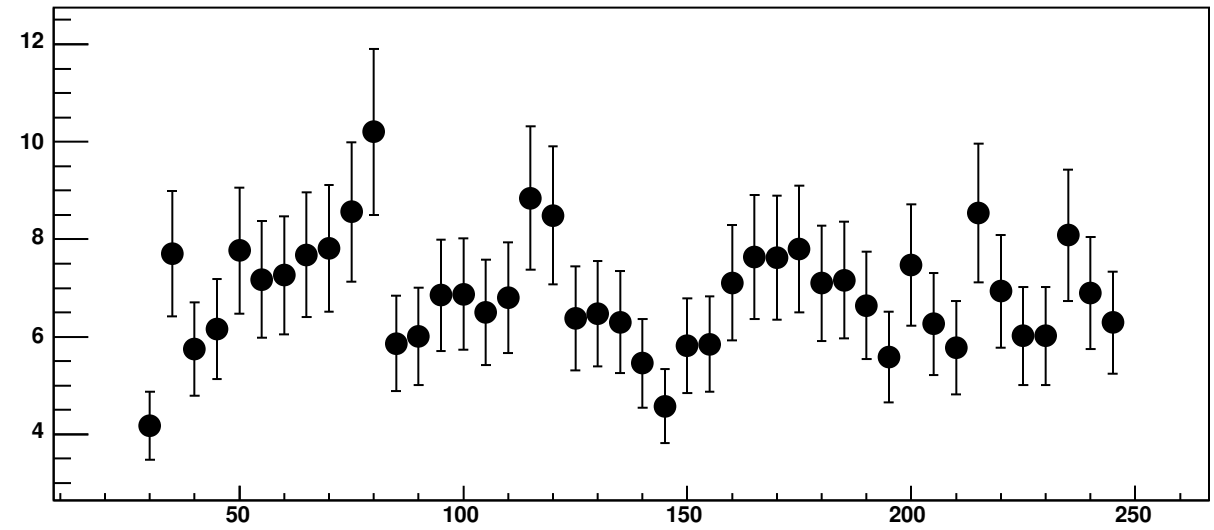
Chip 5, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold



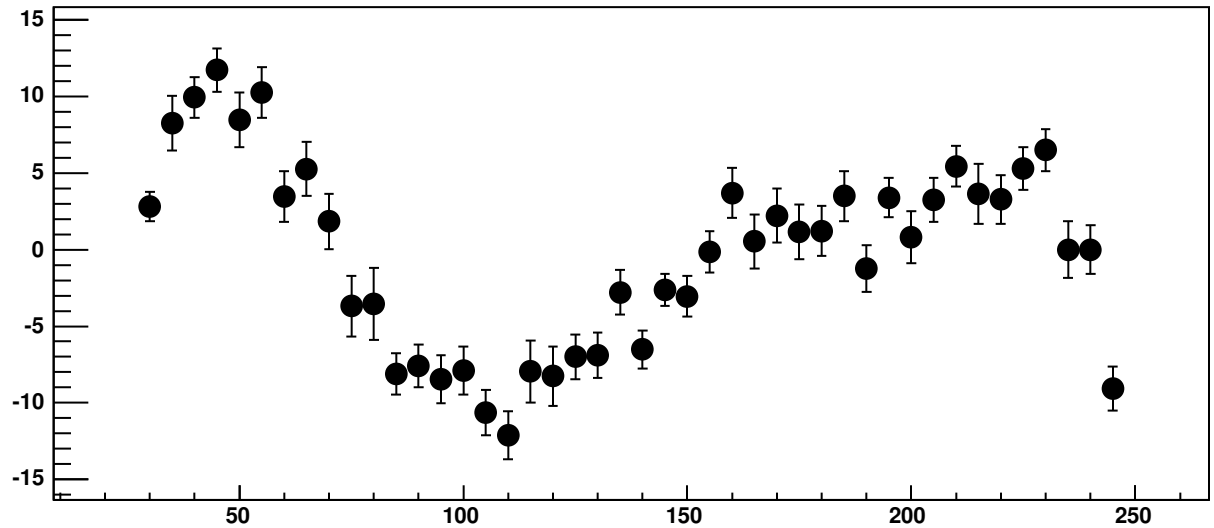
Chip 5, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold



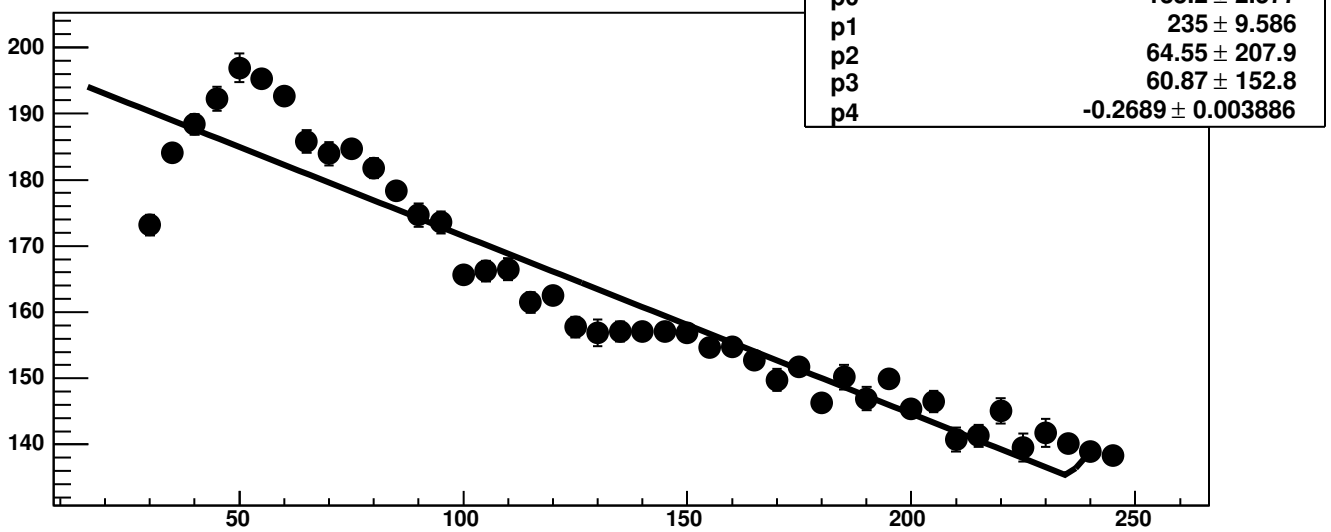
Chip 5, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



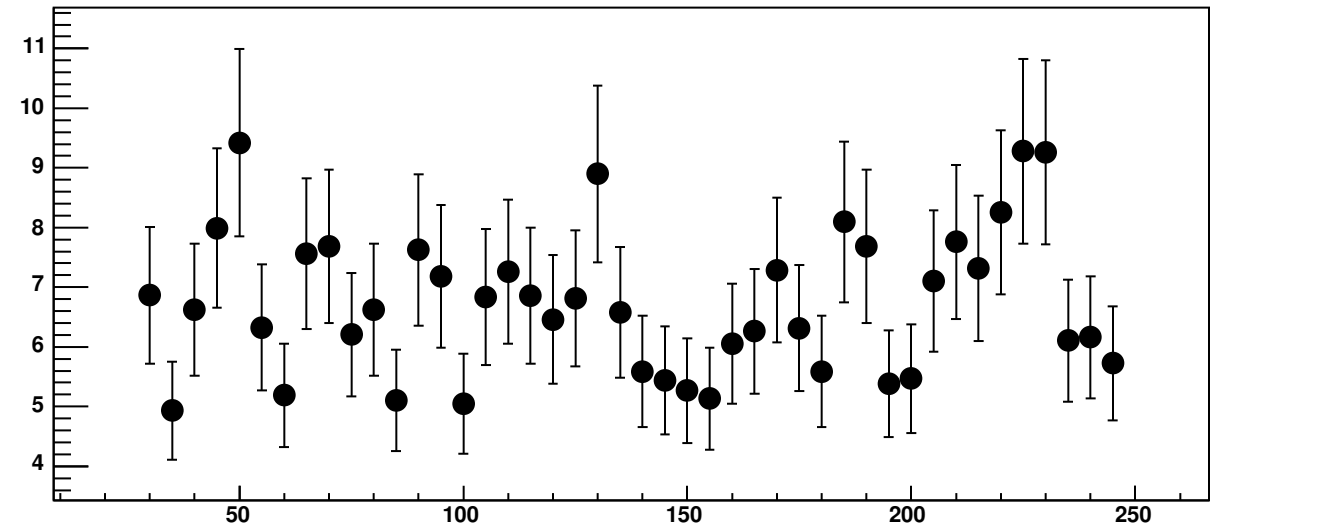
Chip 5, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold



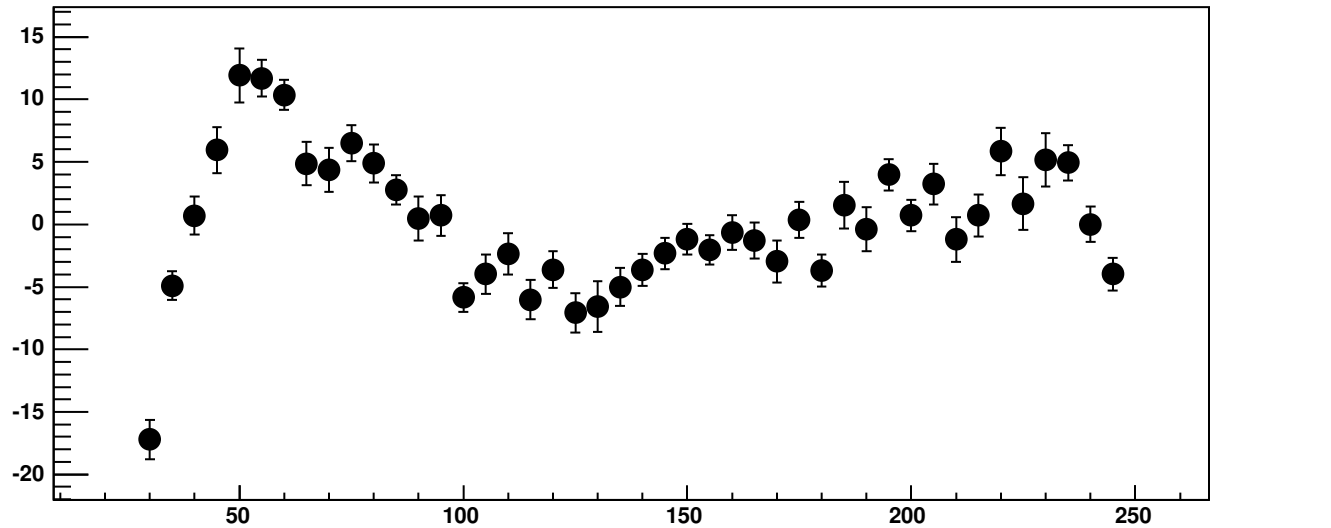
Chip 5, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

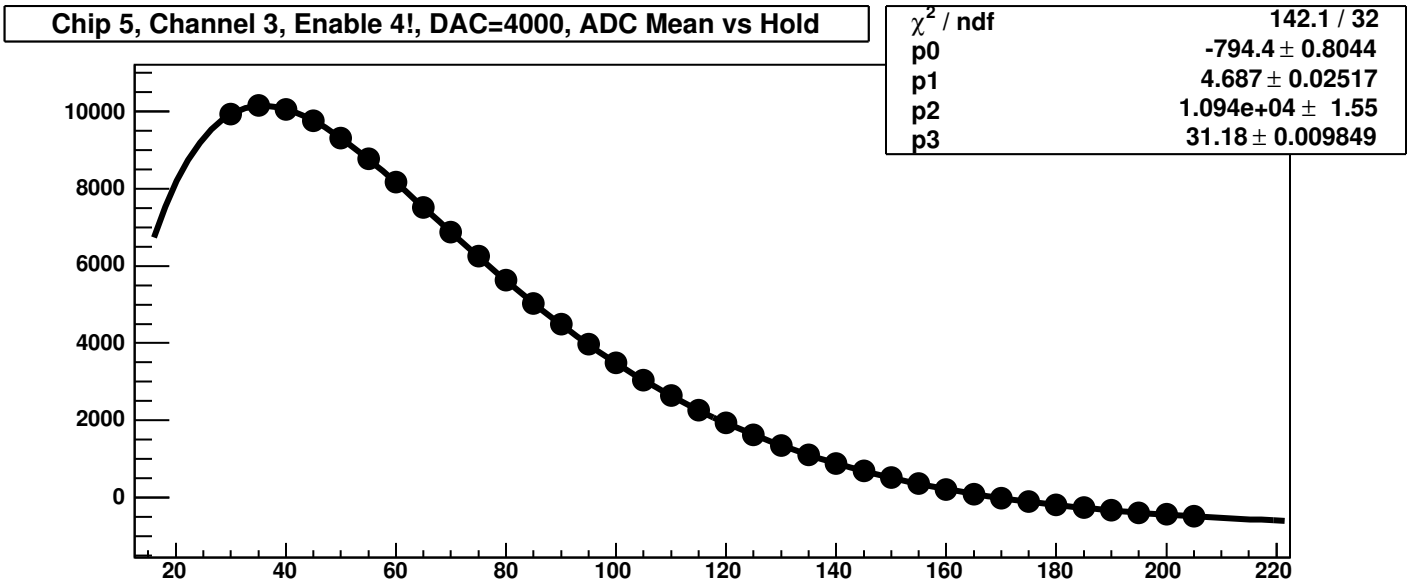


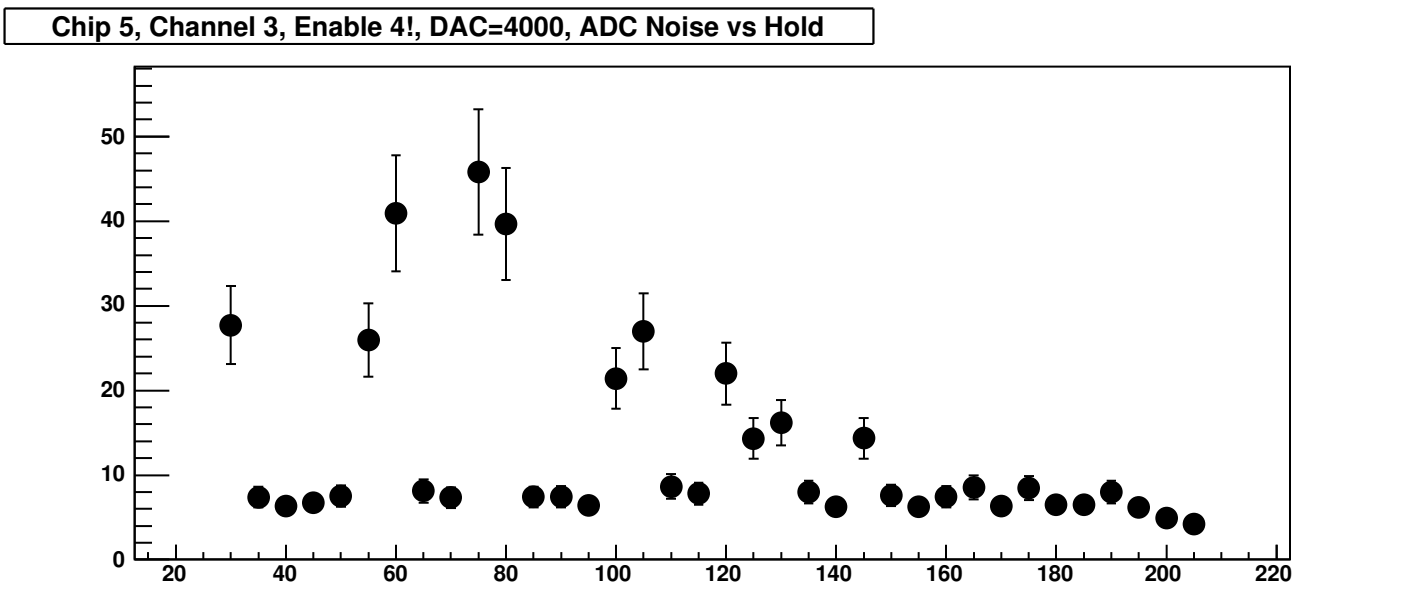
Chip 5, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold

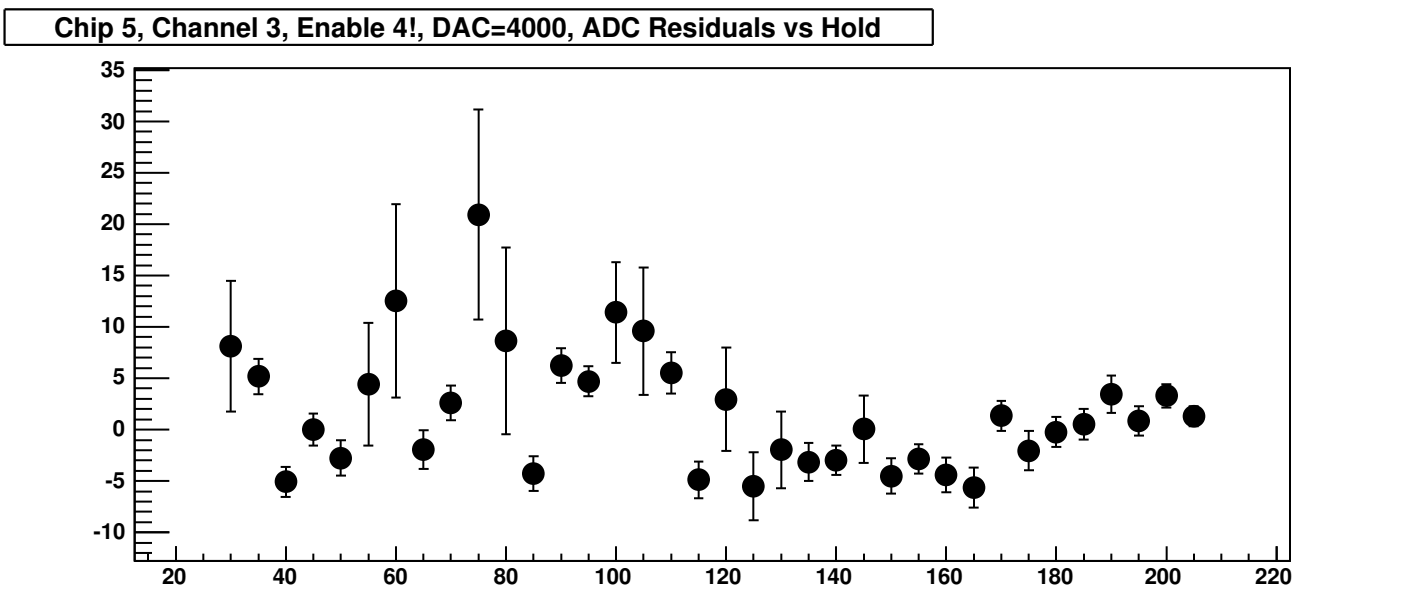


Chip 5, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

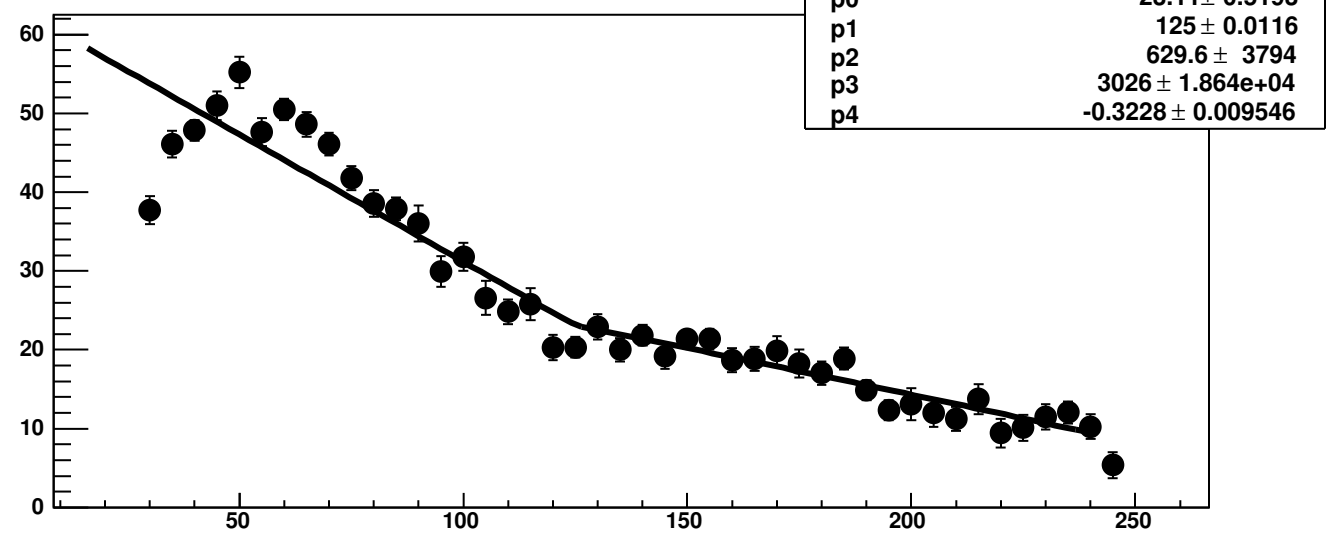



Chip 5, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 142.1 / 32
p0 -794.4 ± 0.8044
p1 4.687 ± 0.02517
p2 1.094e+04 ± 1.55
p3 31.18 ± 0.009849

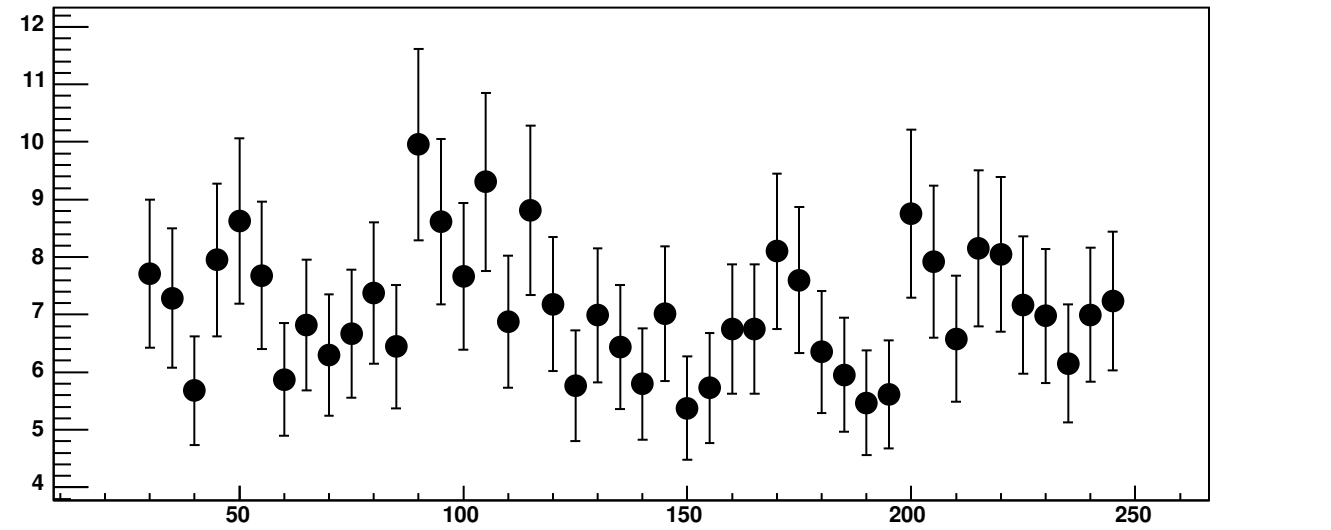

Chip 5, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold


Chip 5, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold

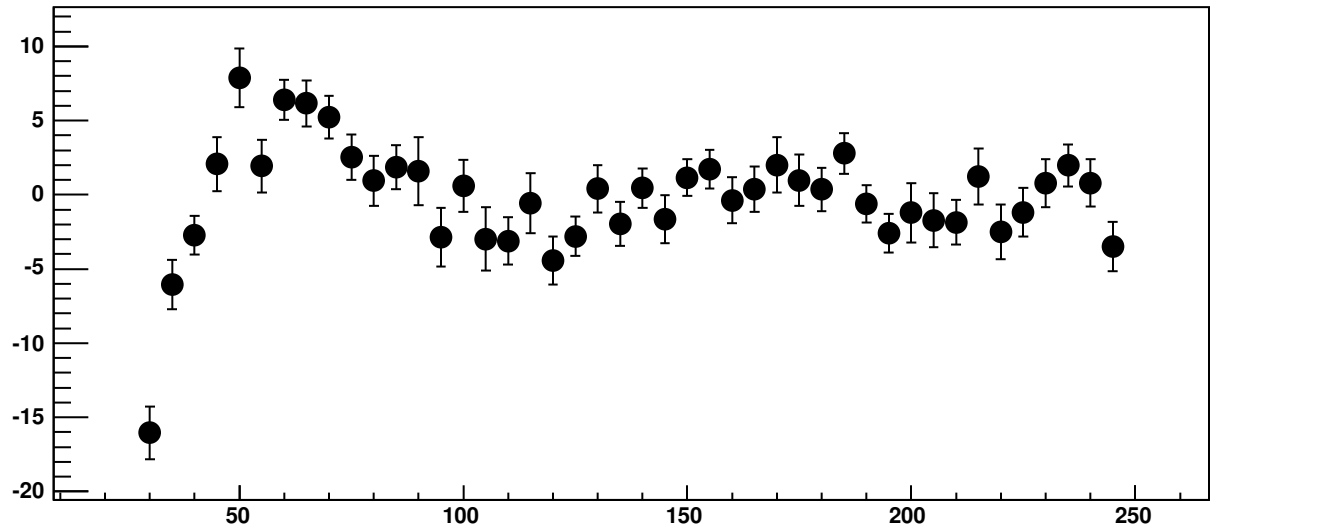
Chip 5, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

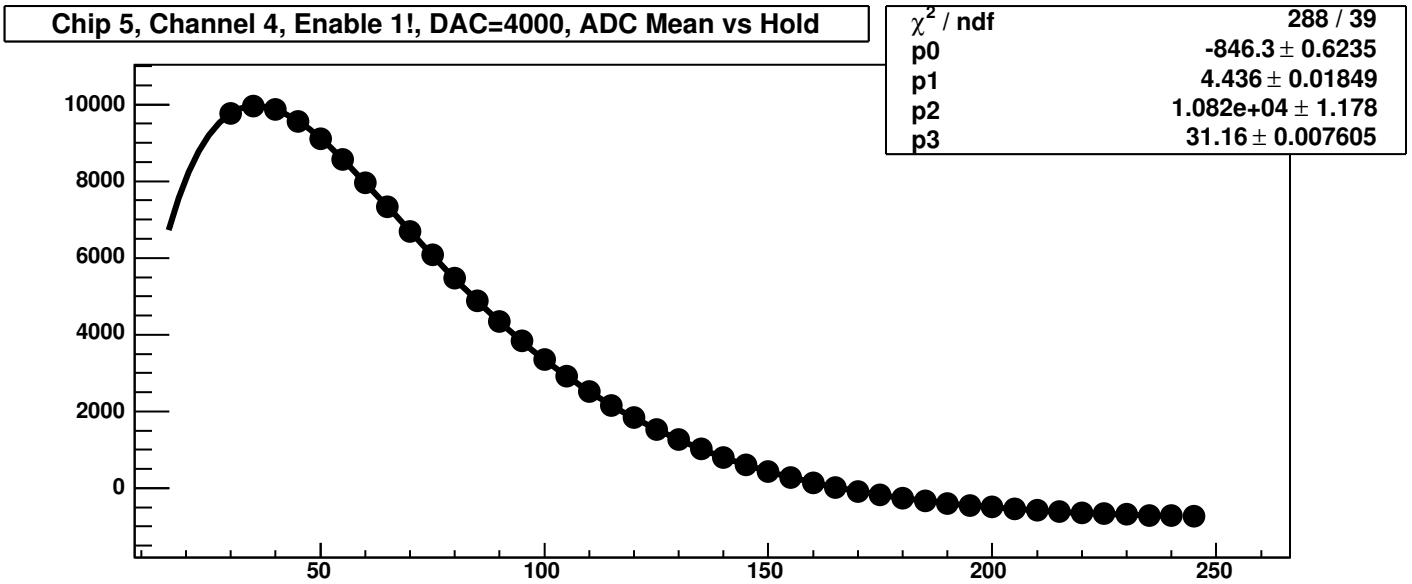


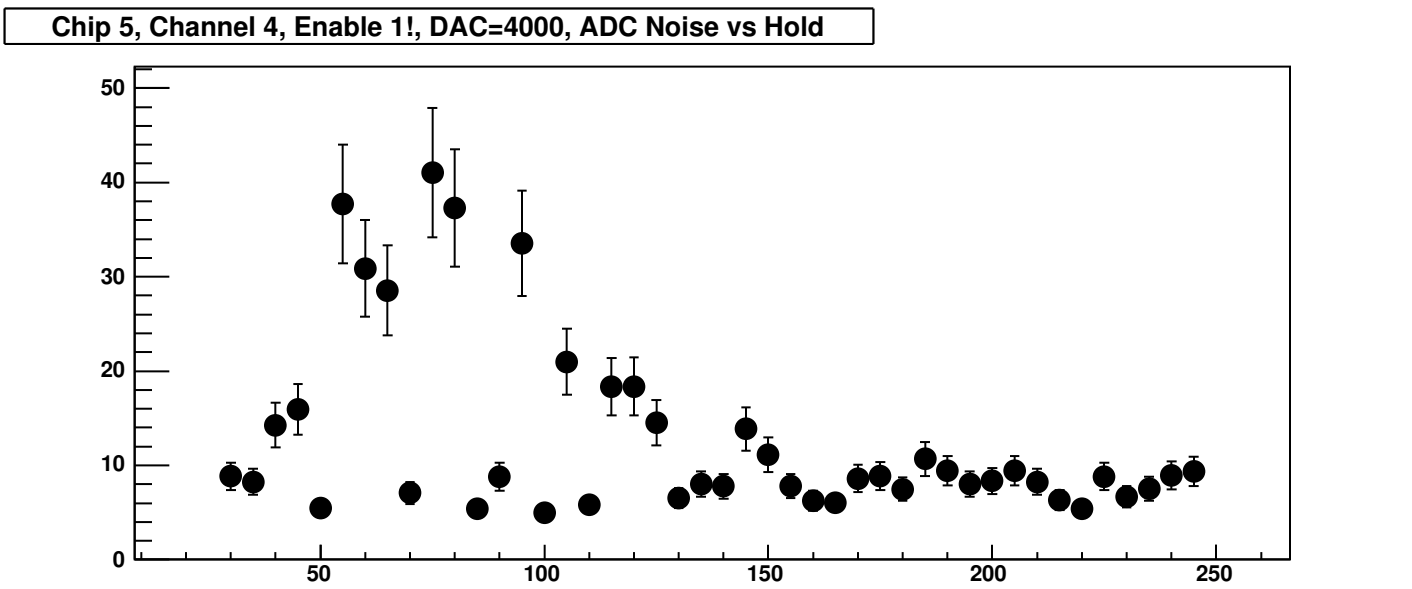
Chip 5, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold

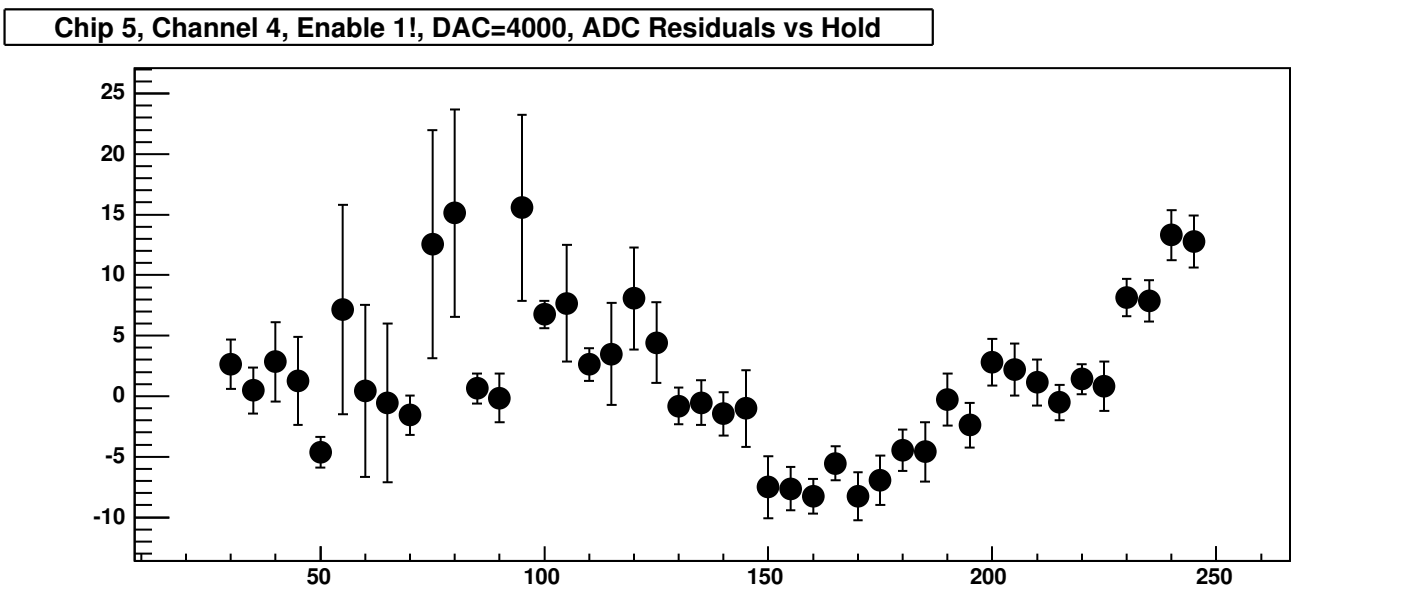


Chip 5, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold



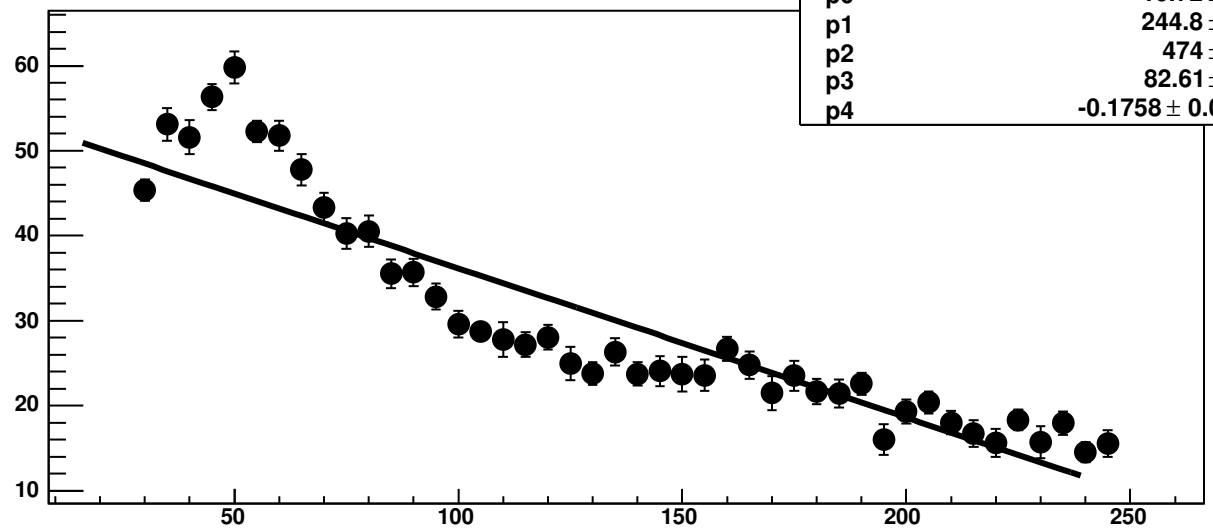

Chip 5, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 288 / 39
p0 -846.3 ± 0.6235
p1 4.436 ± 0.01849
p2 1.082e+04 ± 1.178
p3 31.16 ± 0.007605
10000
8000
6000
4000
2000
0
50
100
150
200
250


Chip 5, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
0
50
100
150
200
250

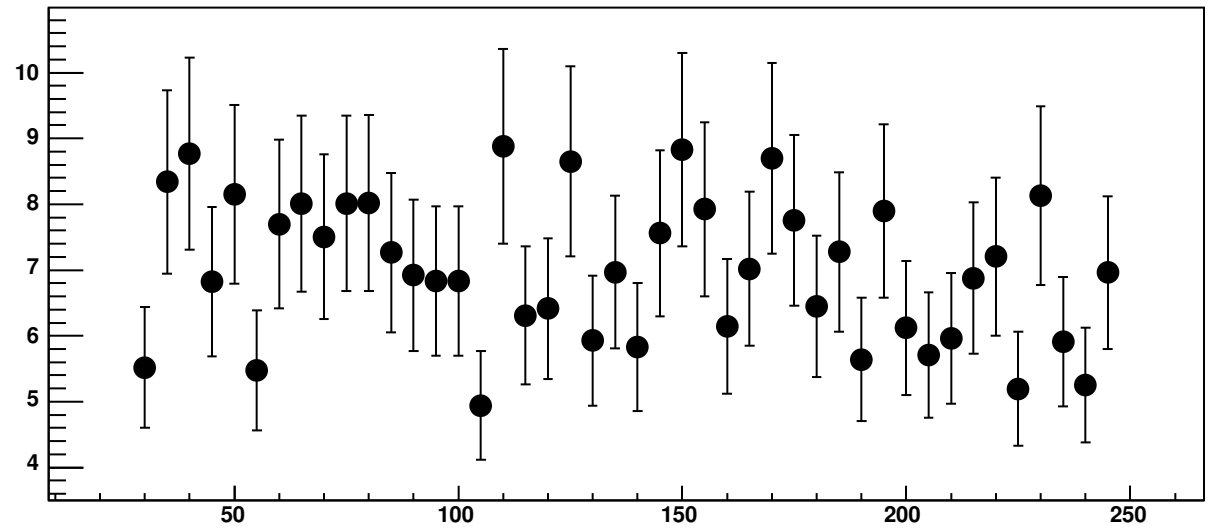

Chip 5, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
50
100
150
200
250

Chip 5, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold

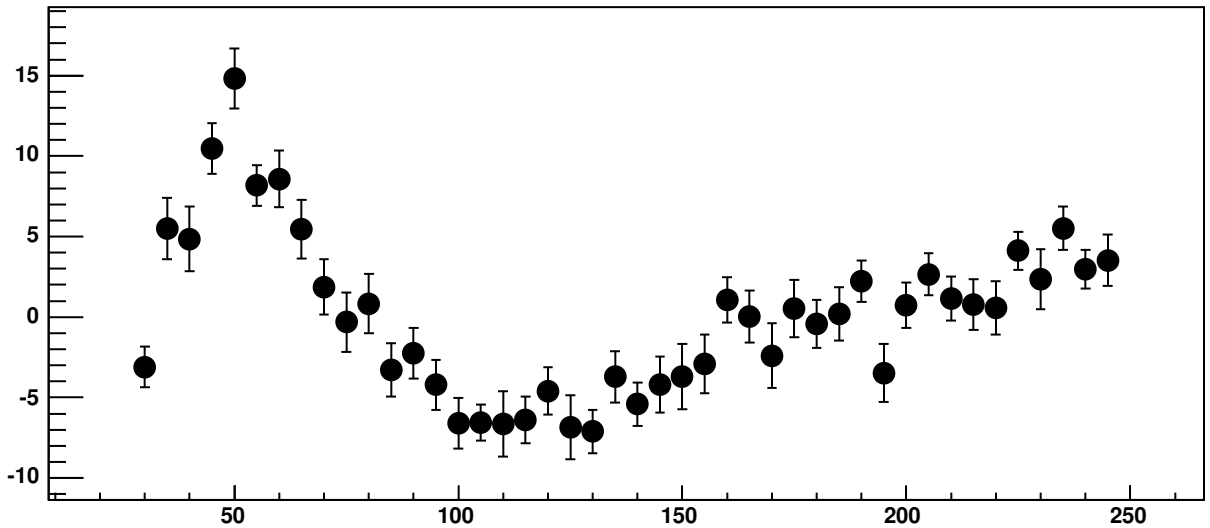
χ^2 / ndf	431.1 / 38
p0	10.72 ± 0.236
p1	244.8 ± 1.342
p2	474 ± 84.85
p3	82.61 ± 1.414
p4	-0.1758 ± 0.001934



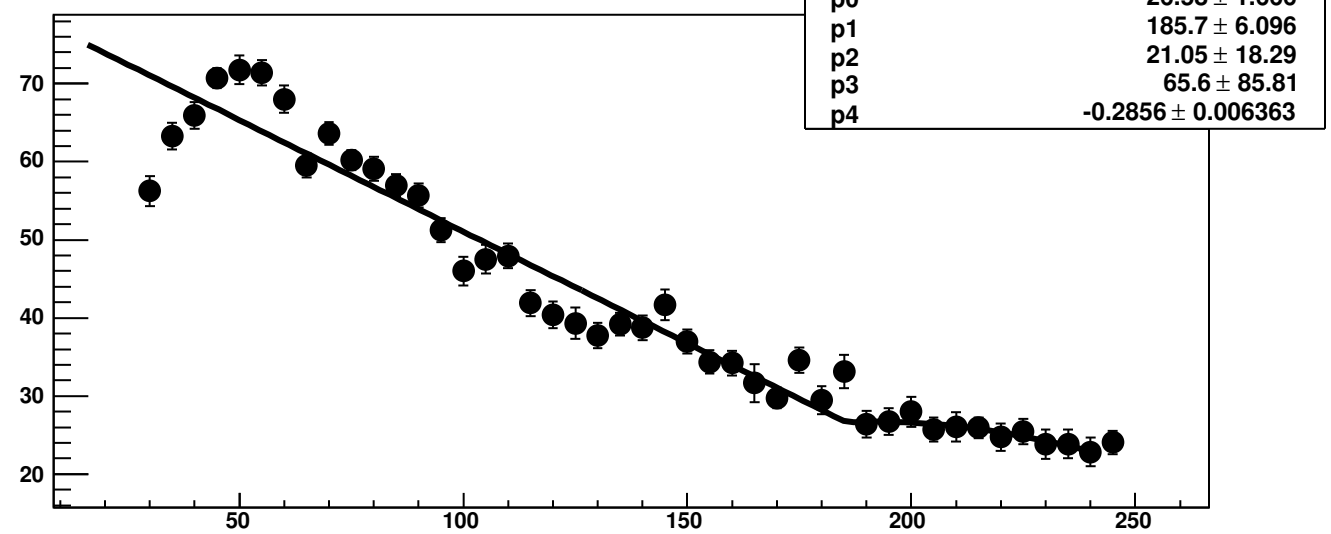
Chip 5, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



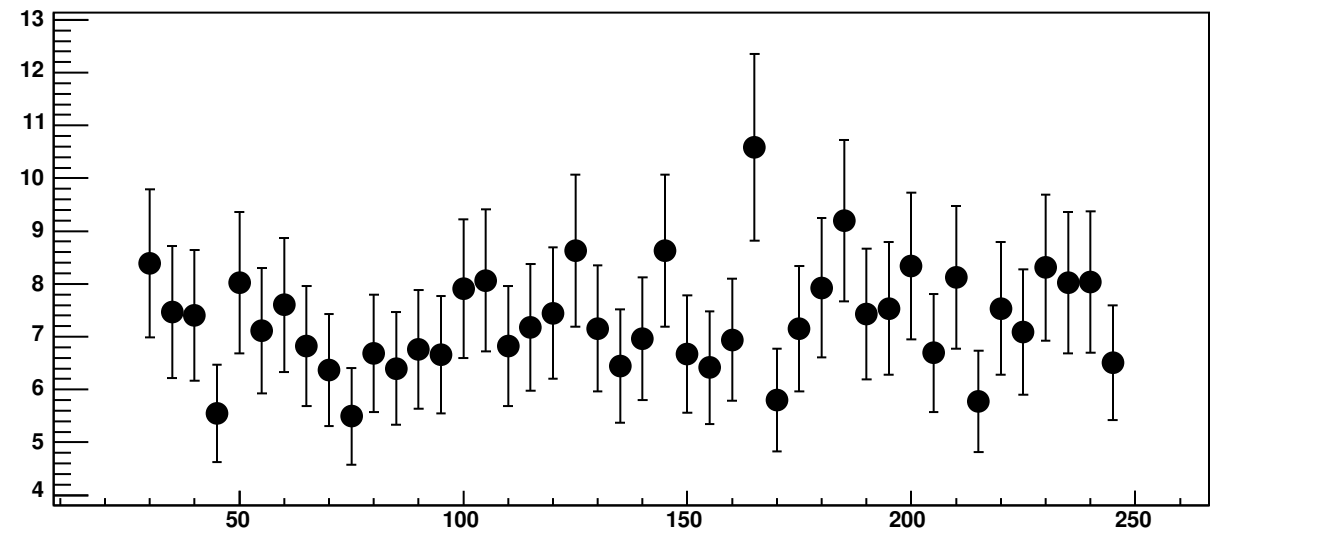
Chip 5, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold



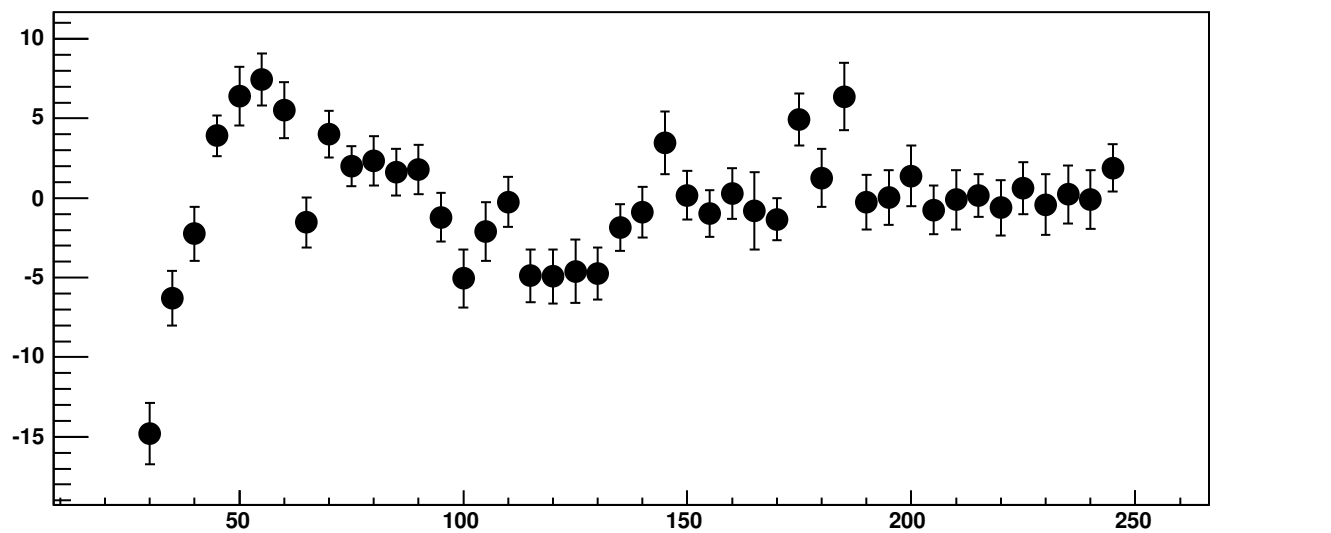
Chip 5, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold



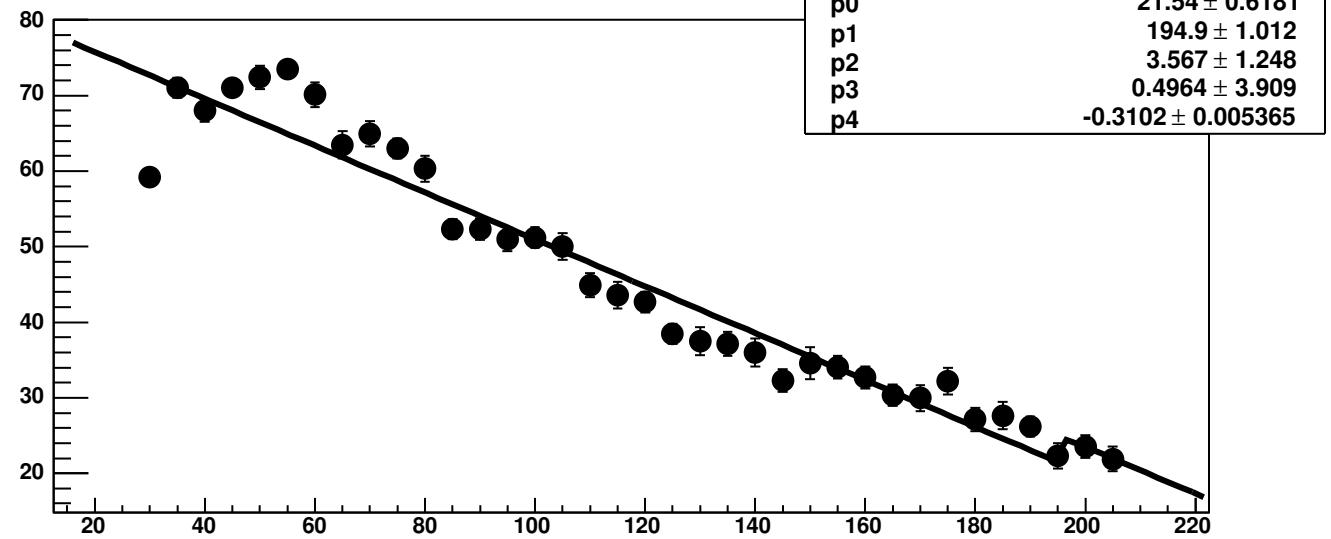
Chip 5, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



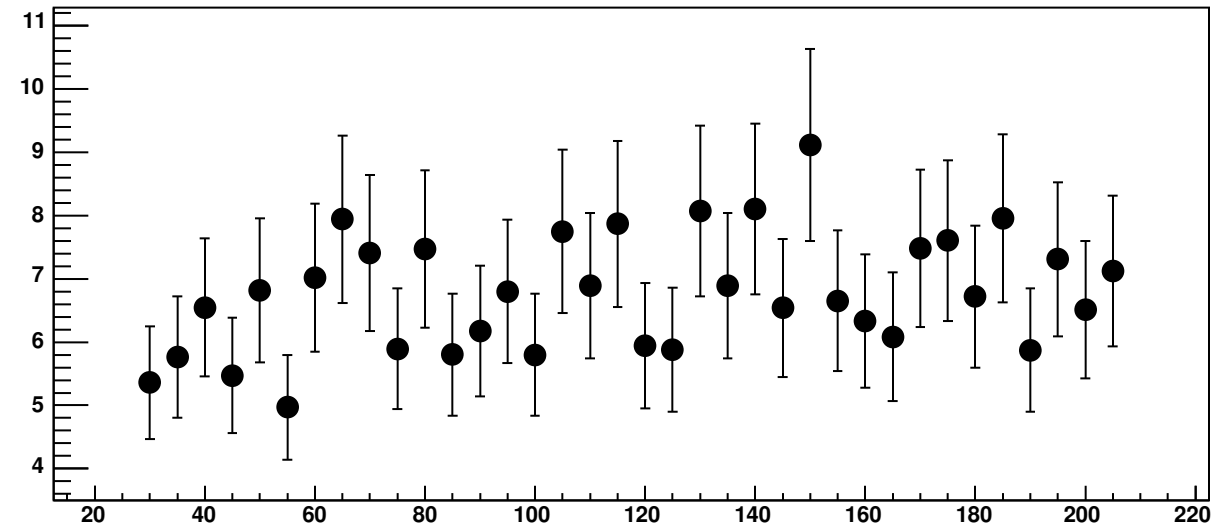
Chip 5, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold



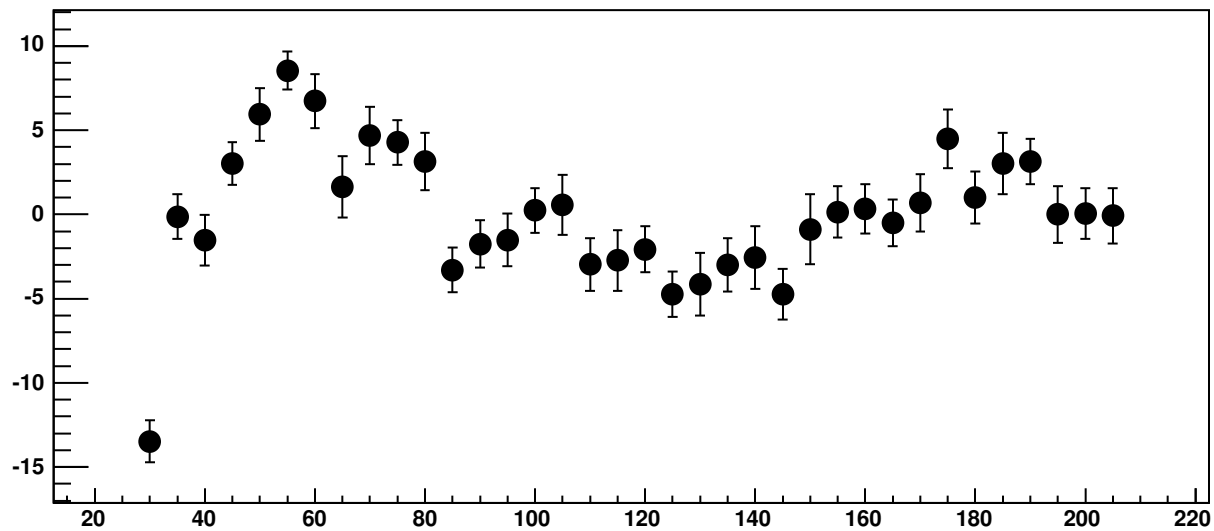
Chip 5, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold



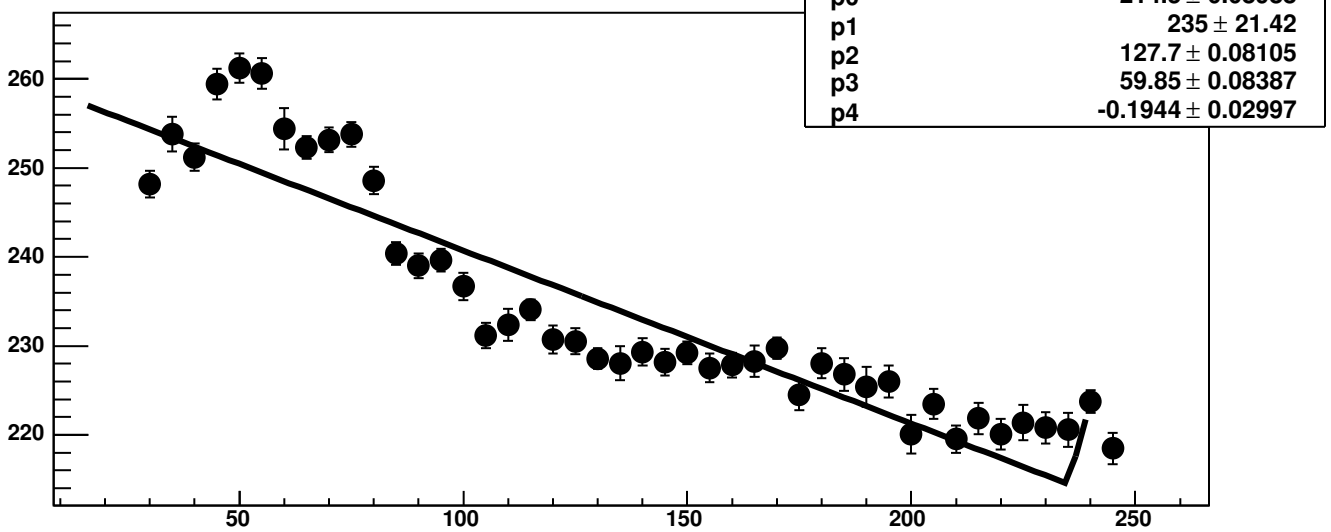
Chip 5, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold



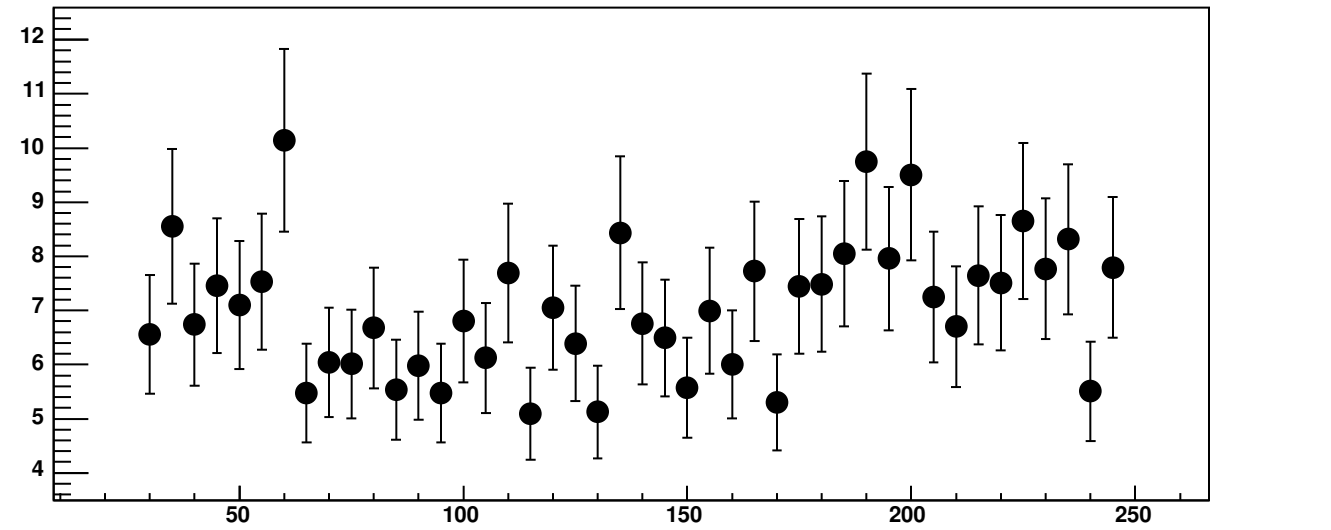
Chip 5, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold



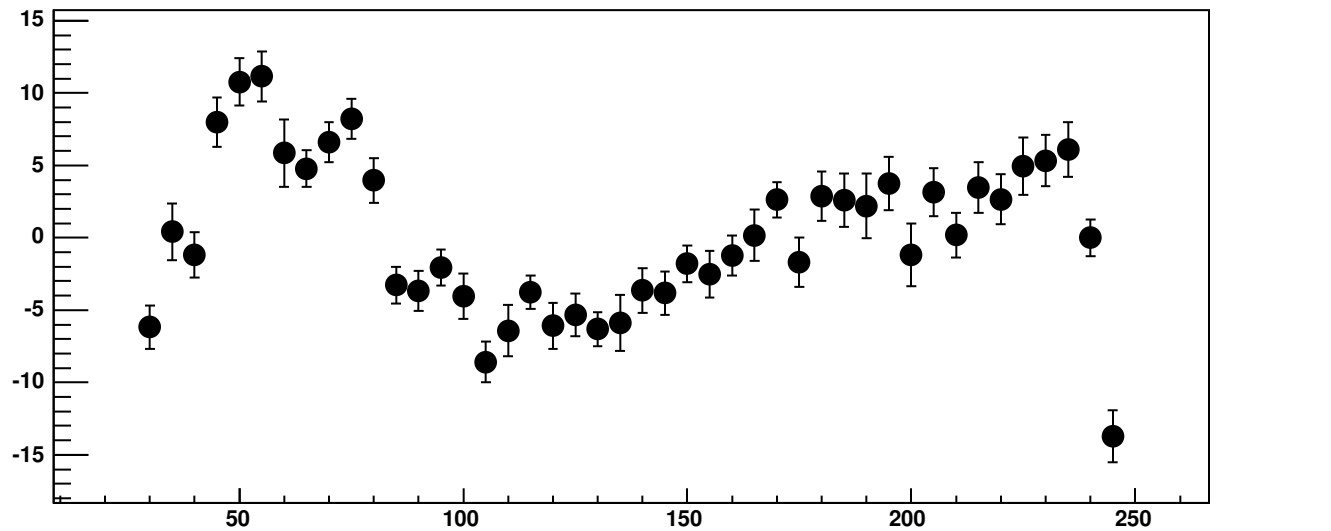
Chip 5, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold



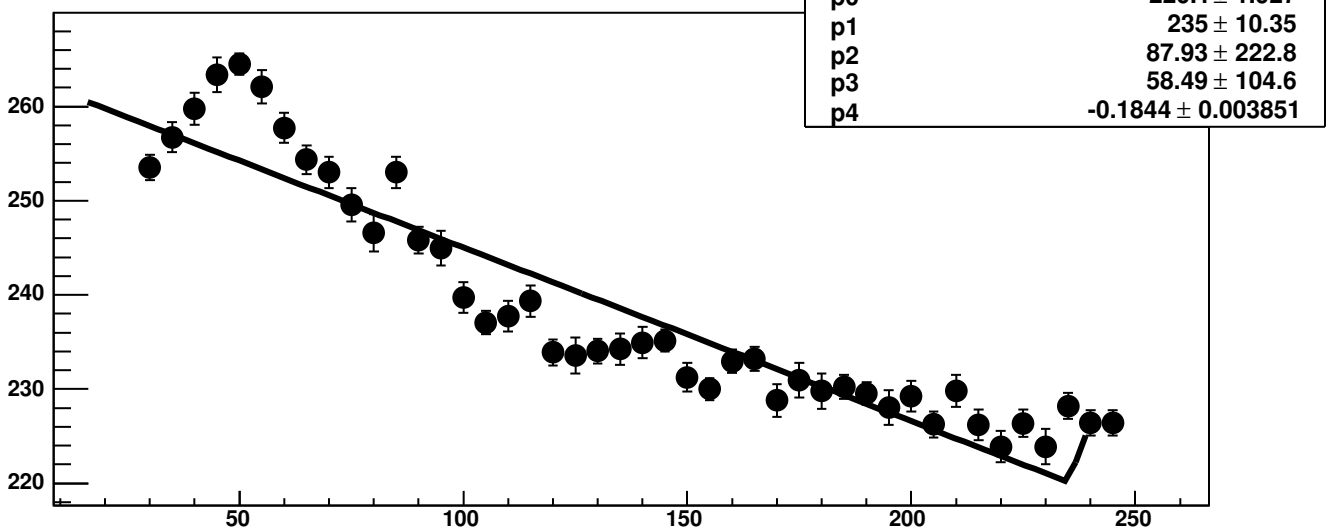
Chip 5, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



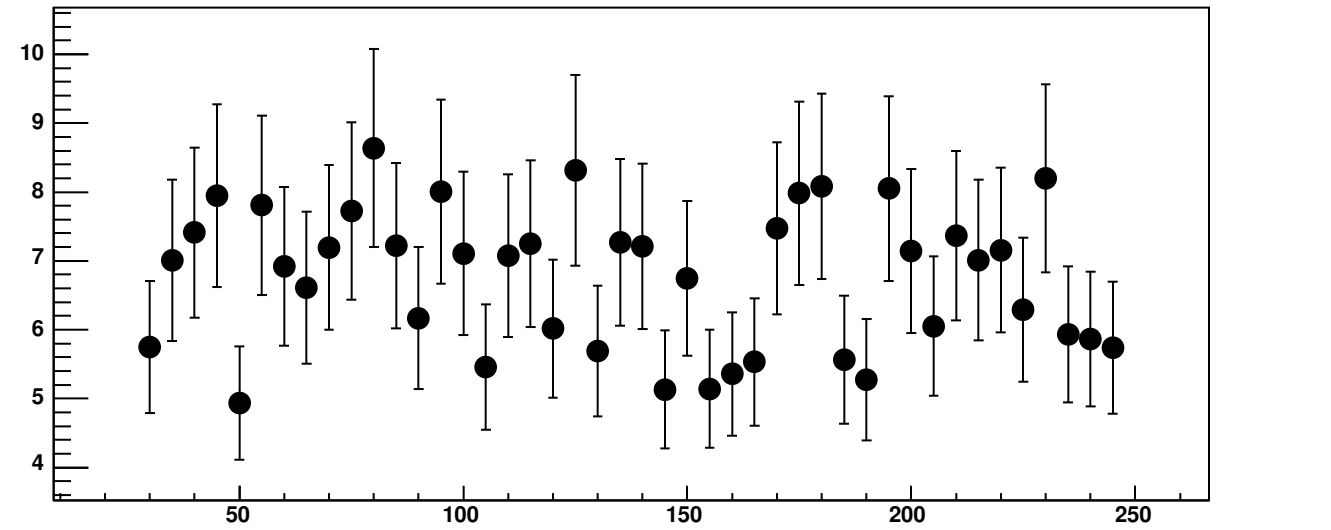
Chip 5, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold



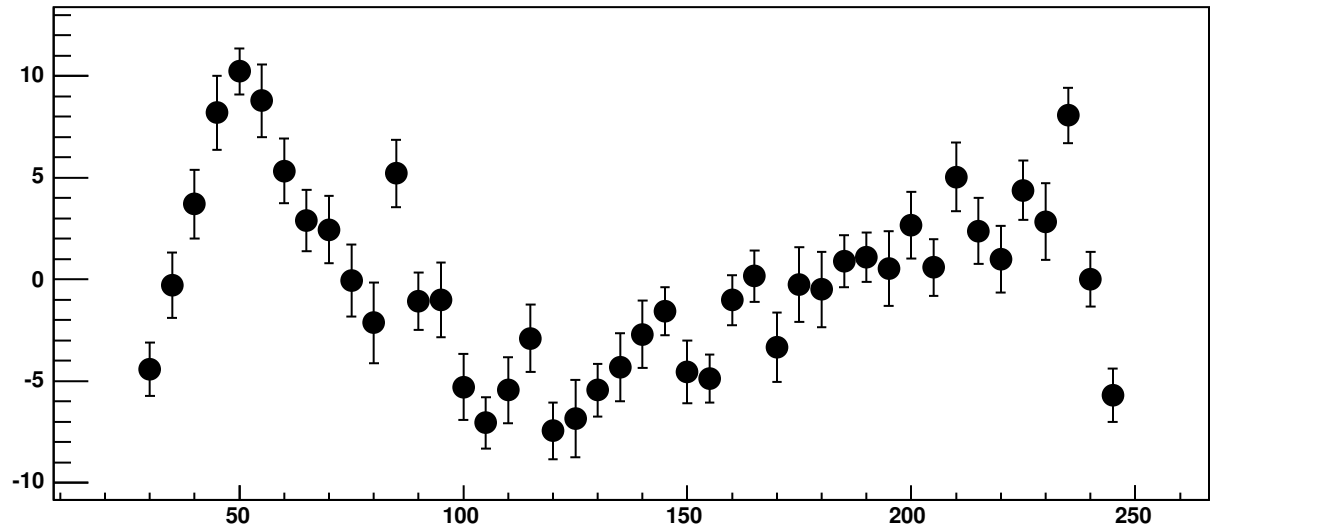
Chip 5, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold



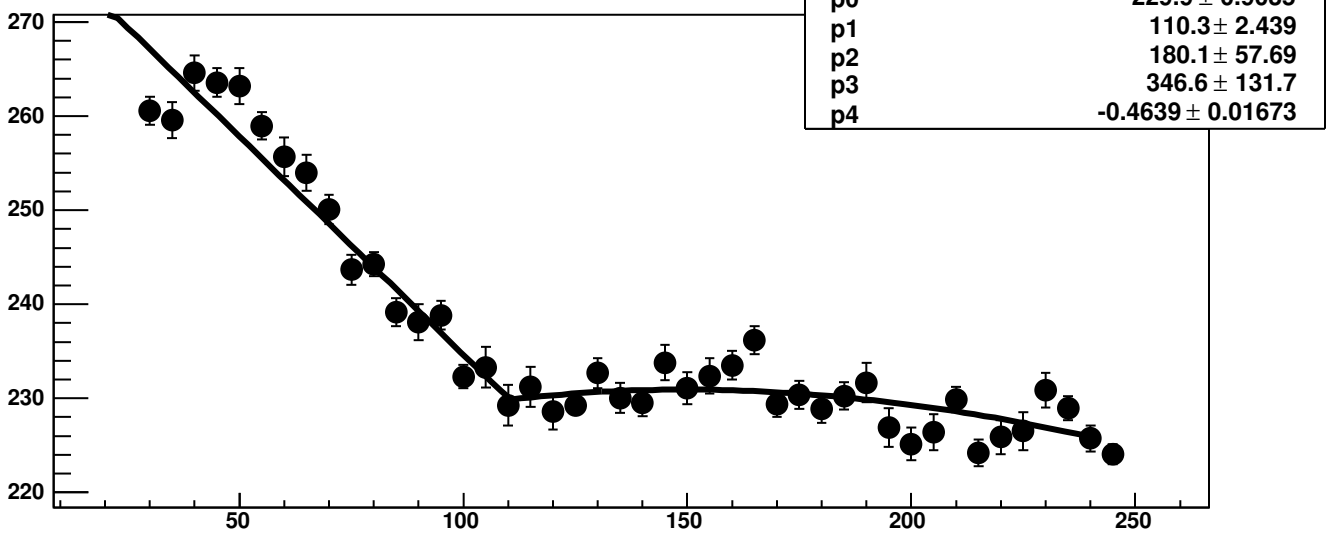
Chip 5, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold



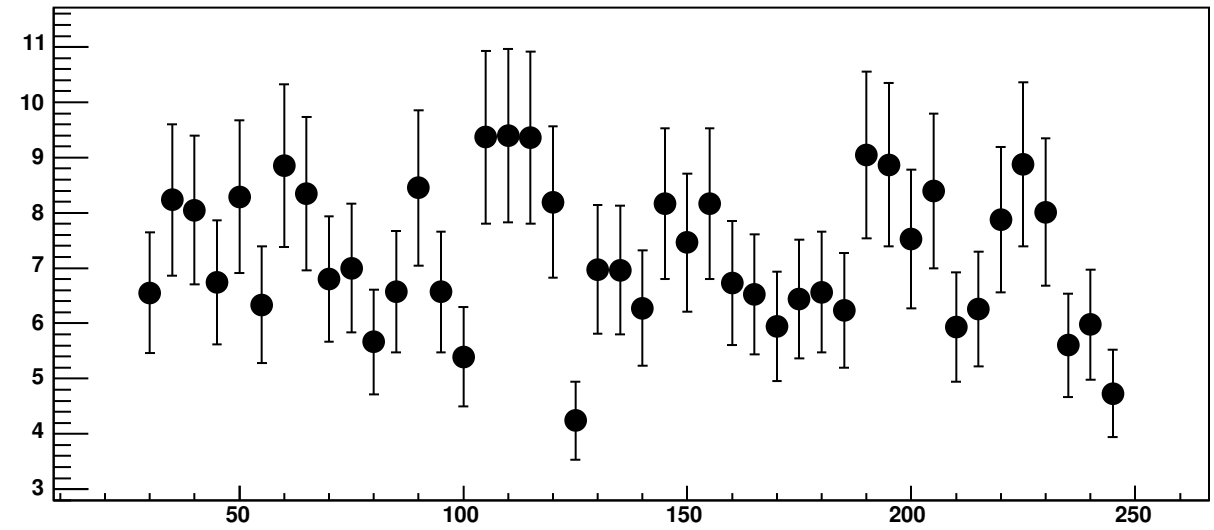
Chip 5, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold



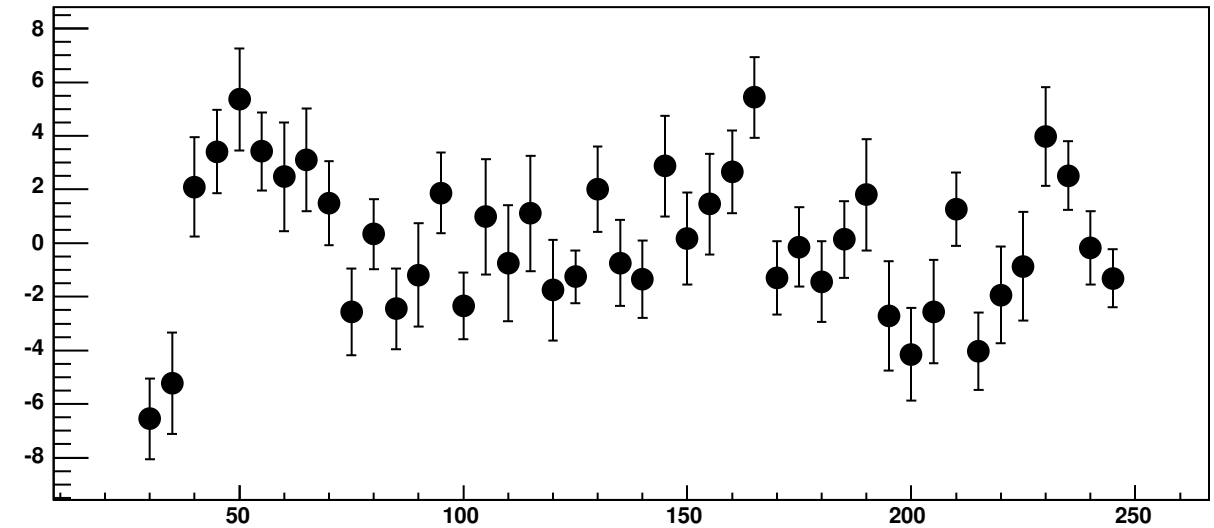
Chip 5, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold



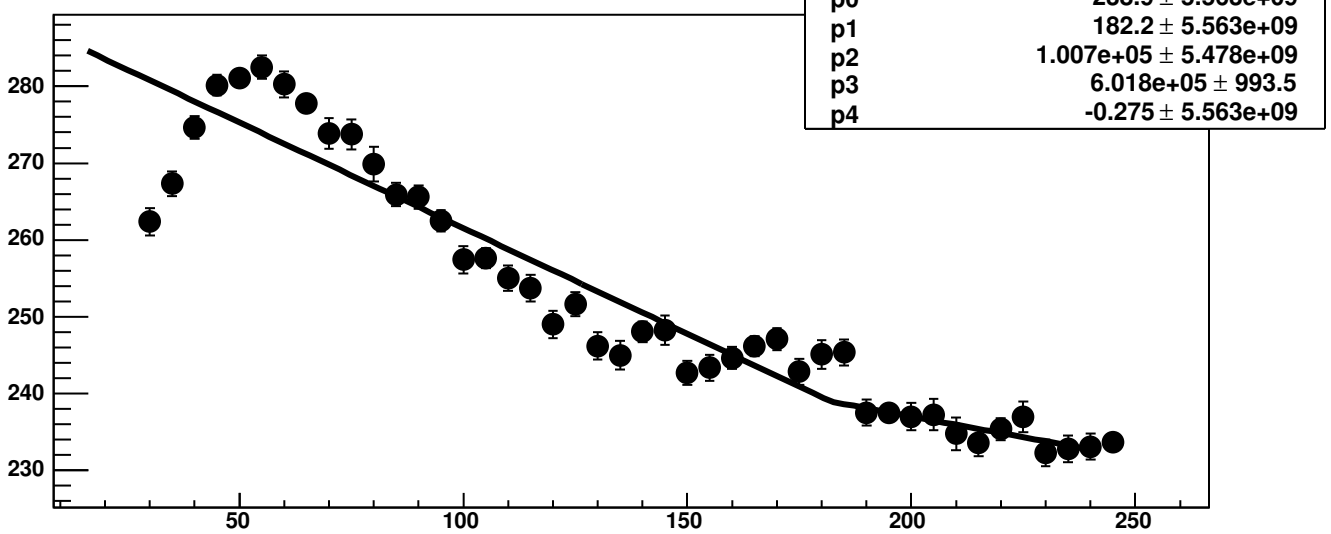
Chip 5, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



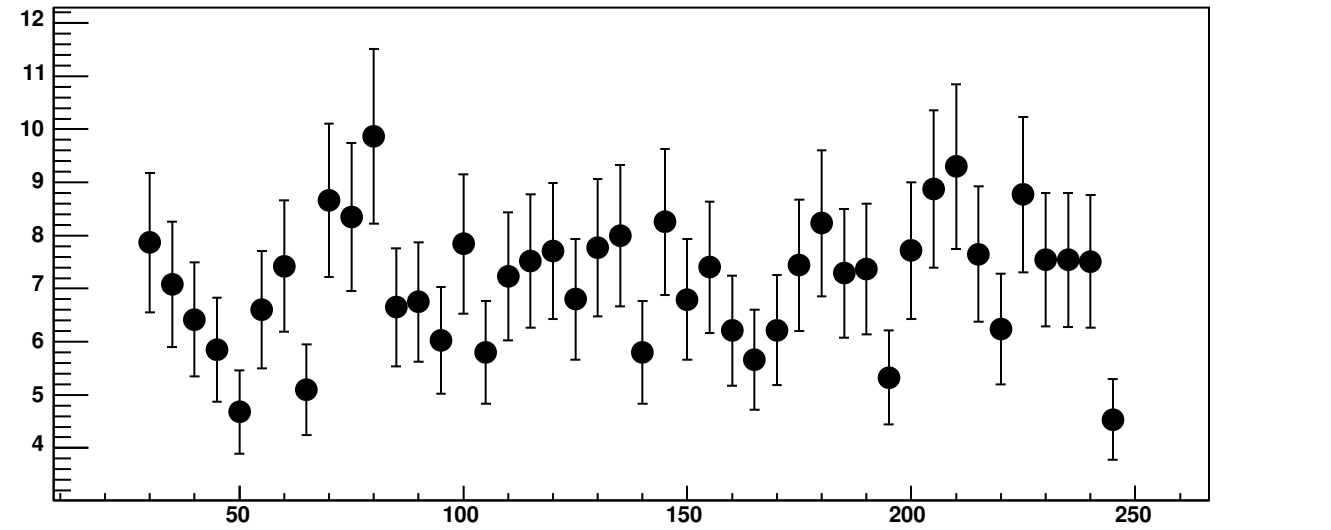
Chip 5, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold



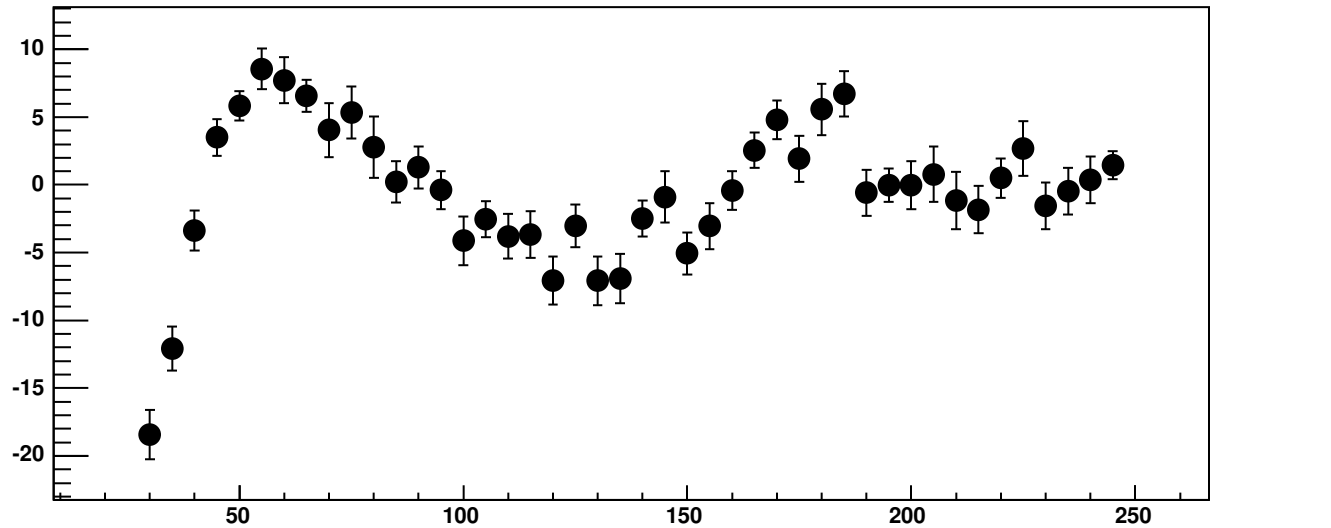
Chip 5, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

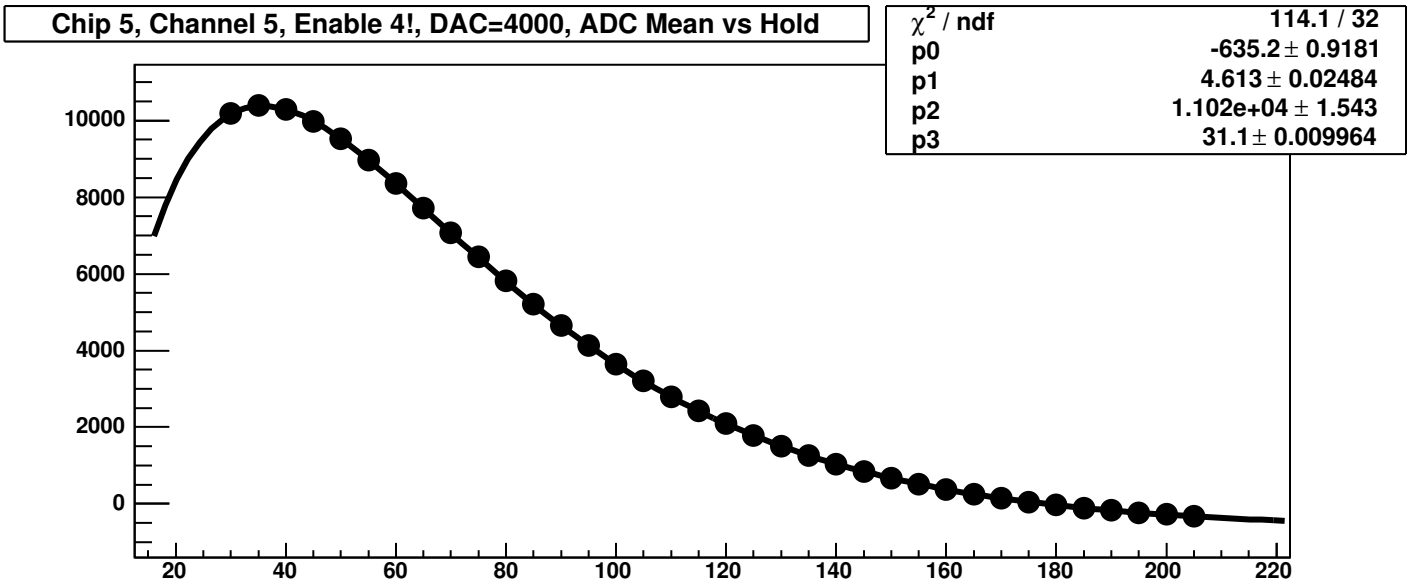


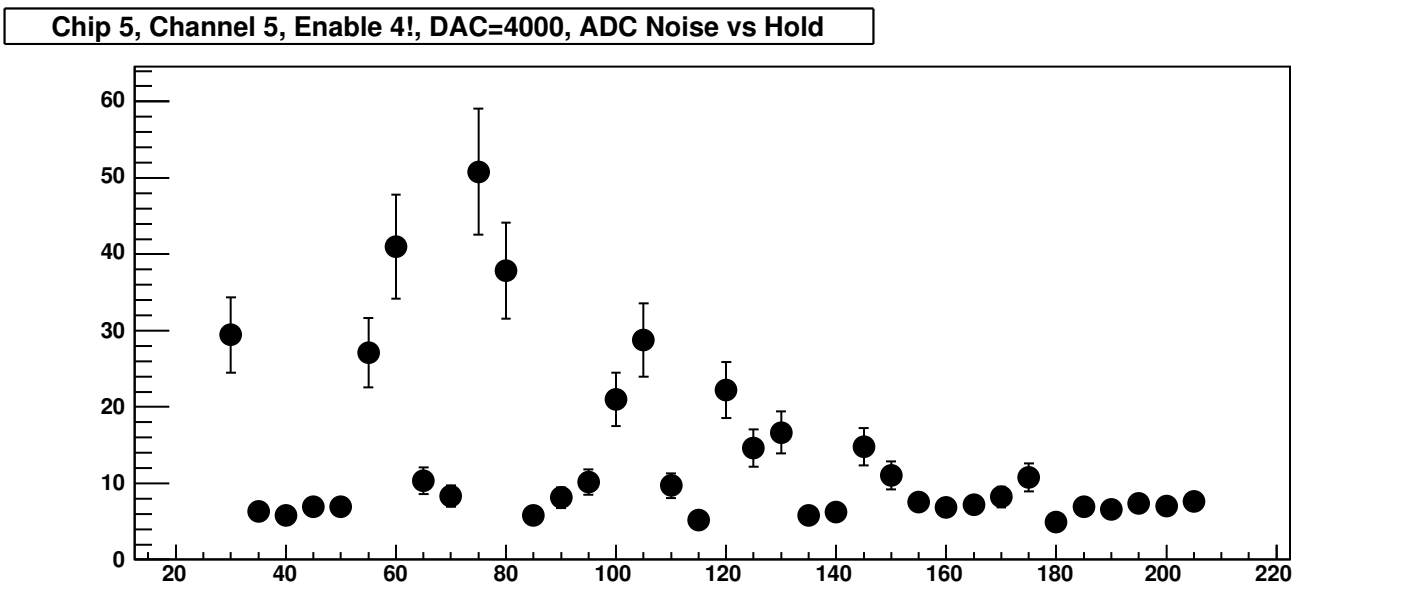
Chip 5, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold

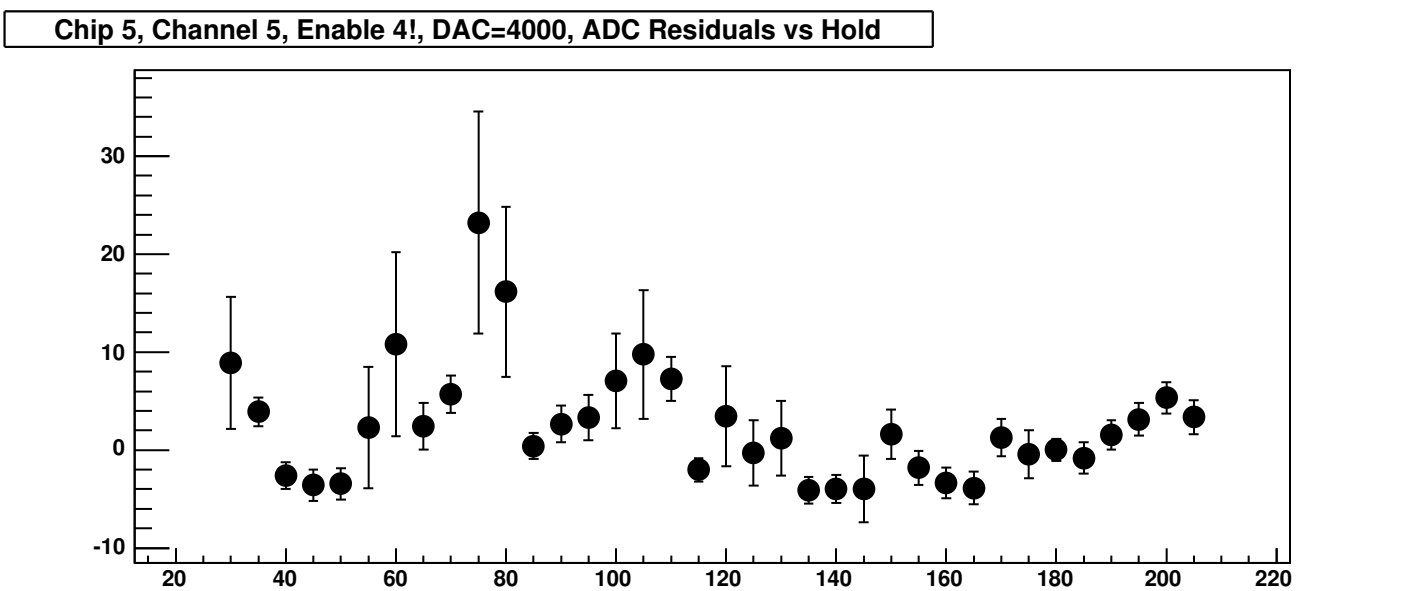


Chip 5, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

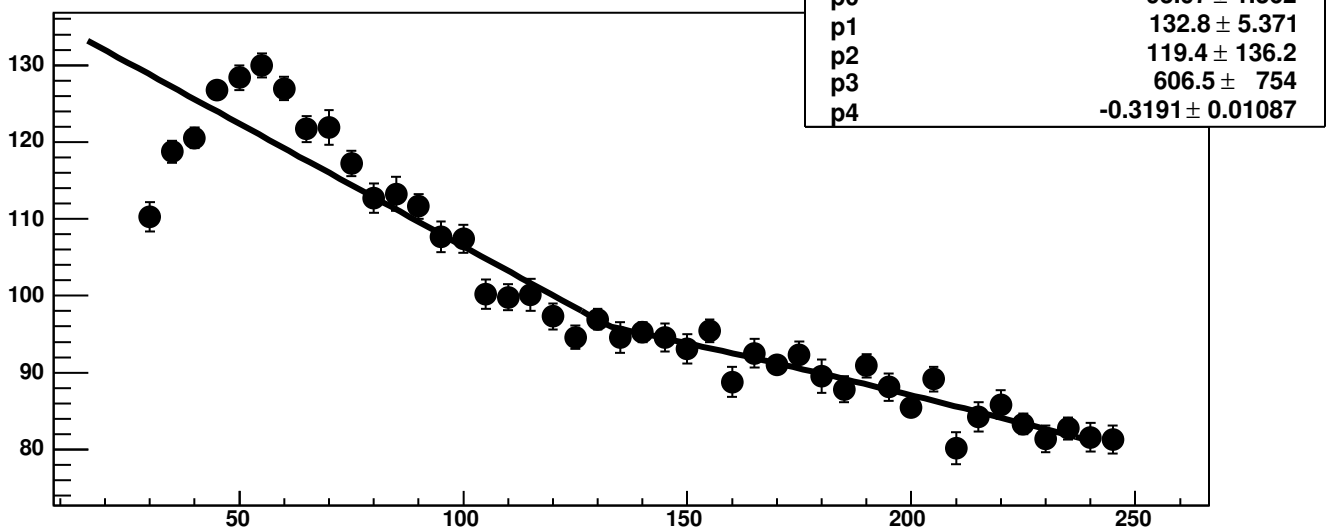



Chip 5, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 114.1 / 32
p0 -635.2 ± 0.9181
p1 4.613 ± 0.02484
p2 1.102e+04 ± 1.543
p3 31.1 ± 0.009964
10000
8000
6000
4000
2000
0
20
40
60
80
100
120
140
160
180
200
220

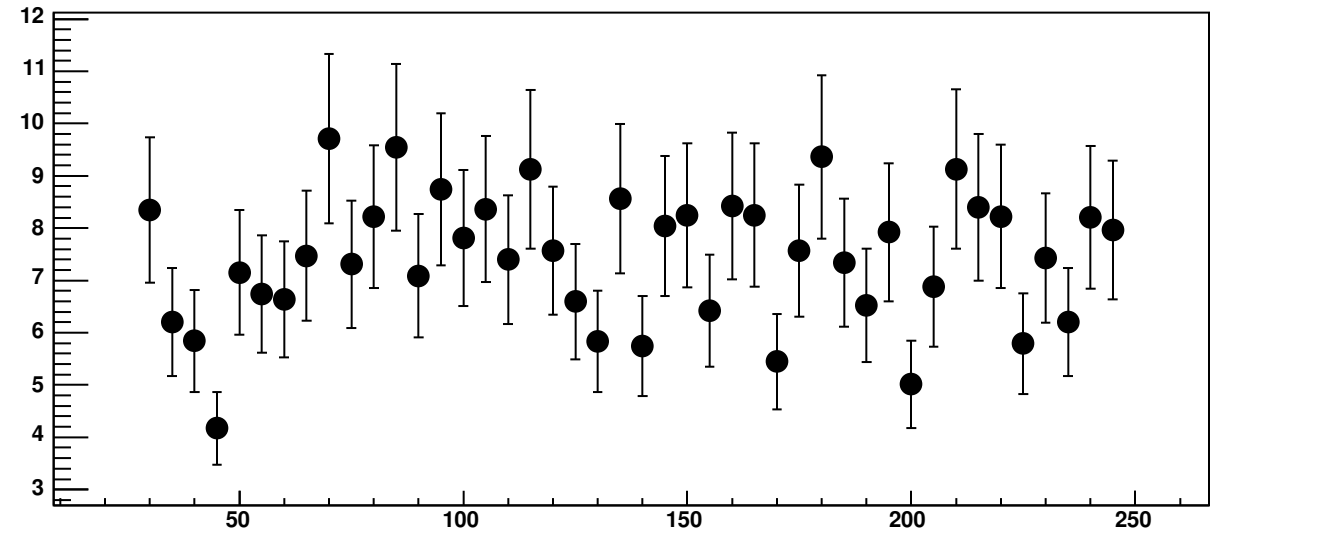

Chip 5, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold
60
50
40
30
20
10
0
20
40
60
80
100
120
140
160
180
200
220


Chip 5, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
20
40
60
80
100
120
140
160
180
200
220

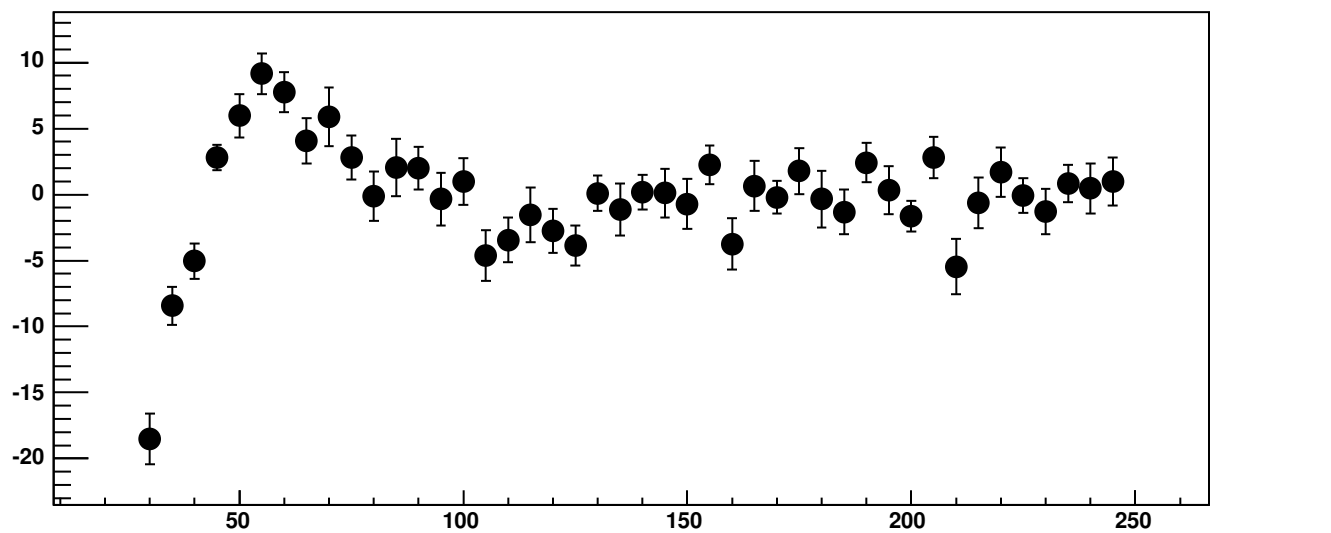
Chip 5, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold



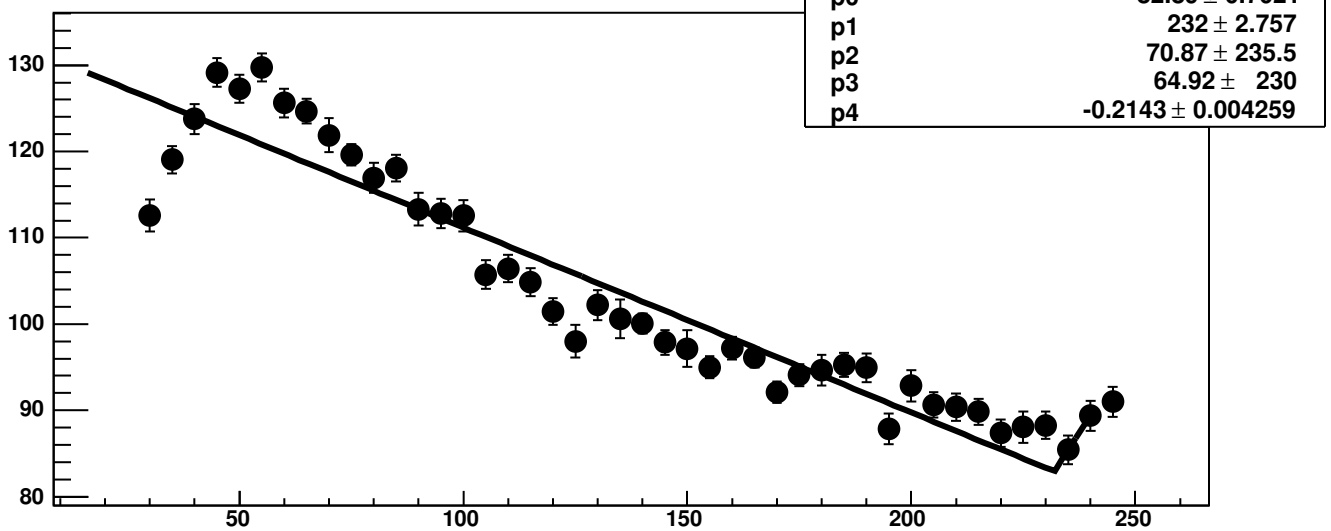
Chip 5, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



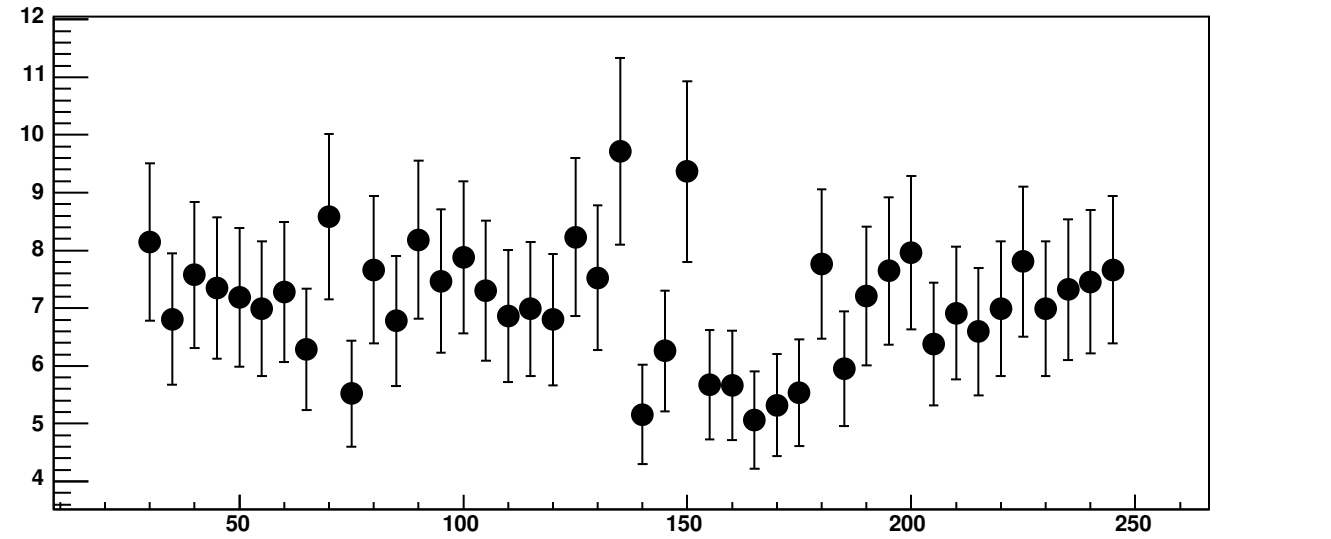
Chip 5, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold



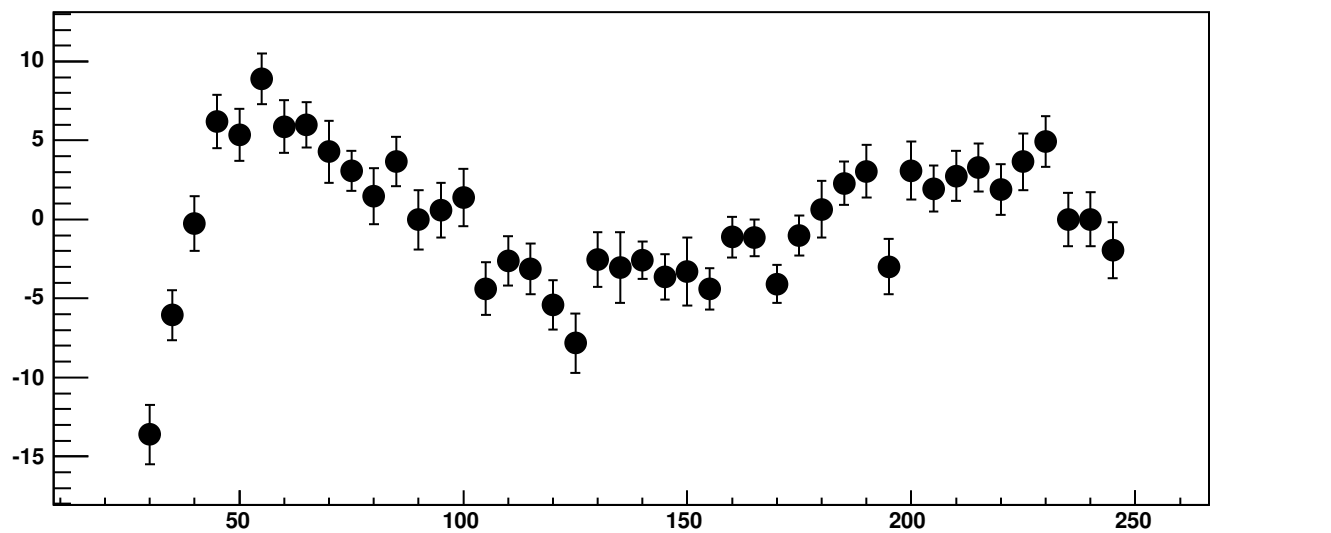
Chip 5, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold

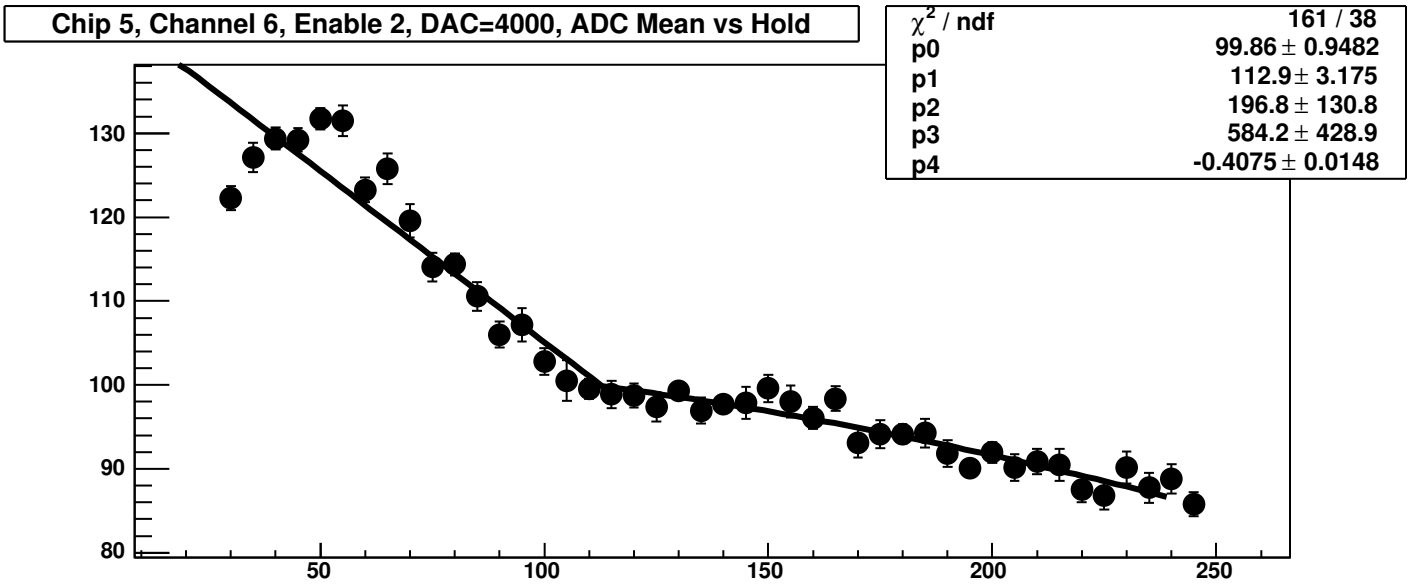


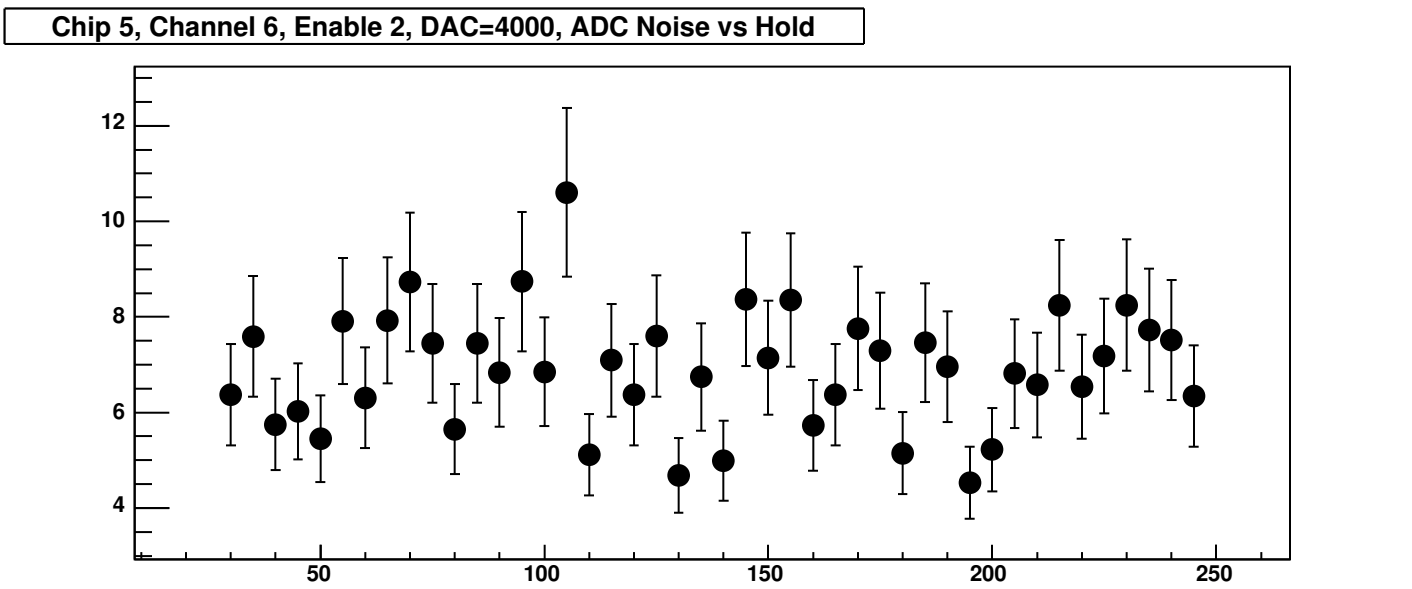
Chip 5, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold

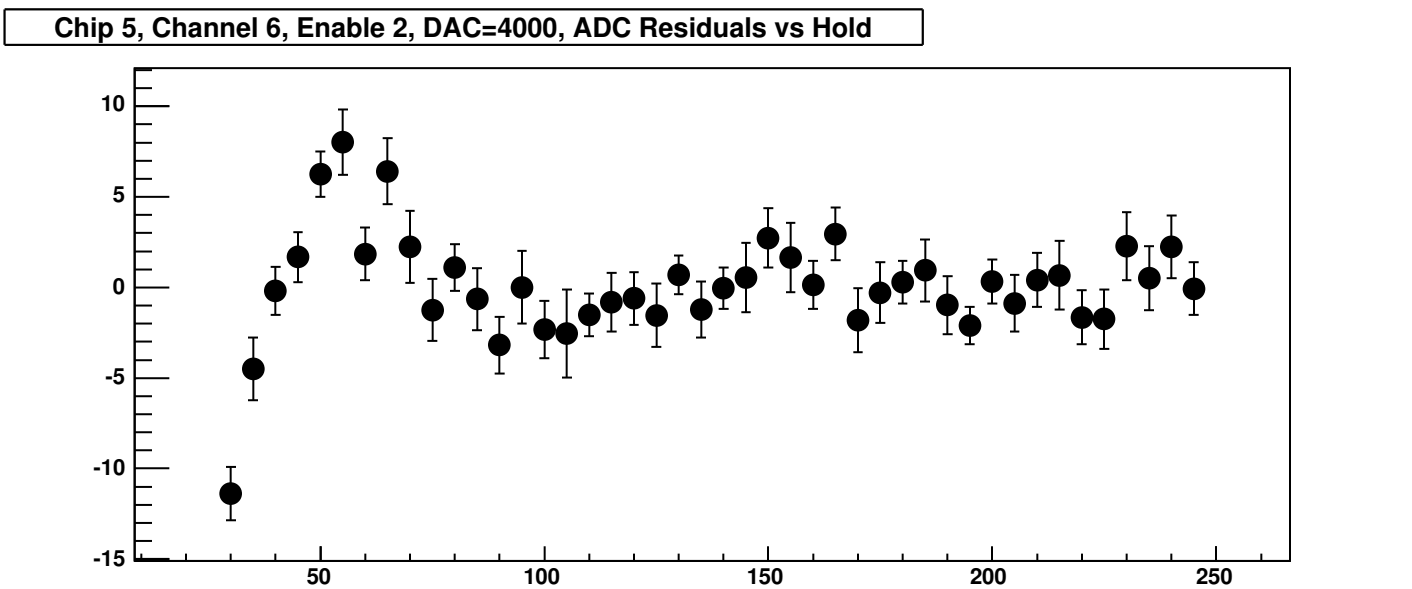


Chip 5, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold

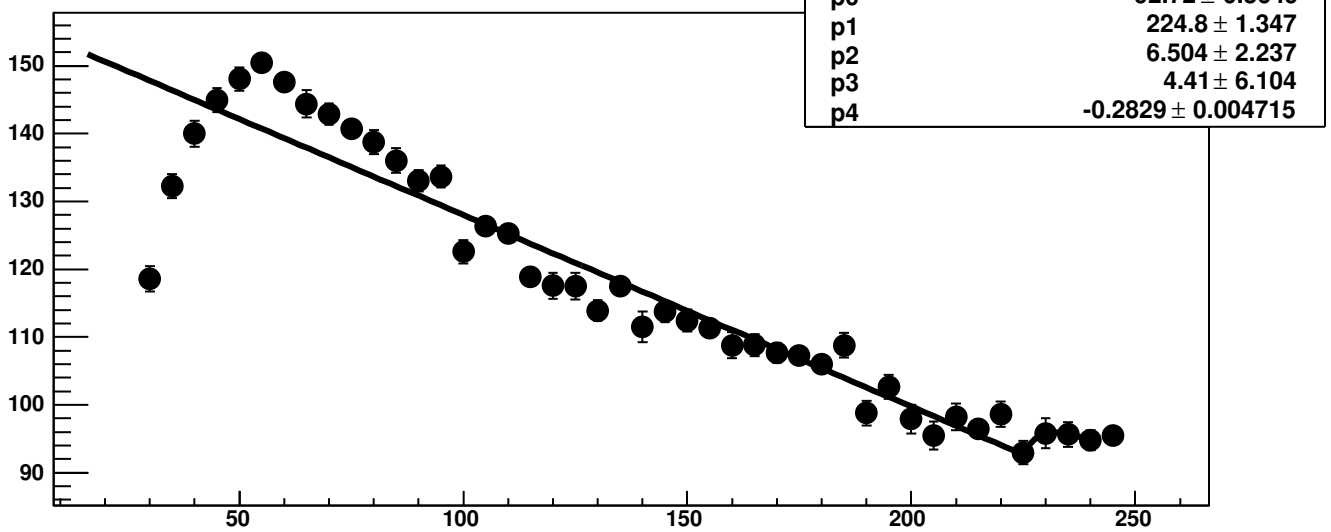



Chip 5, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 161 / 38
p0 99.86 ± 0.9482
p1 112.9 ± 3.175
p2 196.8 ± 130.8
p3 584.2 ± 428.9
p4 -0.4075 ± 0.0148
130
120
110
100
90
80
50
100
150
200
250

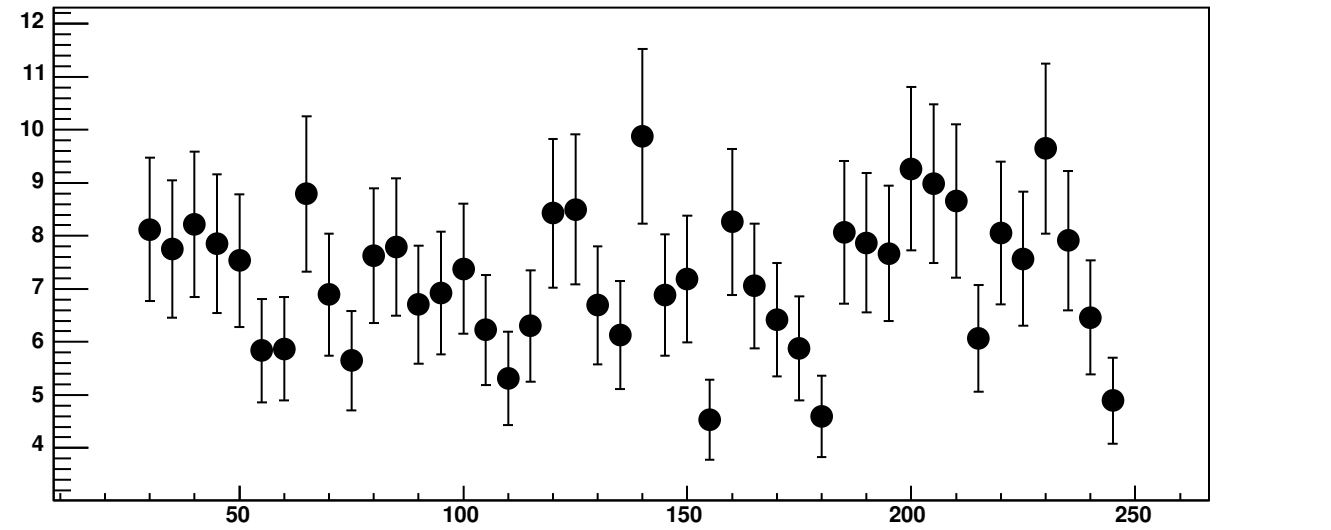

Chip 5, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
50
100
150
200
250


Chip 5, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
-15
50
100
150
200
250

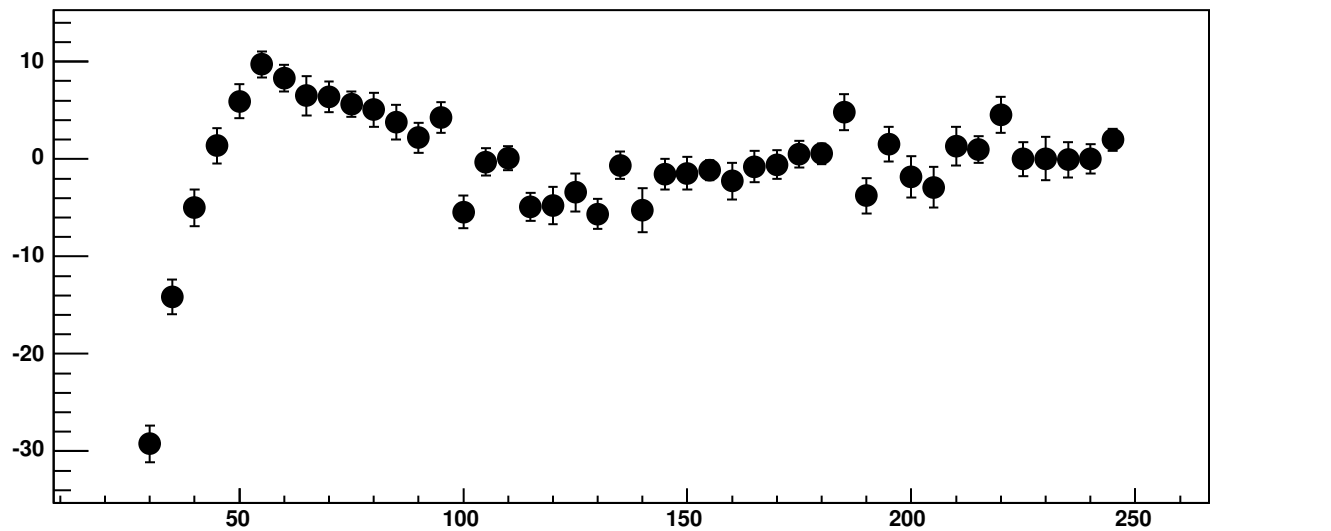
Chip 5, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold



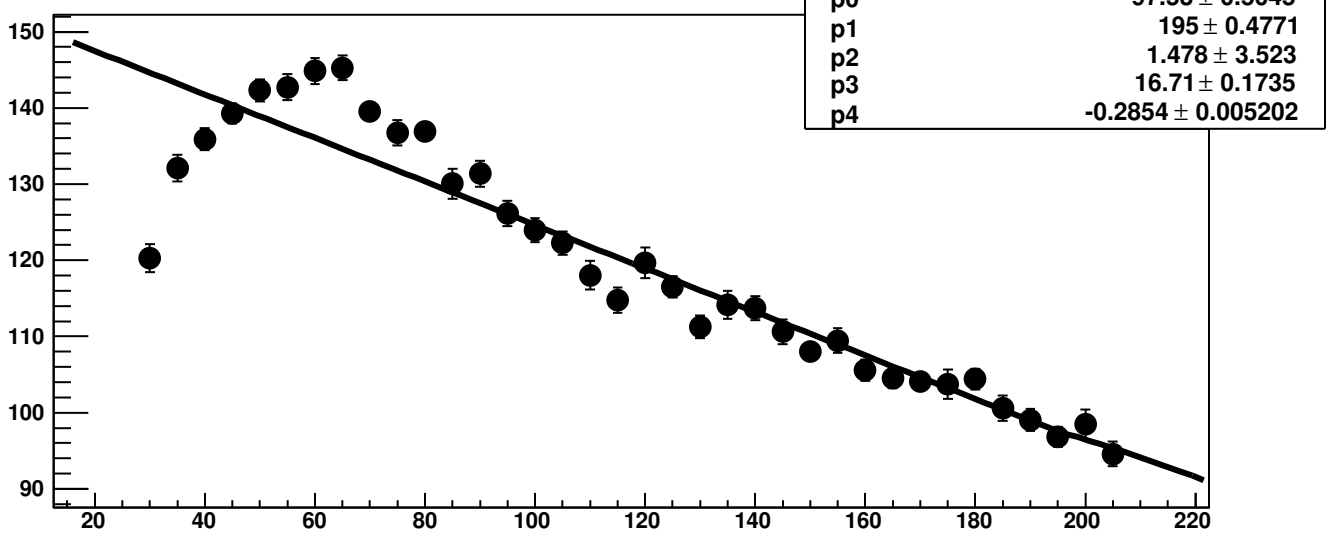
Chip 5, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



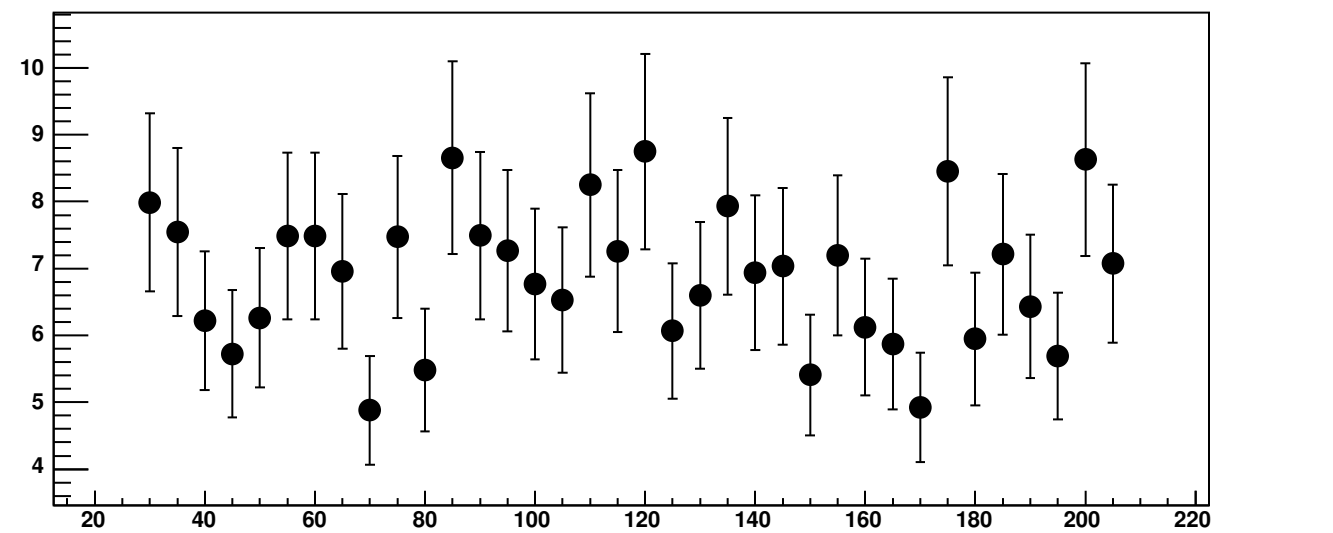
Chip 5, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold



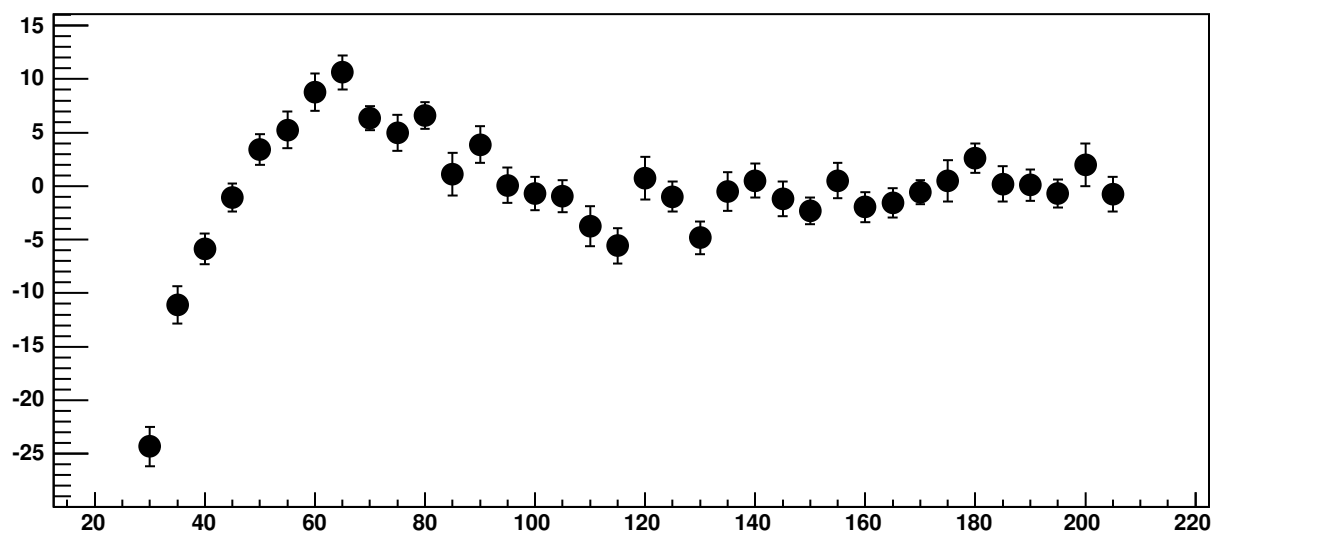
Chip 5, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold



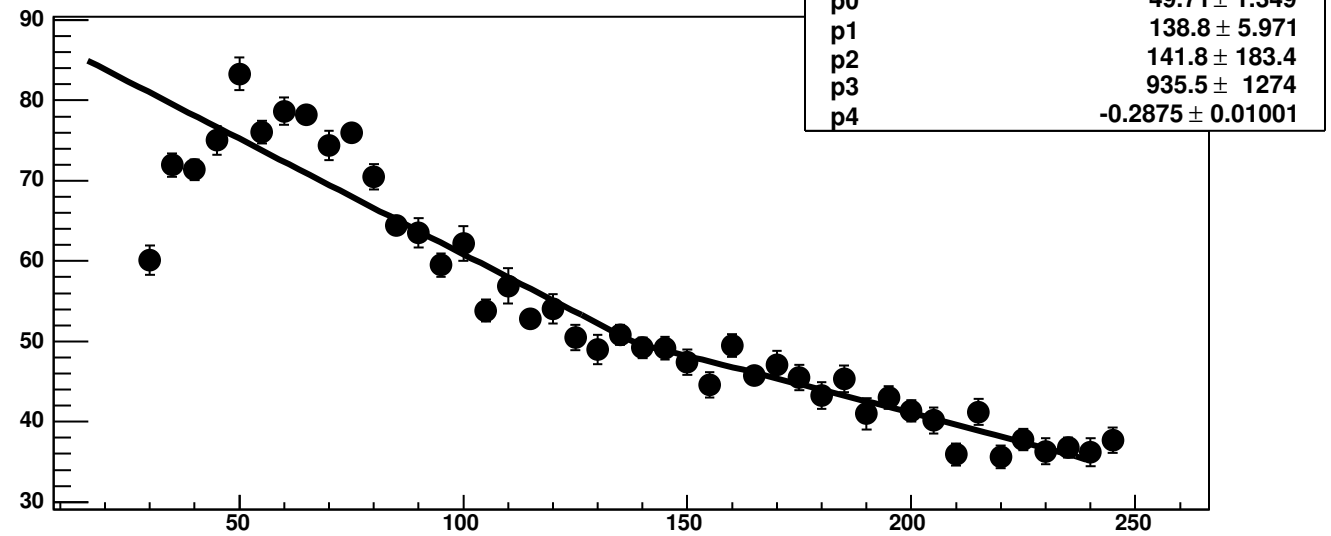
Chip 5, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



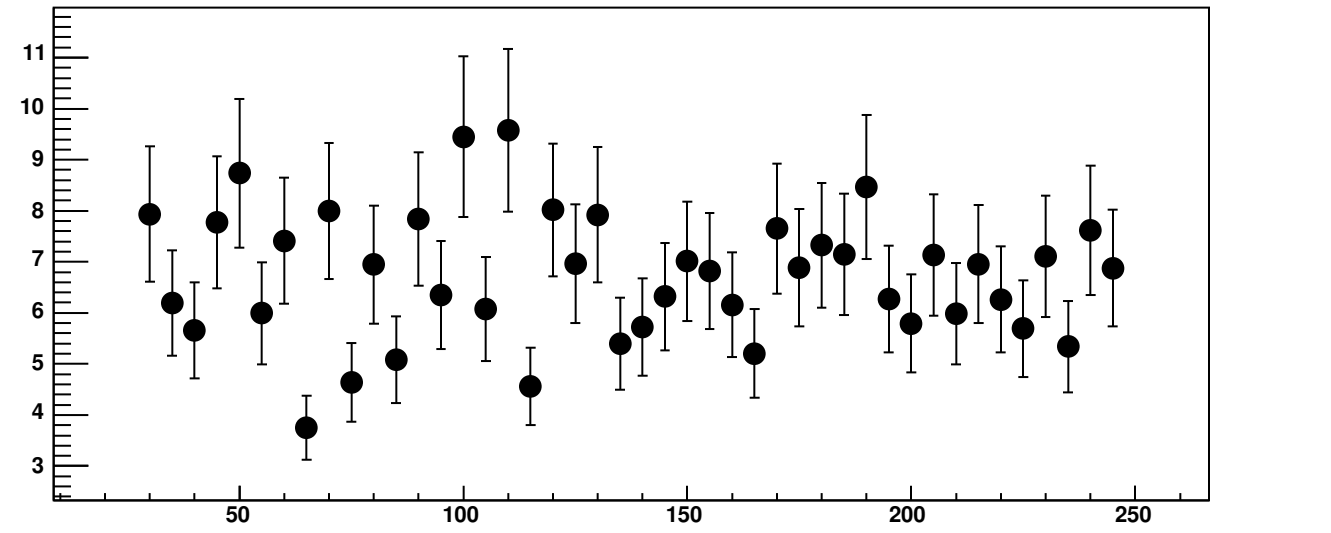
Chip 5, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold



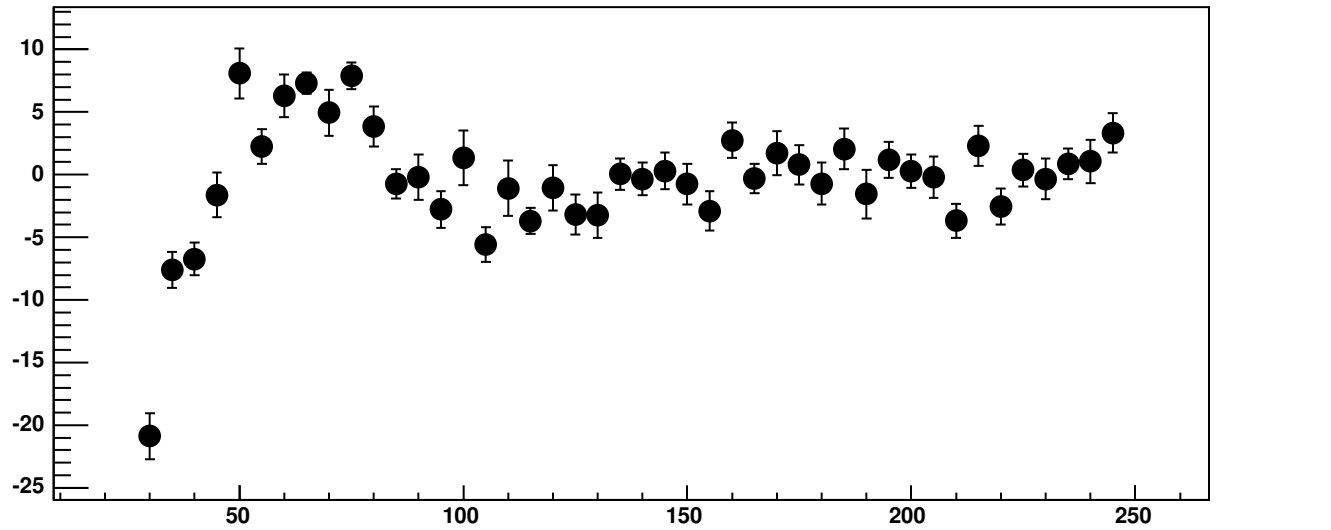
Chip 5, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold



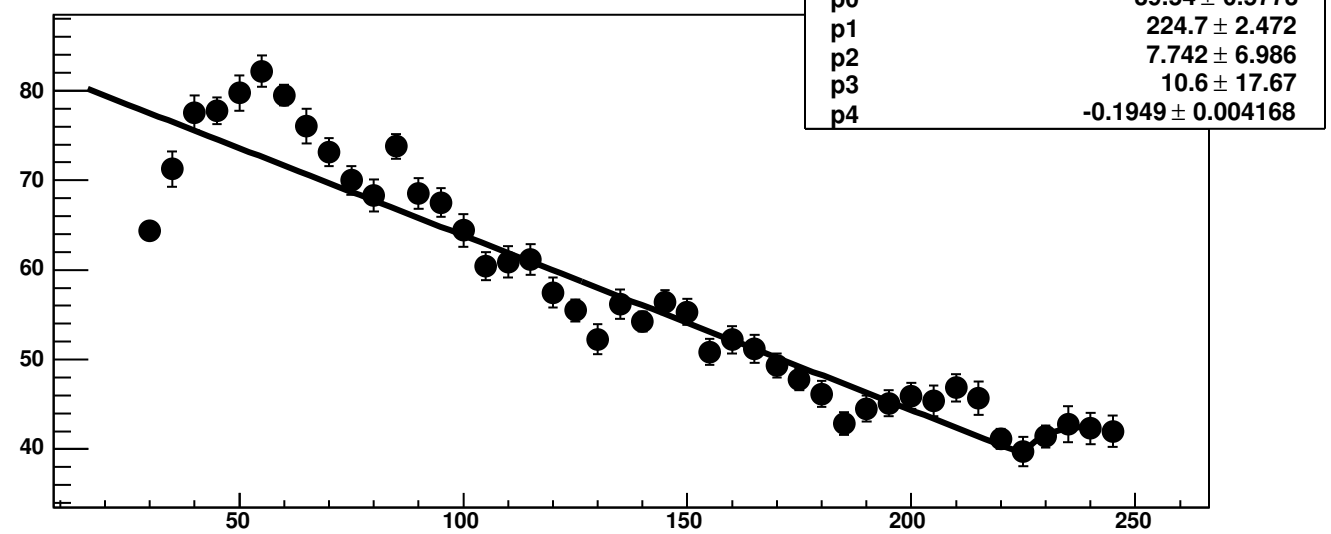
Chip 5, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



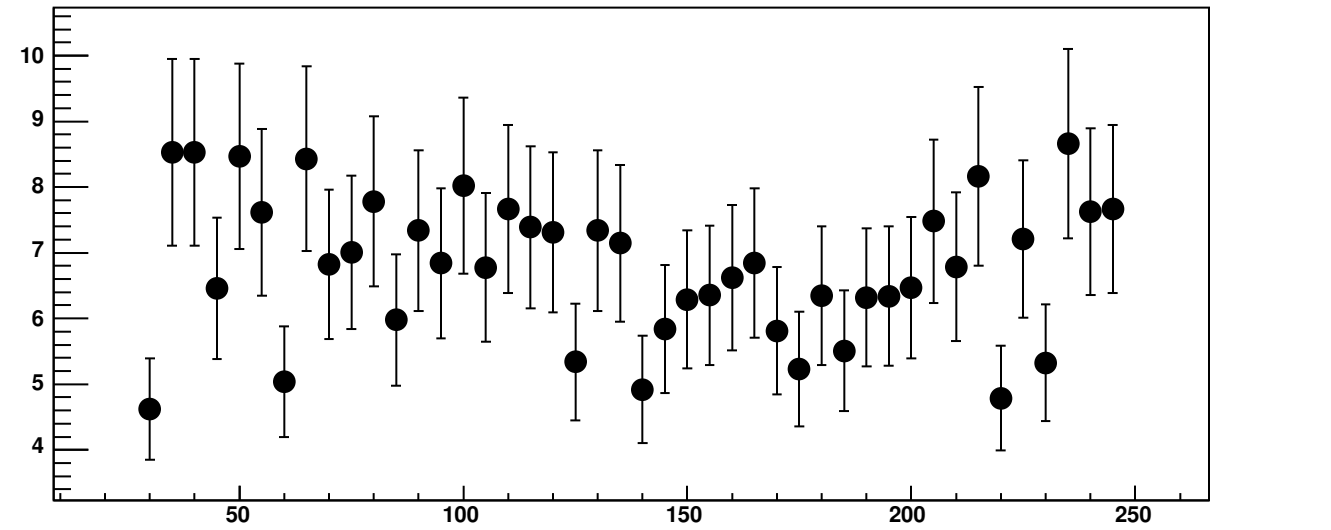
Chip 5, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold



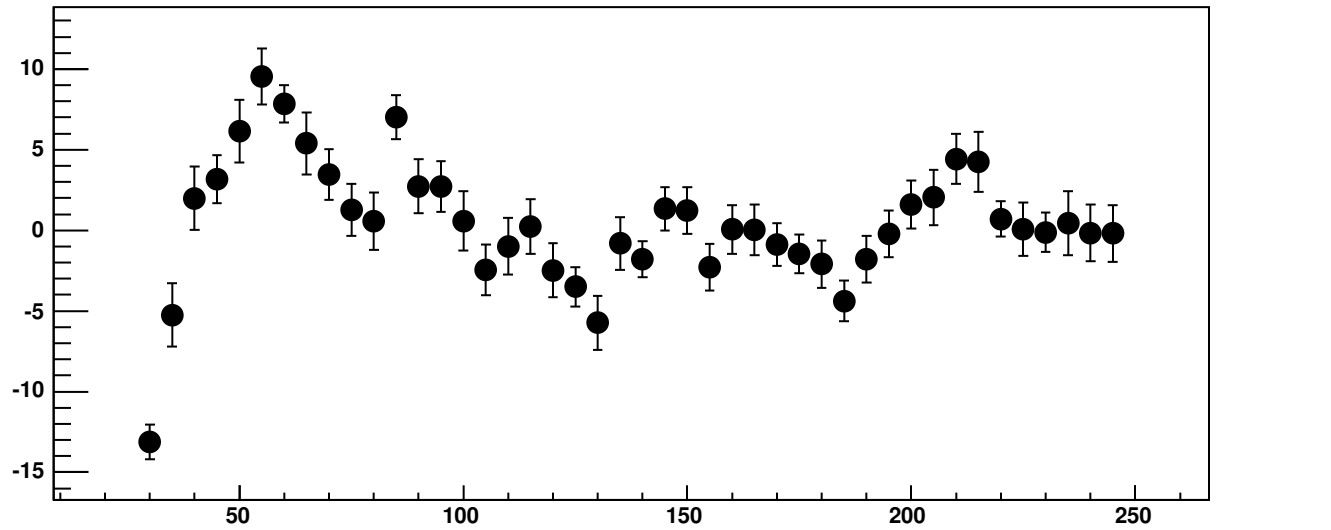
Chip 5, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold

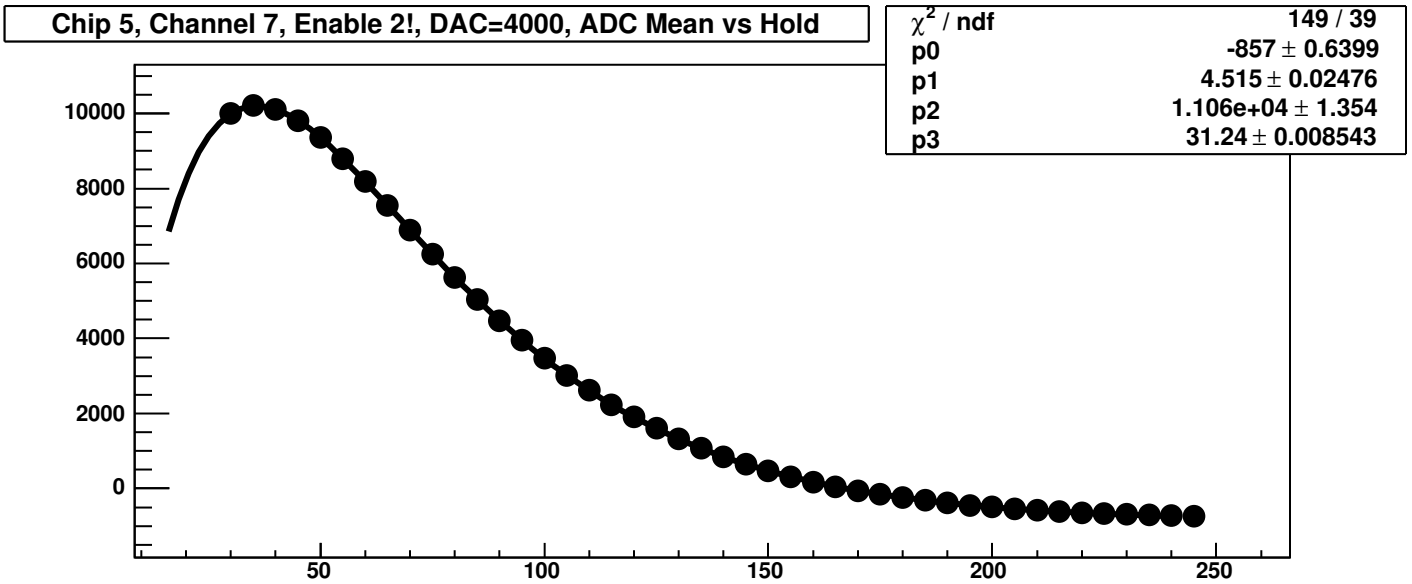


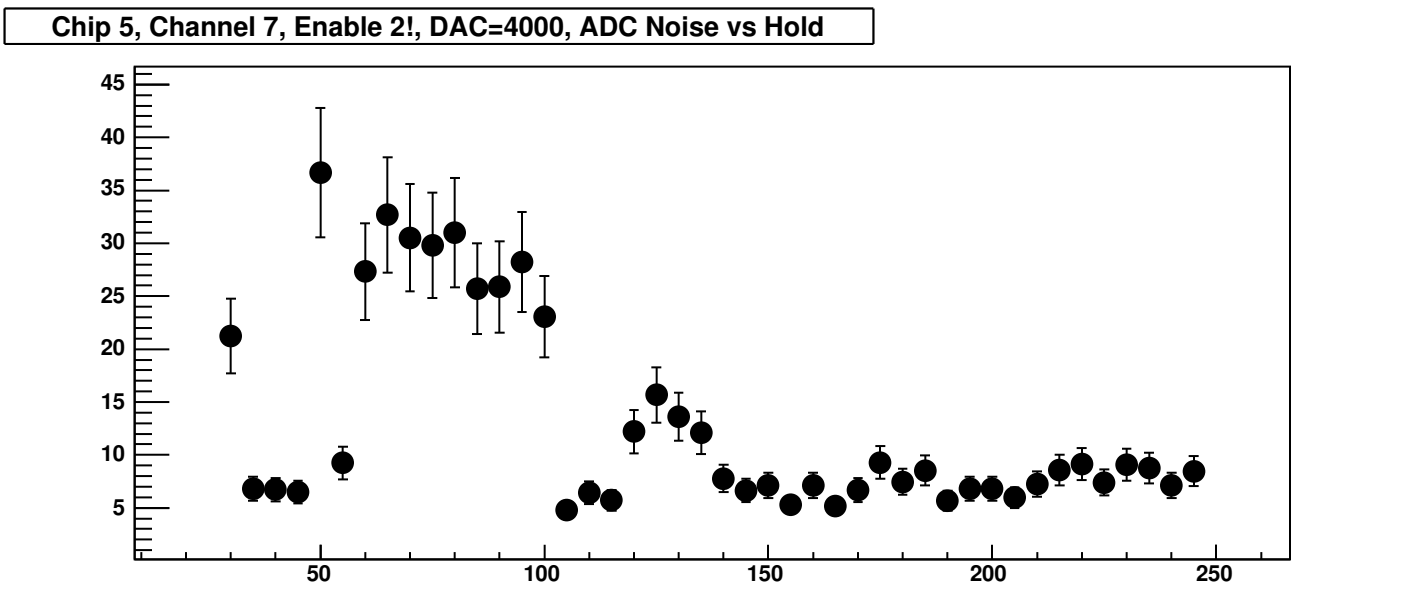
Chip 5, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold

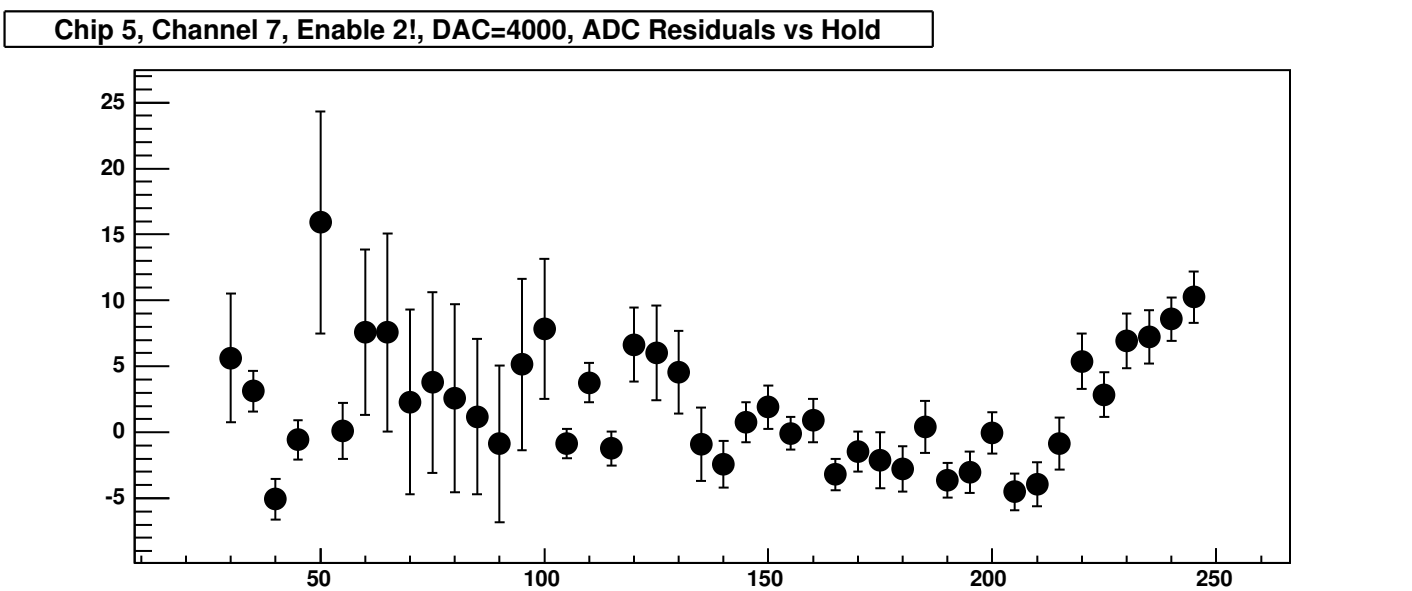


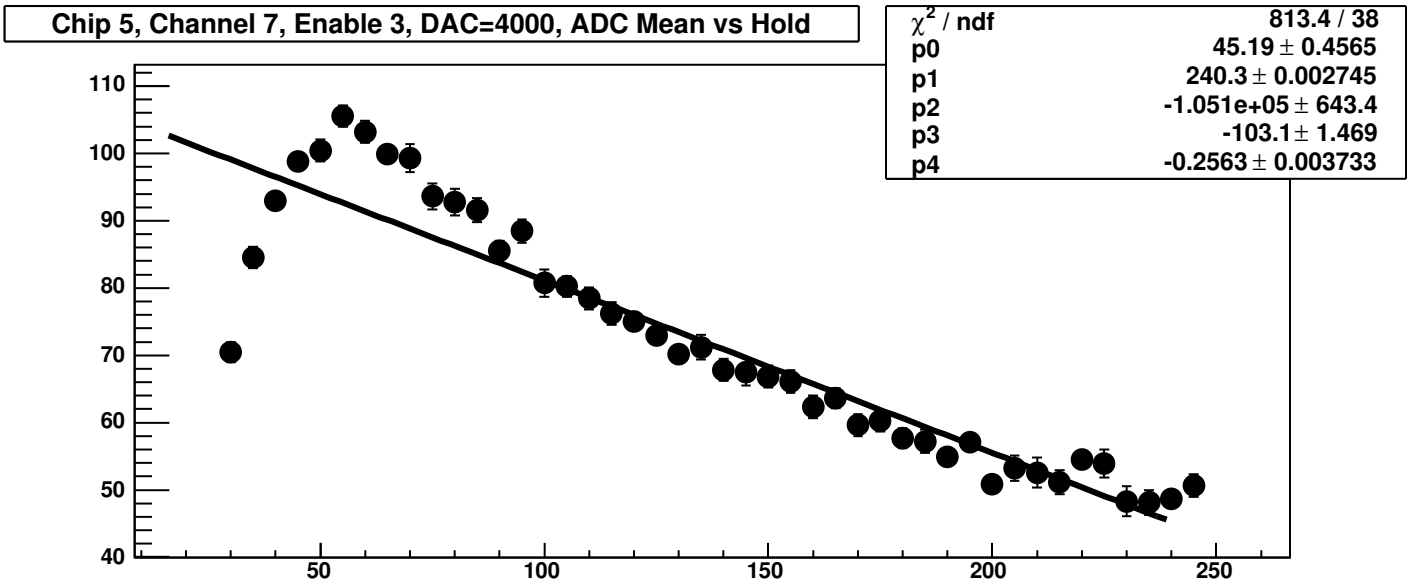
Chip 5, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

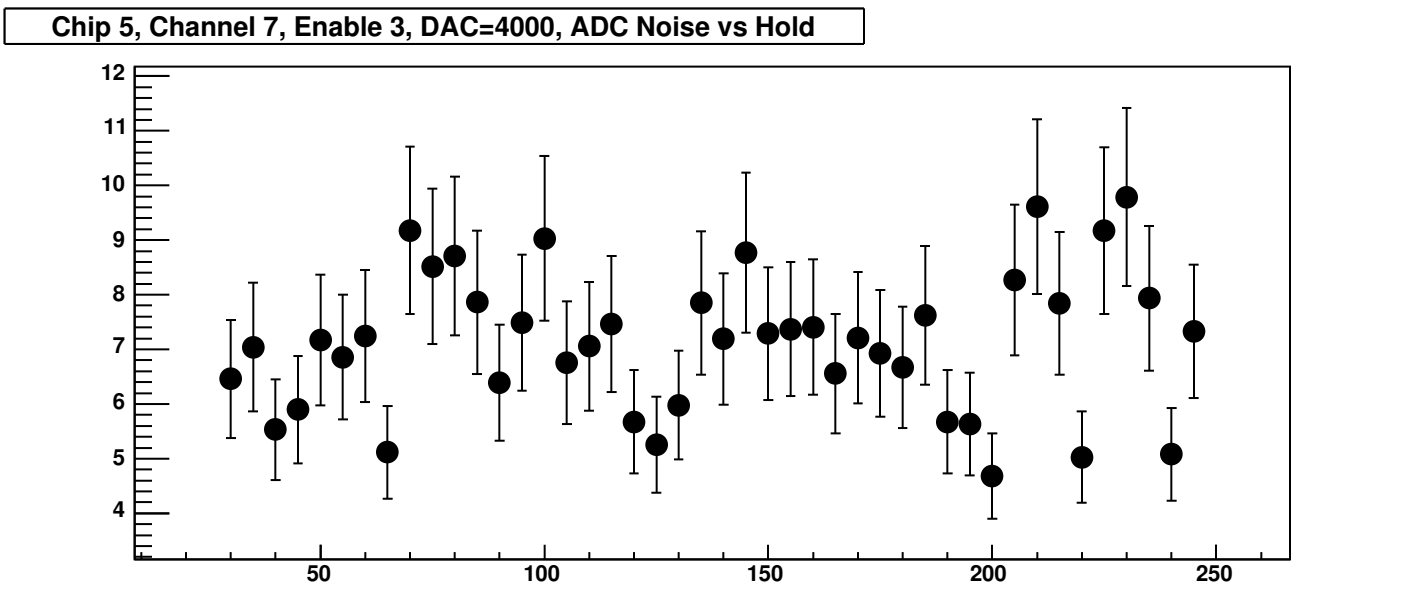


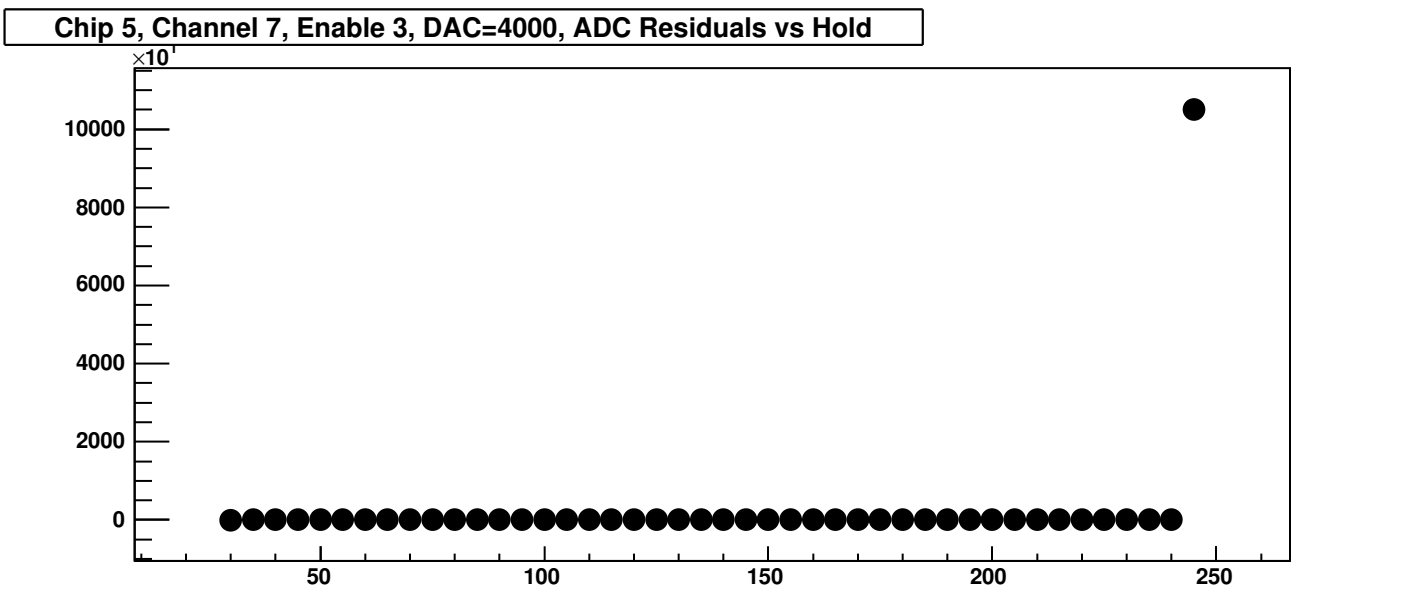

Chip 5, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 149 / 39
p0 -857 ± 0.6399
p1 4.515 ± 0.02476
p2 1.106e+04 ± 1.354
p3 31.24 ± 0.008543
10000
8000
6000
4000
2000
0
50
100
150
200
250


Chip 5, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold
45
40
35
30
25
20
15
10
5
50
100
150
200
250

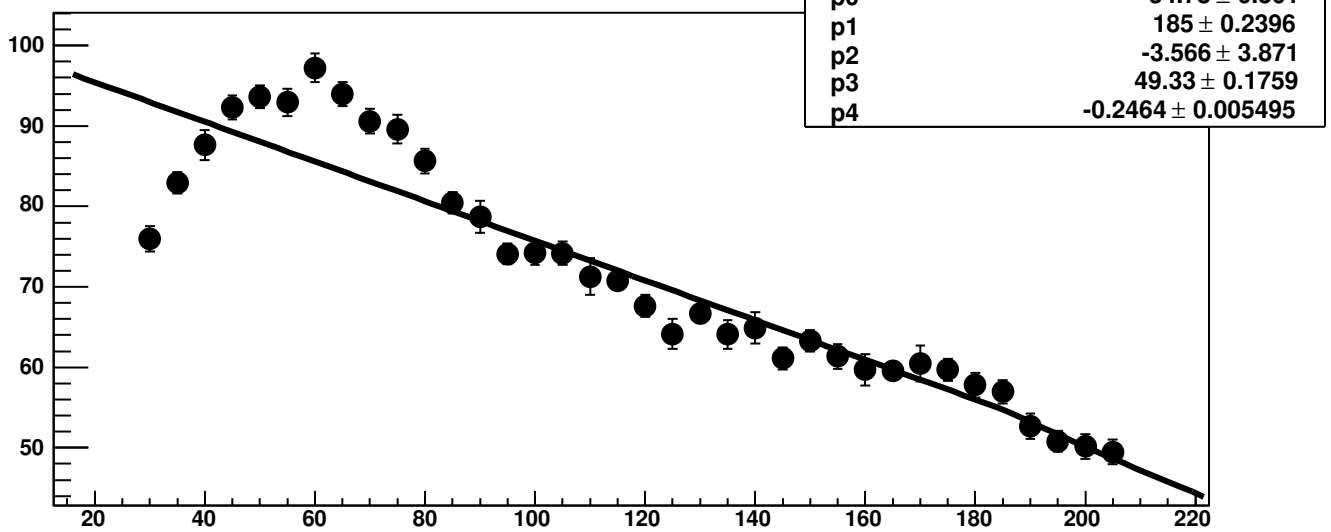

Chip 5, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
50
100
150
200
250


Chip 5, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 813.4 / 38
p0 45.19 ± 0.4565
p1 240.3 ± 0.002745
p2 -1.051e+05 ± 643.4
p3 -103.1 ± 1.469
p4 -0.2563 ± 0.003733
110
100
90
80
70
60
50
40
50
100
150
200
250

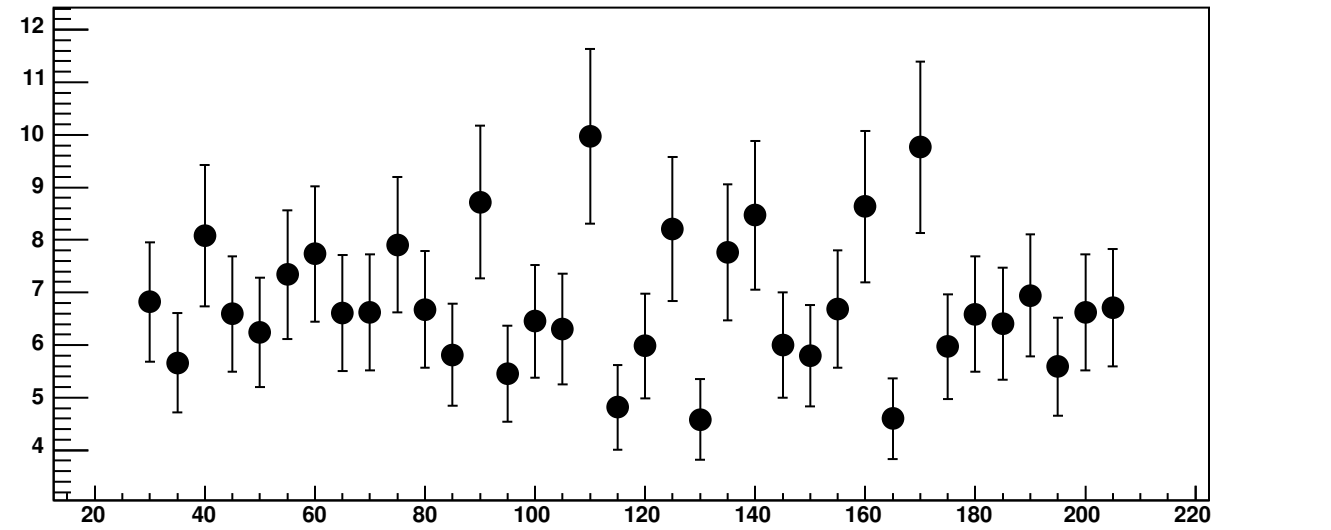

Chip 5, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
50
100
150
200
250


Chip 5, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold
×10¹
10000
8000
6000
4000
2000
0
50
100
150
200
250

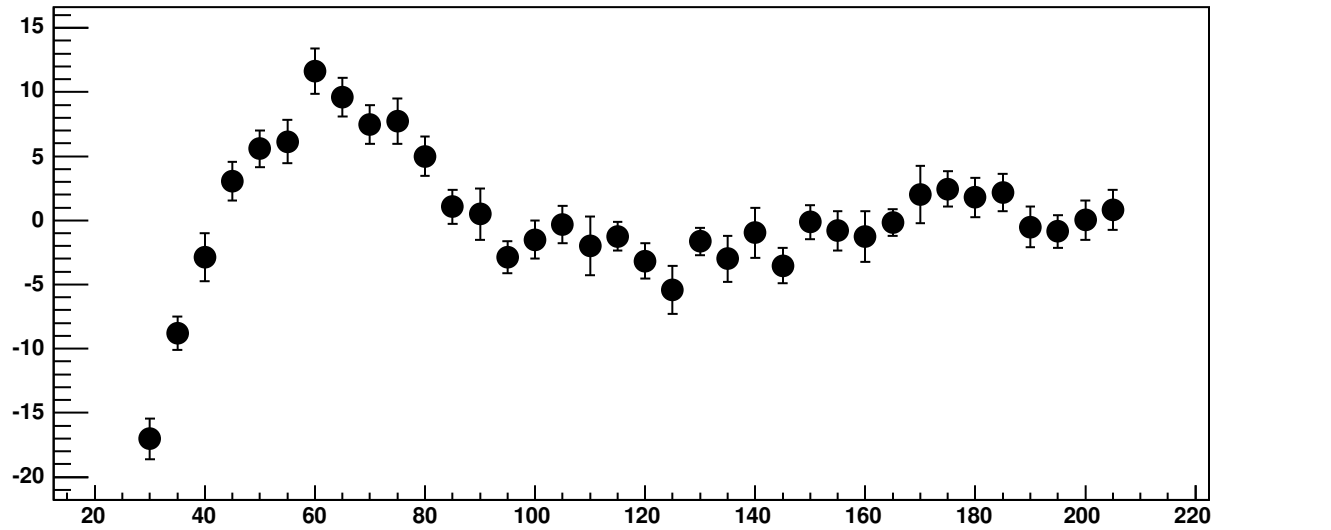
Chip 5, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold



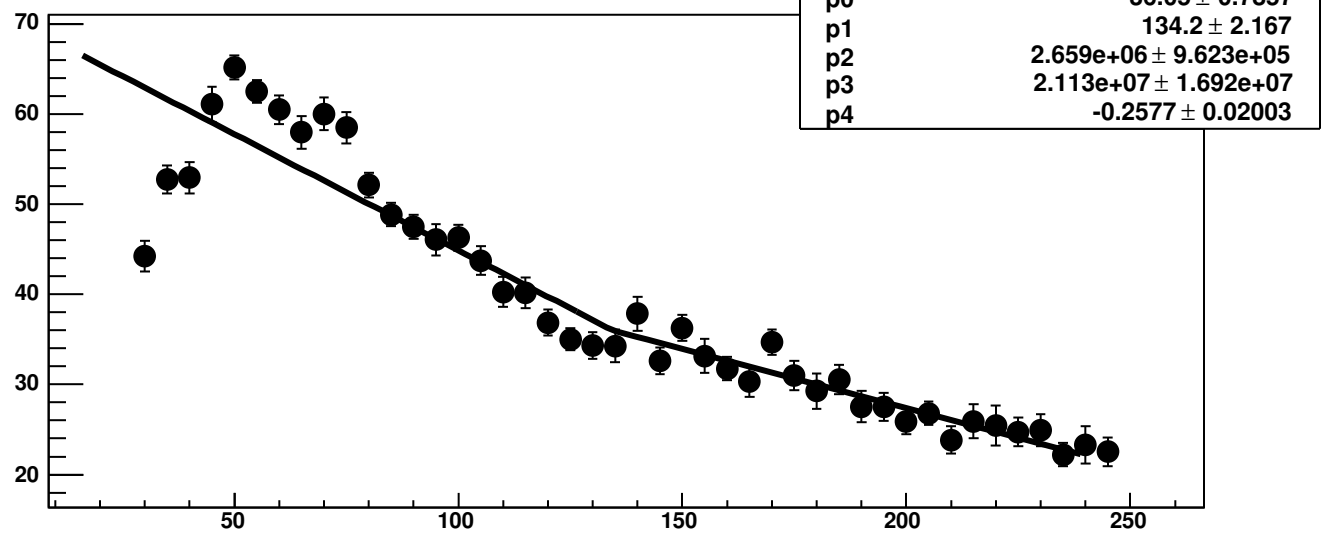
Chip 5, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



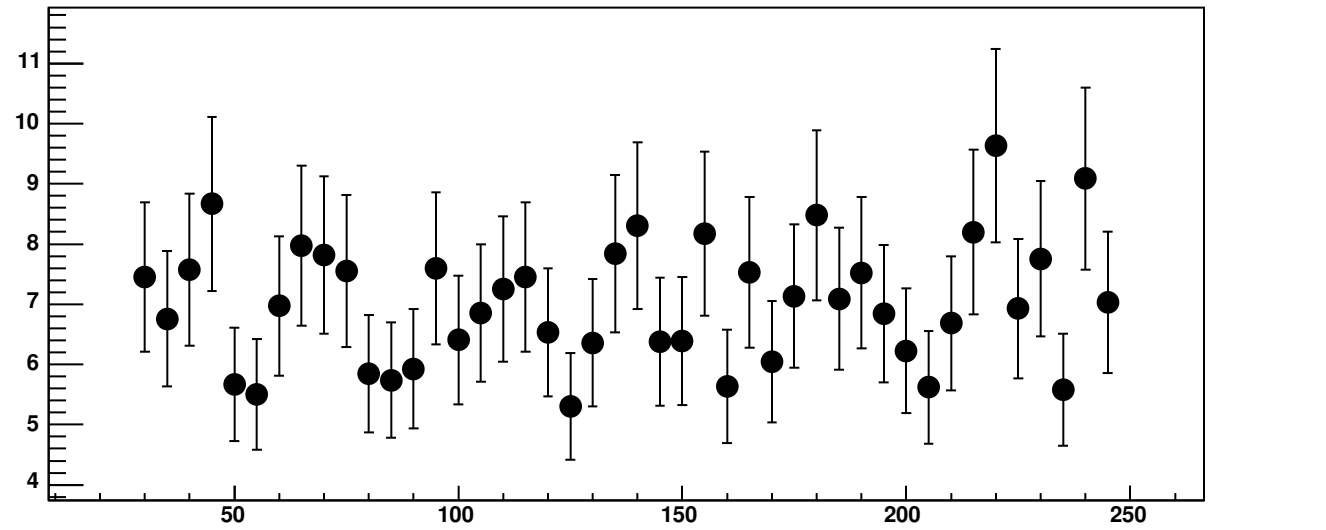
Chip 5, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold



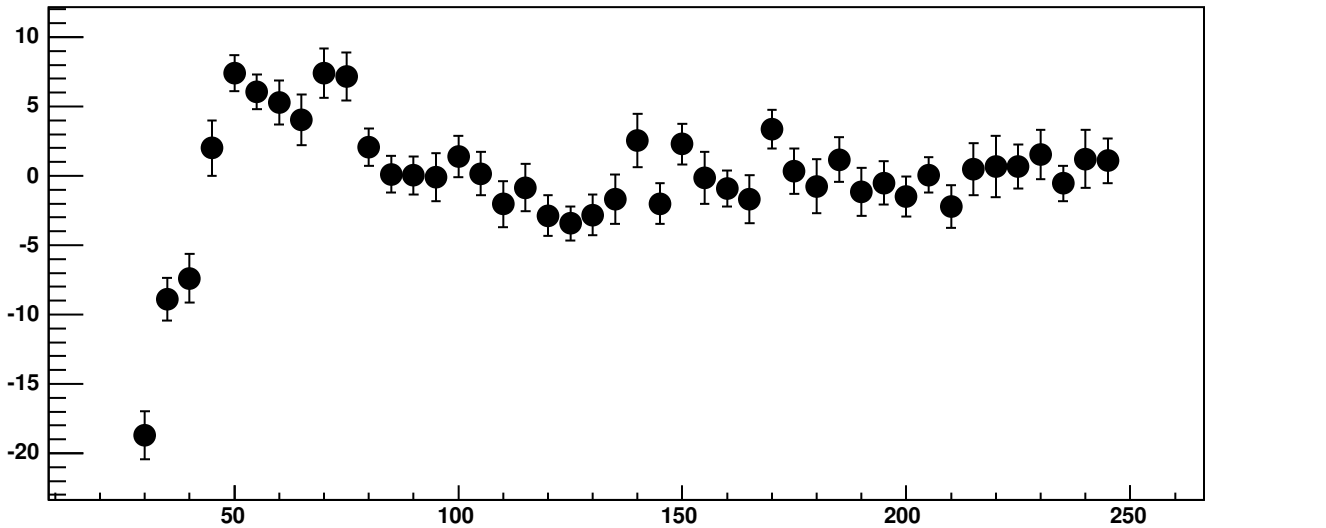
Chip 5, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold



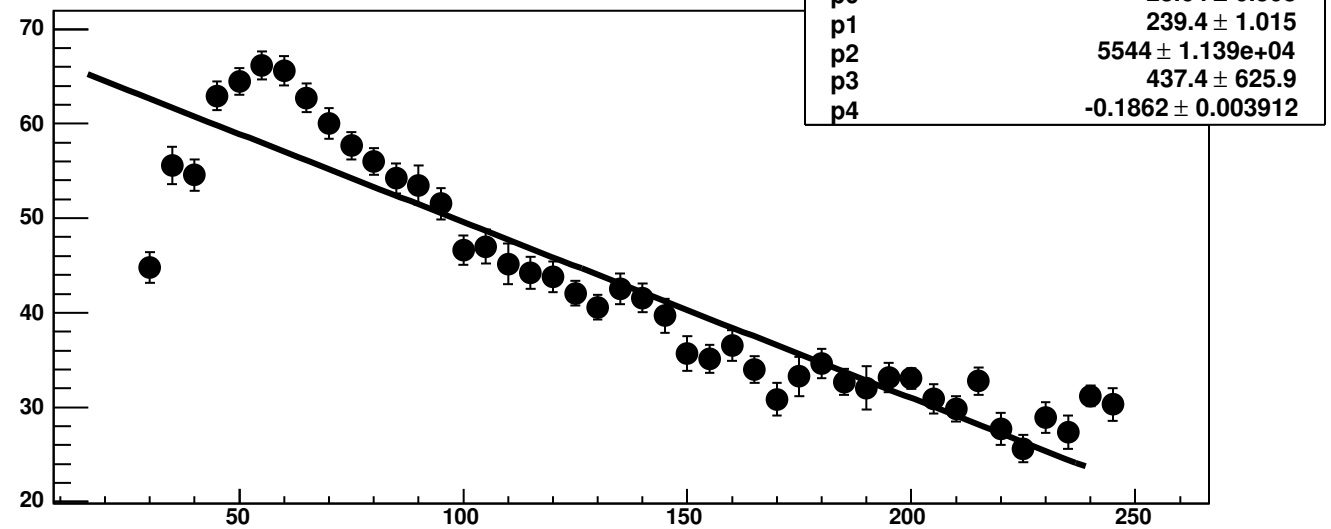
Chip 5, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



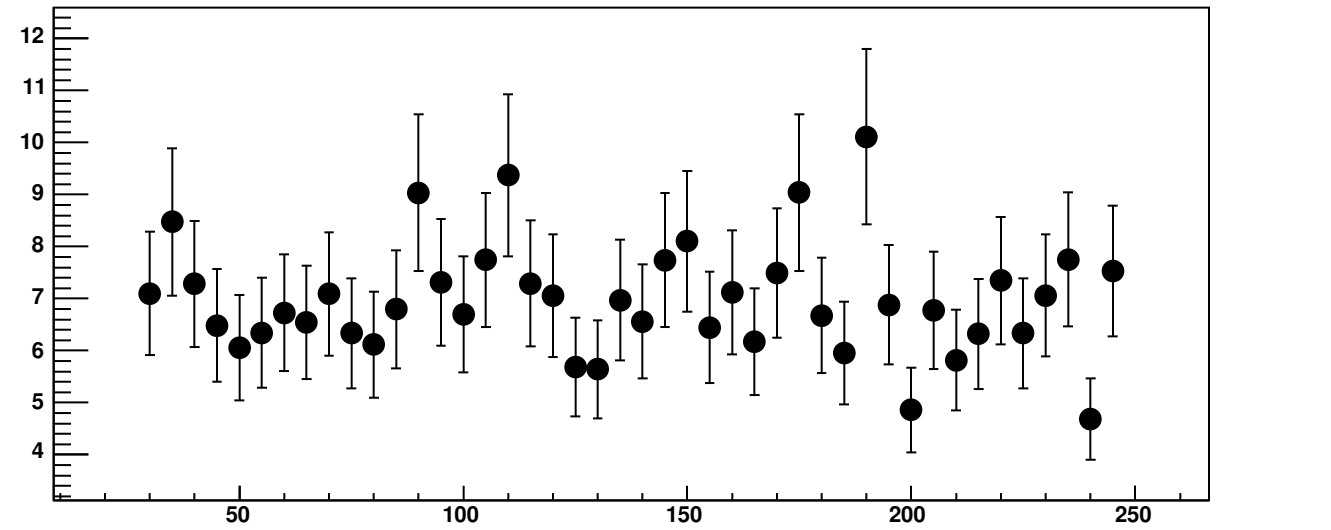
Chip 5, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold



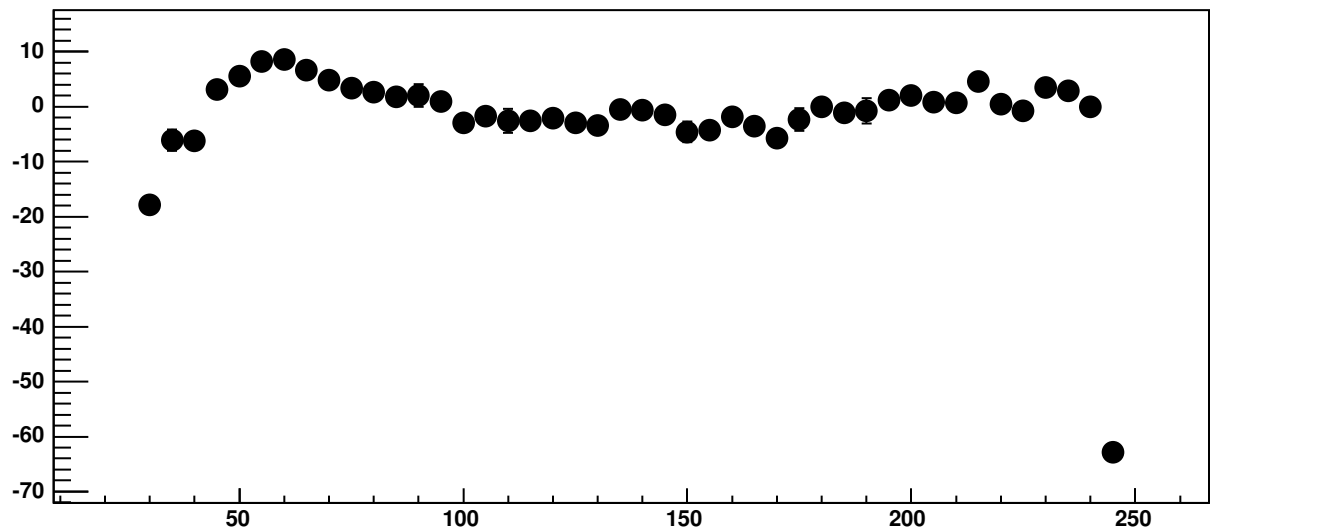
Chip 5, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold



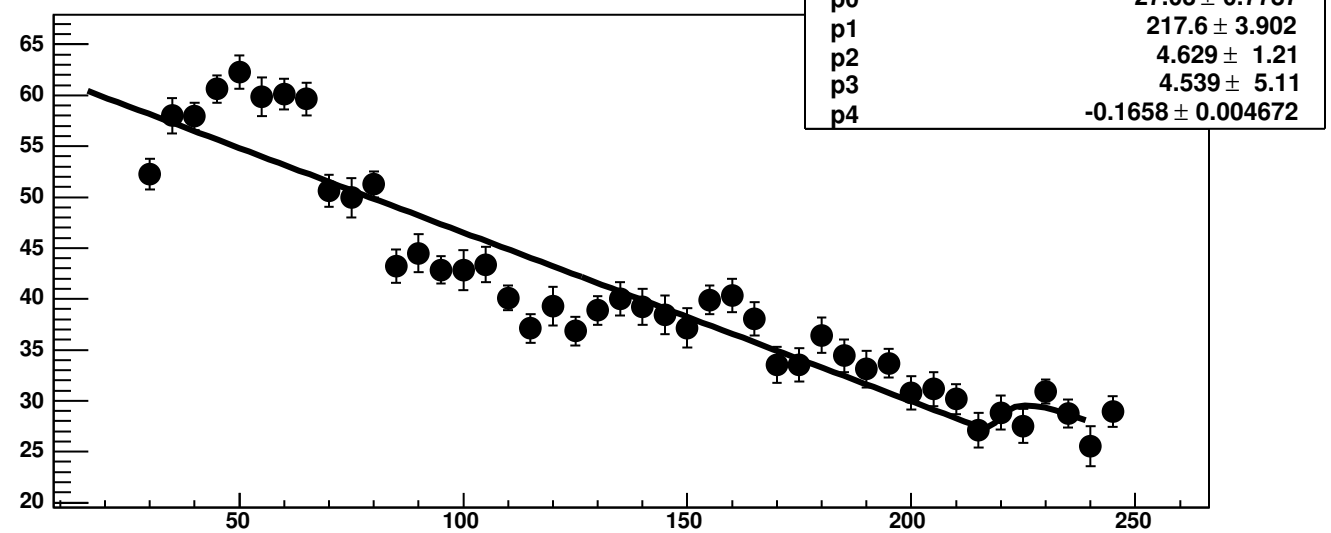
Chip 5, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



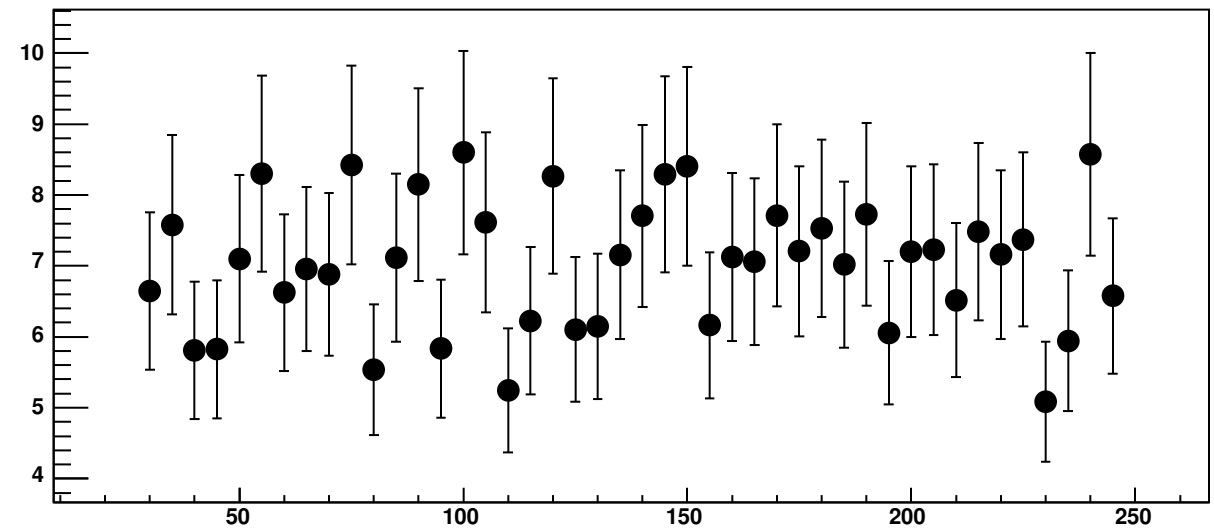
Chip 5, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold



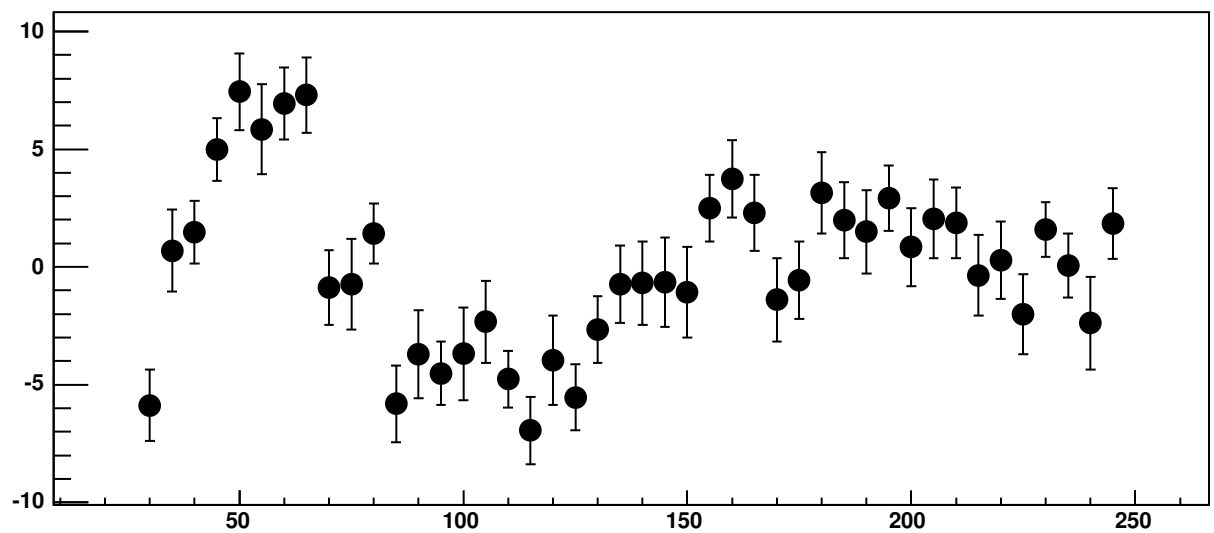
Chip 5, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold



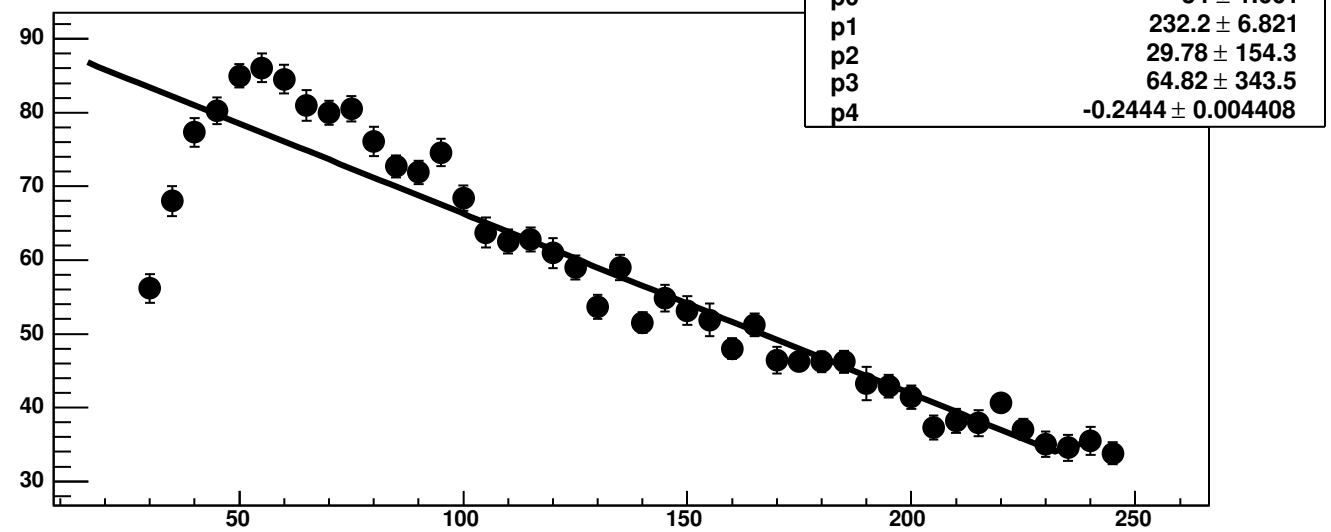
Chip 5, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold



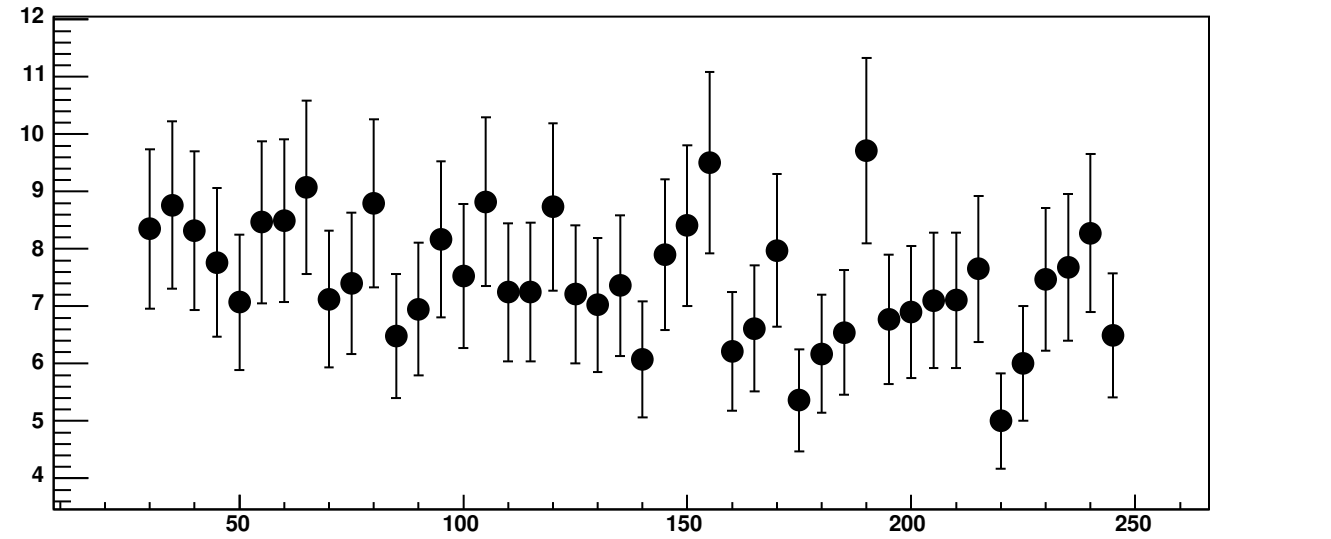
Chip 5, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold



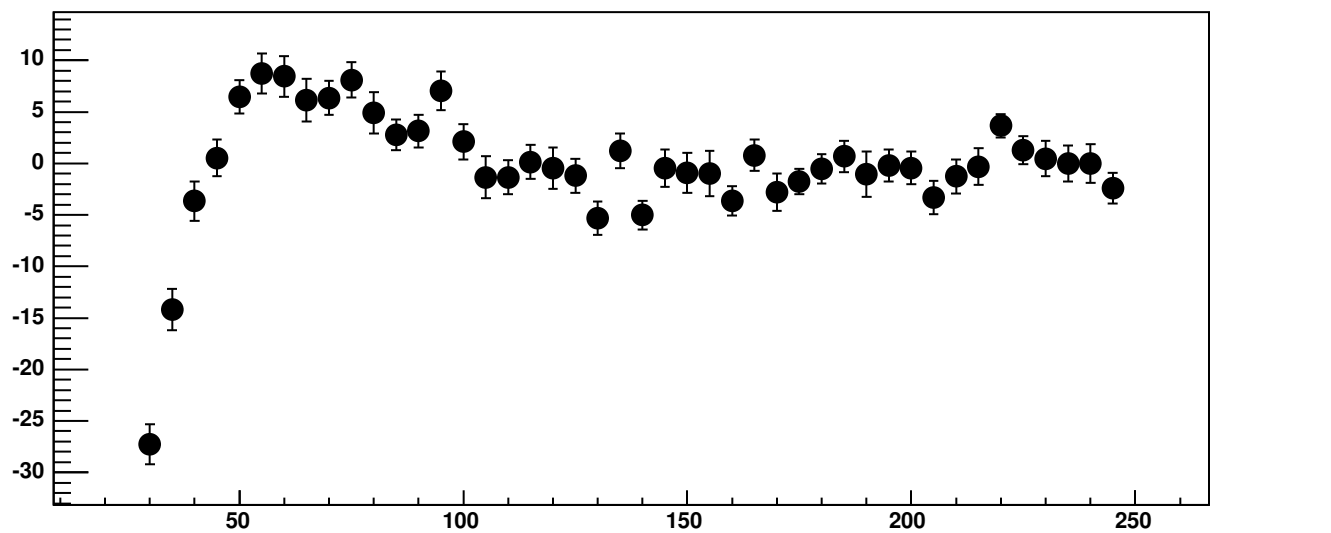
Chip 5, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold



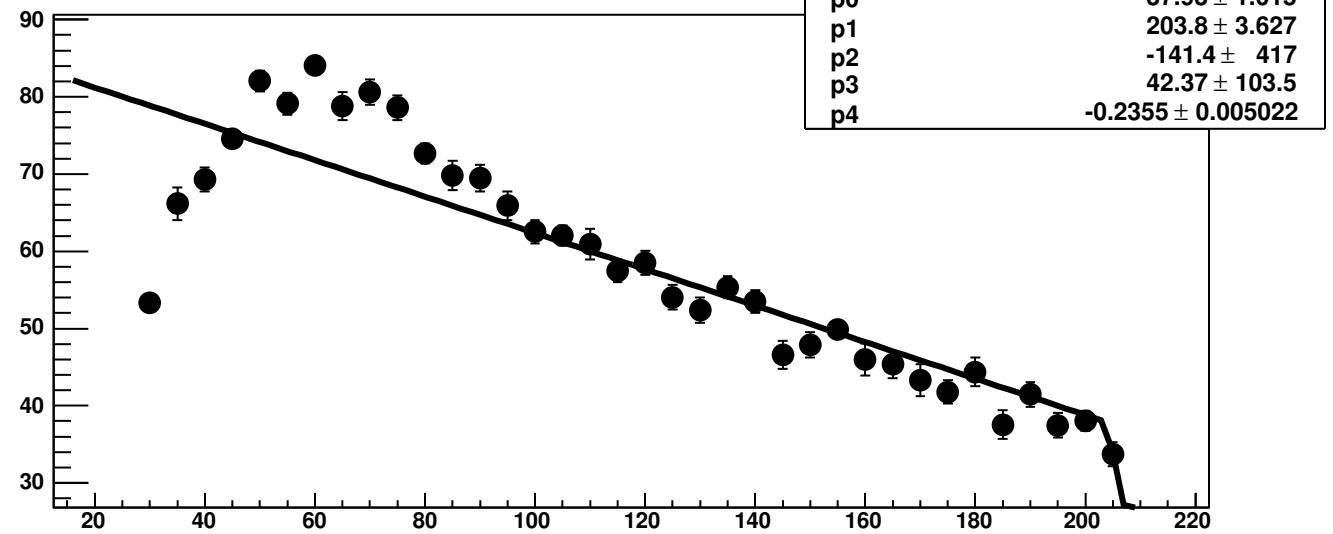
Chip 5, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



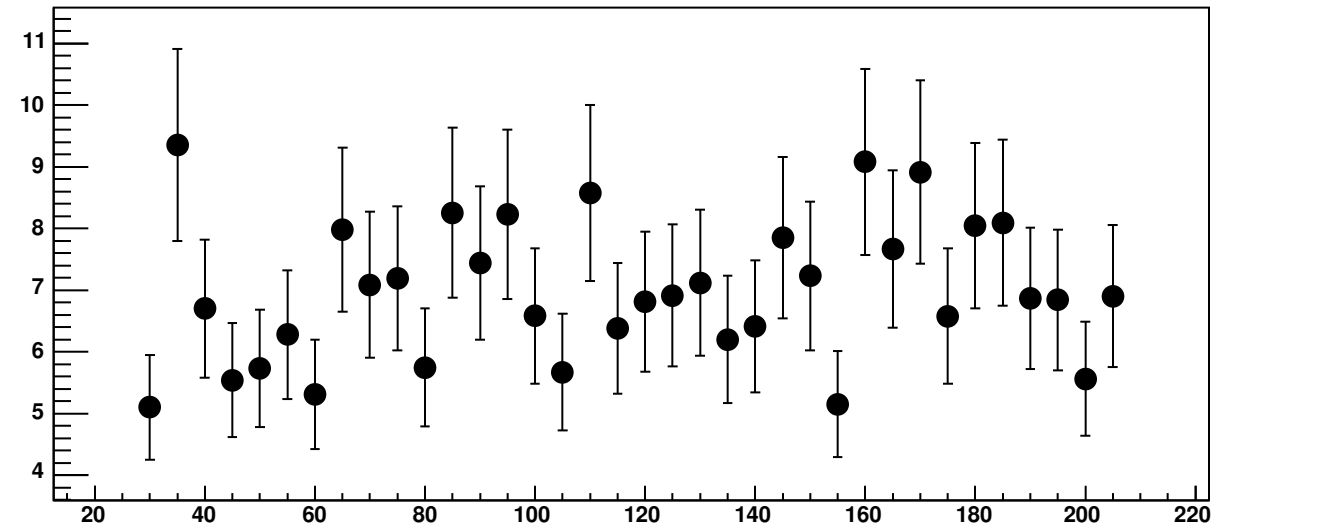
Chip 5, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold



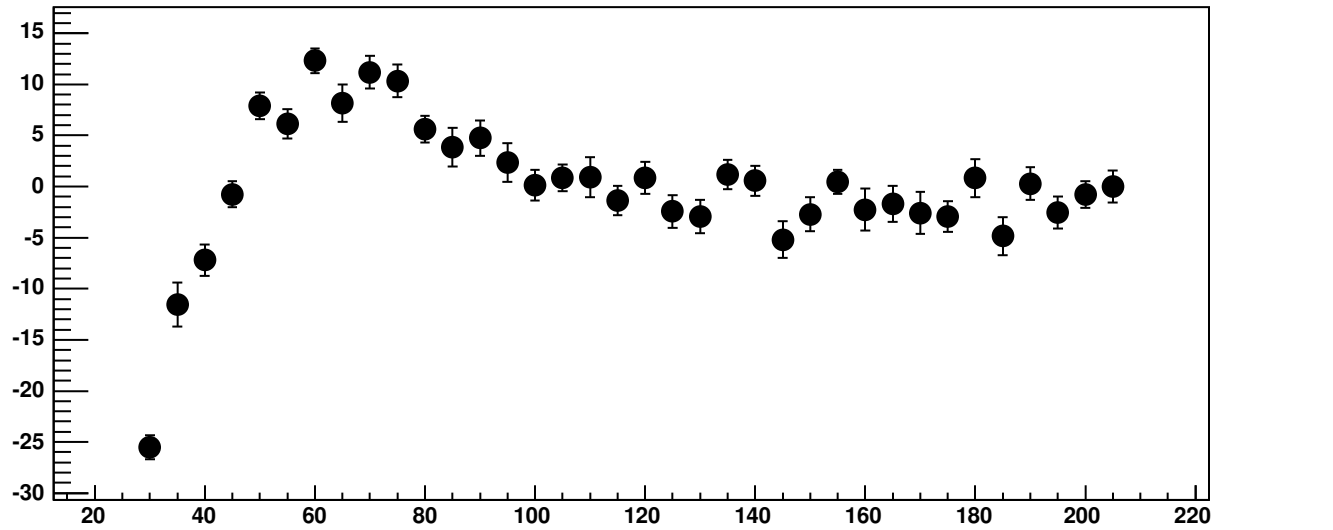
Chip 5, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold



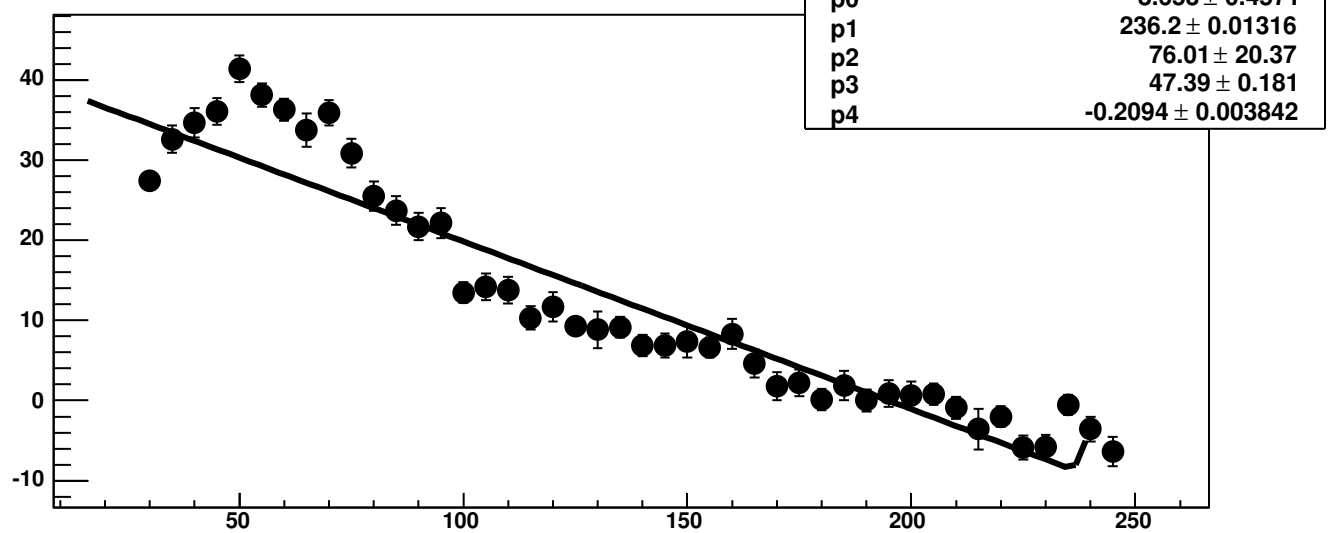
Chip 5, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



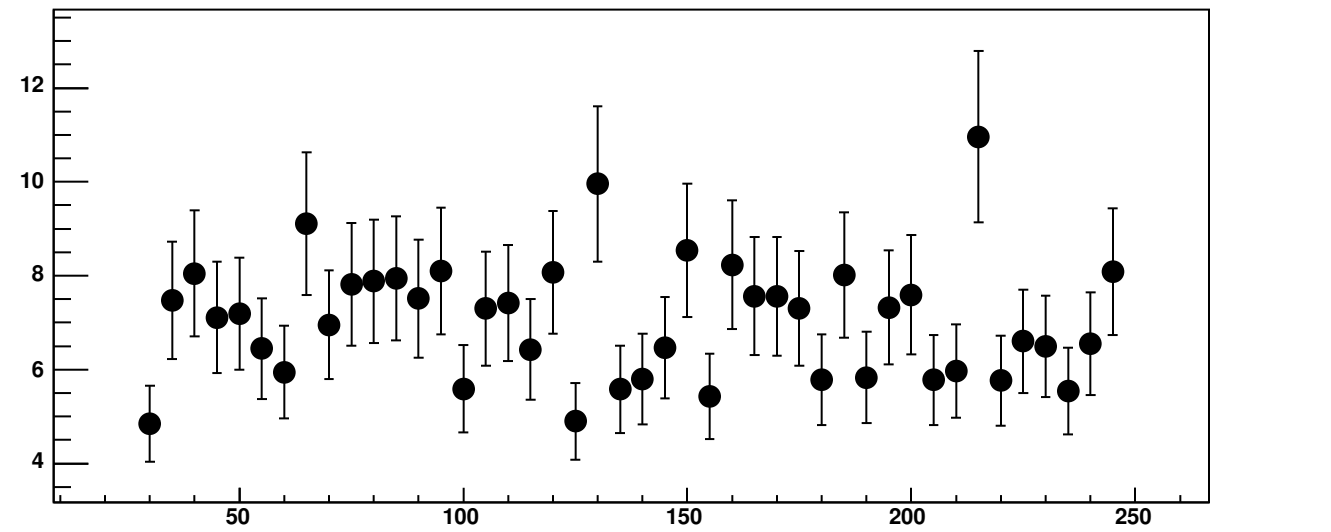
Chip 5, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold



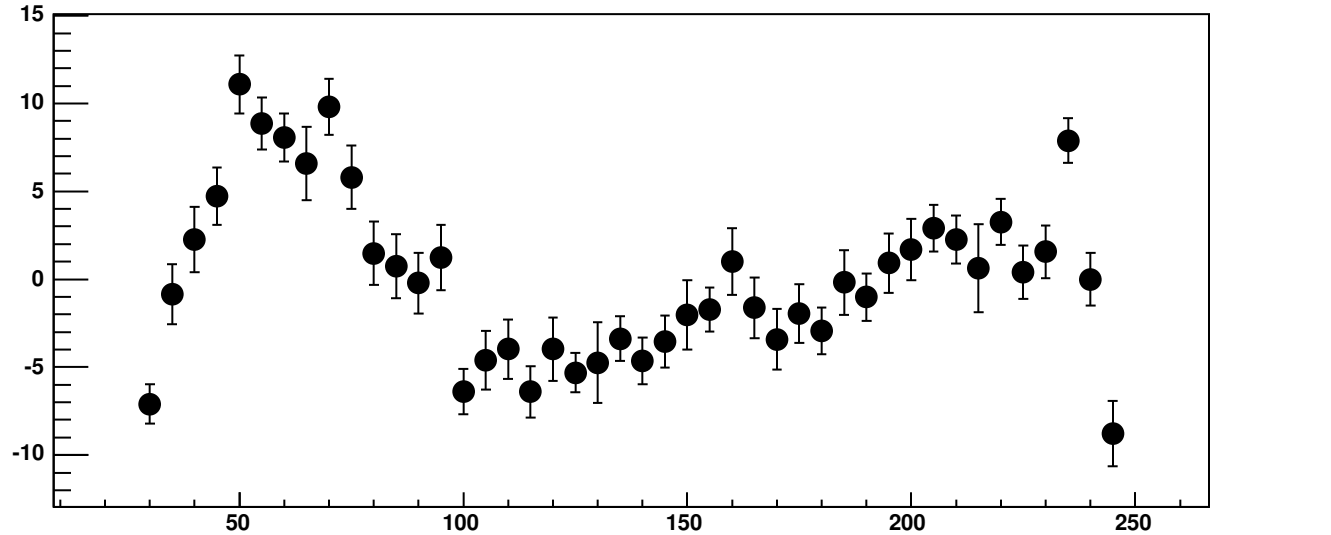
Chip 5, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold



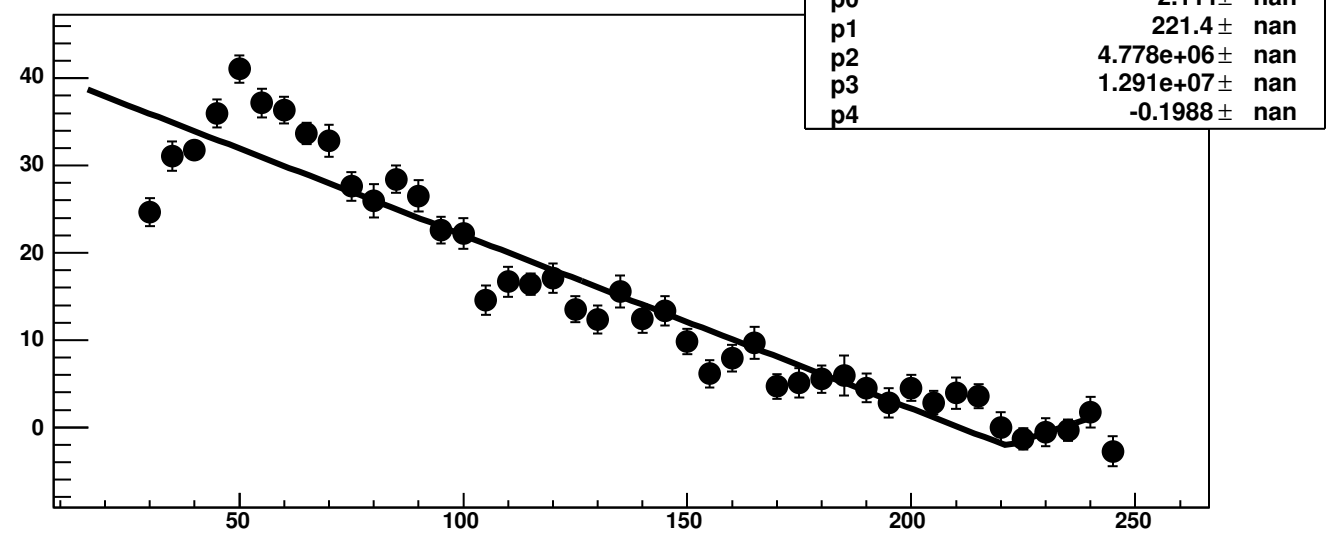
Chip 5, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold



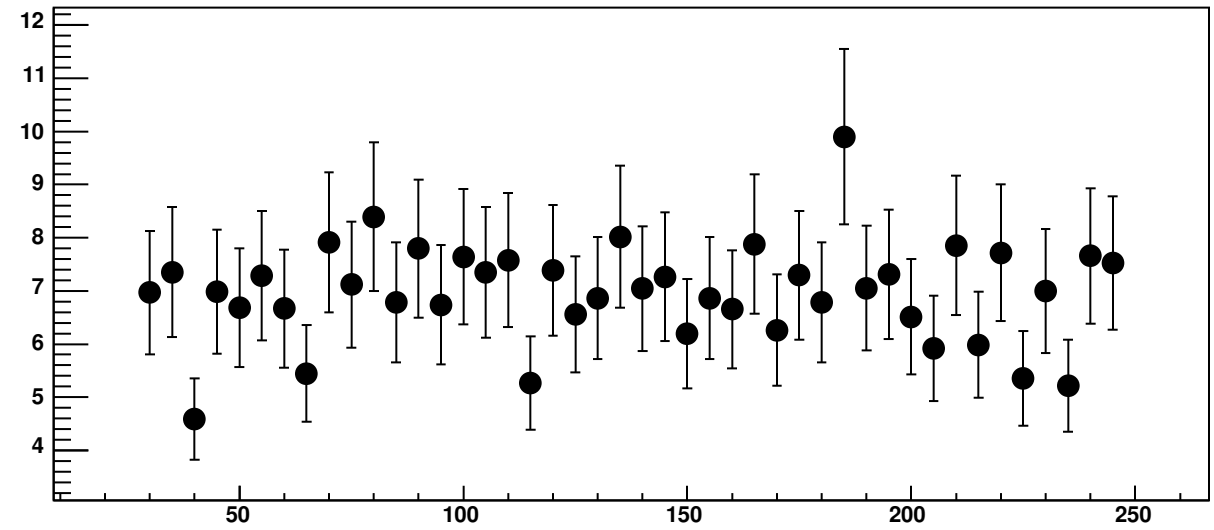
Chip 5, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold



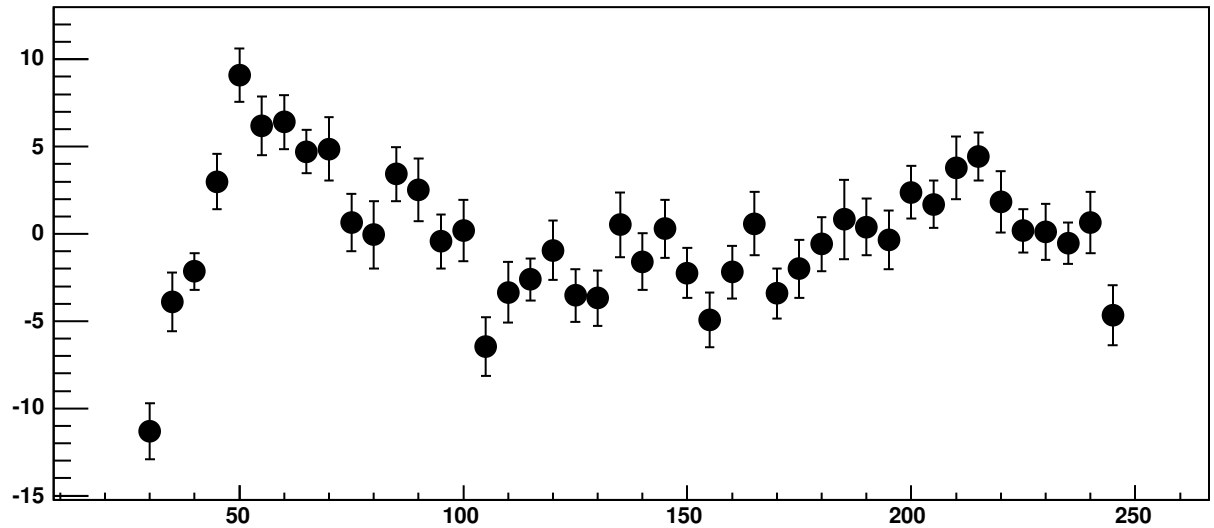
Chip 5, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold



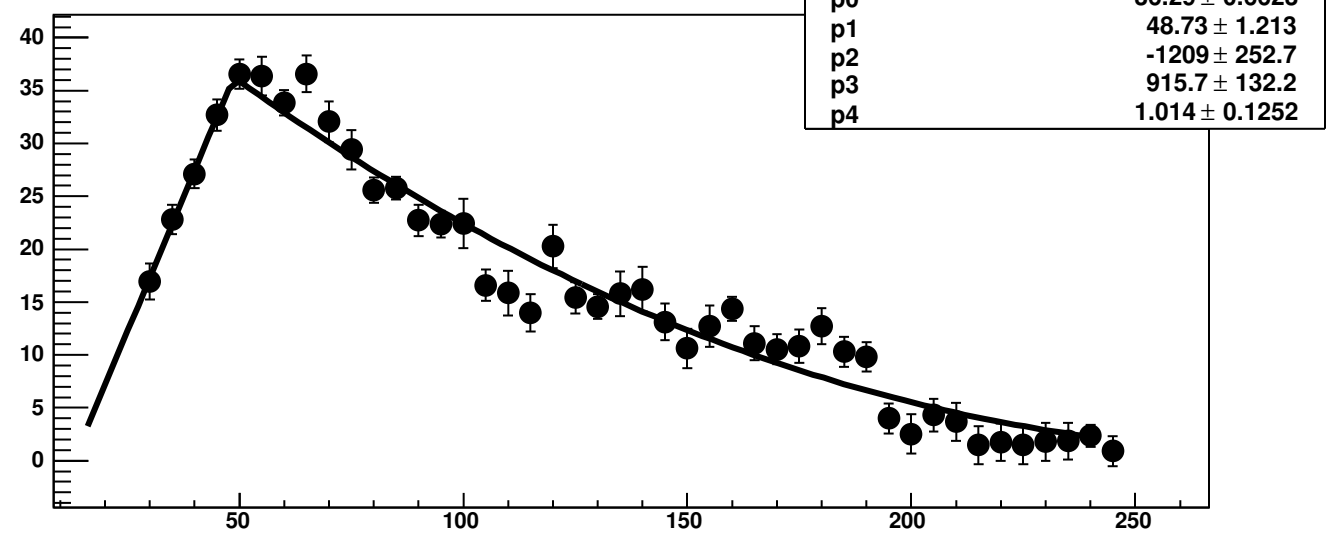
Chip 5, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



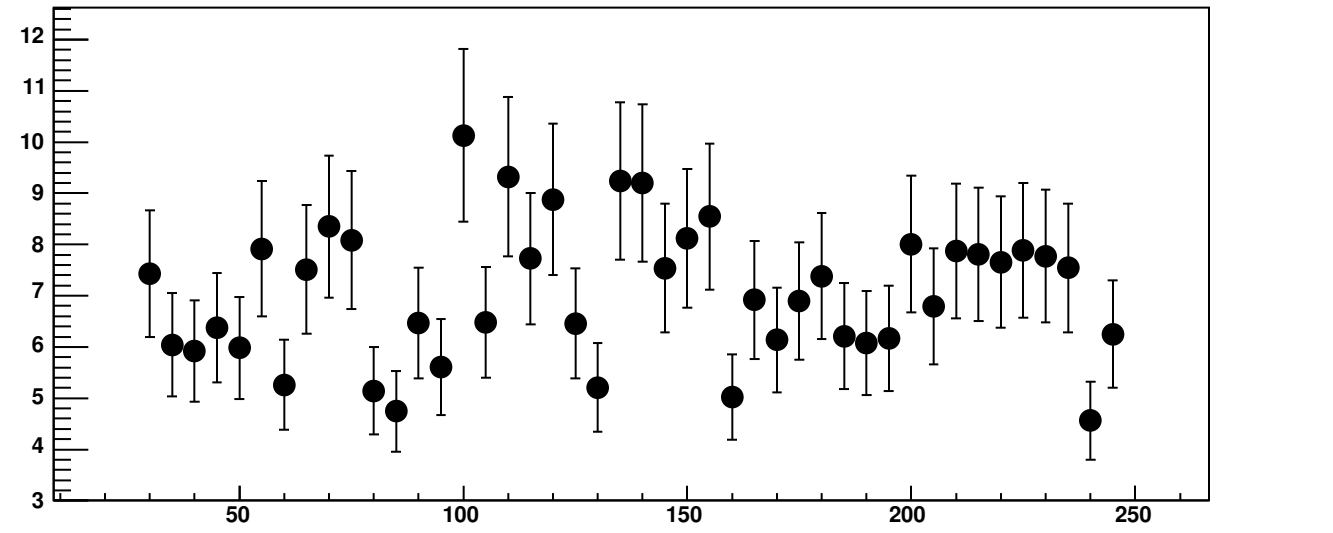
Chip 5, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold



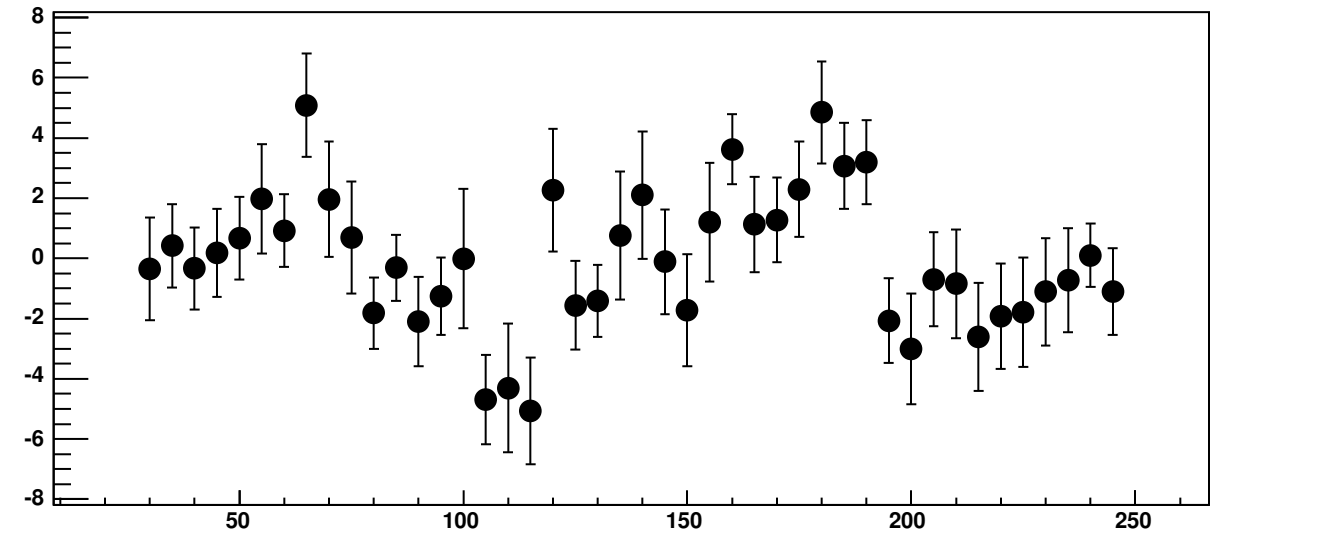
Chip 5, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

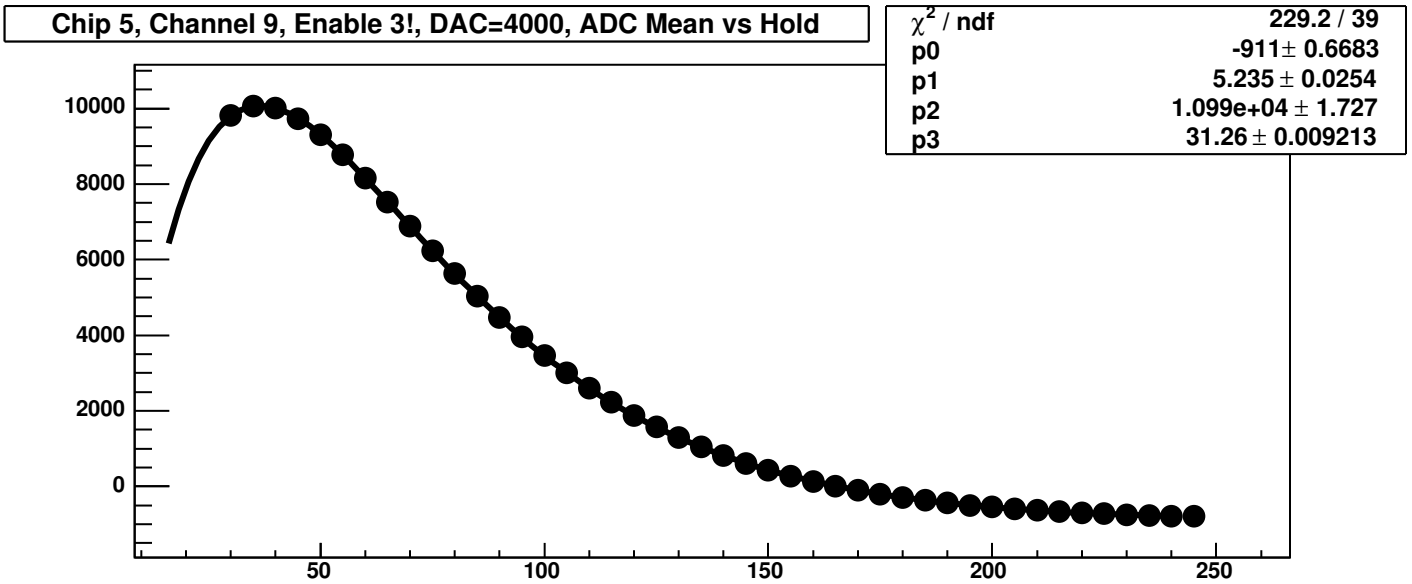


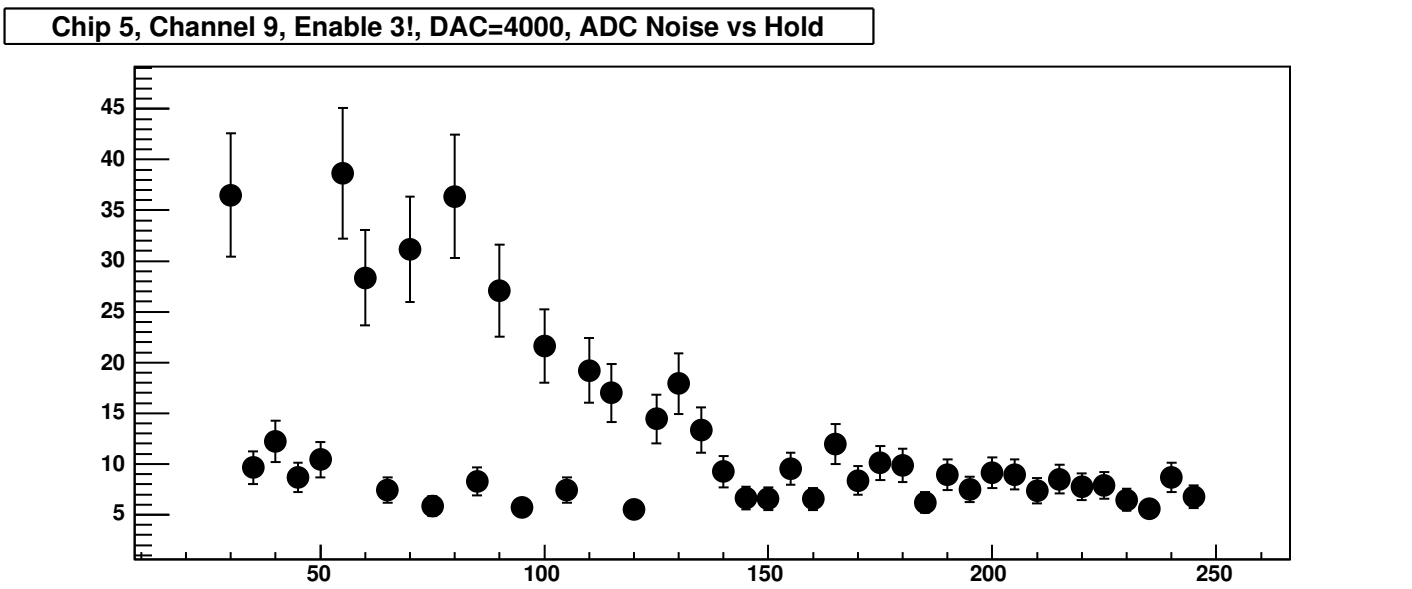
Chip 5, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold

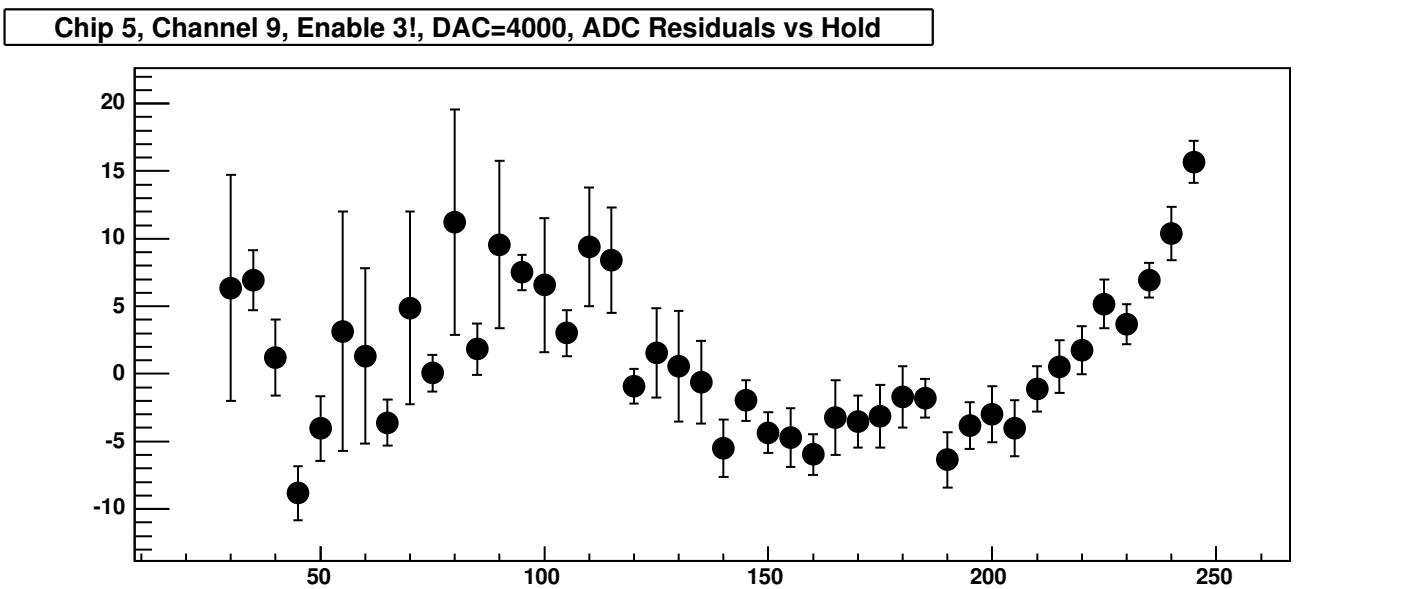


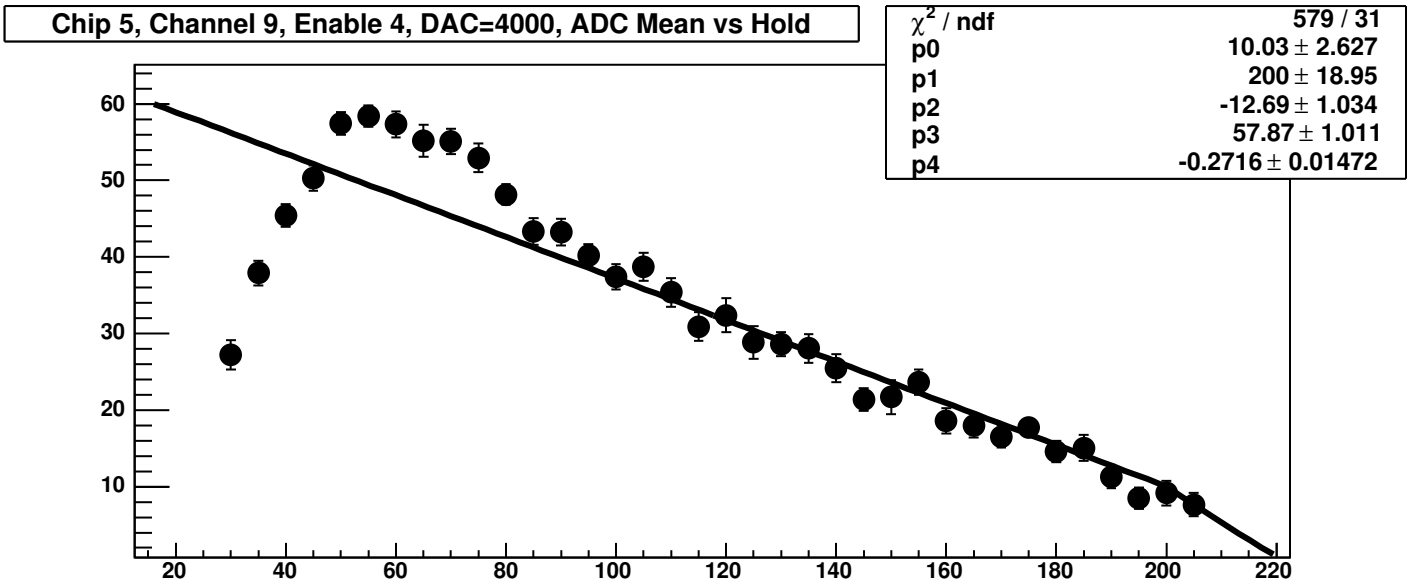
Chip 5, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold

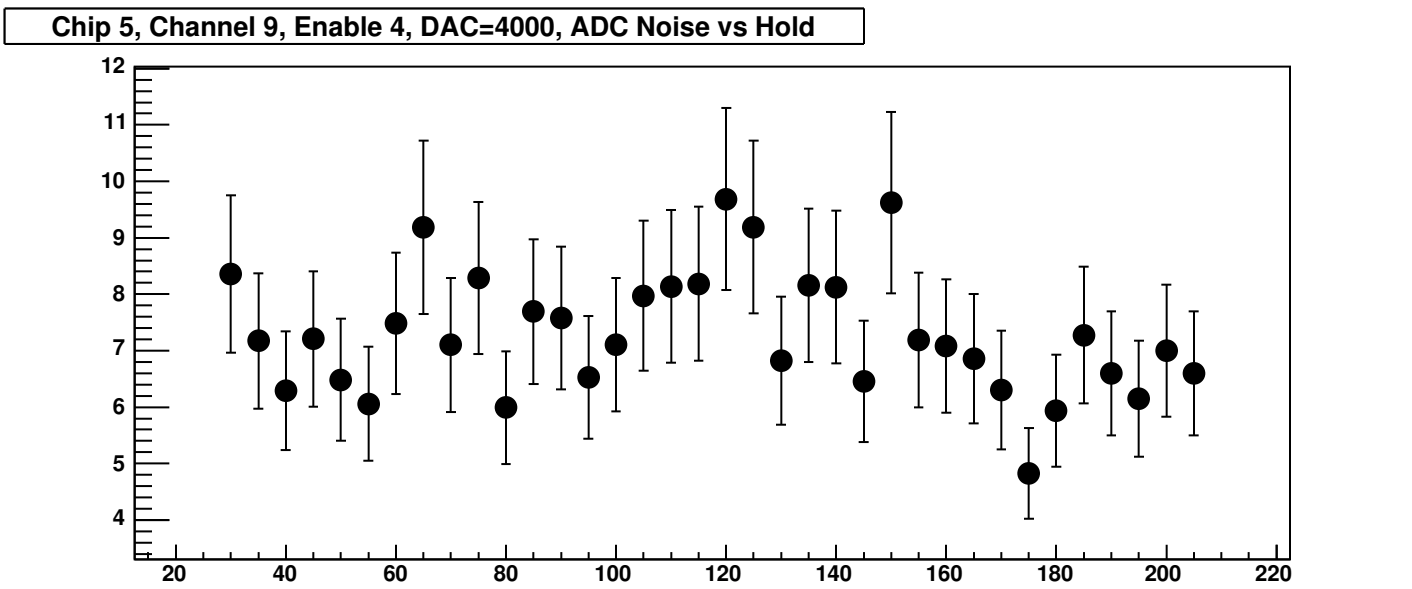


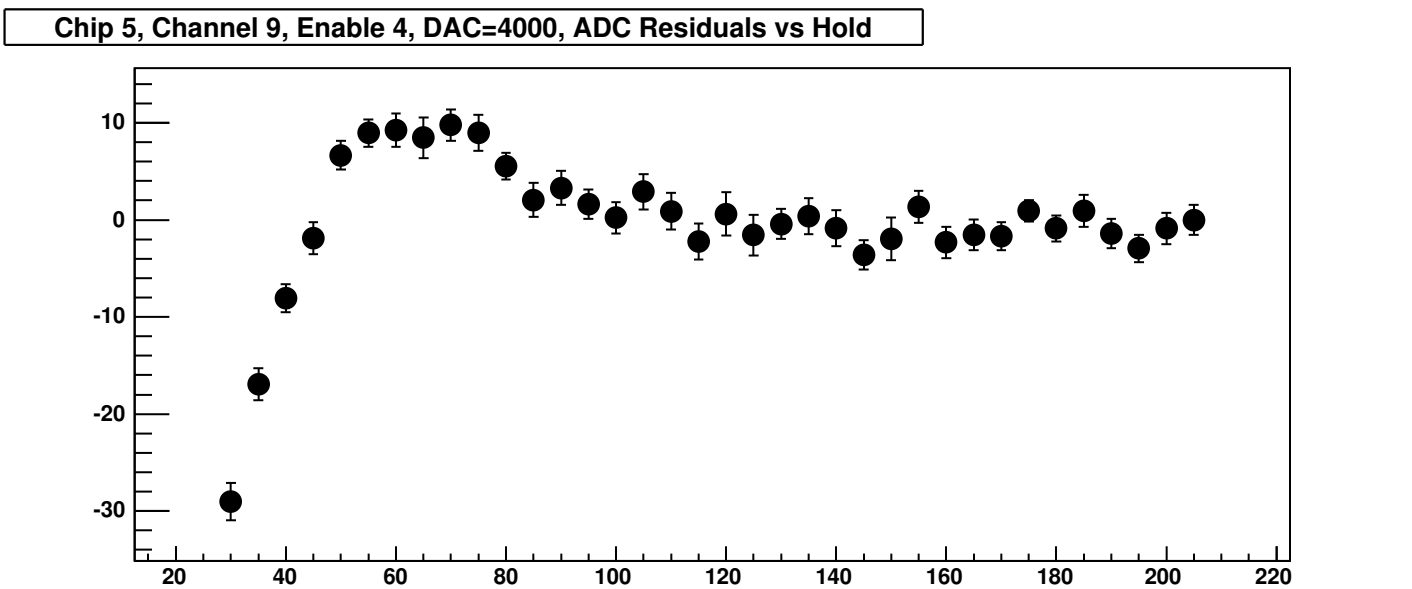

Chip 5, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 229.2 / 39
p0 -911 ± 0.6683
p1 5.235 ± 0.0254
p2 1.099e+04 ± 1.727
p3 31.26 ± 0.009213
10000
8000
6000
4000
2000
0
50
100
150
200
250

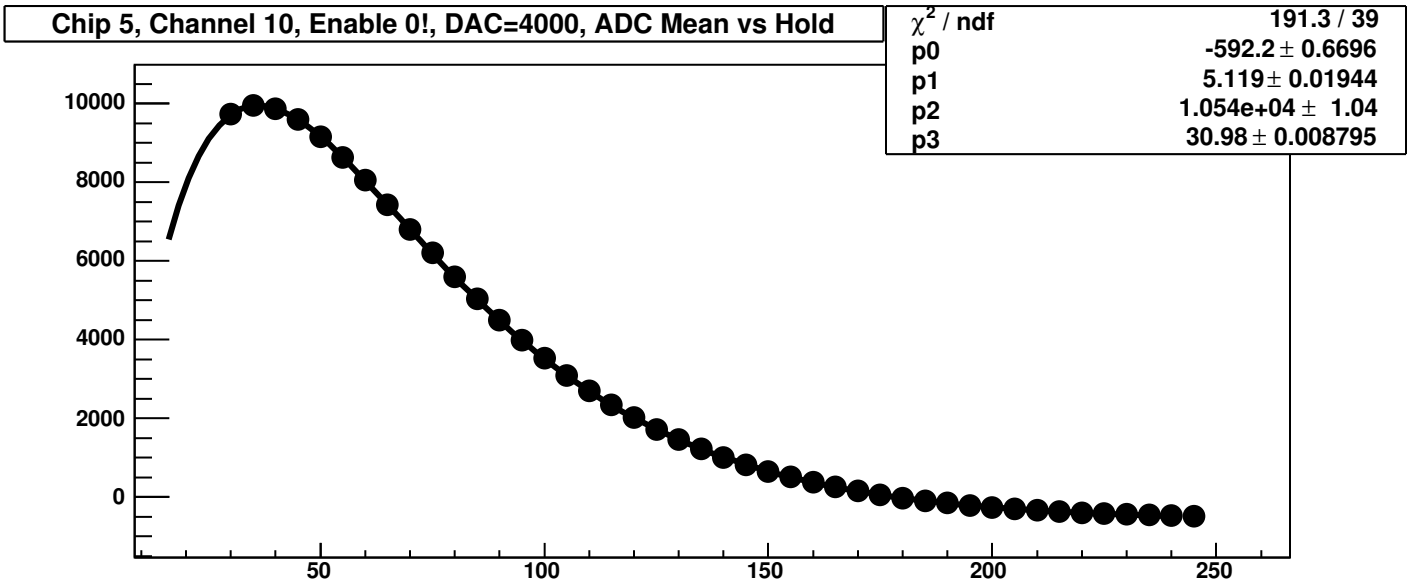

Chip 5, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold
45
40
35
30
25
20
15
10
5
50
100
150
200
250

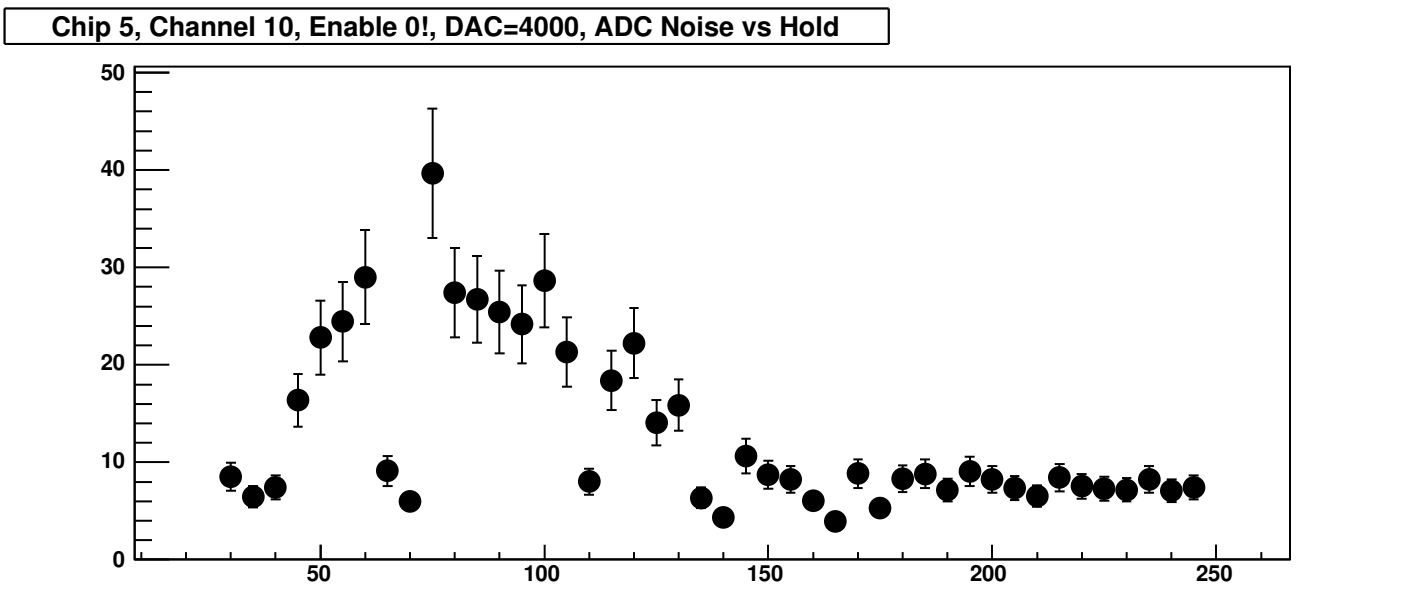

Chip 5, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
50
100
150
200
250

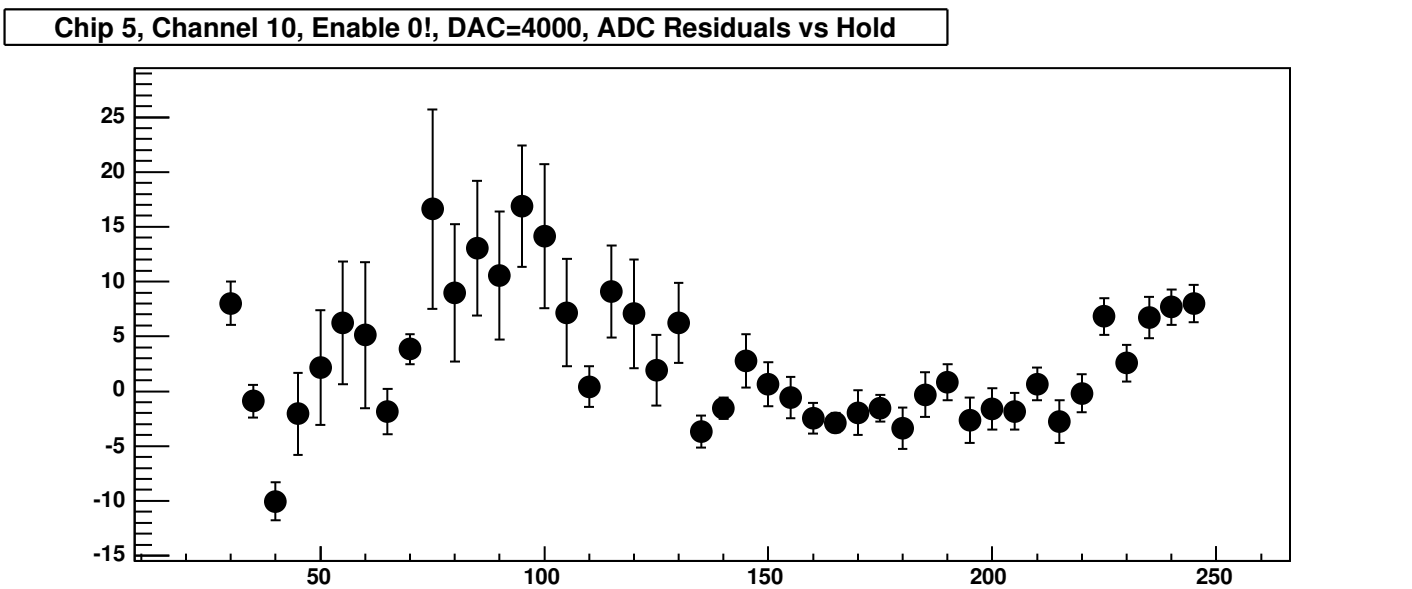

Chip 5, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 579 / 31
p0 10.03 ± 2.627
p1 200 ± 18.95
p2 -12.69 ± 1.034
p3 57.87 ± 1.011
p4 -0.2716 ± 0.01472
60
50
40
30
20
10
20
40
60
80
100
120
140
160
180
200
220


Chip 5, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
20
40
60
80
100
120
140
160
180
200
220

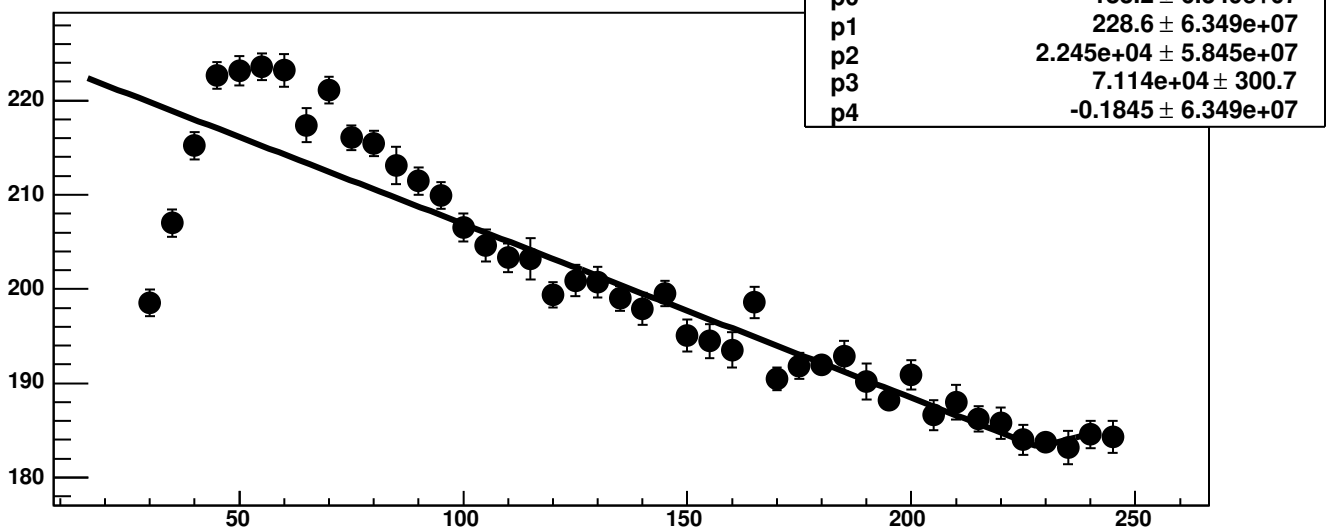

Chip 5, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold
10
0
-10
-20
-30
20
40
60
80
100
120
140
160
180
200
220


Chip 5, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 191.3 / 39
p0 -592.2 ± 0.6696
p1 5.119 ± 0.01944
p2 1.054e+04 ± 1.04
p3 30.98 ± 0.008795
10000
8000
6000
4000
2000
0
50
100
150
200
250

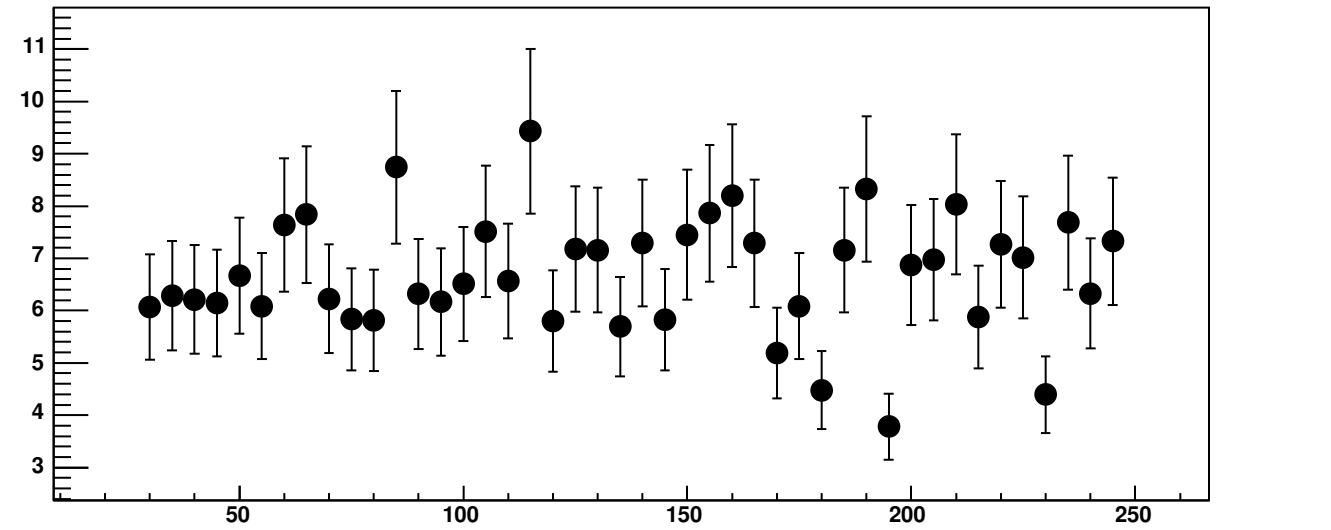

Chip 5, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
0
50
100
150
200
250


Chip 5, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
-15
50
100
150
200
250

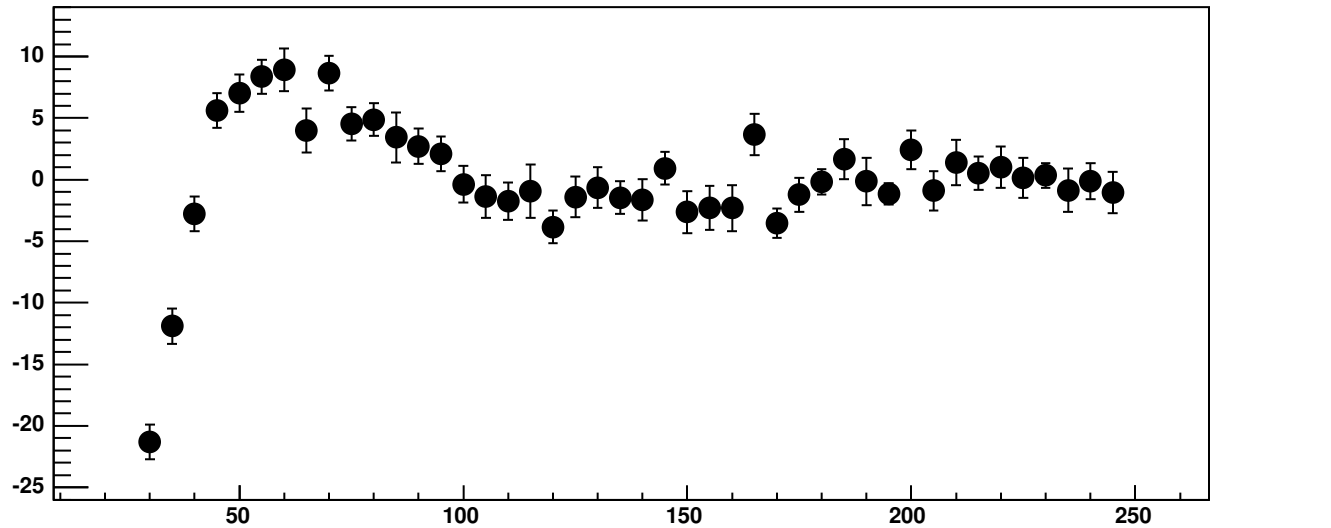
Chip 5, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold



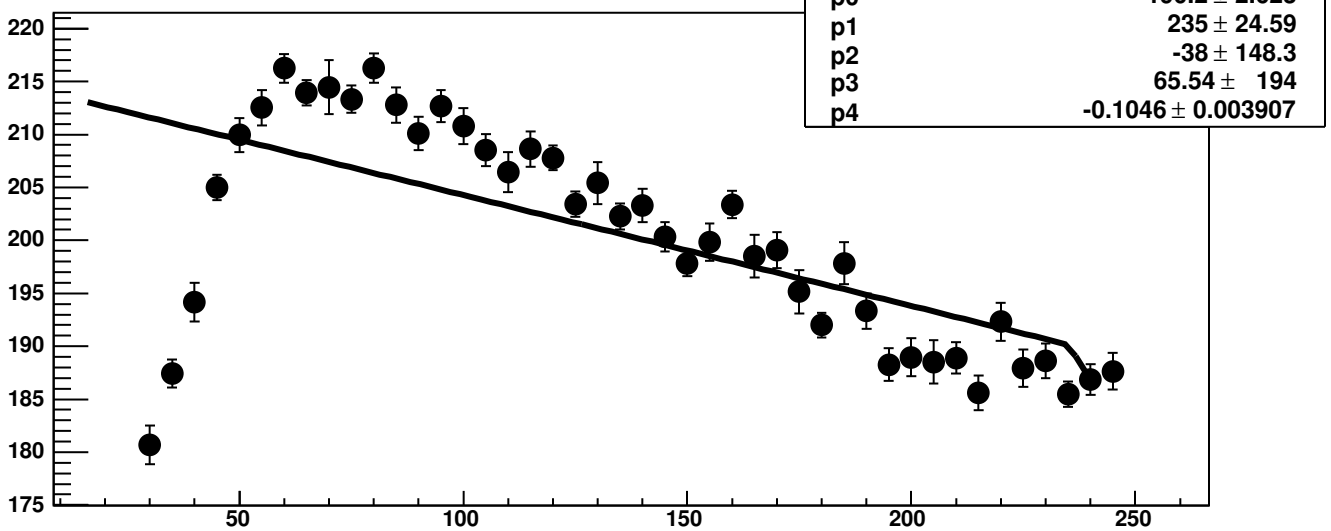
Chip 5, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



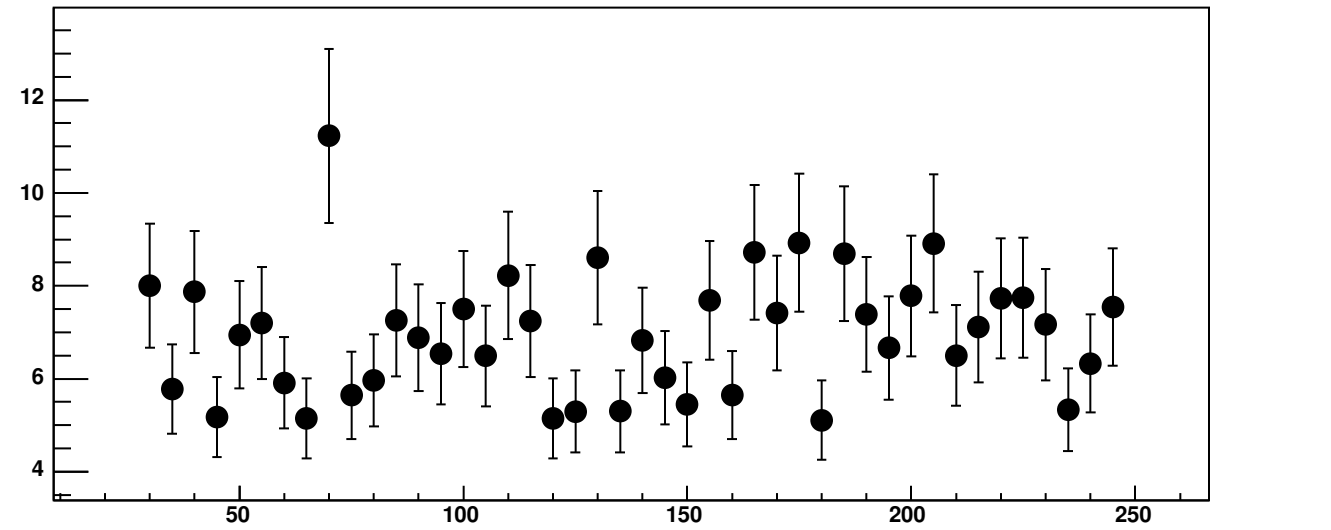
Chip 5, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold



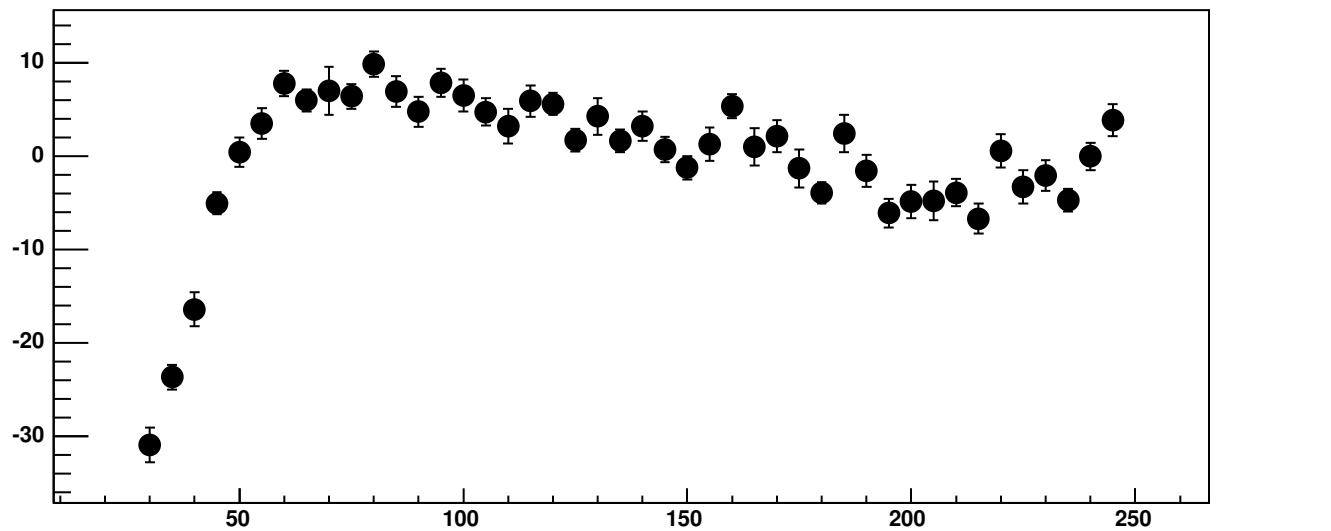
Chip 5, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold



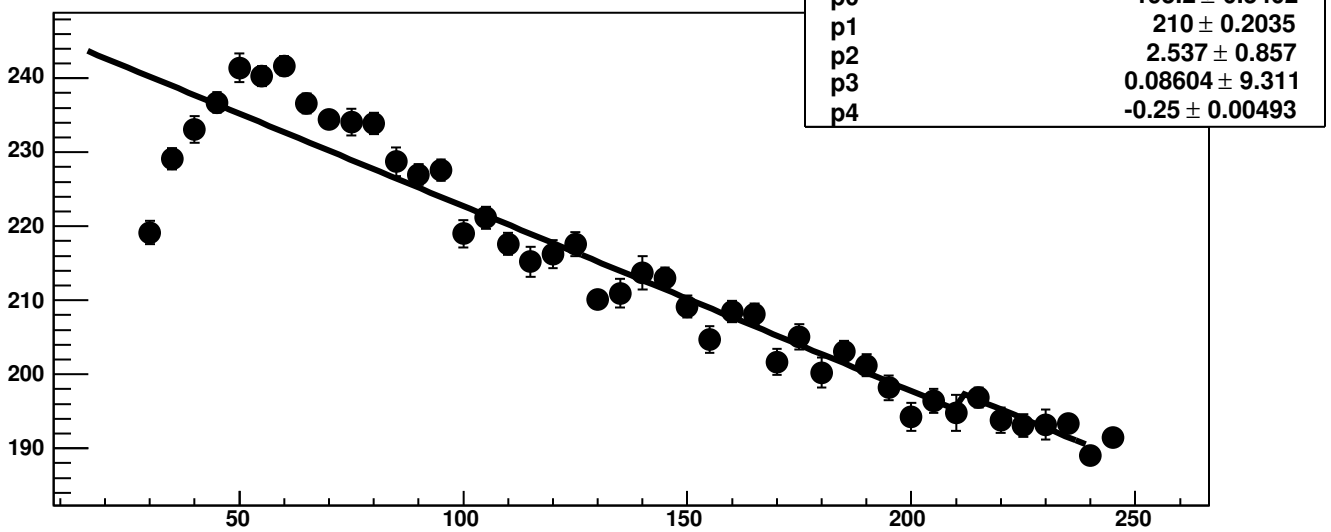
Chip 5, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



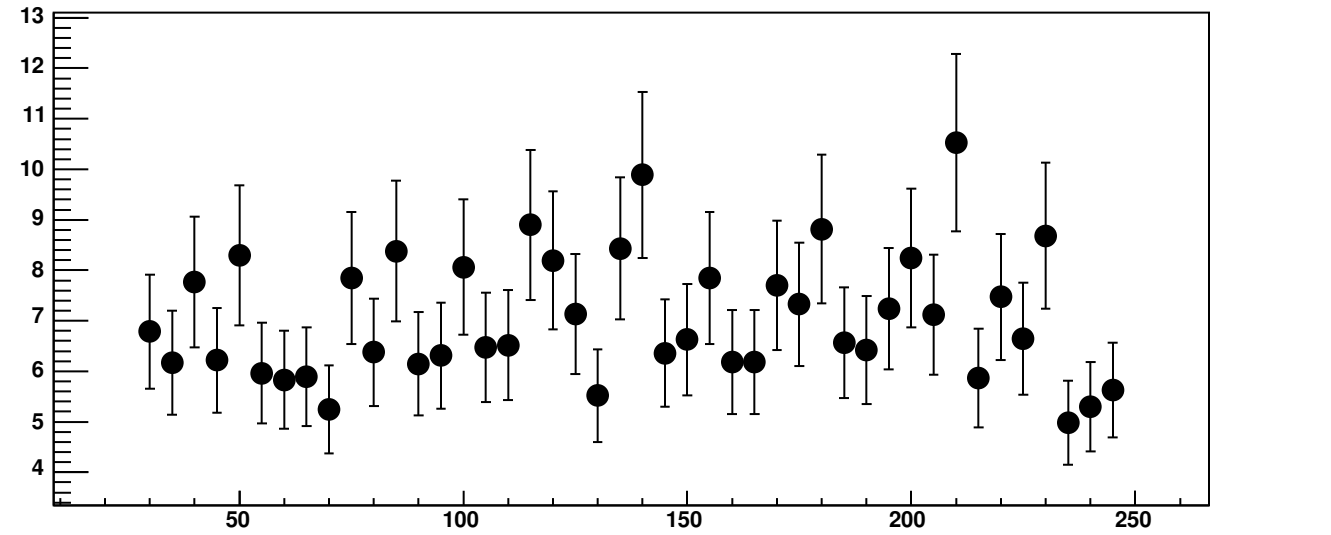
Chip 5, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold



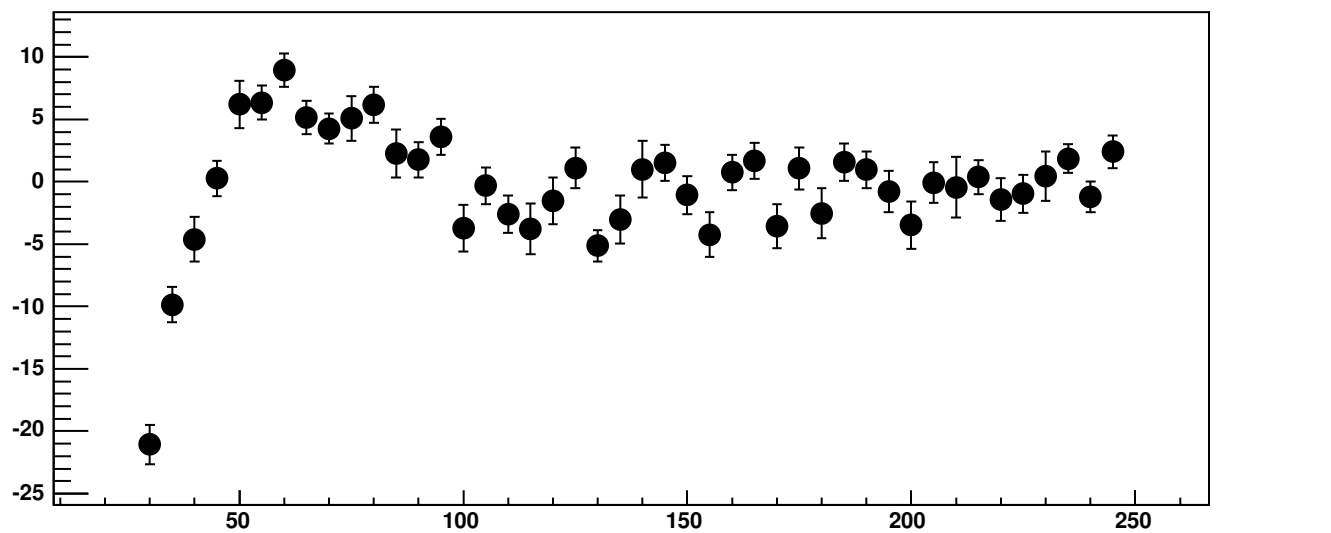
Chip 5, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold



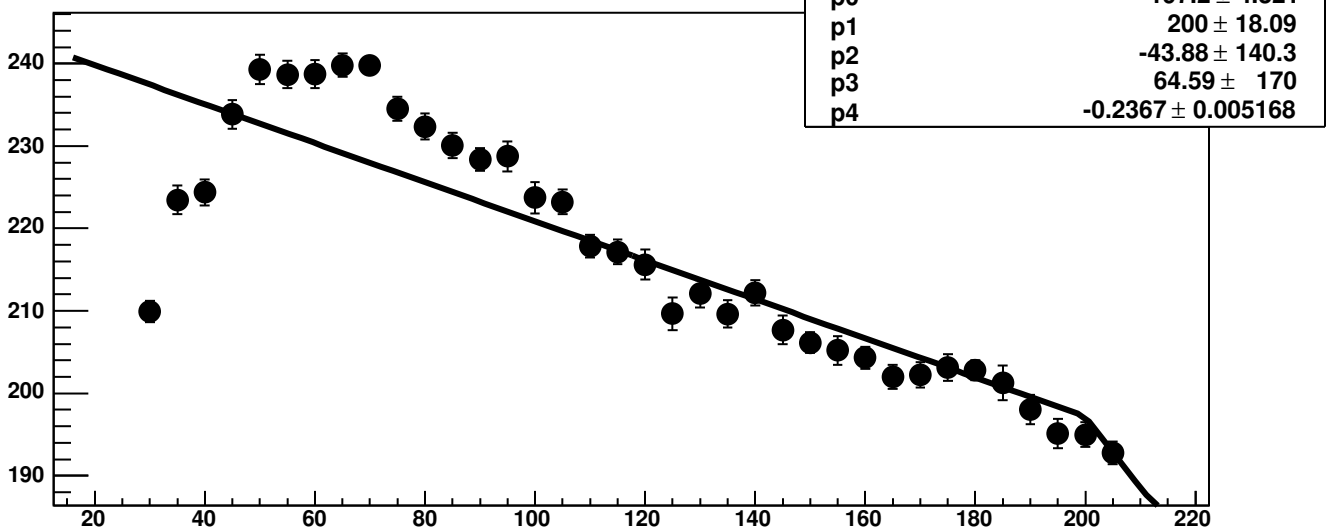
Chip 5, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold



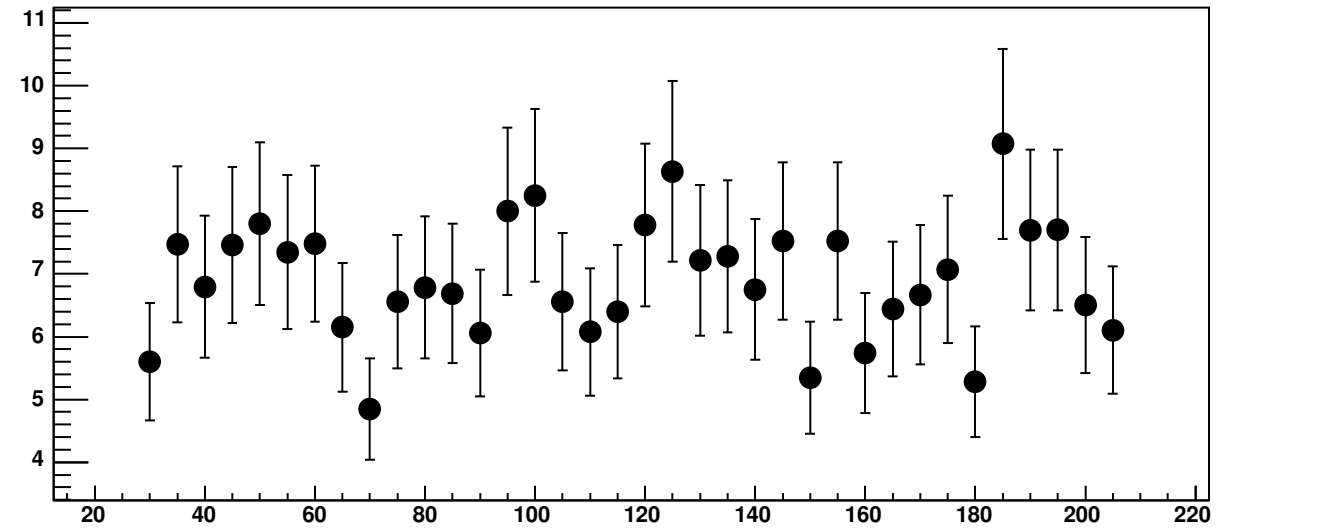
Chip 5, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold



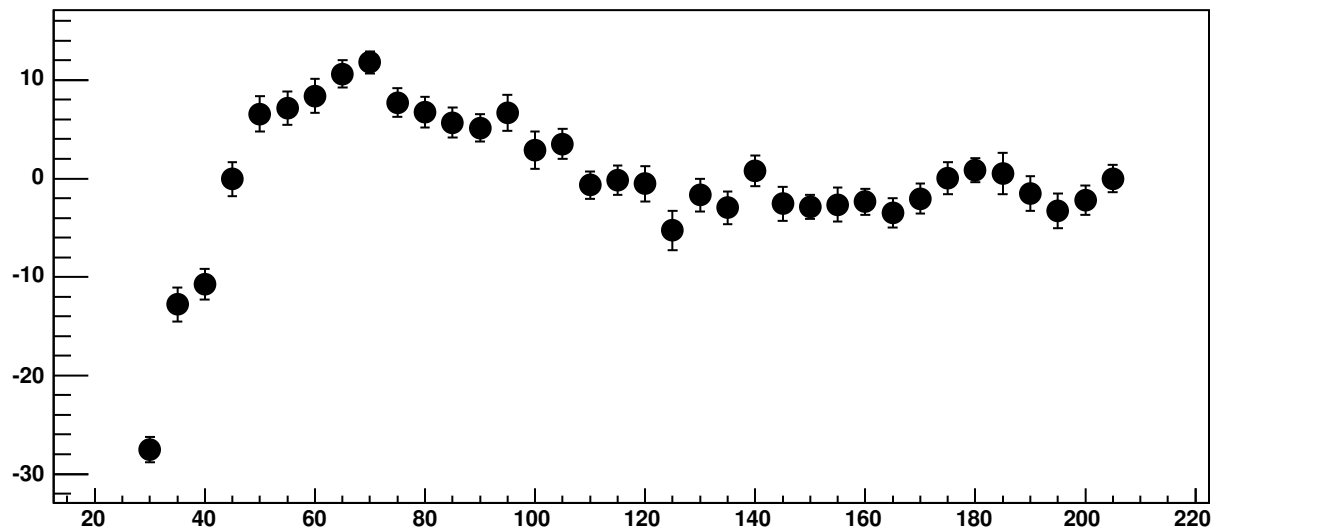
Chip 5, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold



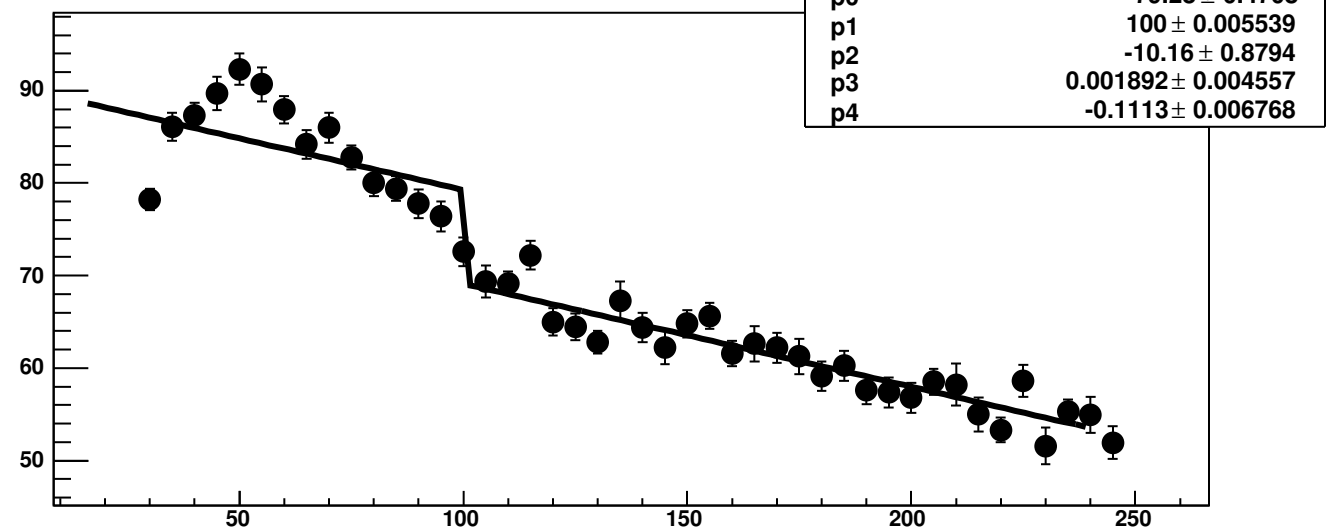
Chip 5, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



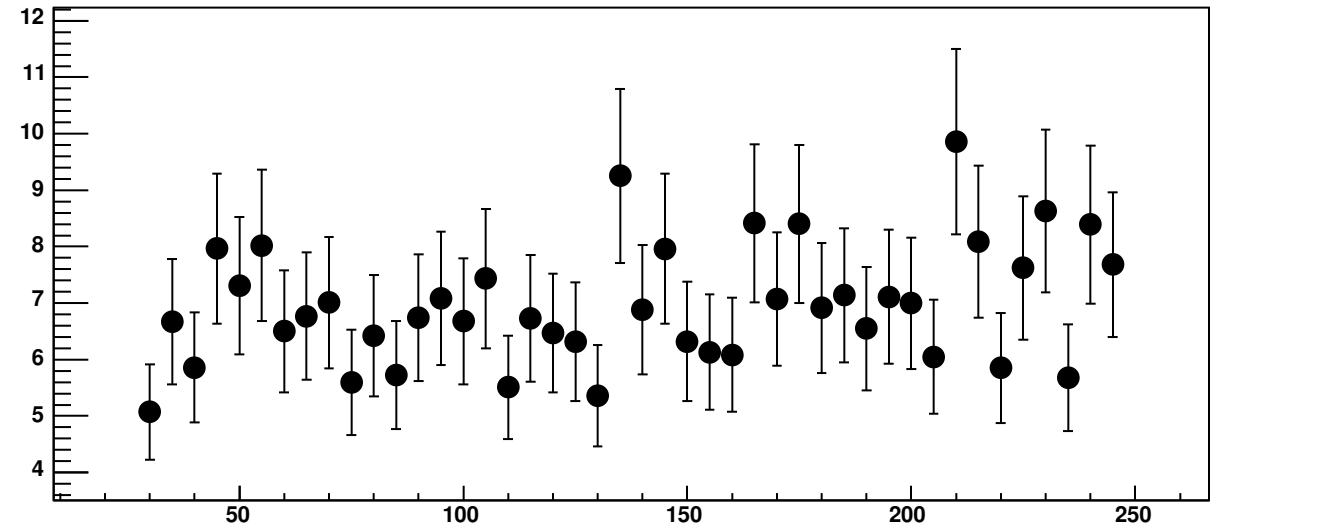
Chip 5, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold



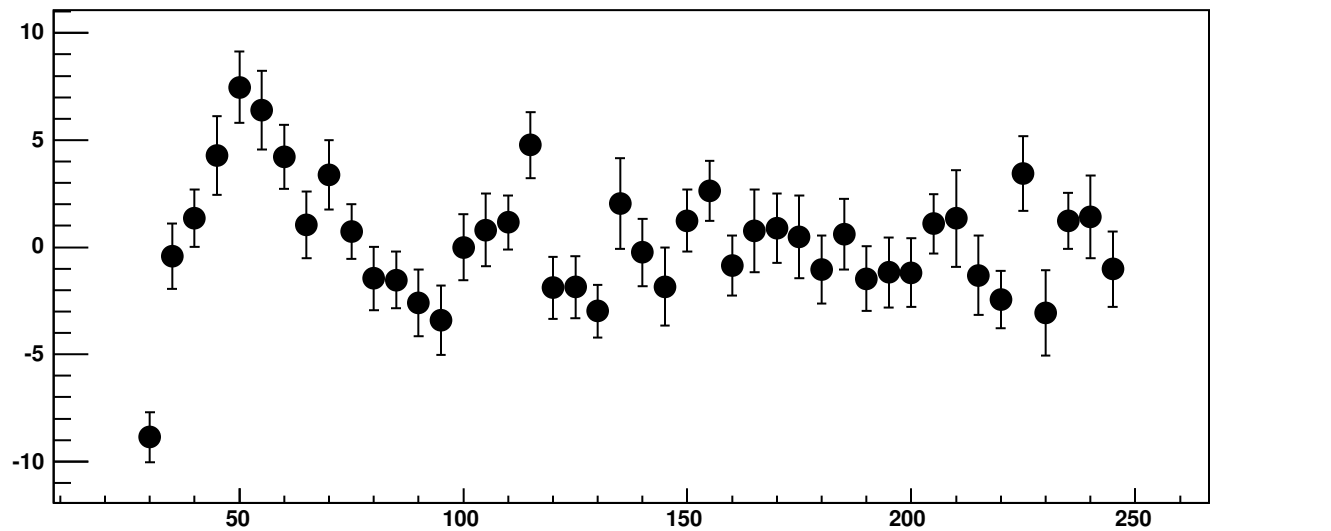
Chip 5, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold



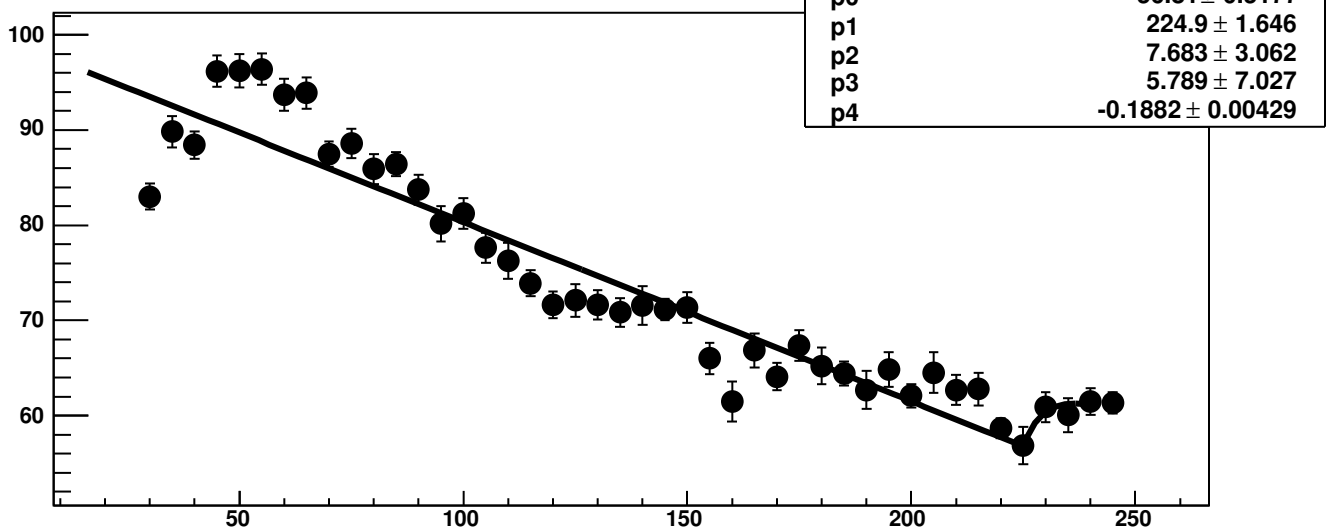
Chip 5, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold



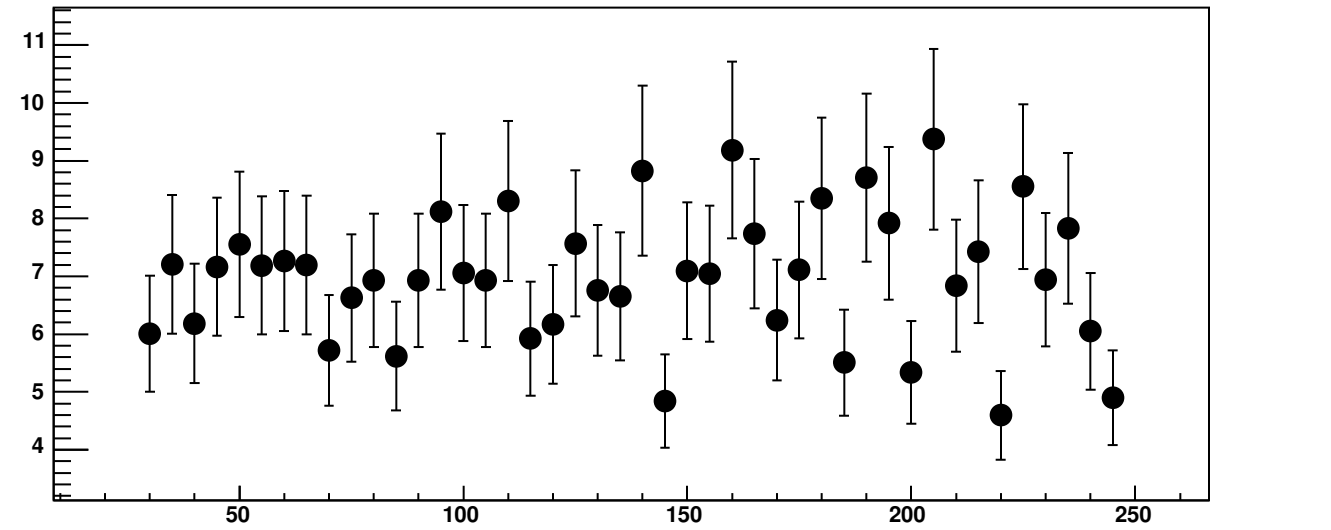
Chip 5, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold



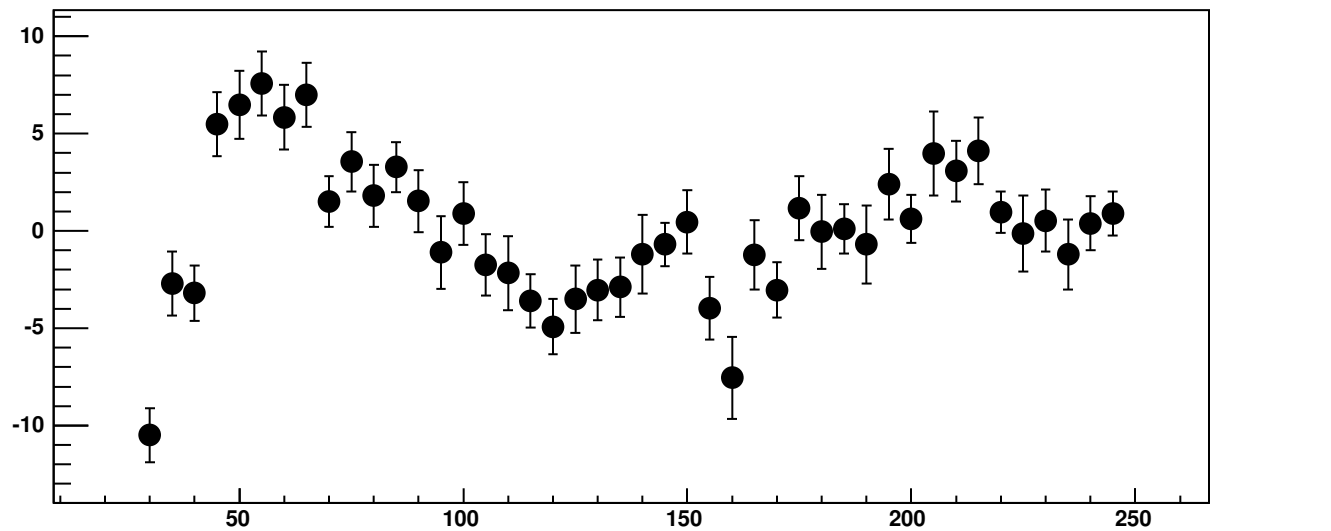
Chip 5, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold



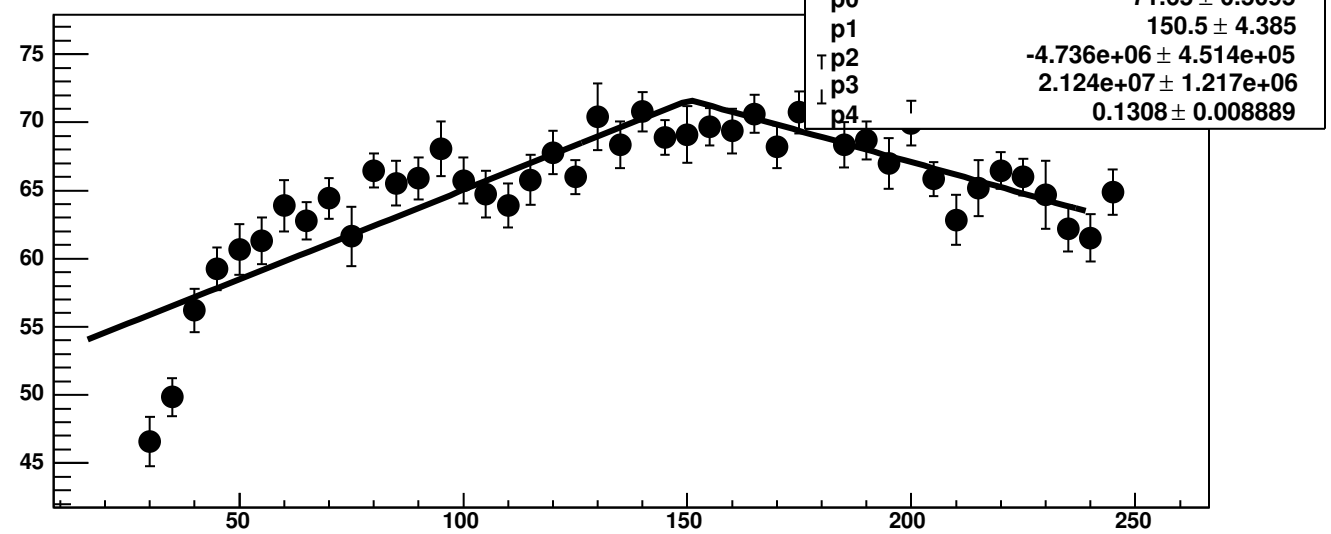
Chip 5, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



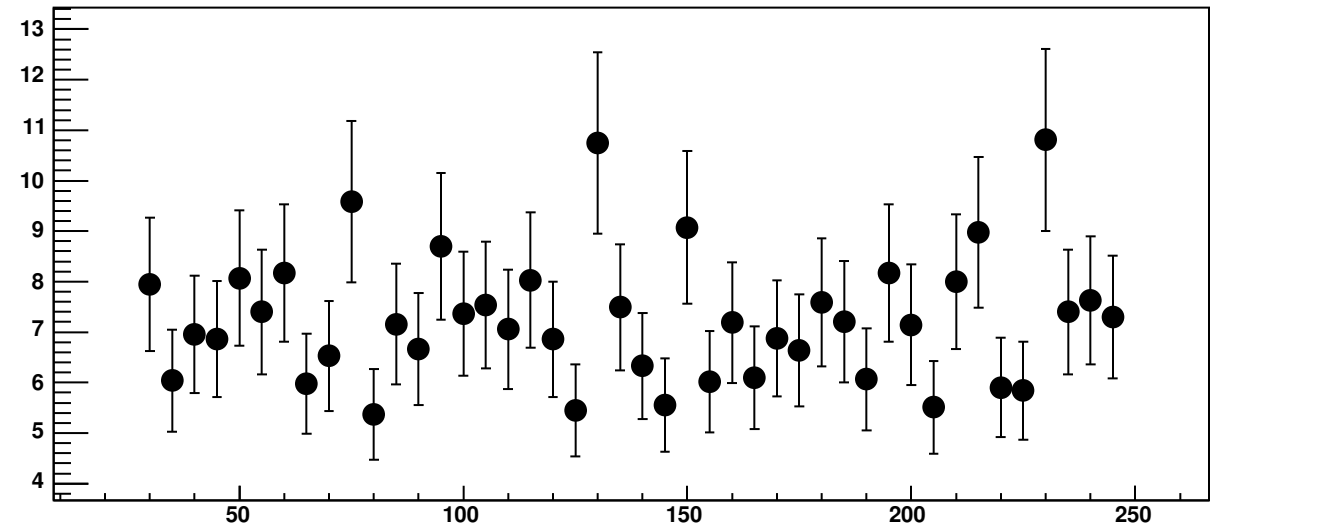
Chip 5, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold



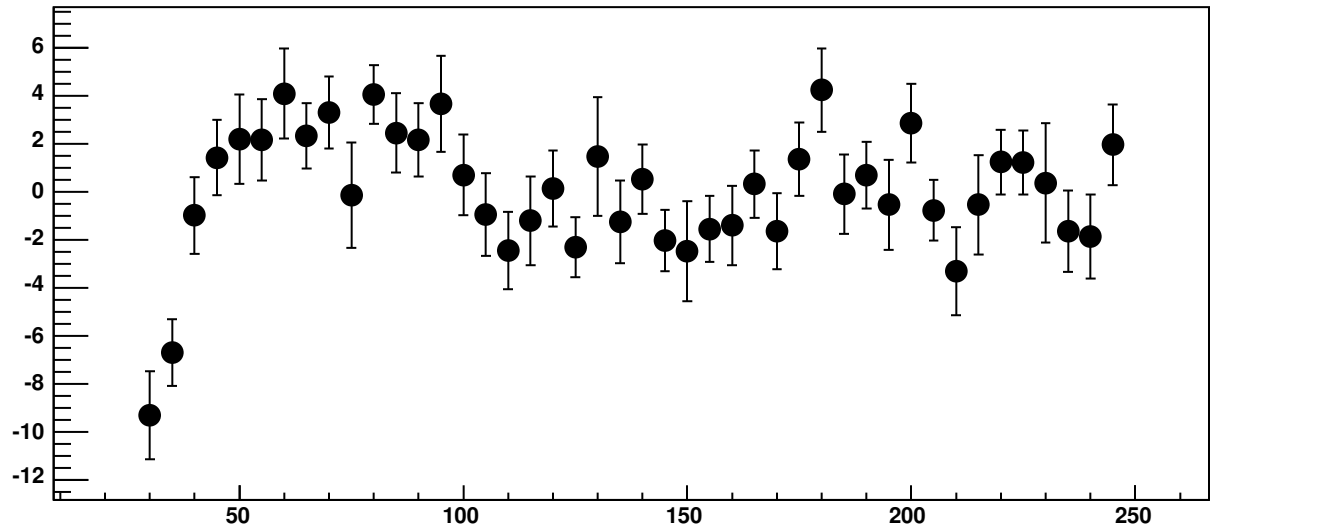
Chip 5, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

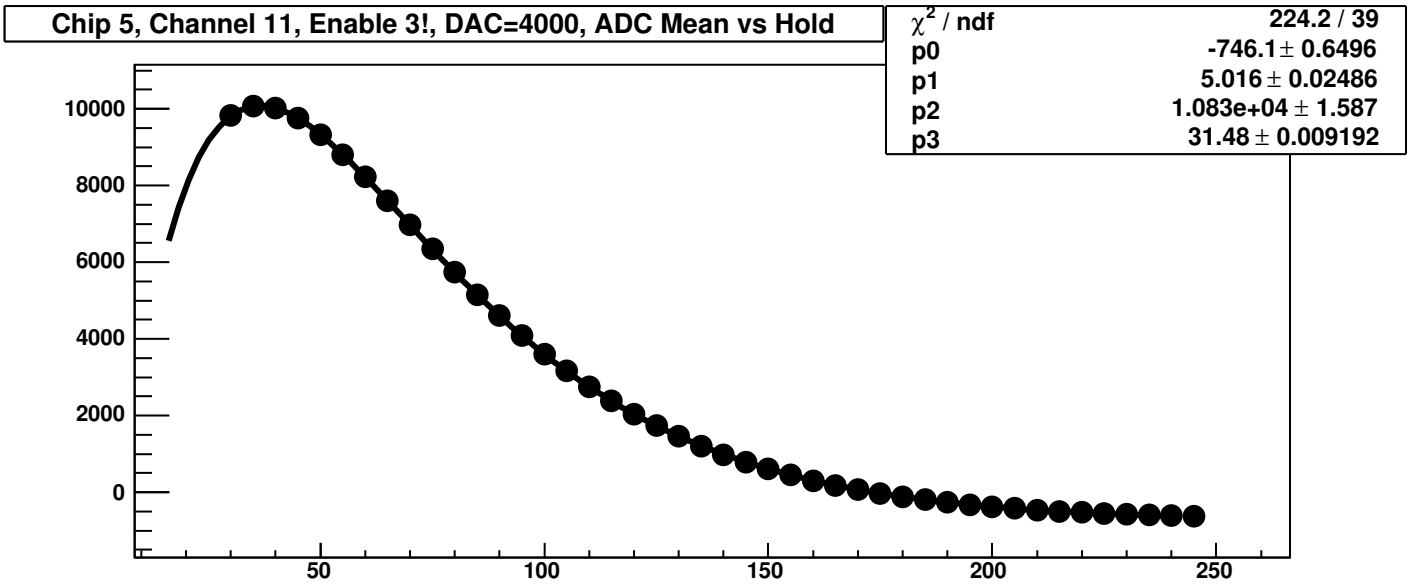


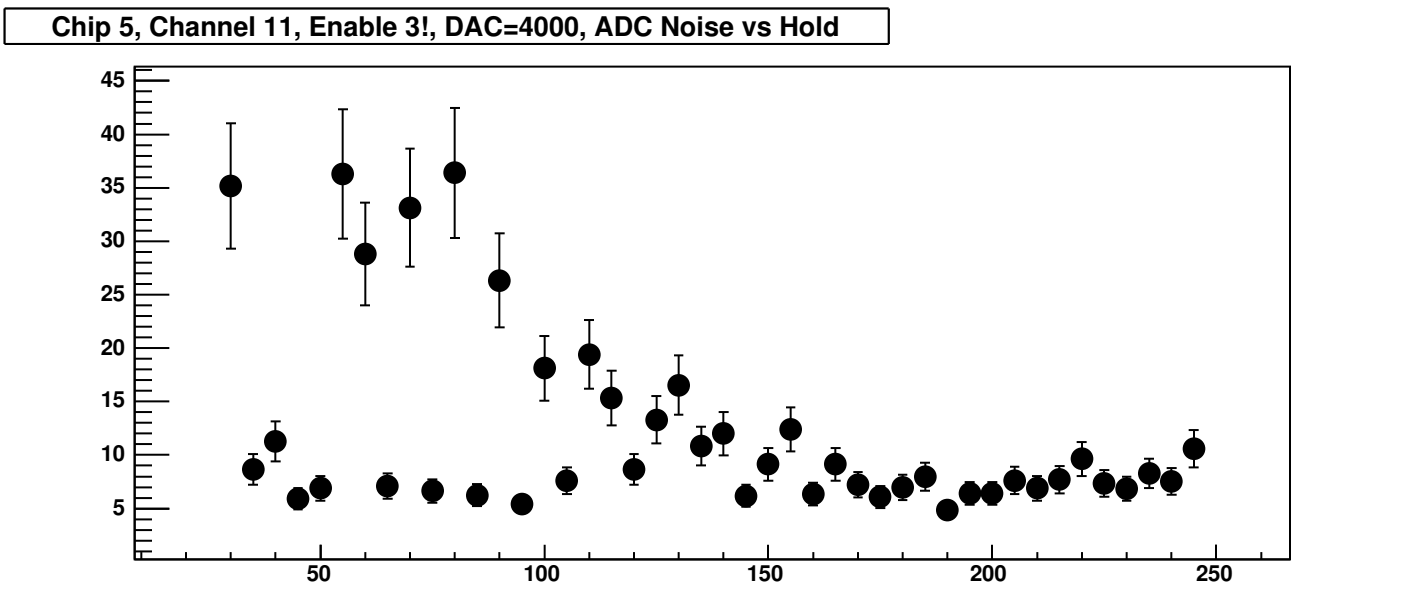
Chip 5, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold

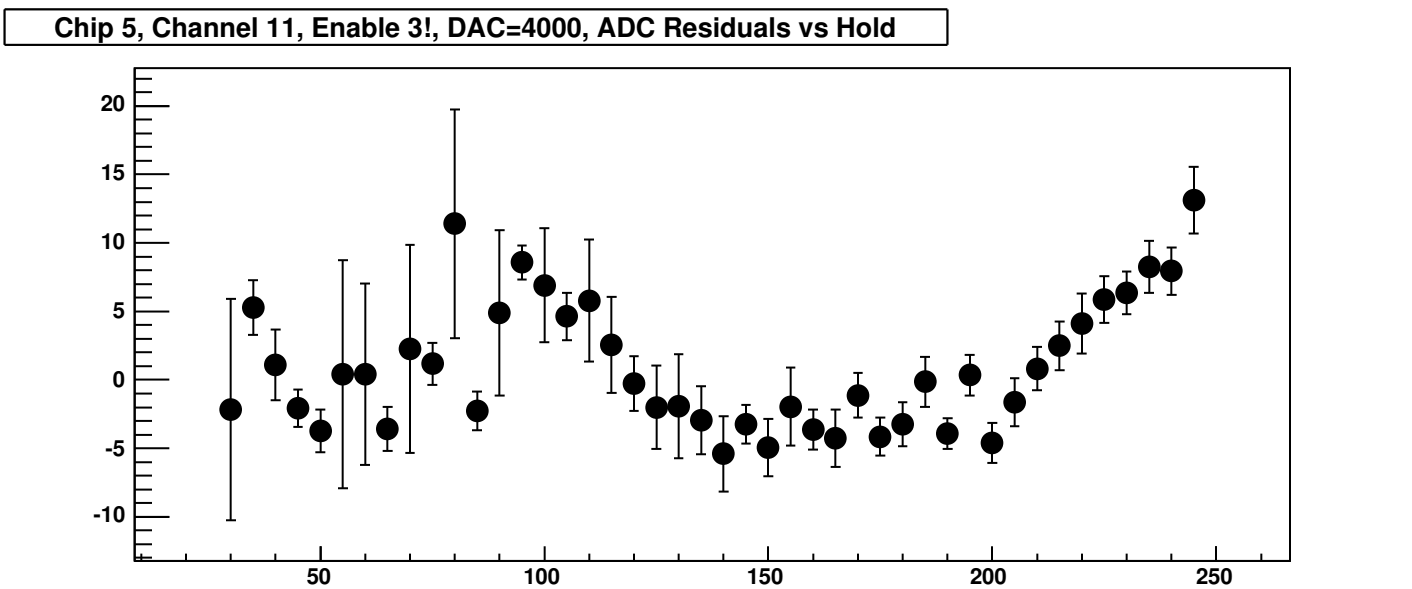


Chip 5, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

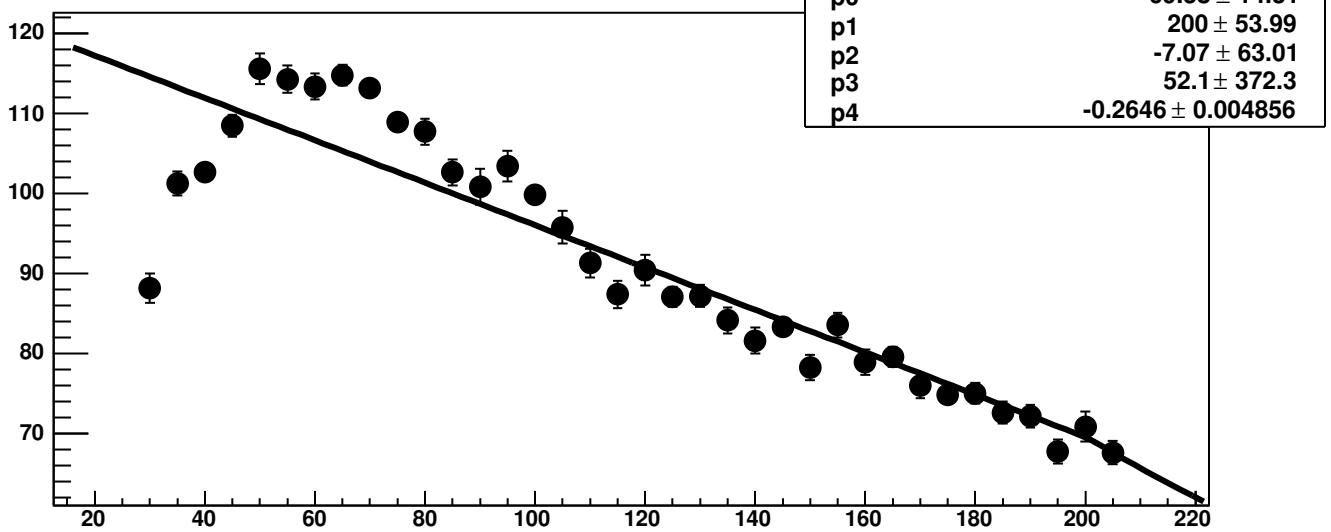



Chip 5, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 224.2 / 39
p0 -746.1 ± 0.6496
p1 5.016 ± 0.02486
p2 1.083e+04 ± 1.587
p3 31.48 ± 0.009192
10000
8000
6000
4000
2000
0
50
100
150
200
250

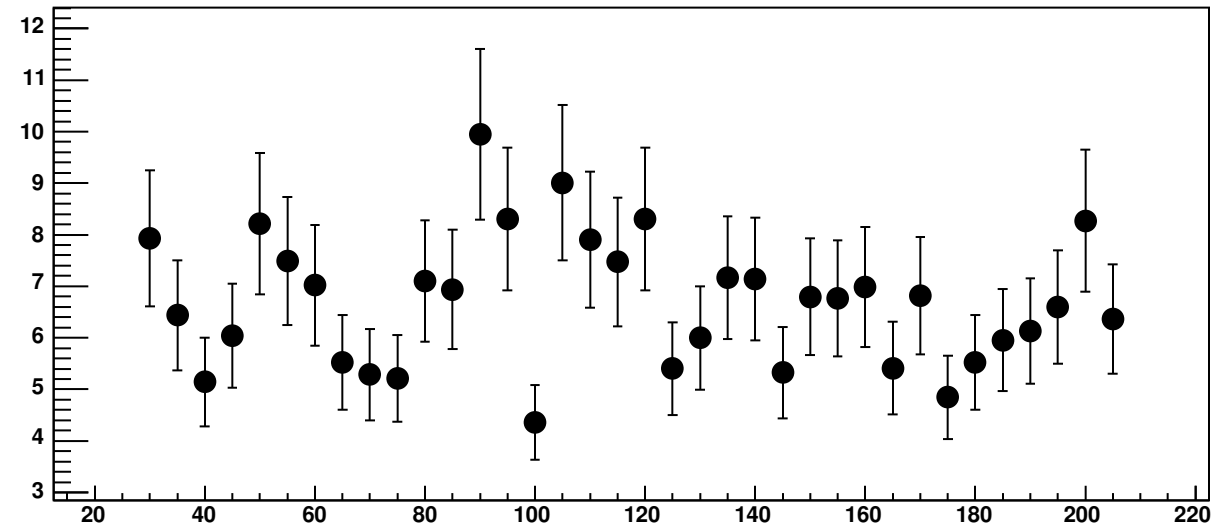

Chip 5, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold
45
40
35
30
25
20
15
10
5
50
100
150
200
250


Chip 5, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
50
100
150
200
250

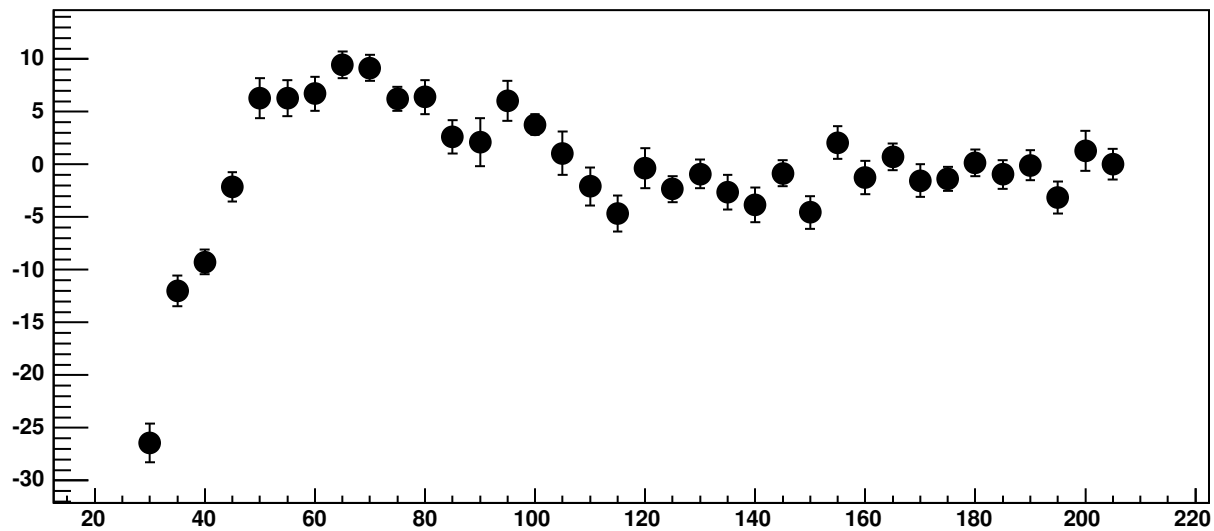
Chip 5, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold



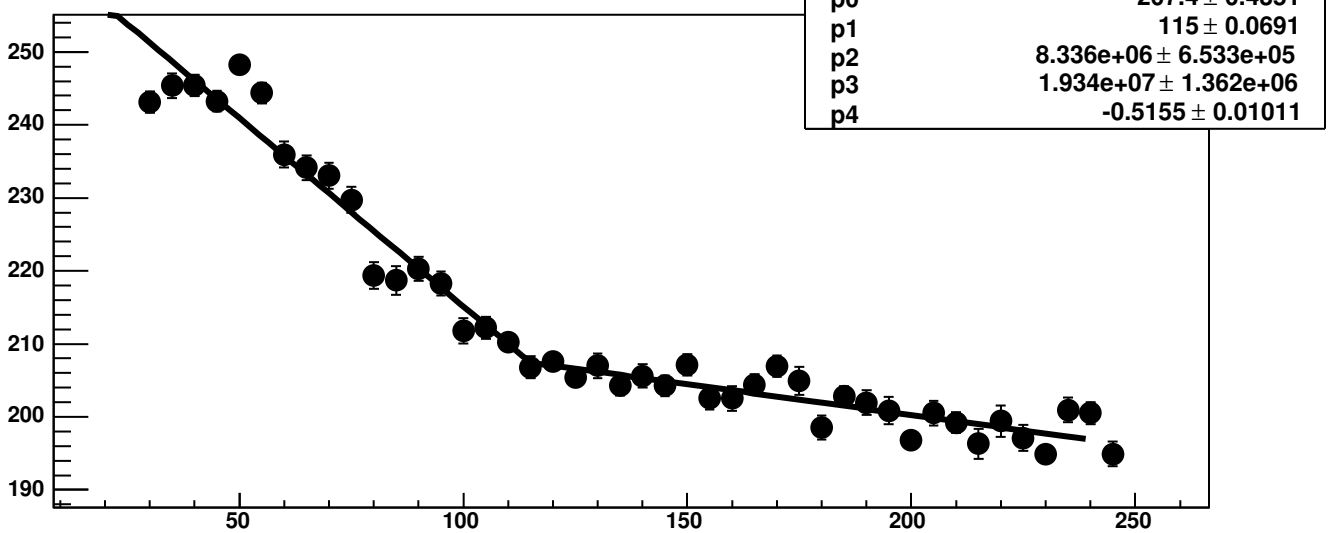
Chip 5, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



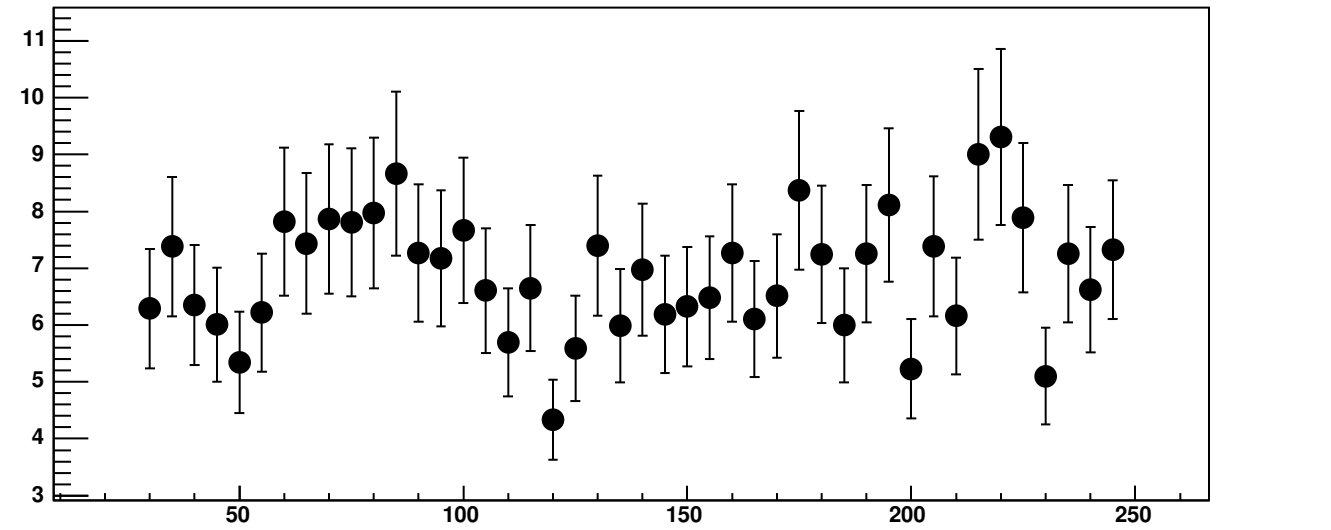
Chip 5, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold



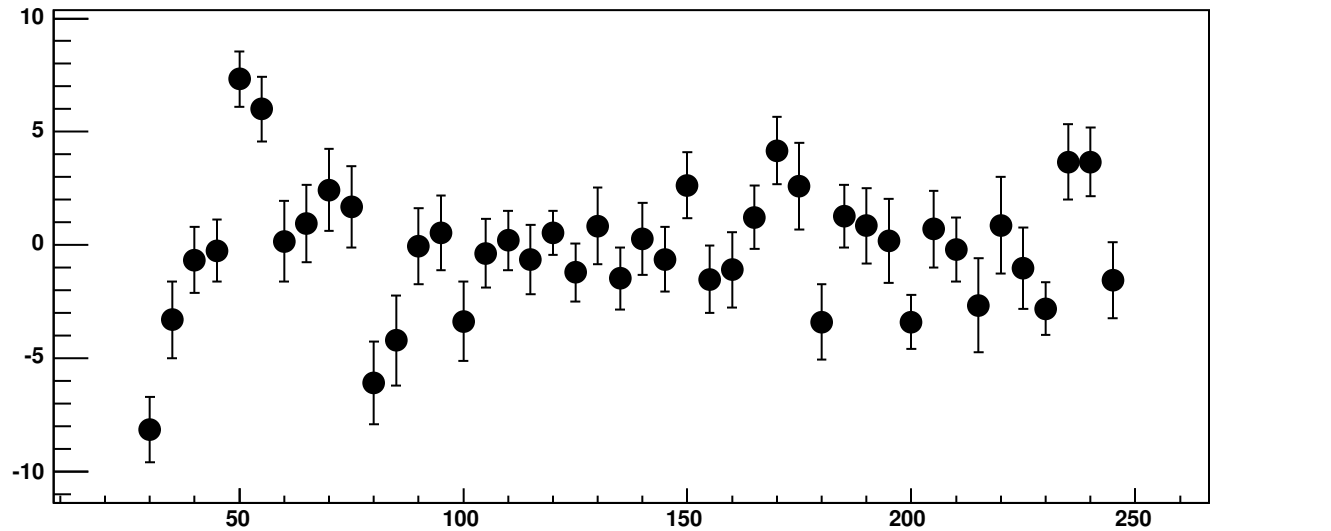
Chip 5, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold



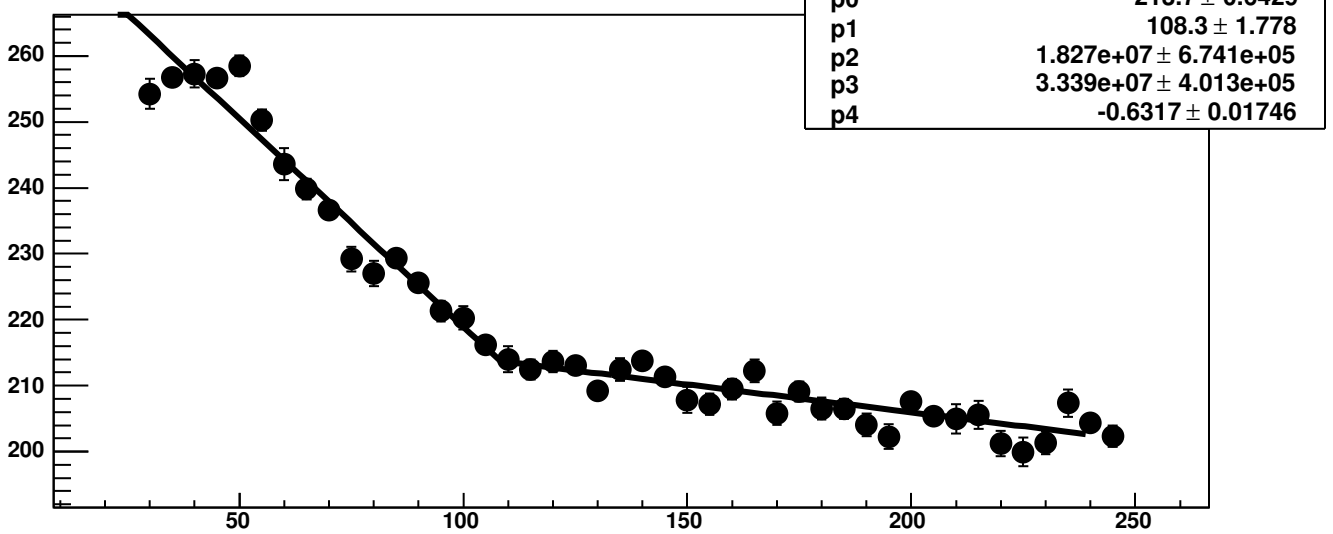
Chip 5, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



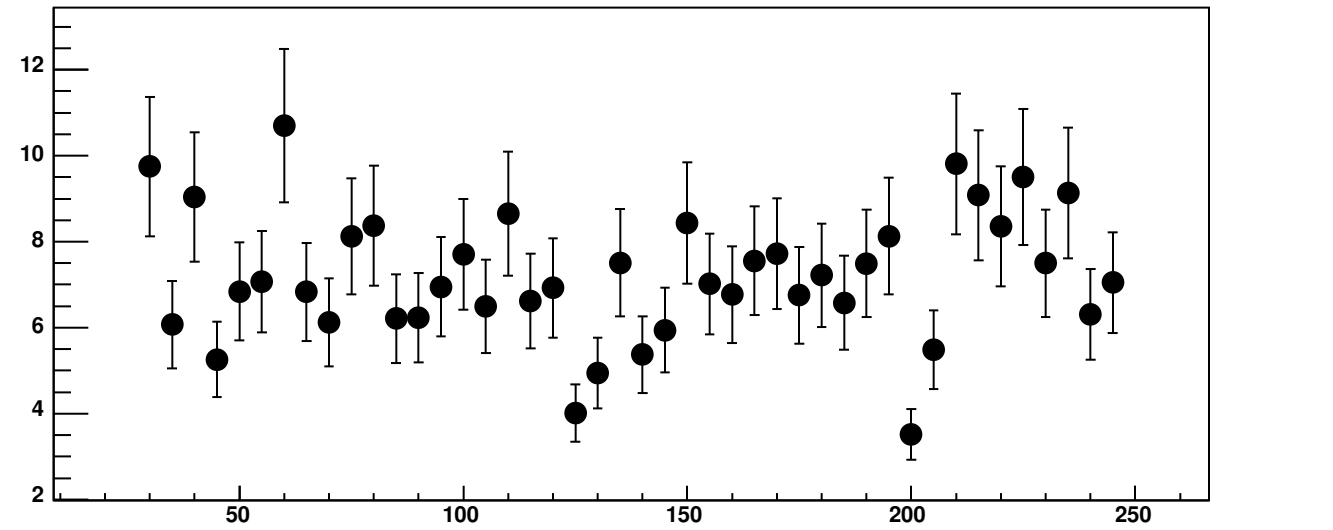
Chip 5, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold



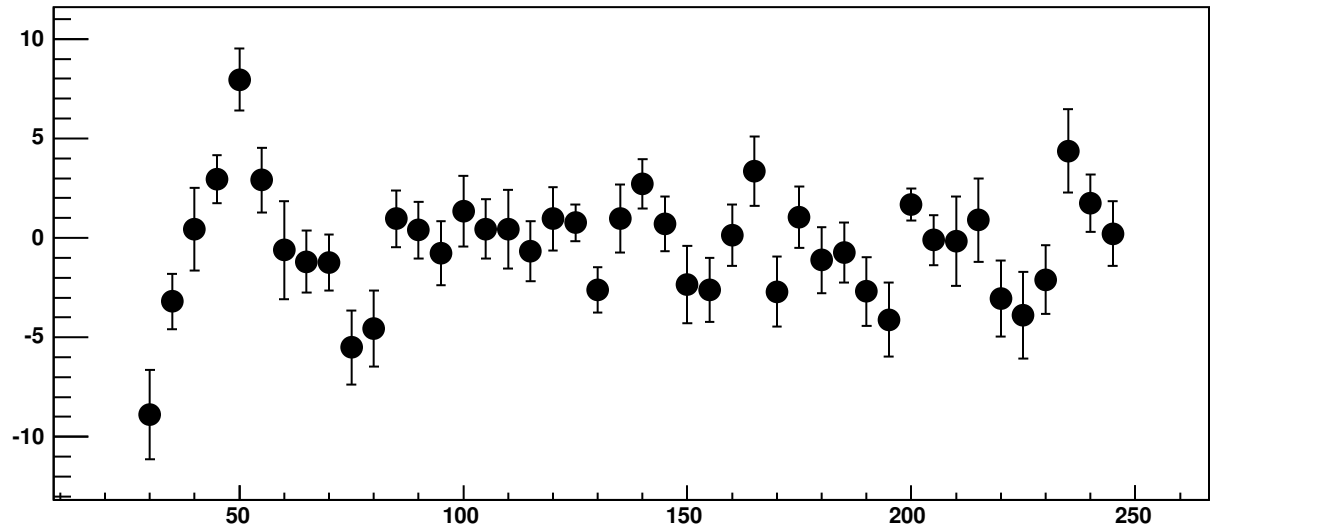
Chip 5, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold



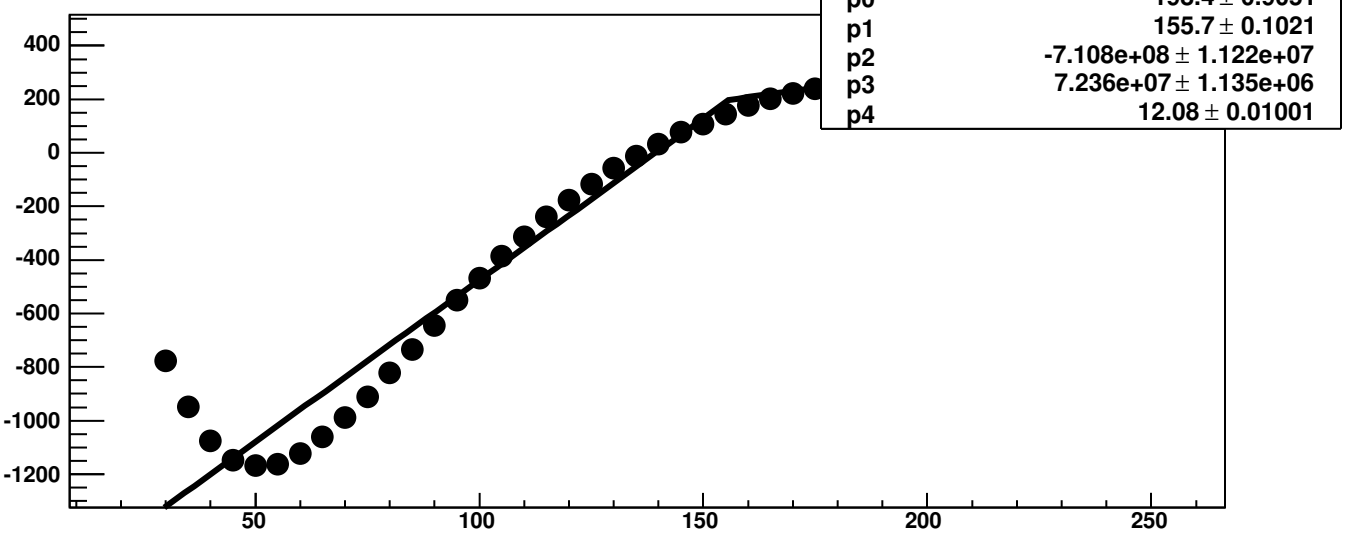
Chip 5, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold



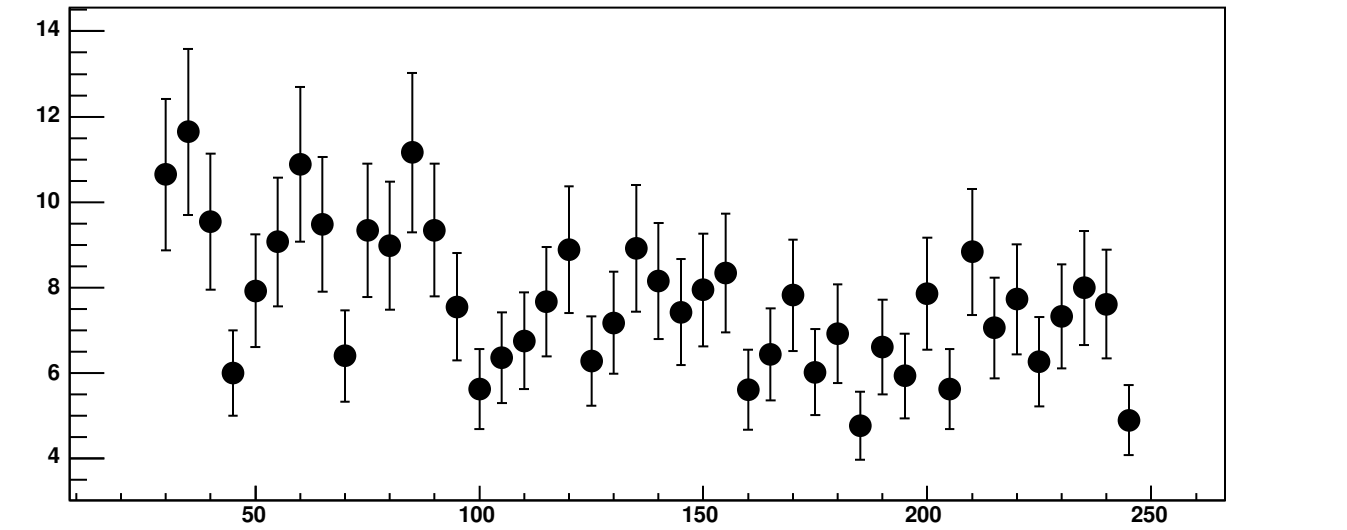
Chip 5, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold



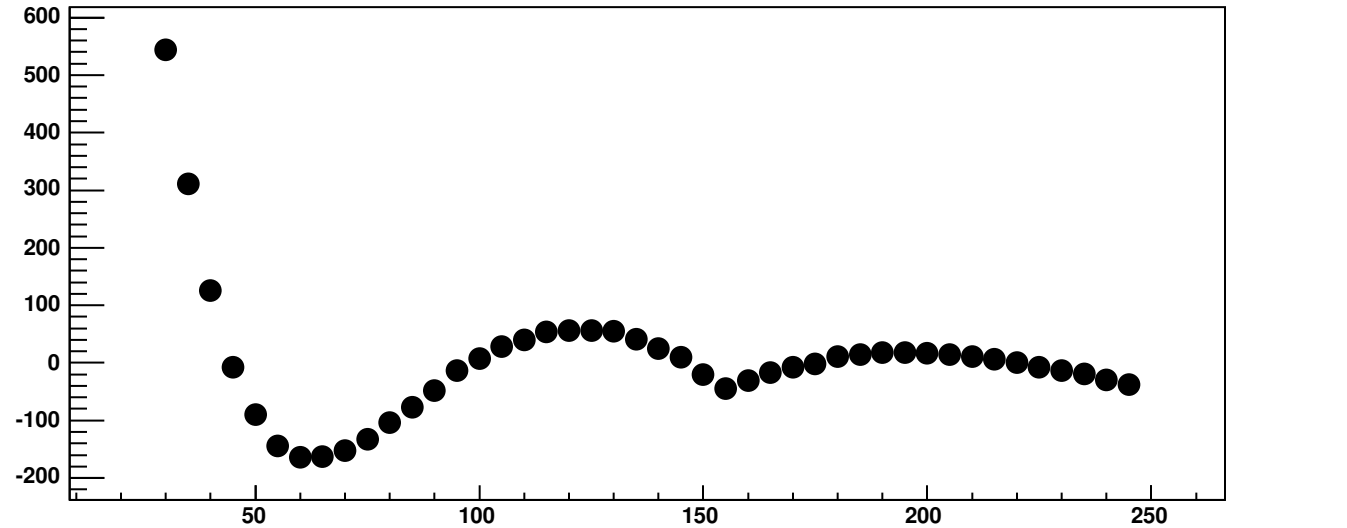
Chip 5, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold



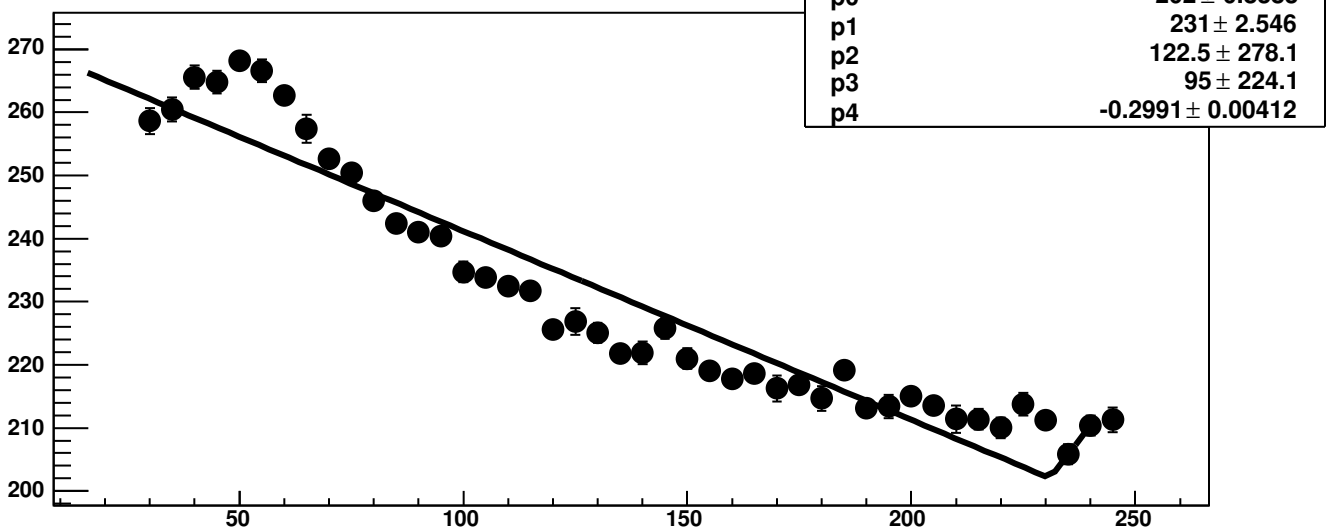
Chip 5, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



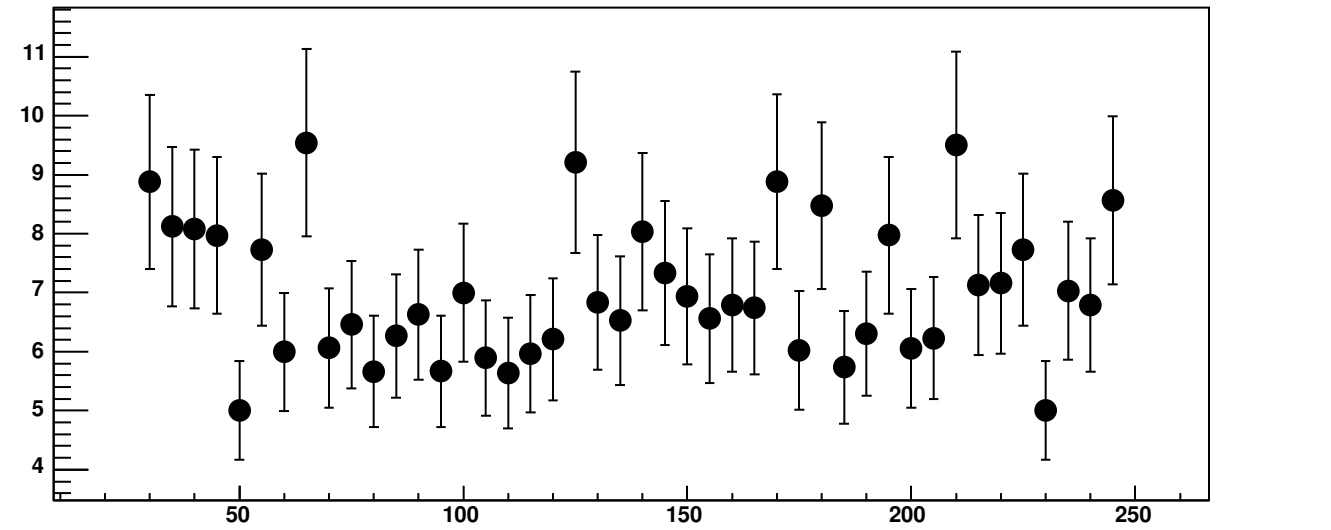
Chip 5, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold



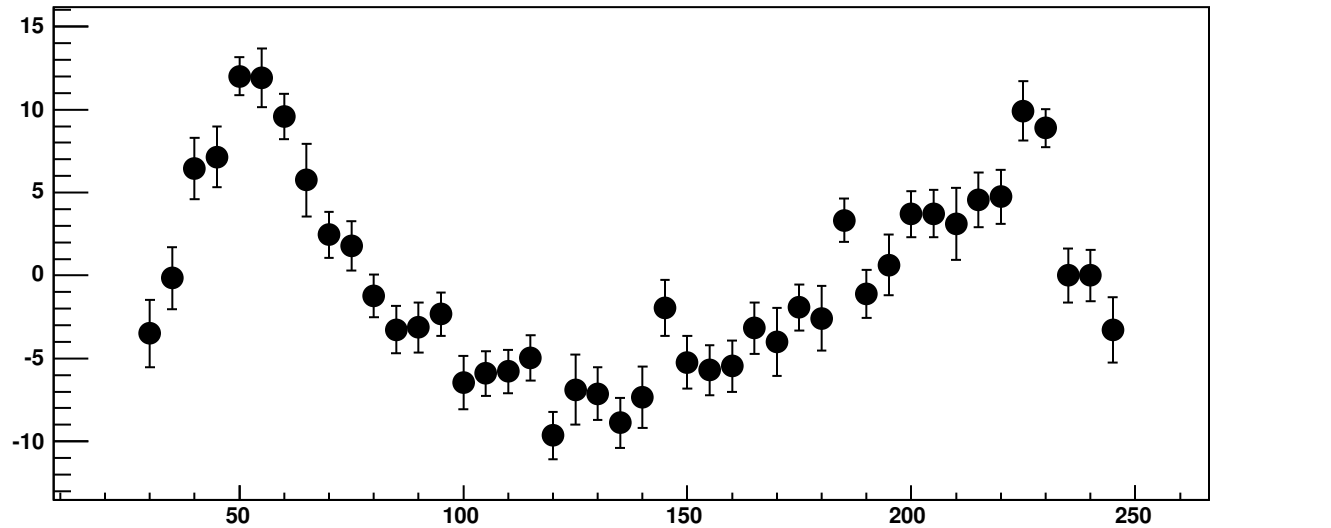
Chip 5, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

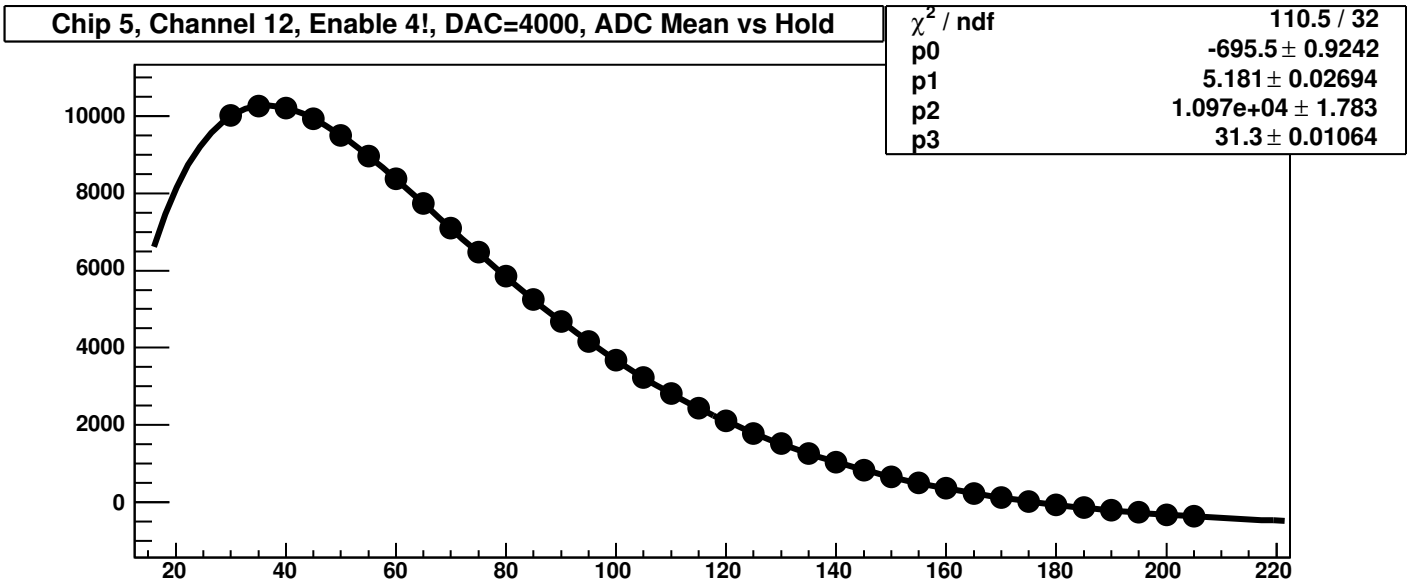


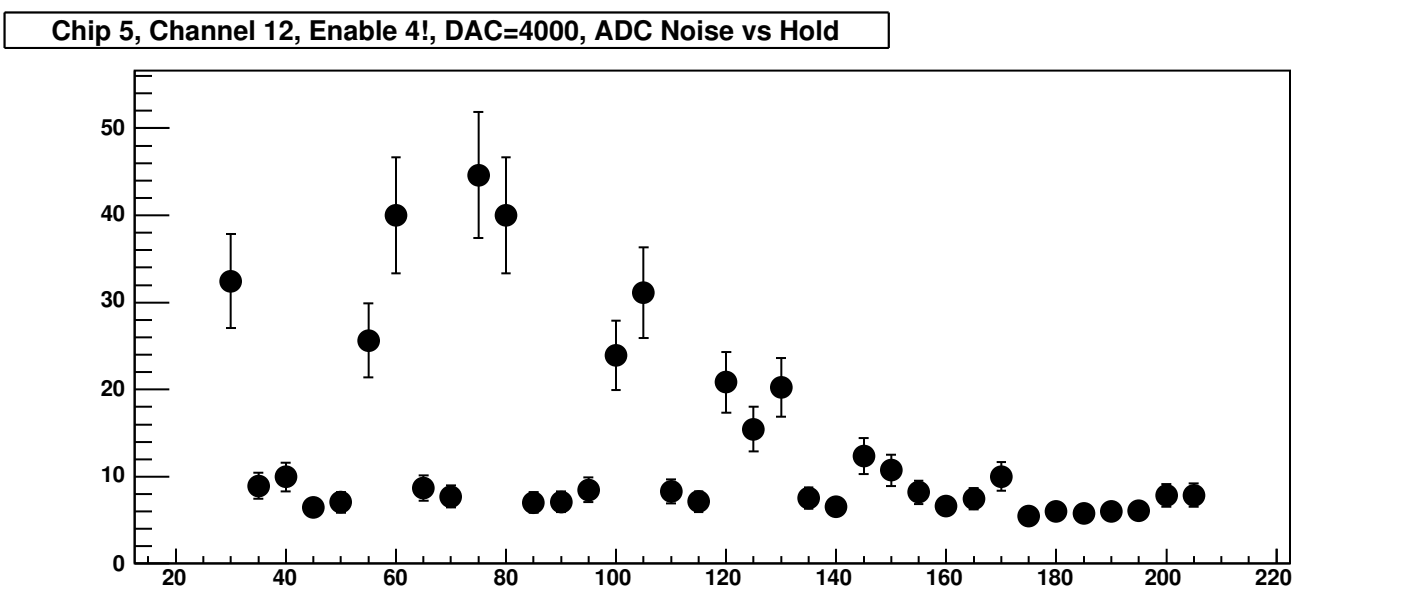
Chip 5, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold

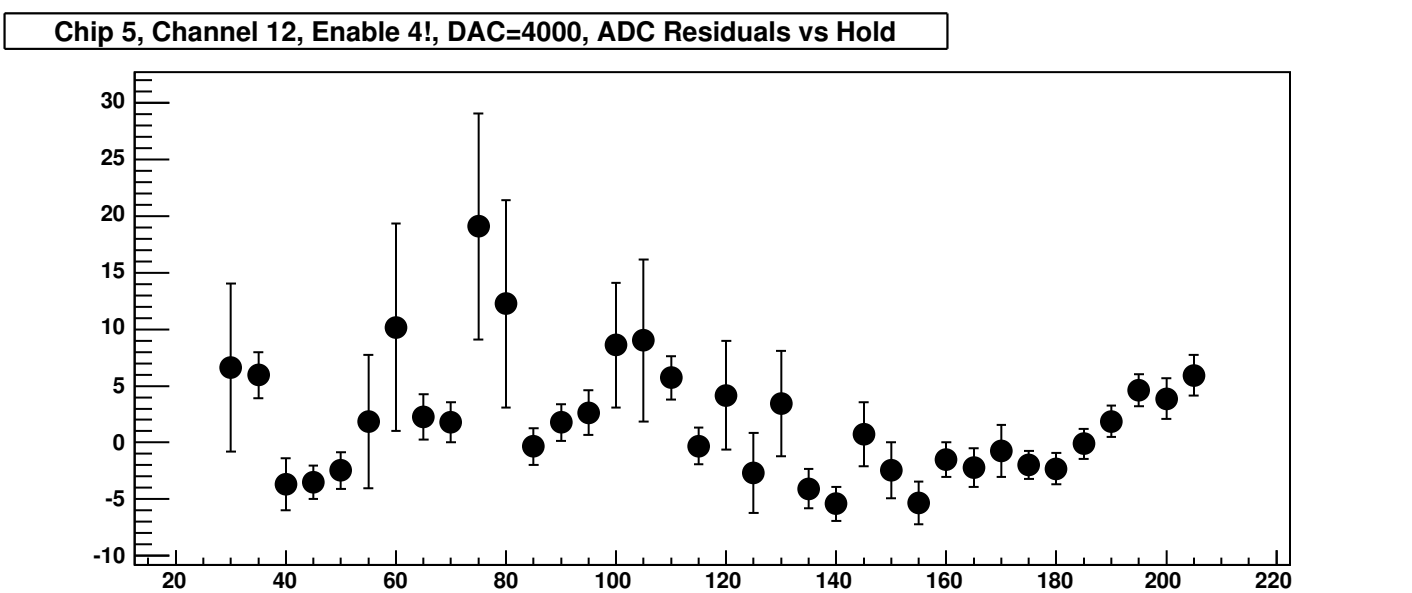


Chip 5, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

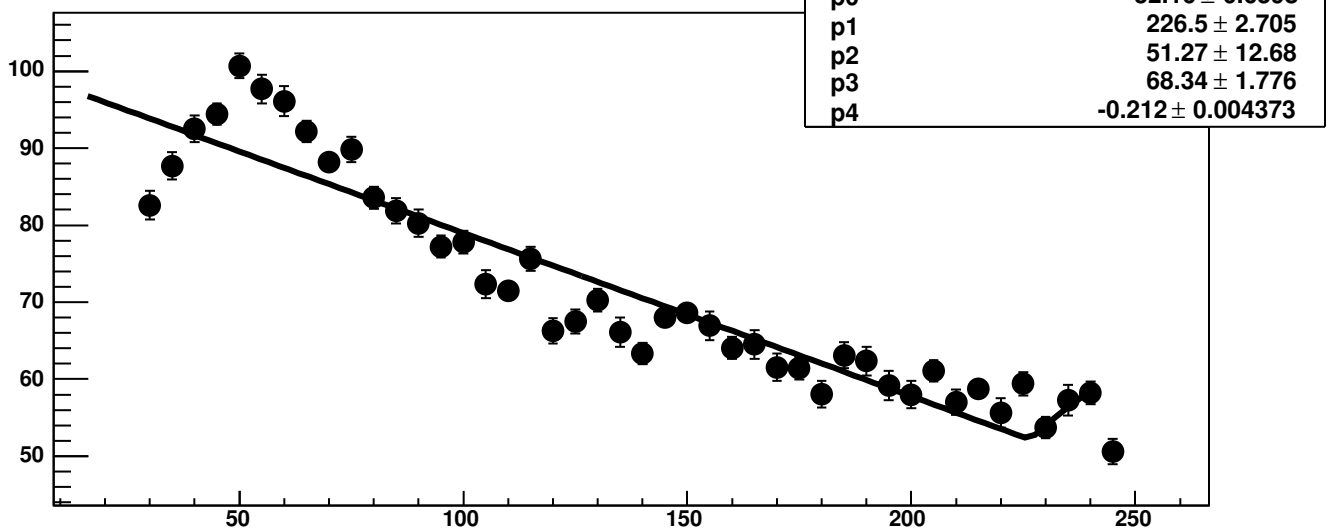



Chip 5, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 110.5 / 32
p0 -695.5 ± 0.9242
p1 5.181 ± 0.02694
p2 1.097e+04 ± 1.783
p3 31.3 ± 0.01064
10000
8000
6000
4000
2000
0
20
40
60
80
100
120
140
160
180
200
220

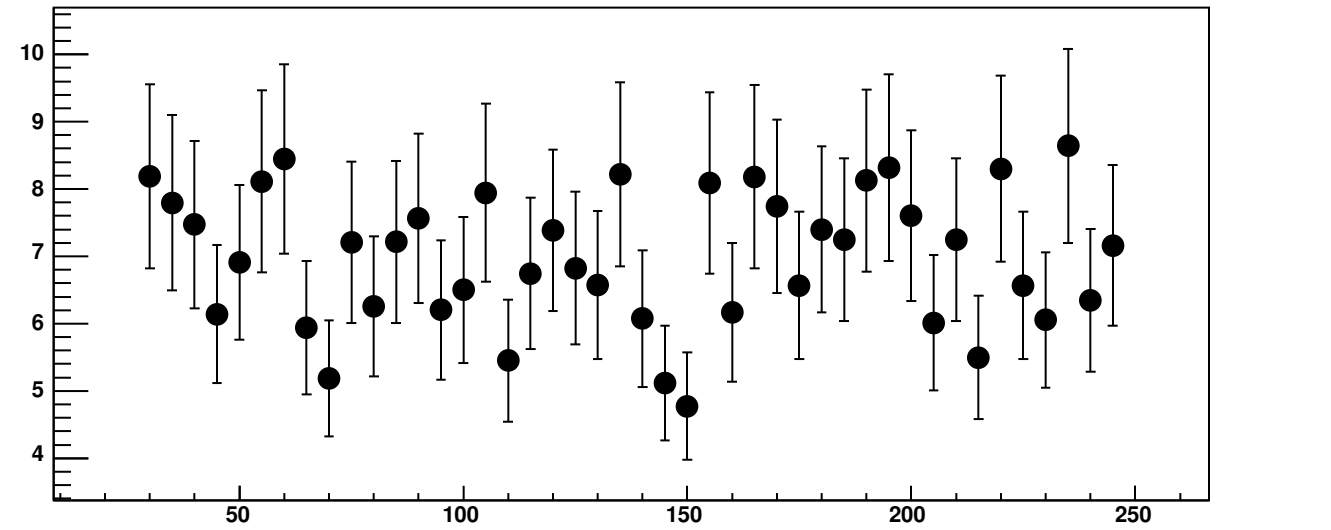

Chip 5, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
0
20
40
60
80
100
120
140
160
180
200
220


Chip 5, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
20
40
60
80
100
120
140
160
180
200
220

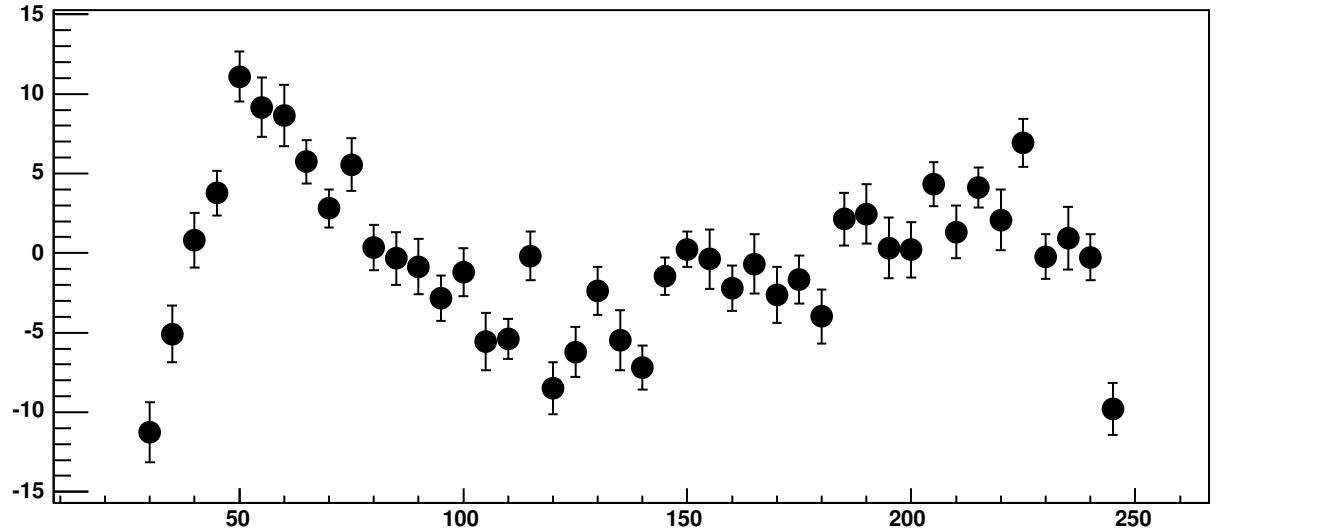
Chip 5, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold

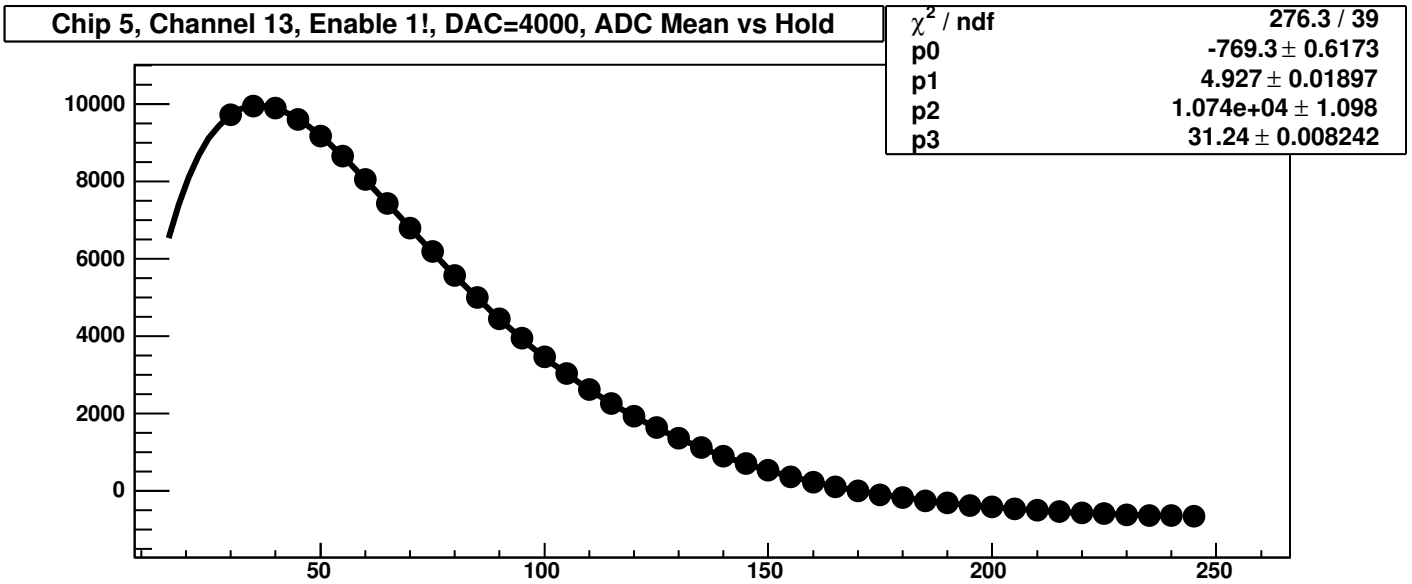


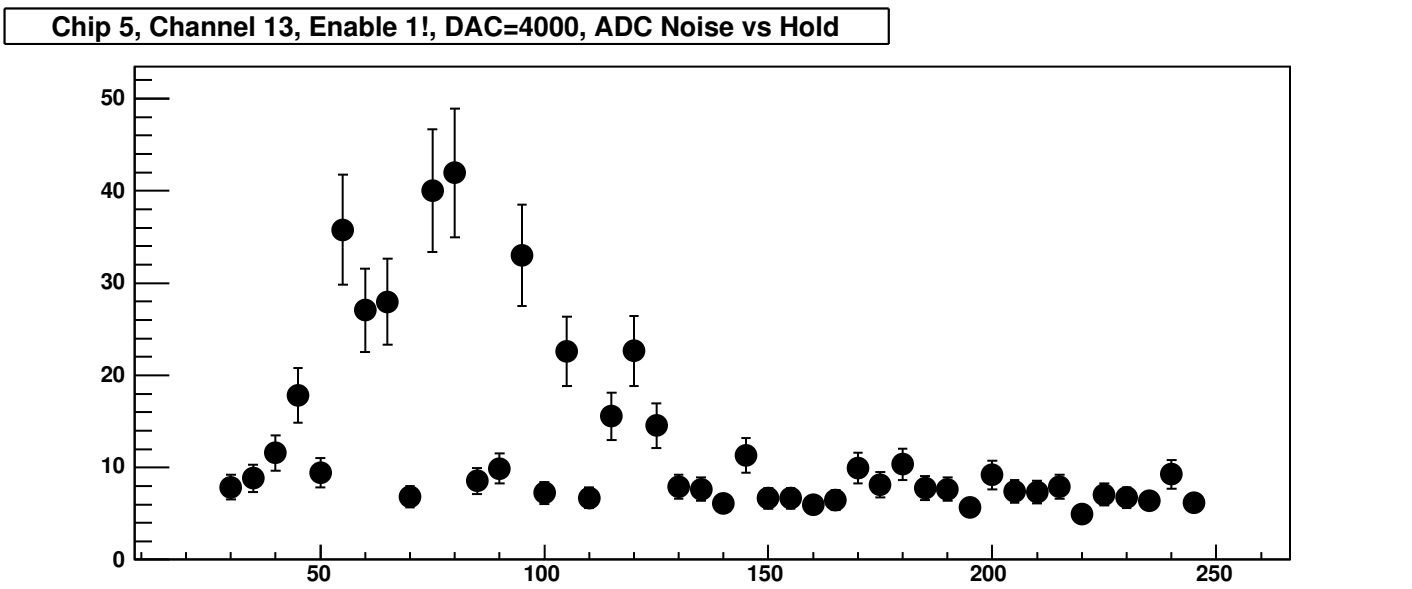
Chip 5, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold

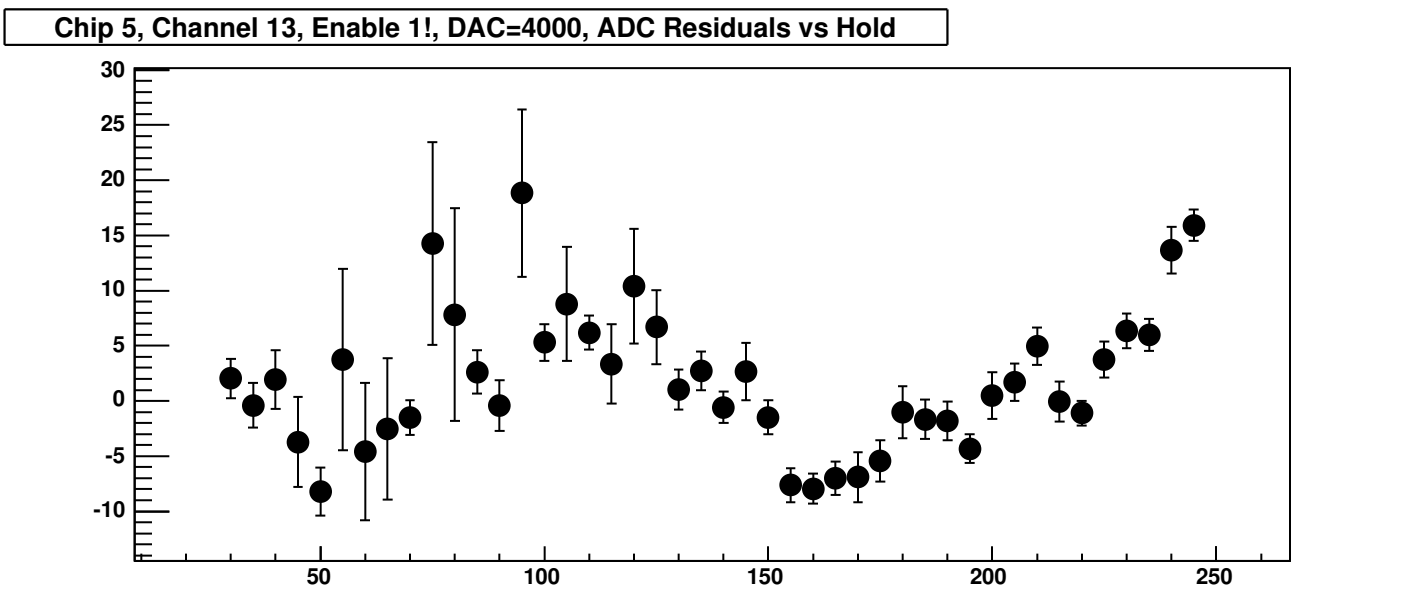


Chip 5, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold

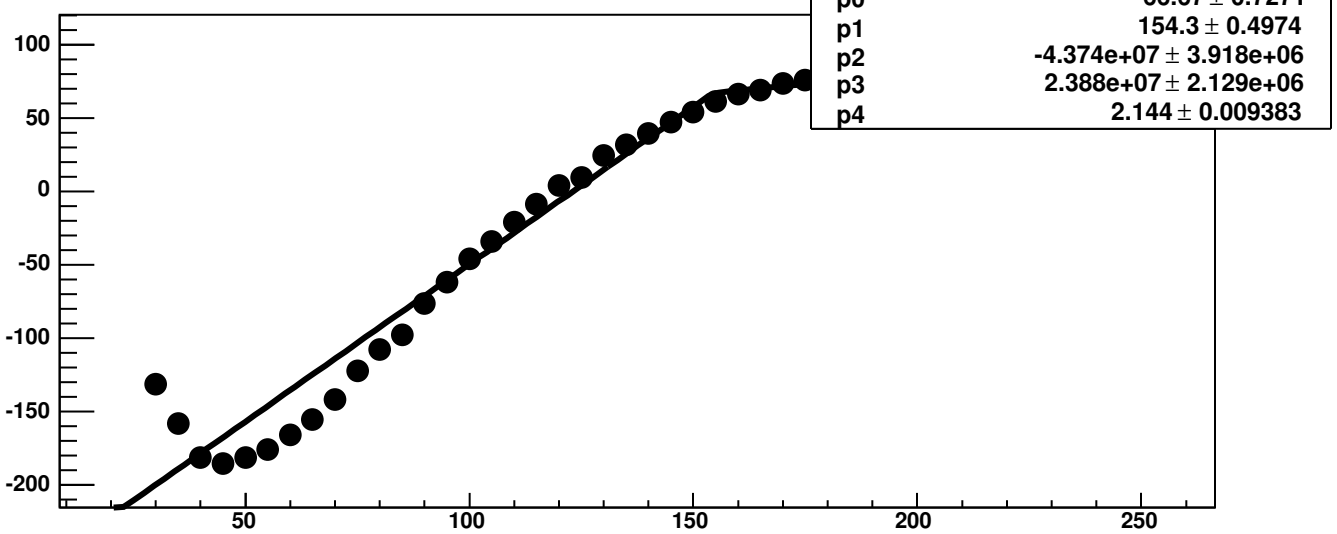



Chip 5, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 276.3 / 39
p0 -769.3 ± 0.6173
p1 4.927 ± 0.01897
p2 1.074e+04 ± 1.098
p3 31.24 ± 0.008242
10000
8000
6000
4000
2000
0
50
100
150
200
250

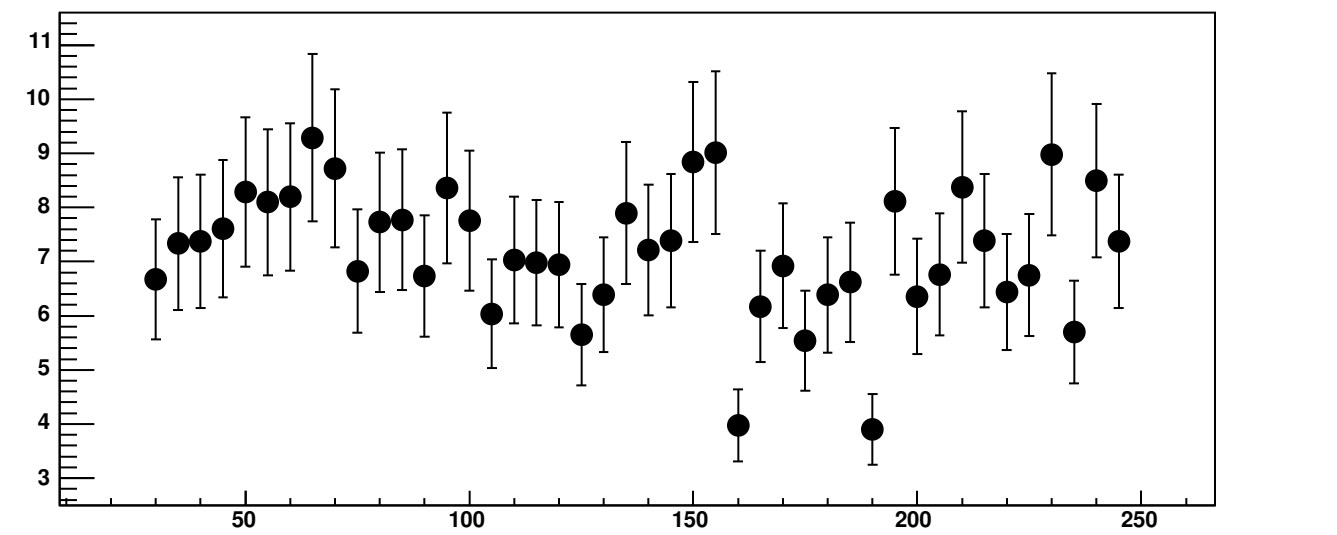

Chip 5, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold
50
40
30
20
10
0
50
100
150
200
250


Chip 5, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
50
100
150
200
250

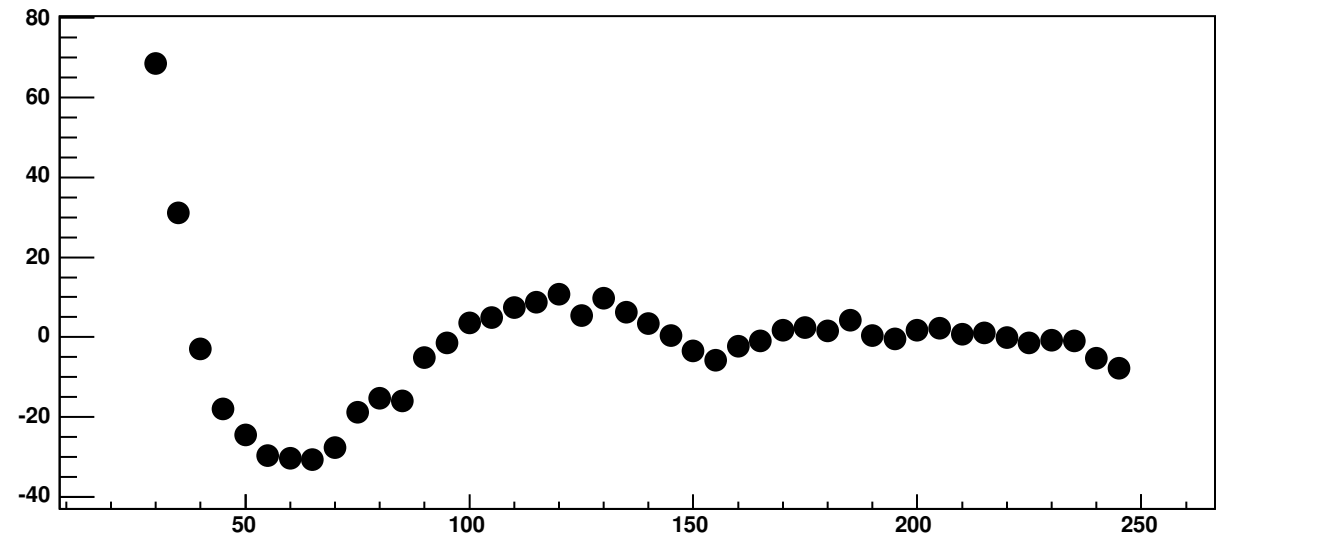
Chip 5, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold



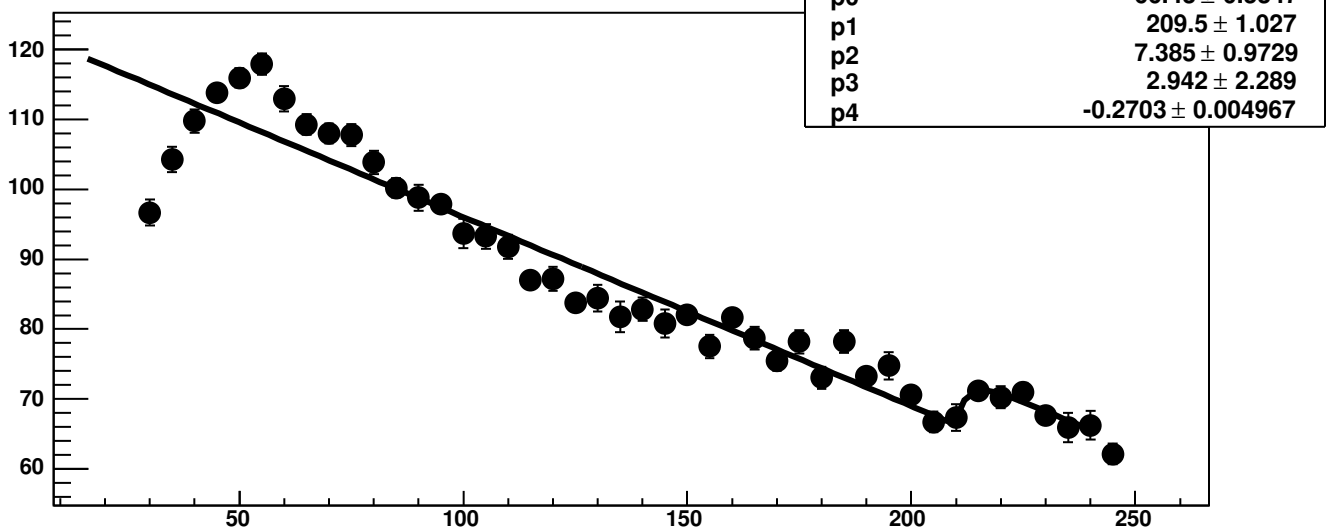
Chip 5, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



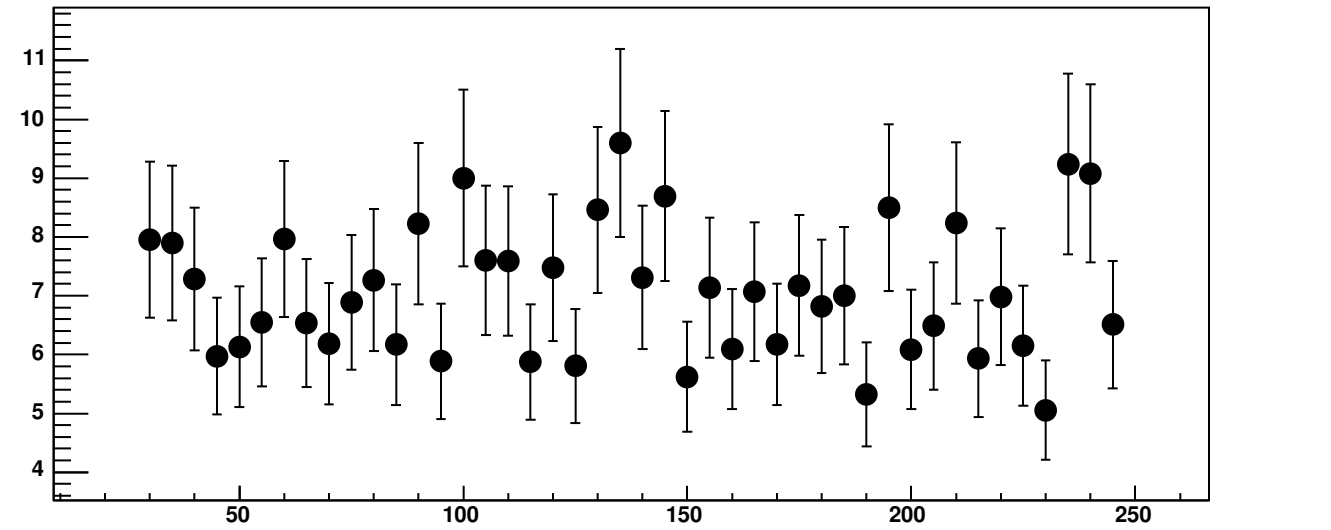
Chip 5, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold



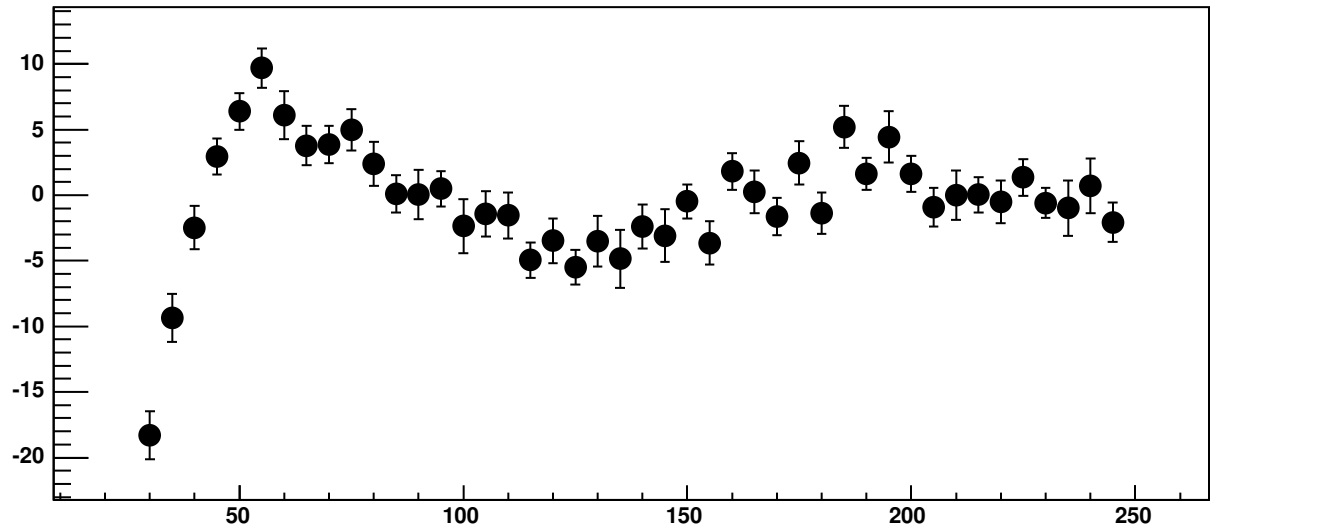
Chip 5, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold



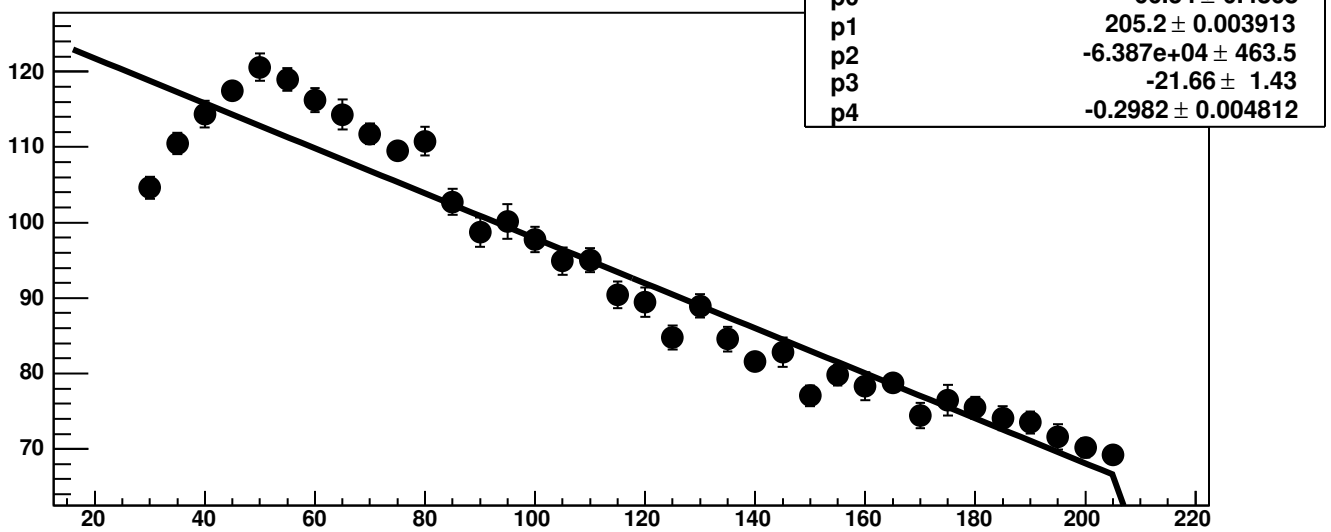
Chip 5, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



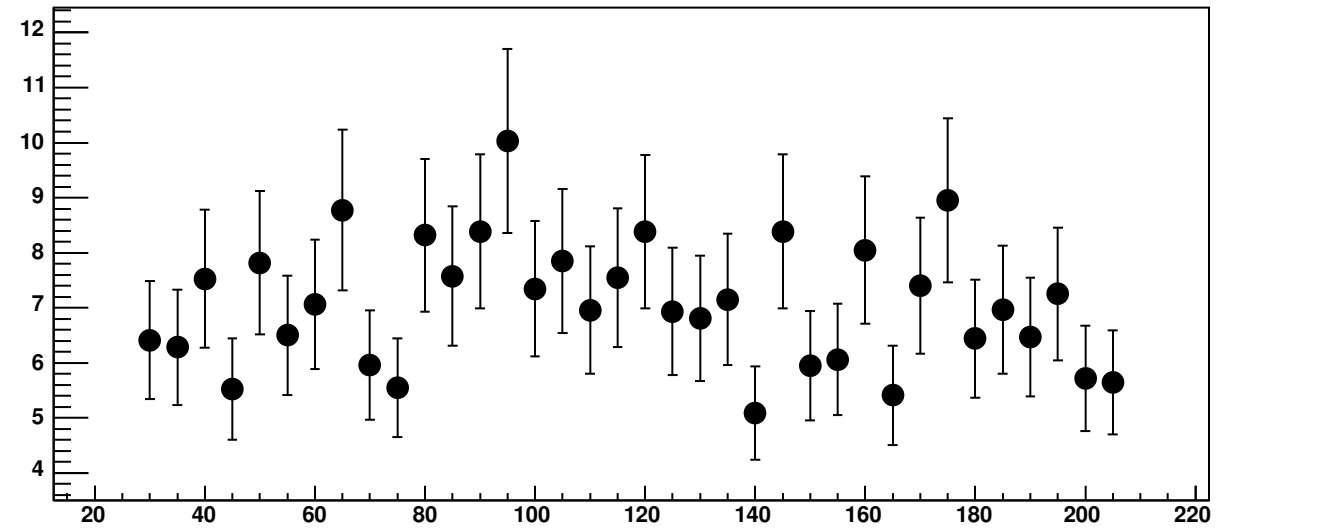
Chip 5, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold



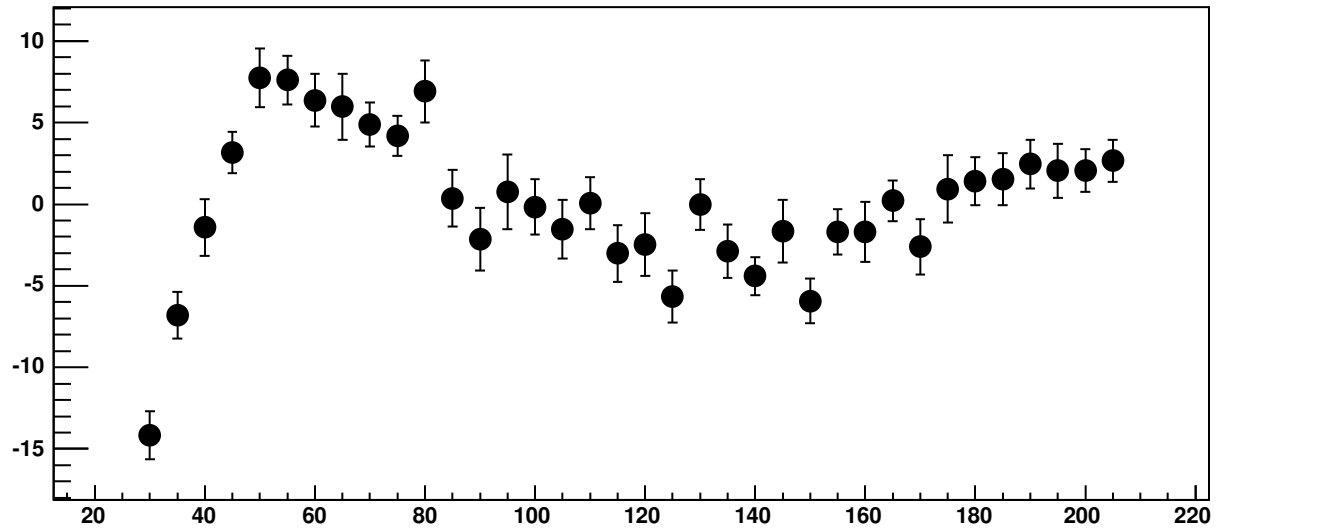
Chip 5, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold



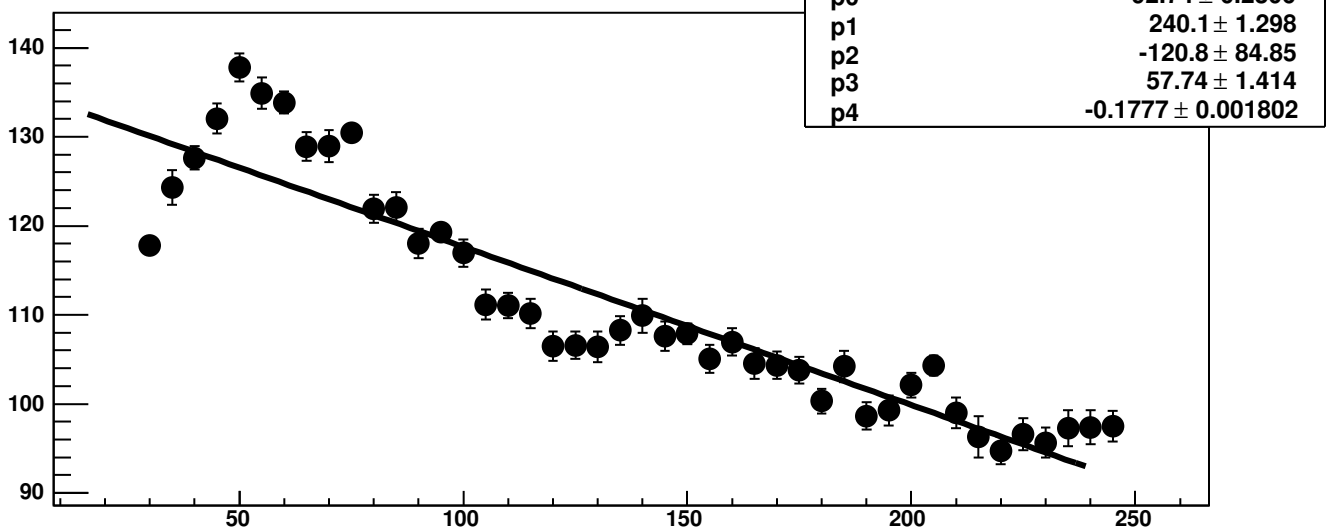
Chip 5, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold



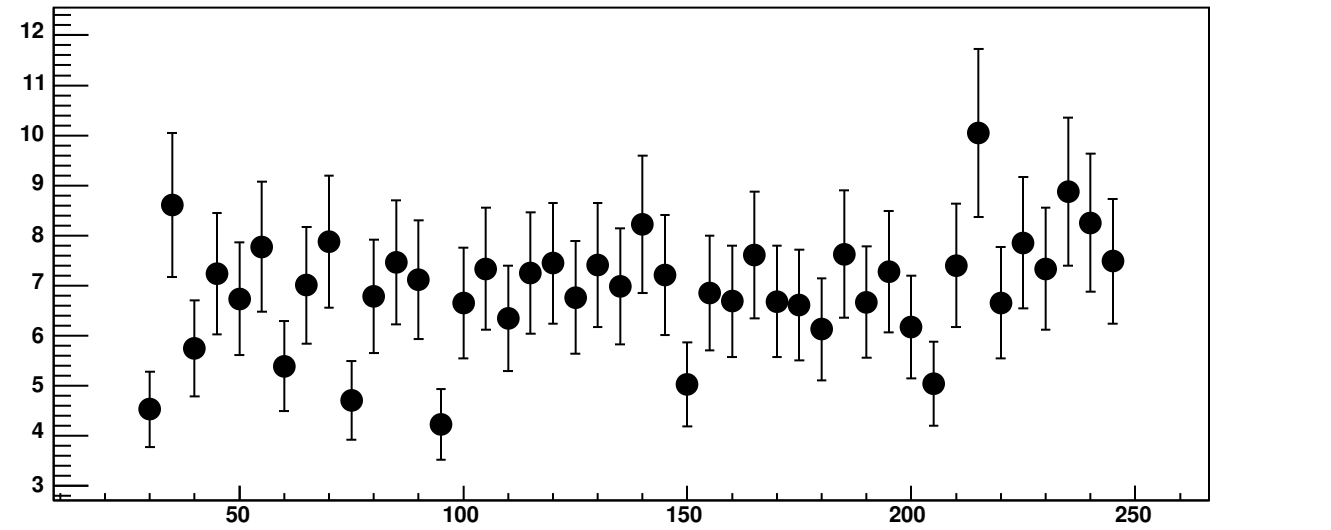
Chip 5, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold



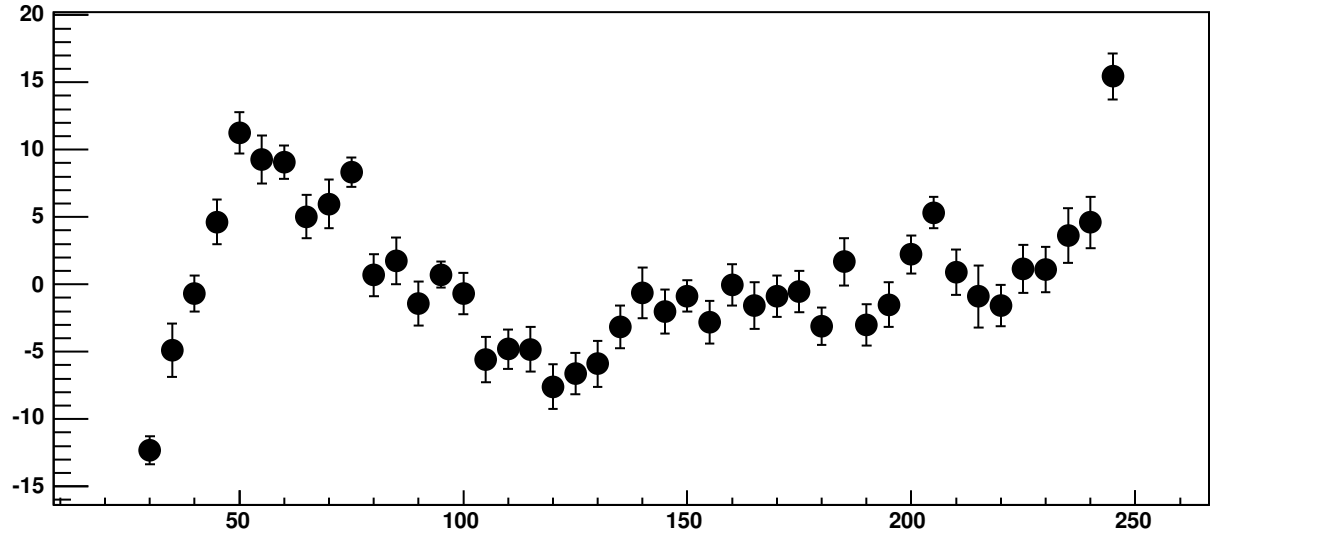
Chip 5, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold



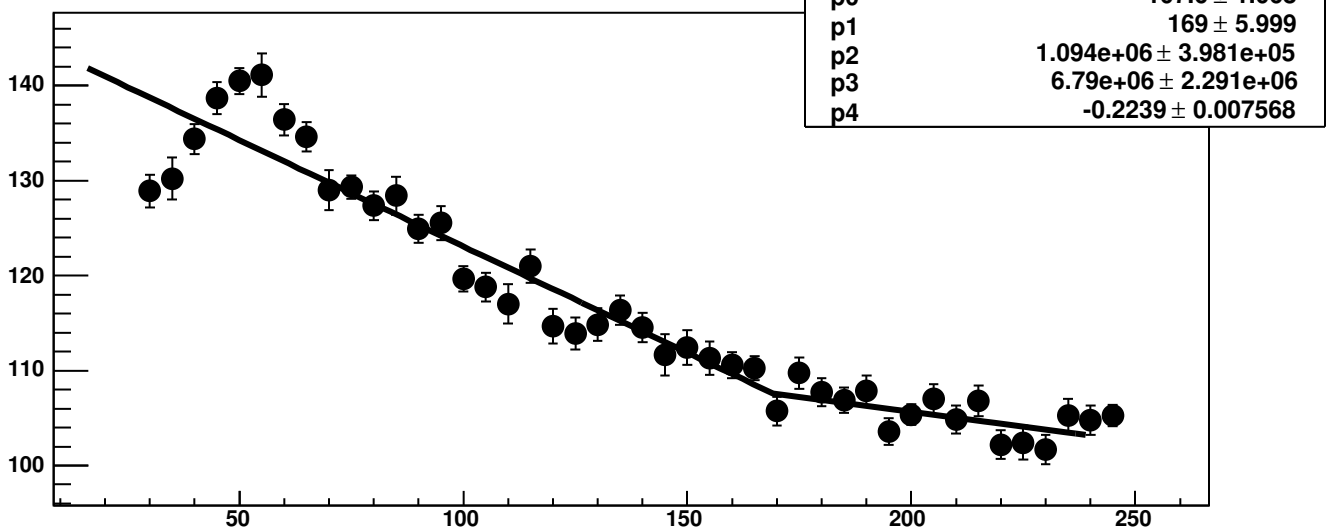
Chip 5, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



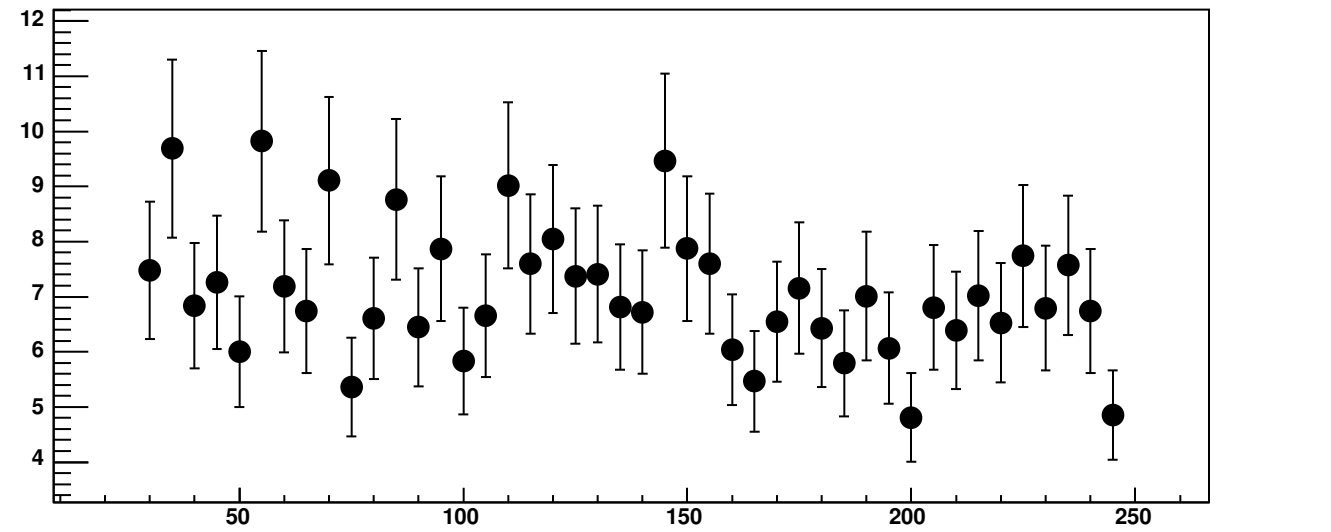
Chip 5, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold



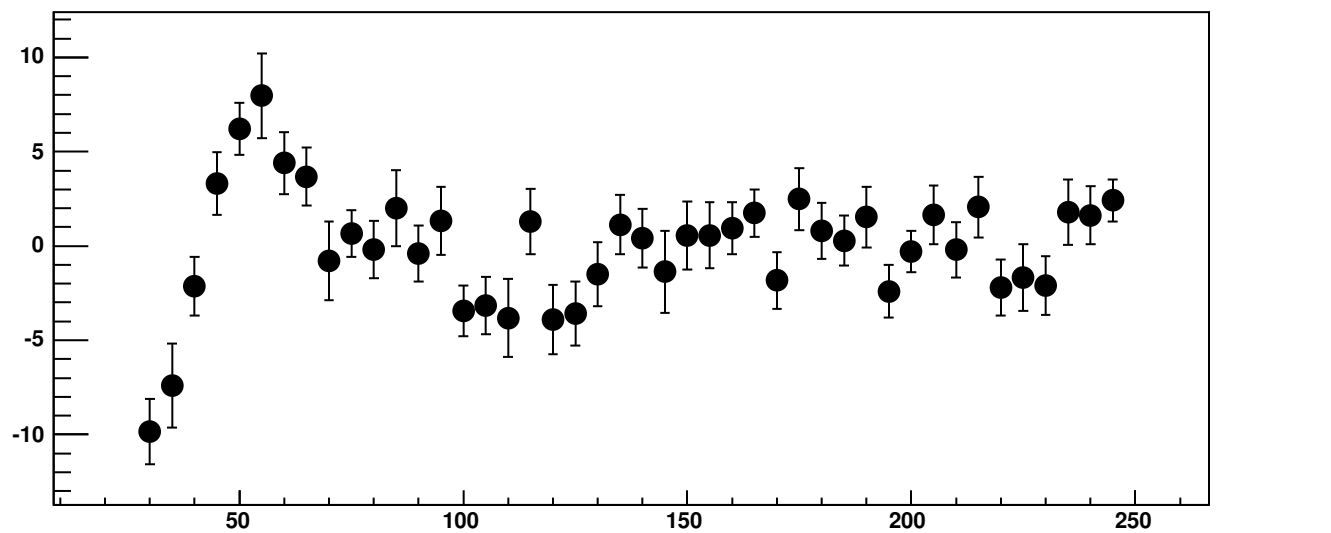
Chip 5, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold



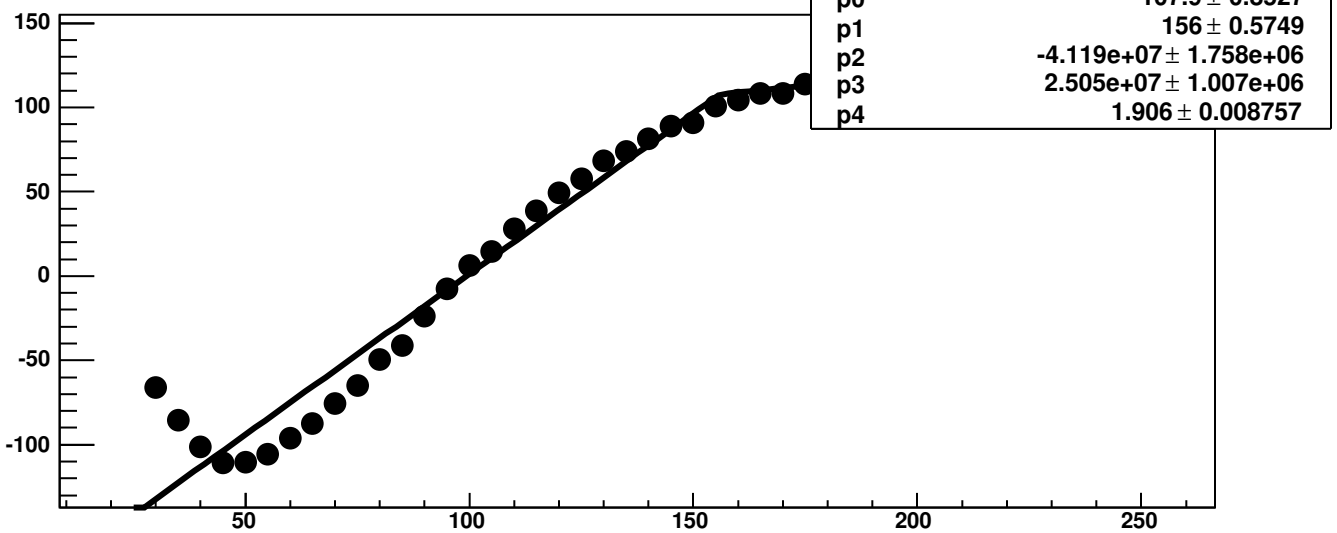
Chip 5, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold



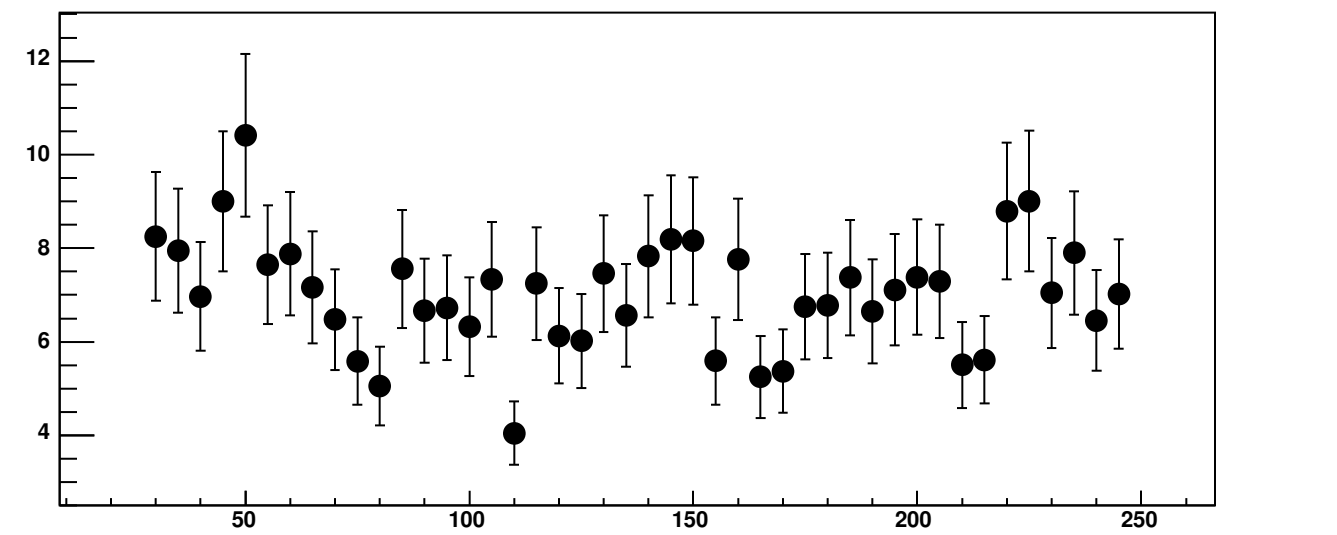
Chip 5, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold



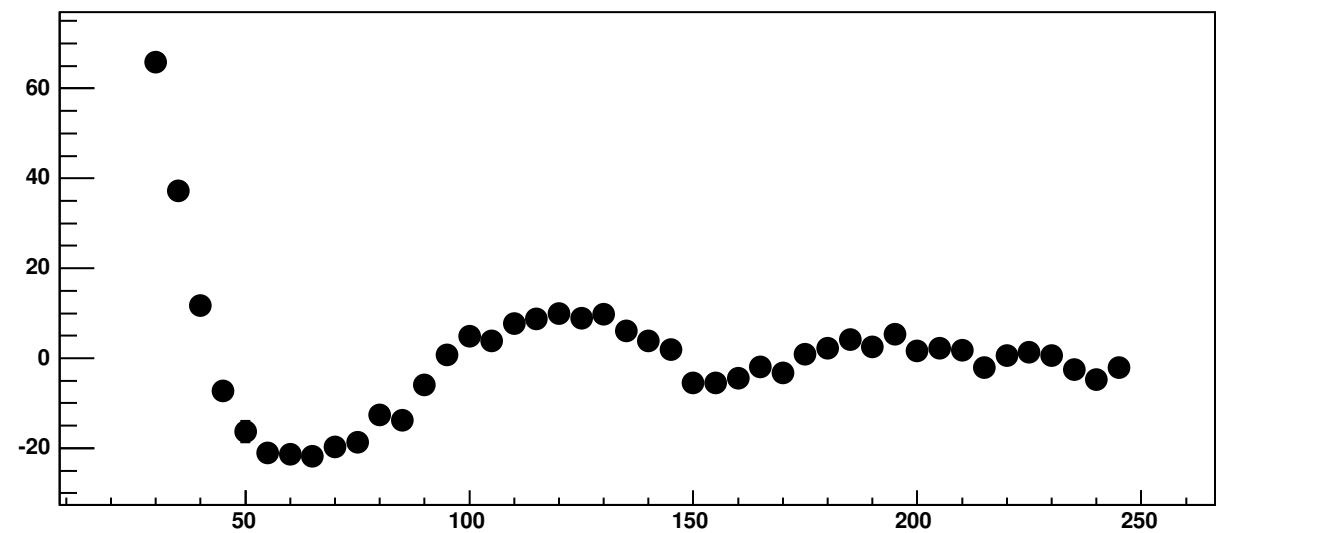
Chip 5, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold



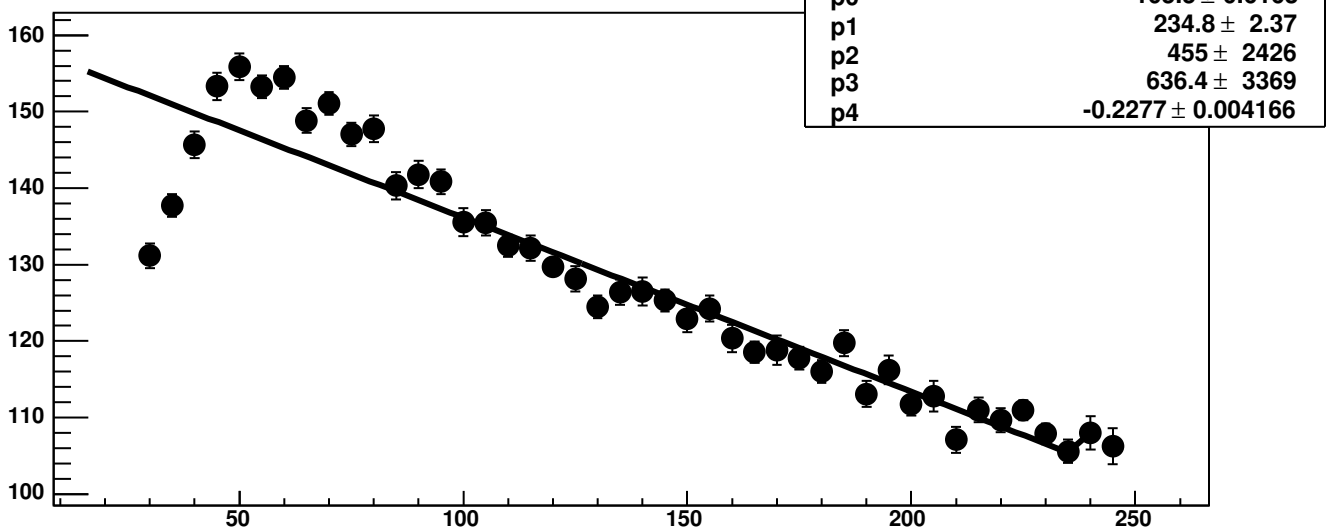
Chip 5, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



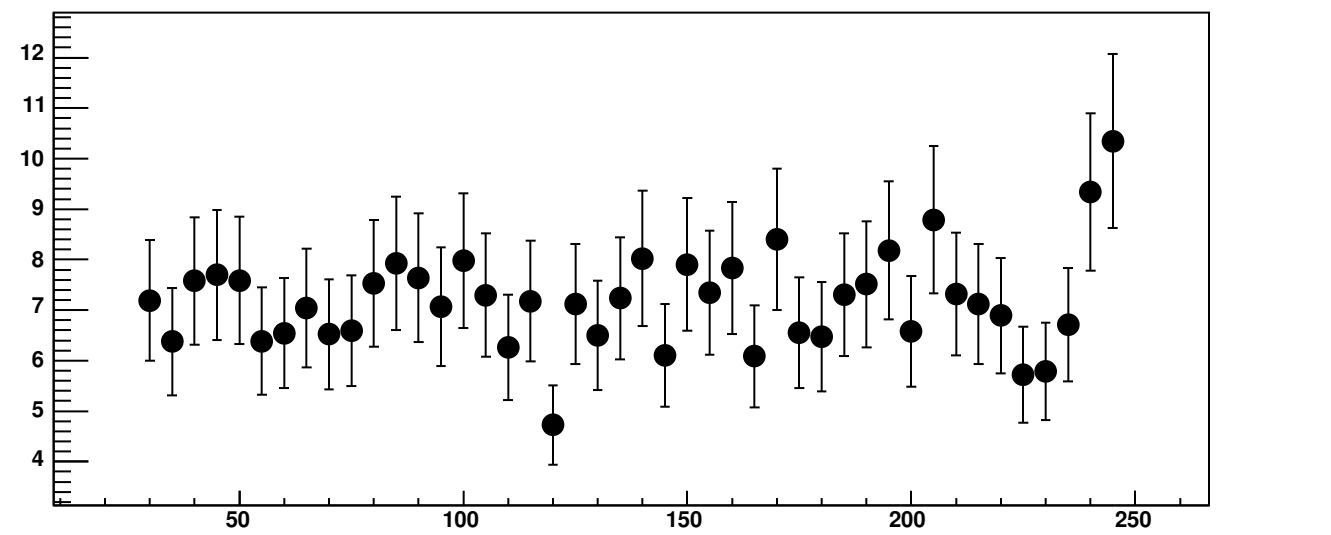
Chip 5, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold



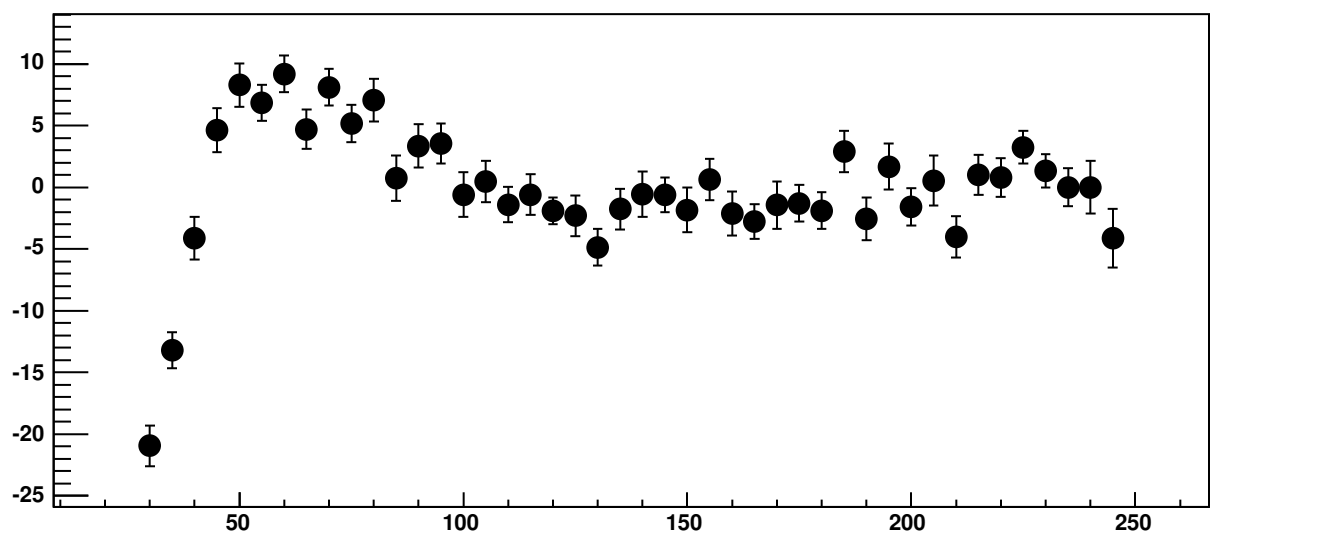
Chip 5, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

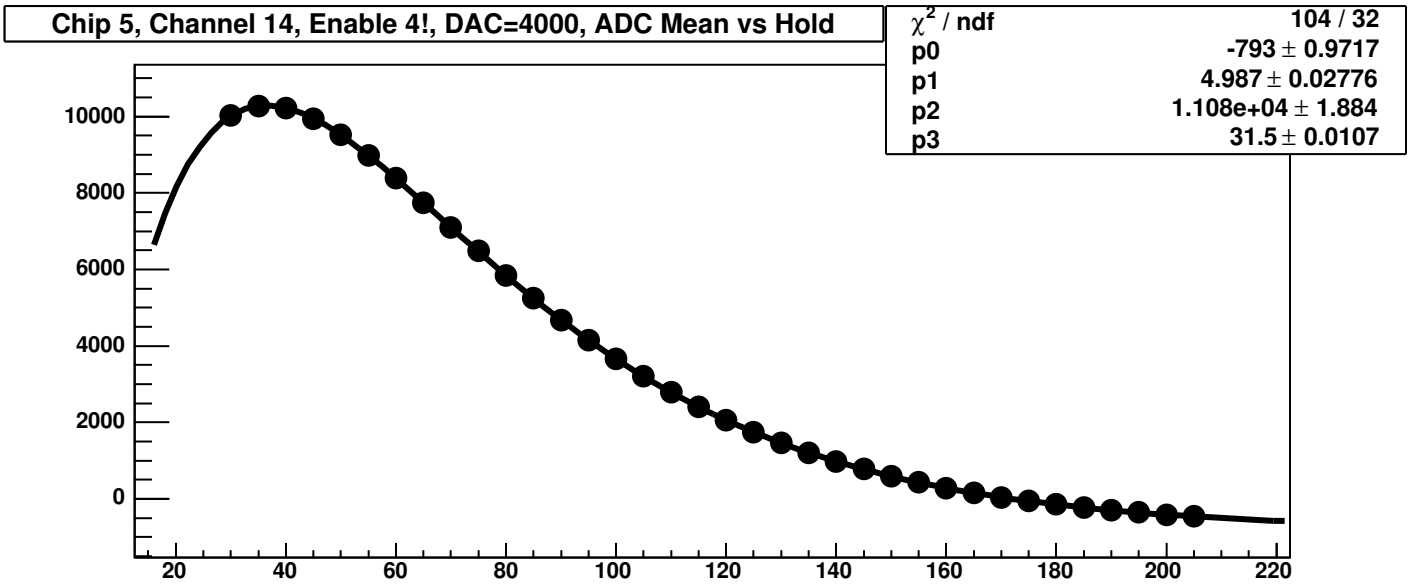


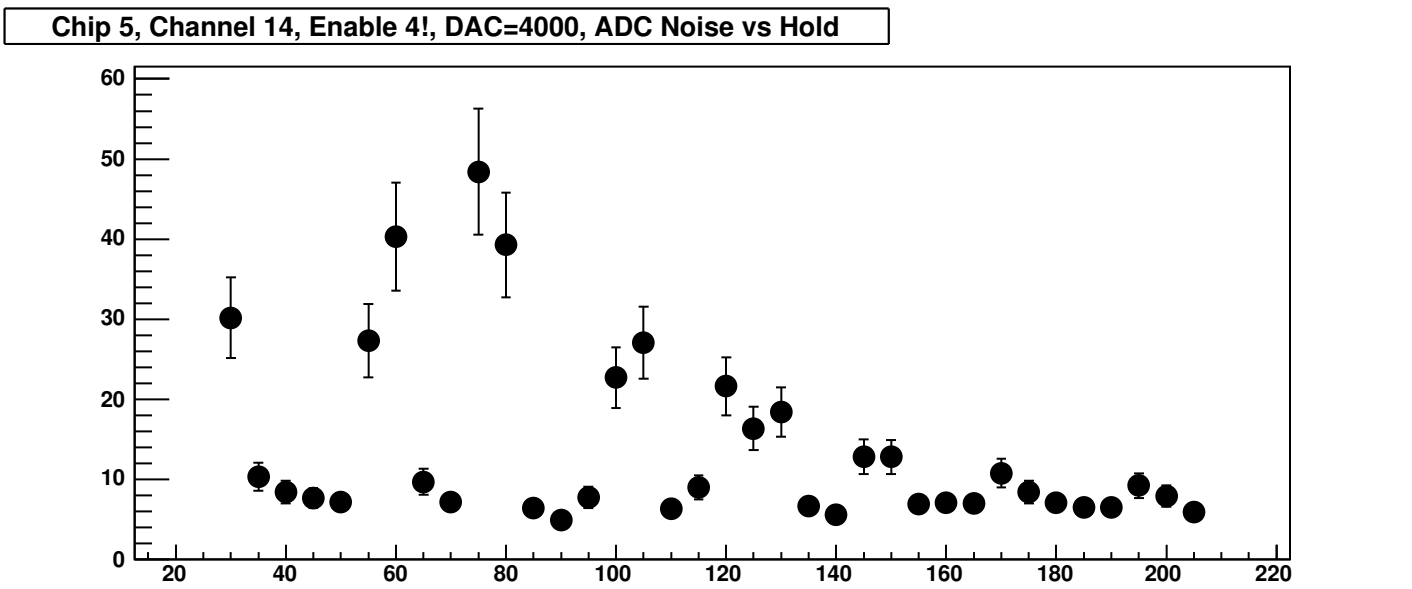
Chip 5, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold

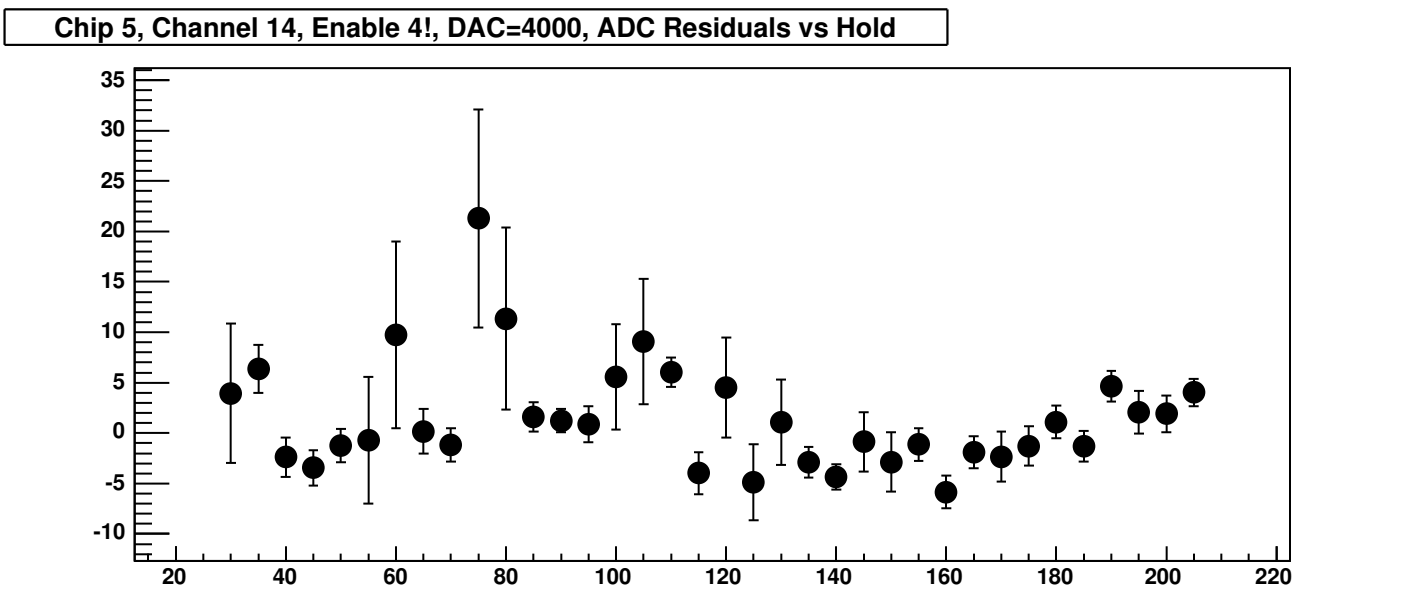


Chip 5, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

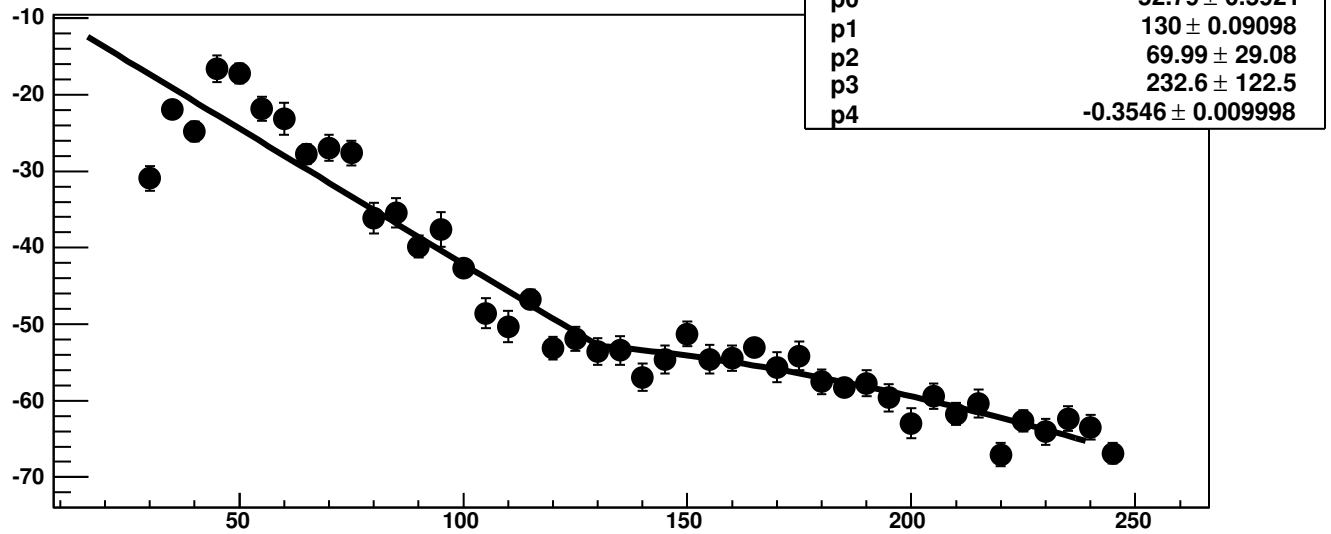



Chip 5, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 104 / 32
p0 -793 ± 0.9717
p1 4.987 ± 0.02776
p2 1.108e+04 ± 1.884
p3 31.5 ± 0.0107
10000
8000
6000
4000
2000
0
20
40
60
80
100
120
140
160
180
200
220

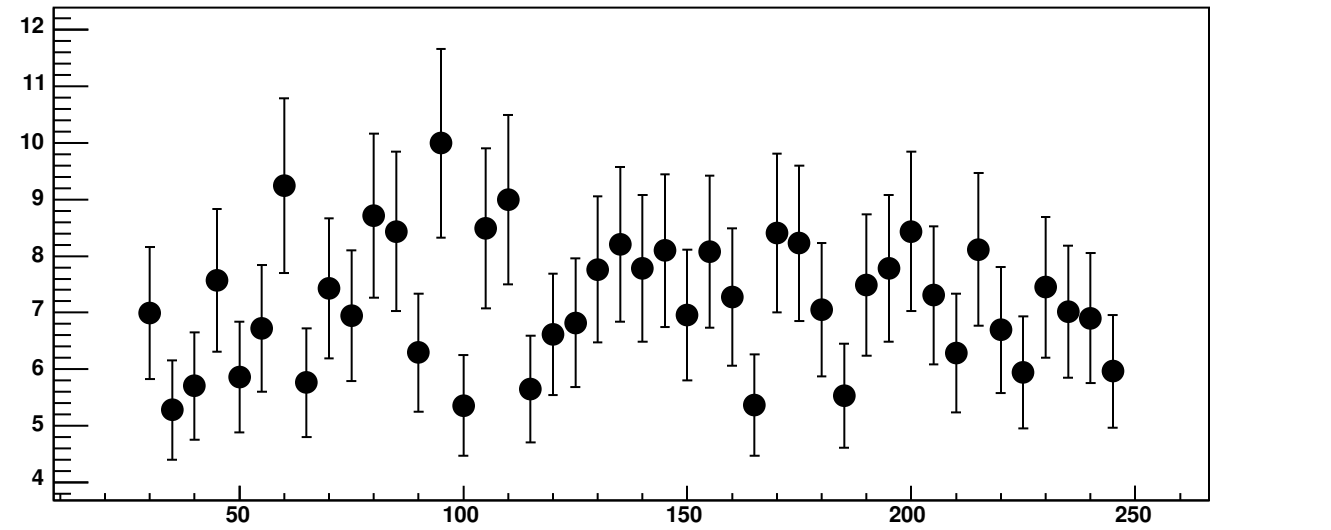

Chip 5, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold
60
50
40
30
20
10
0
20
40
60
80
100
120
140
160
180
200
220


Chip 5, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold
35
30
25
20
15
10
5
0
-5
-10
20
40
60
80
100
120
140
160
180
200
220

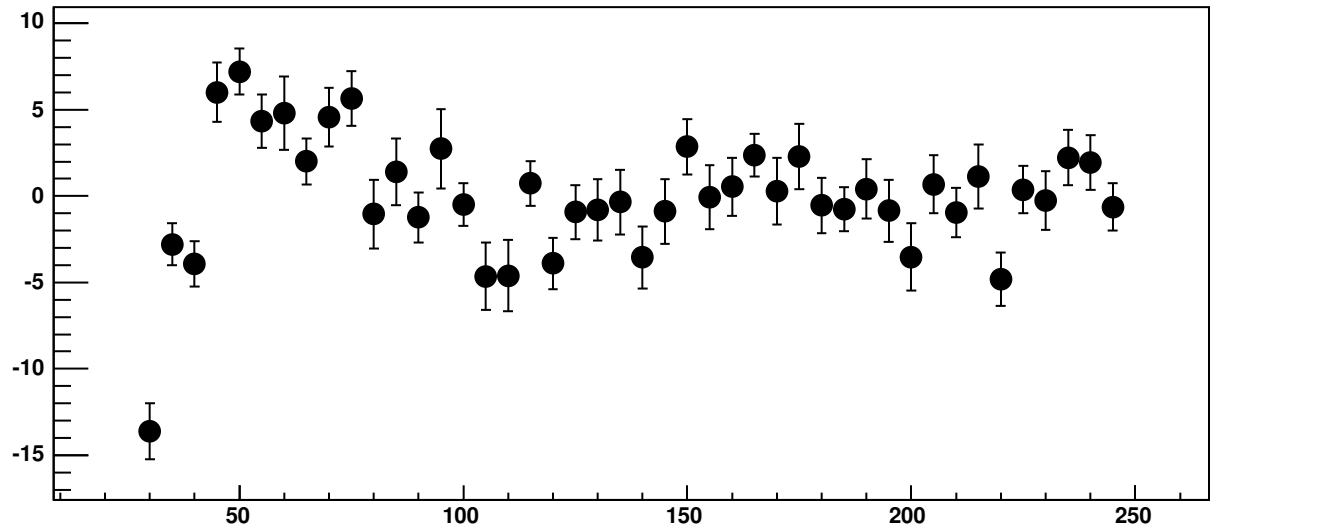
Chip 5, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold



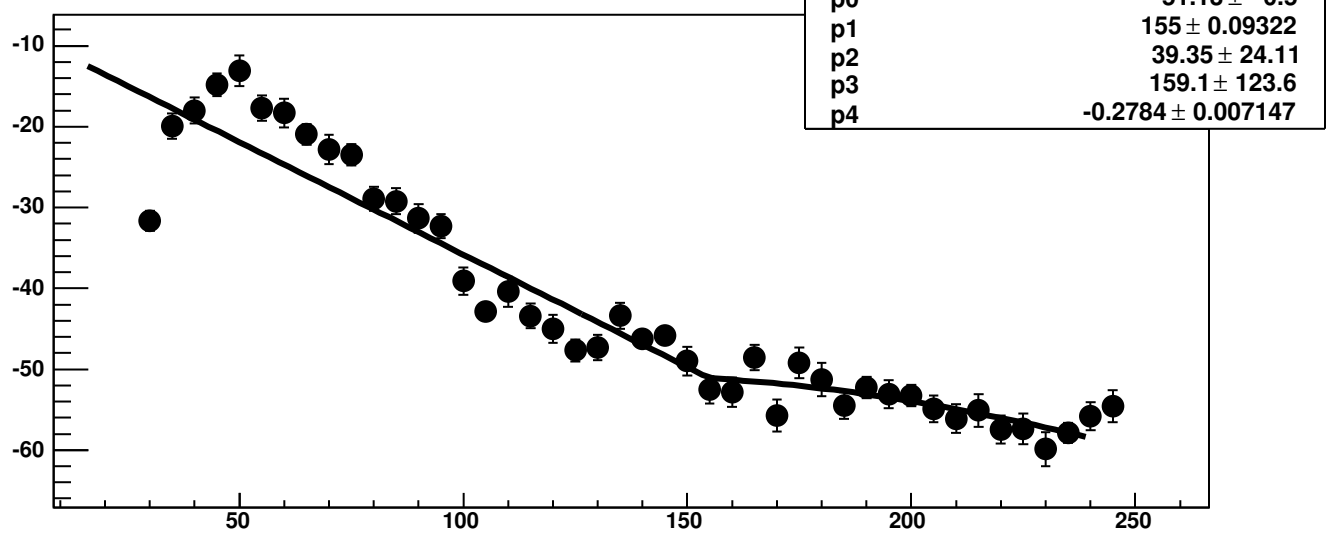
Chip 5, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



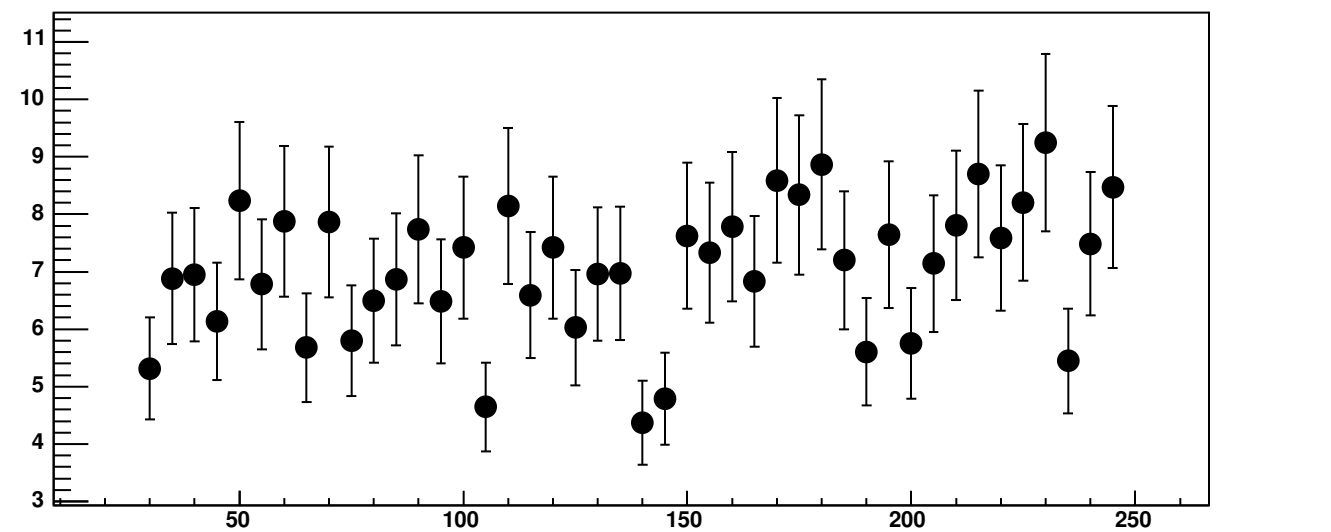
Chip 5, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold



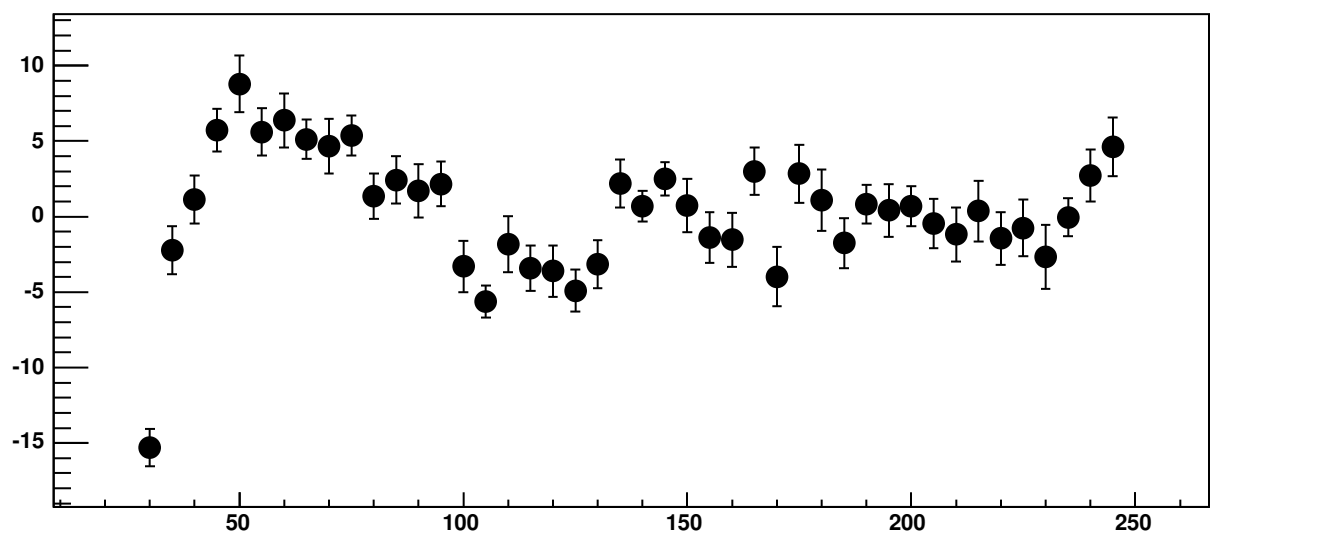
Chip 5, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold



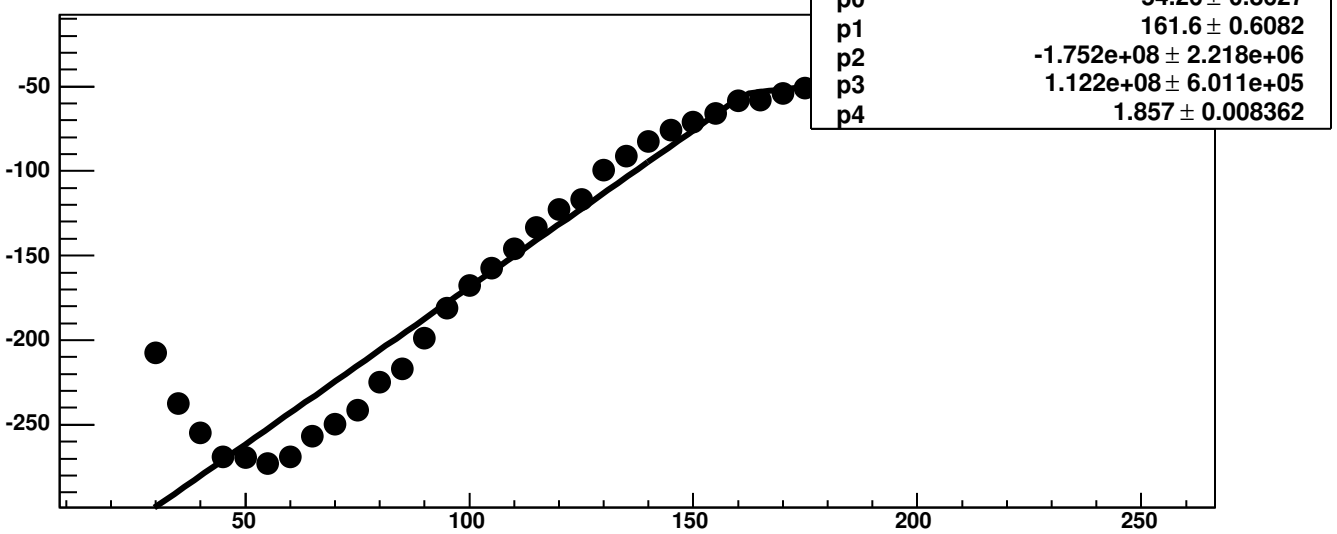
Chip 5, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



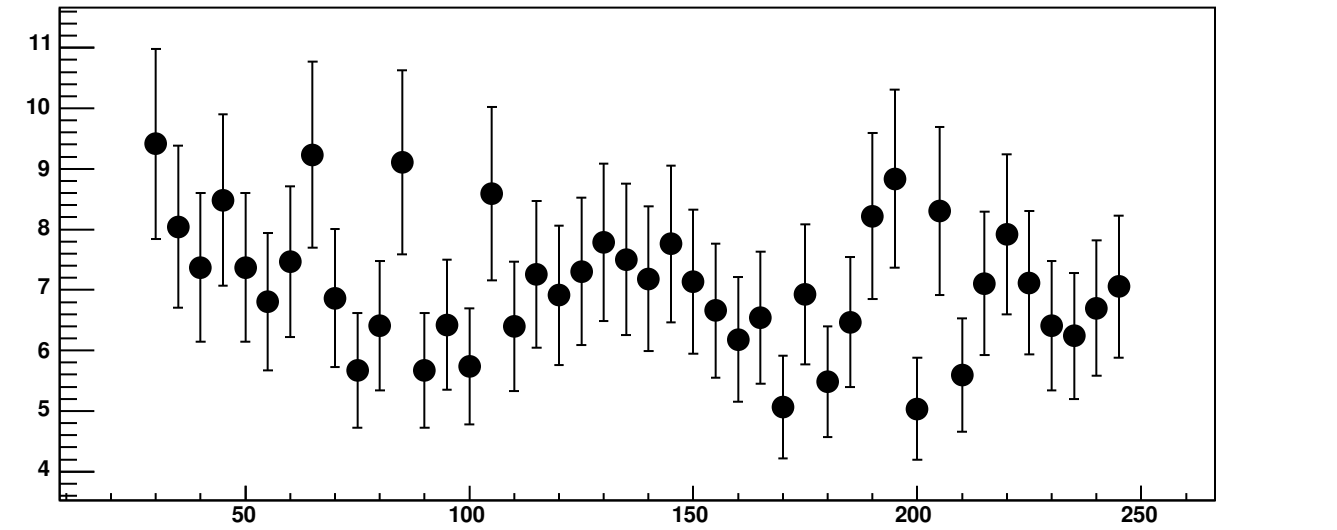
Chip 5, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold



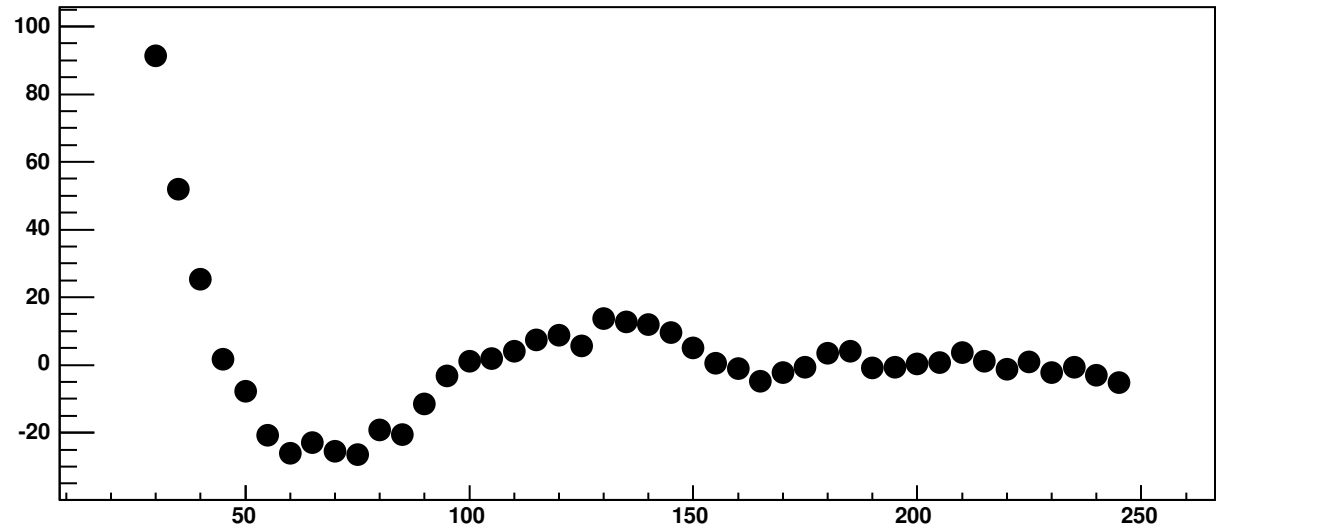
Chip 5, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold



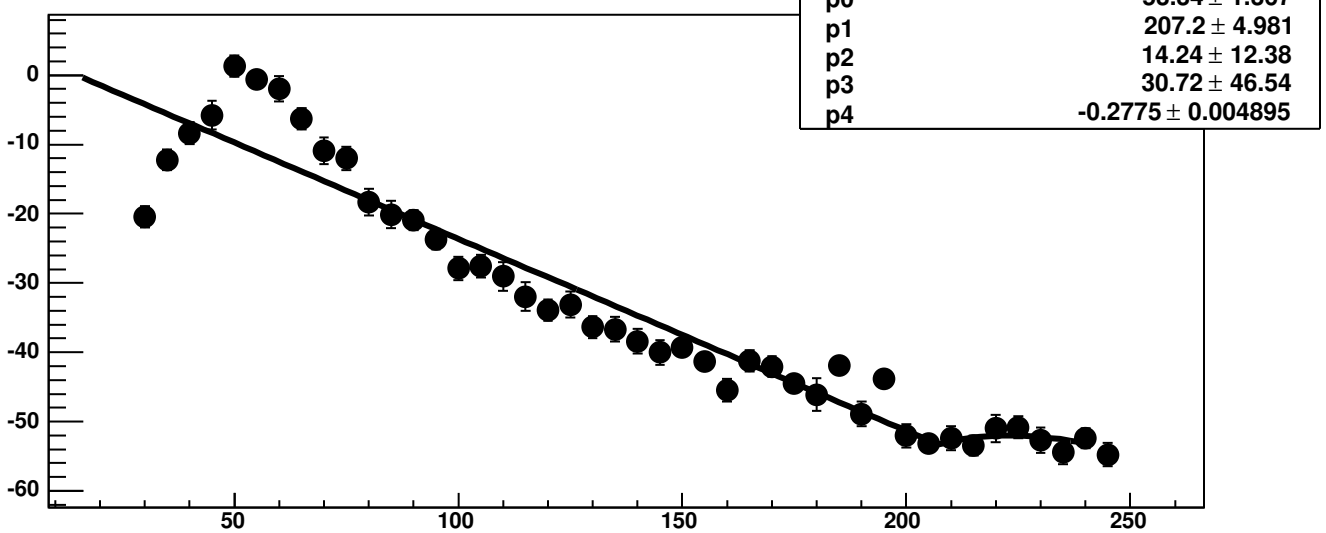
Chip 5, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold



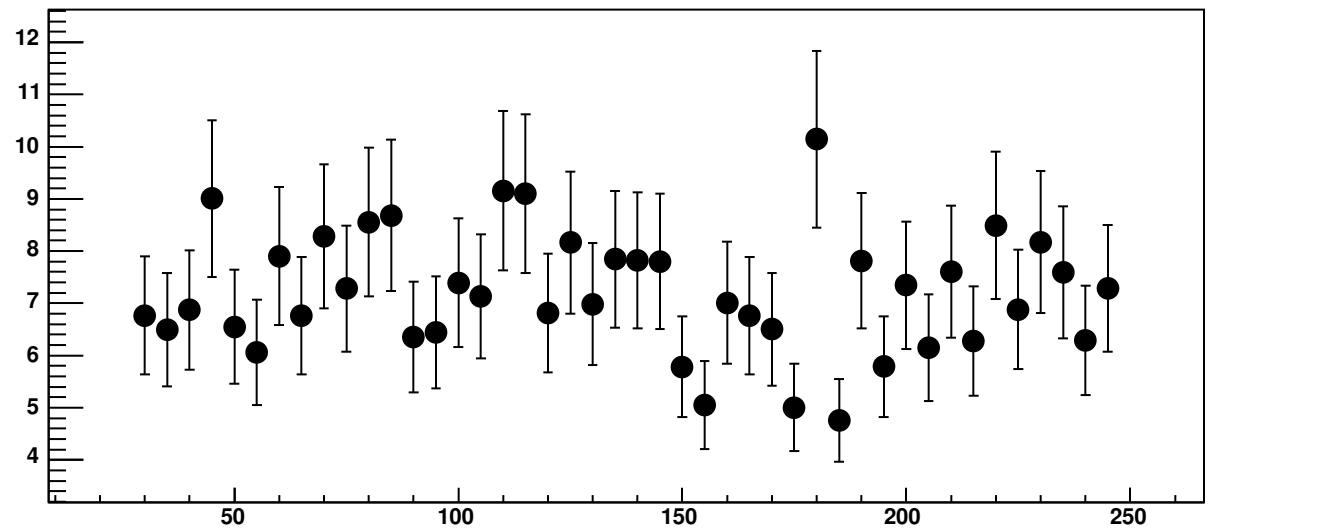
Chip 5, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold



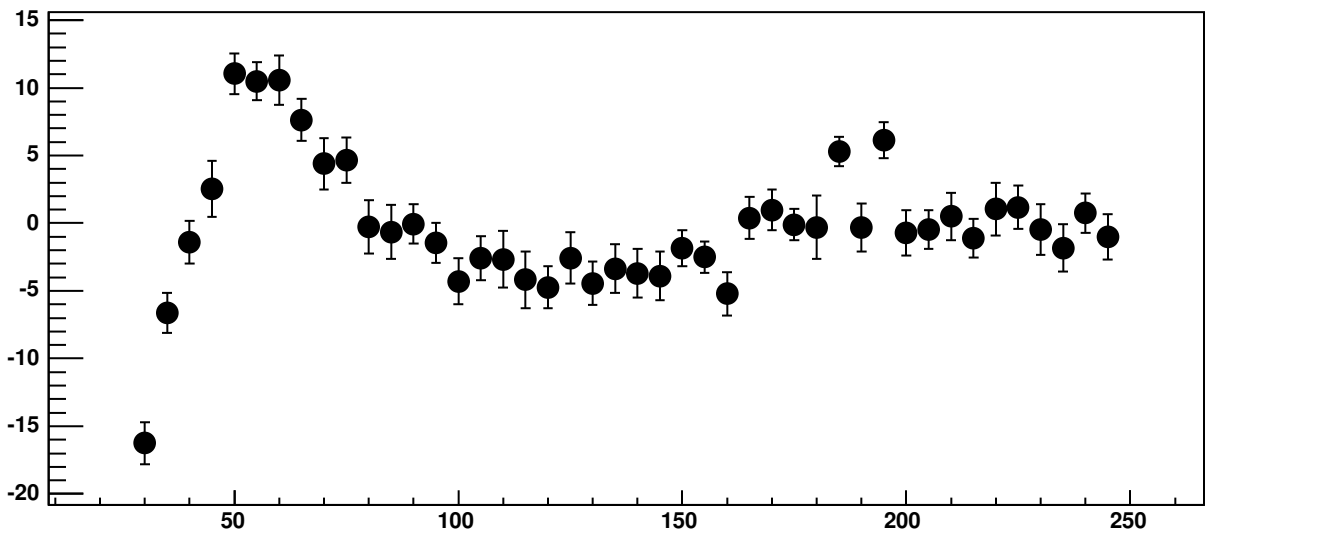
Chip 5, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold



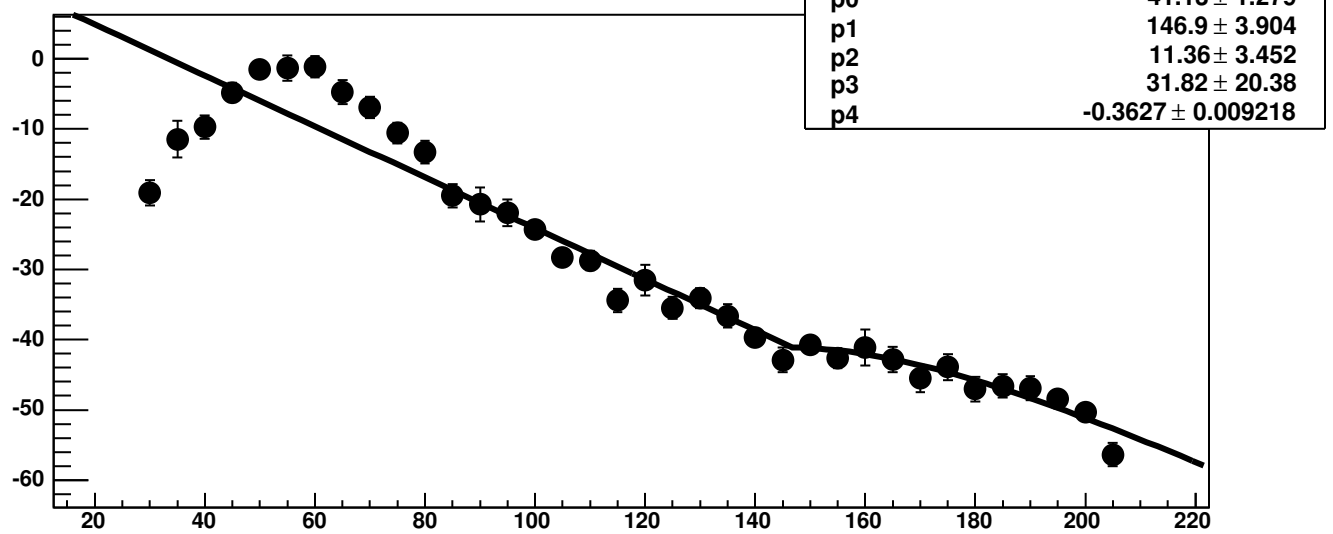
Chip 5, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



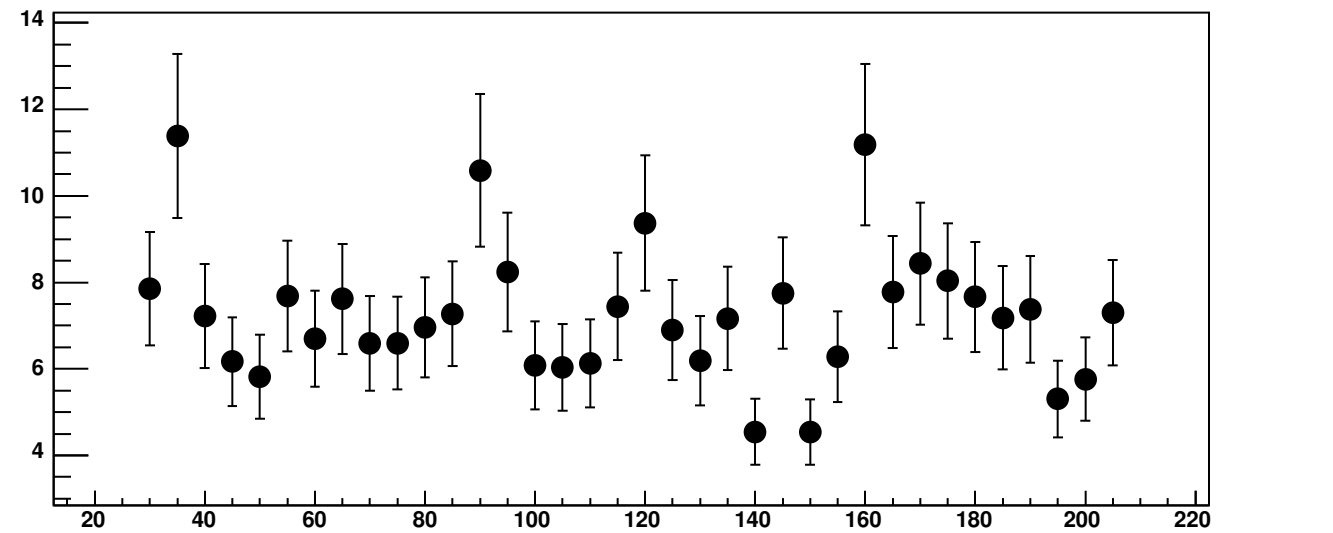
Chip 5, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold



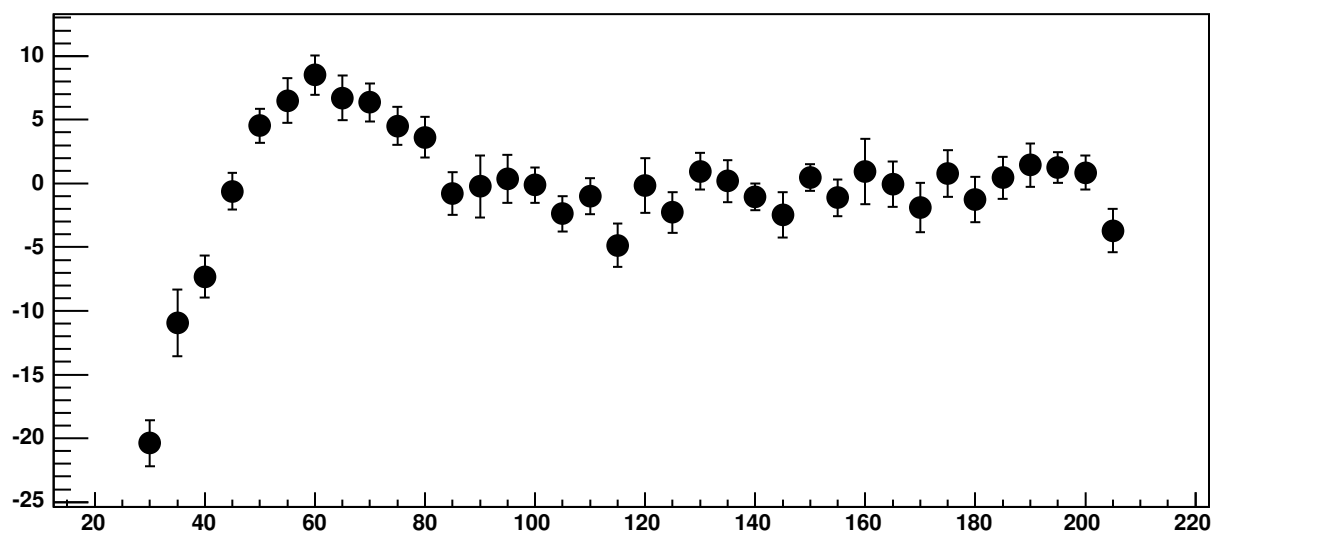
Chip 5, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold



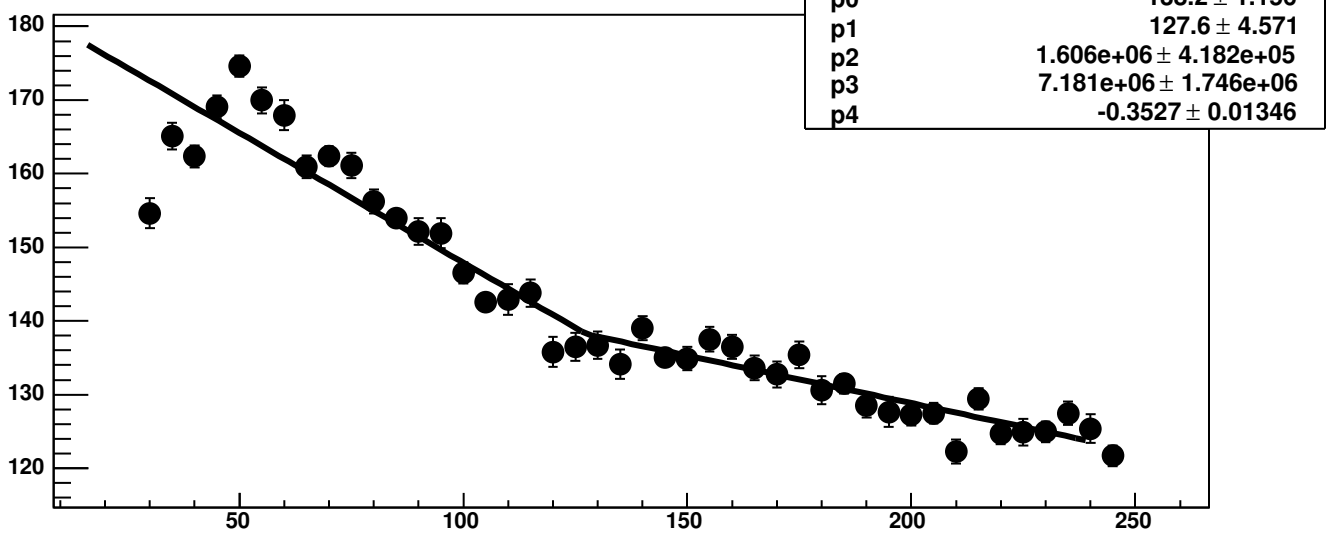
Chip 5, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



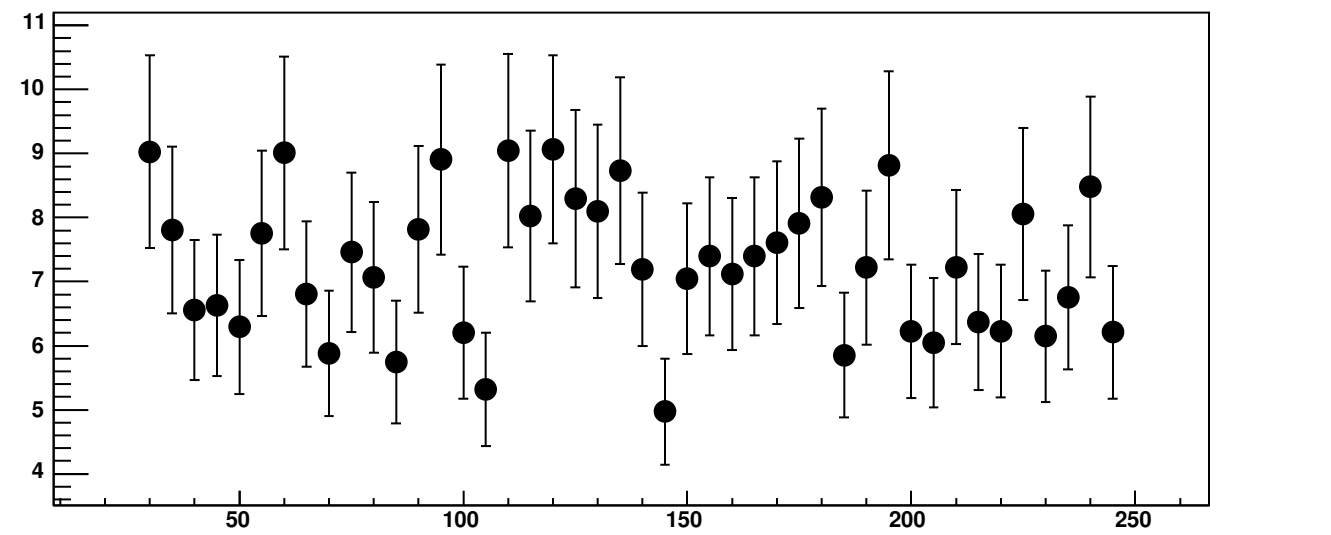
Chip 5, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold



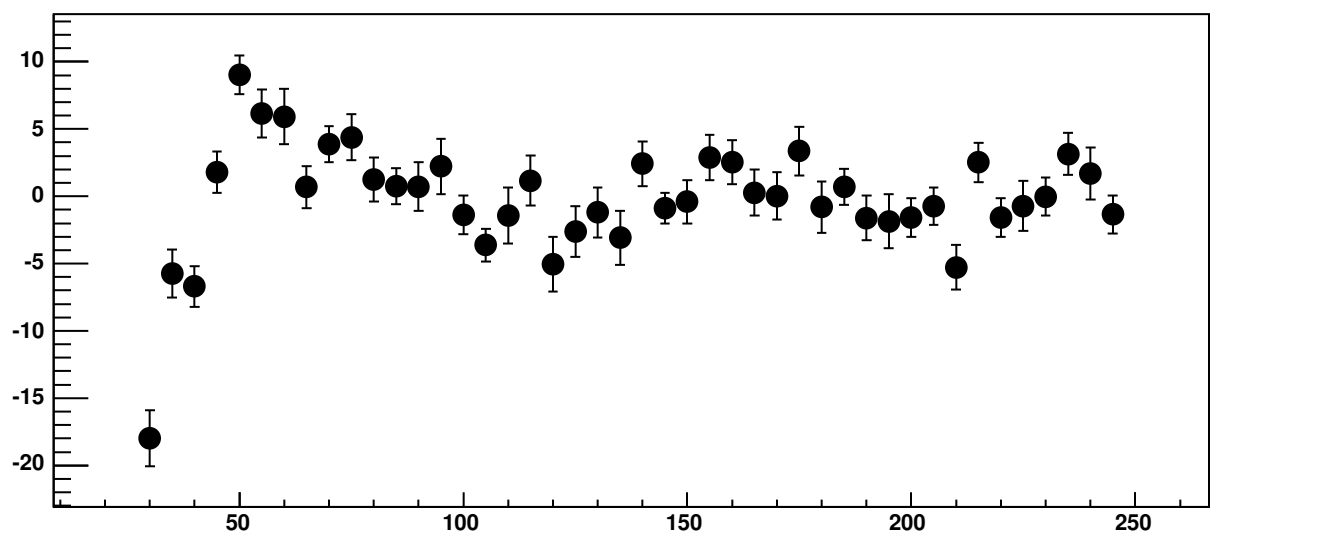
Chip 5, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold



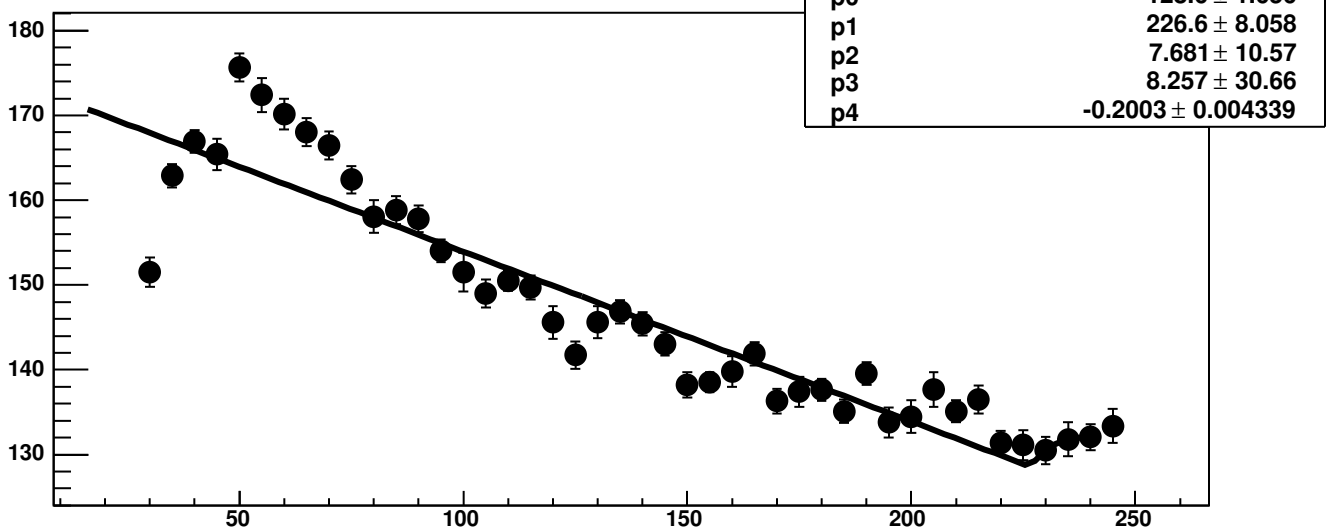
Chip 5, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



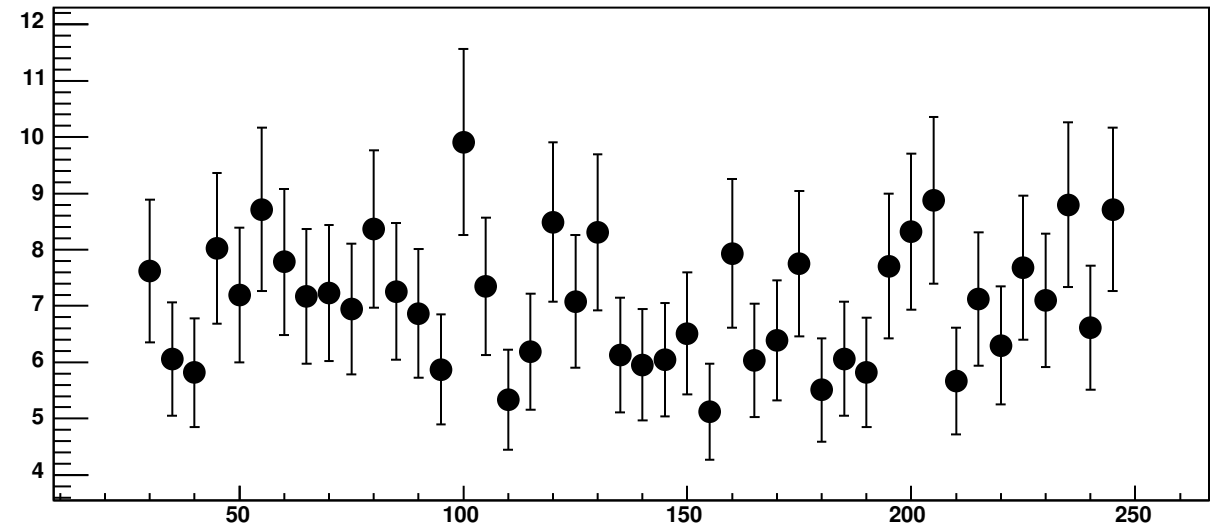
Chip 5, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold



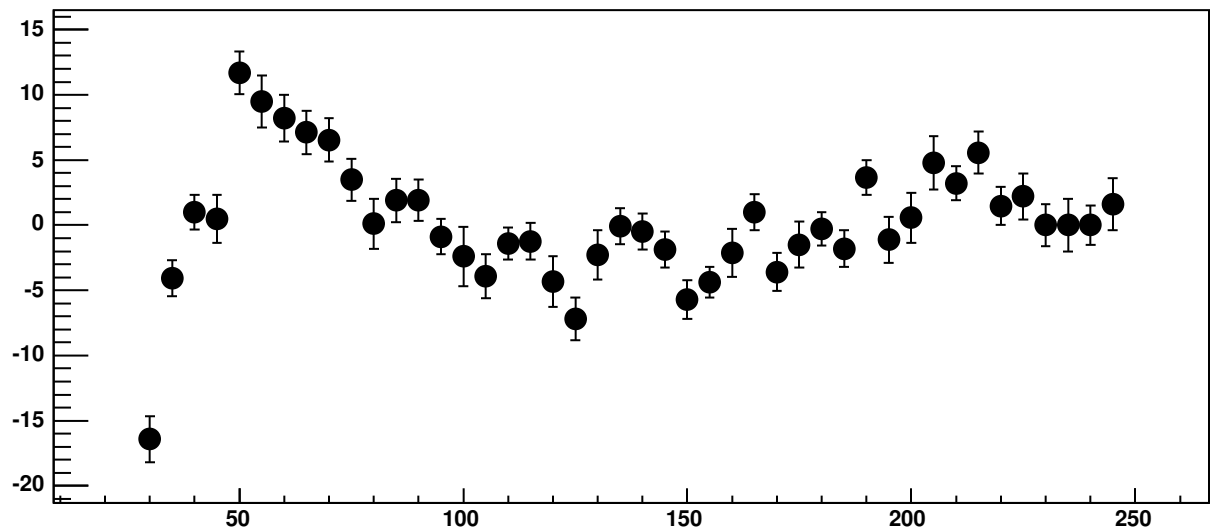
Chip 5, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold

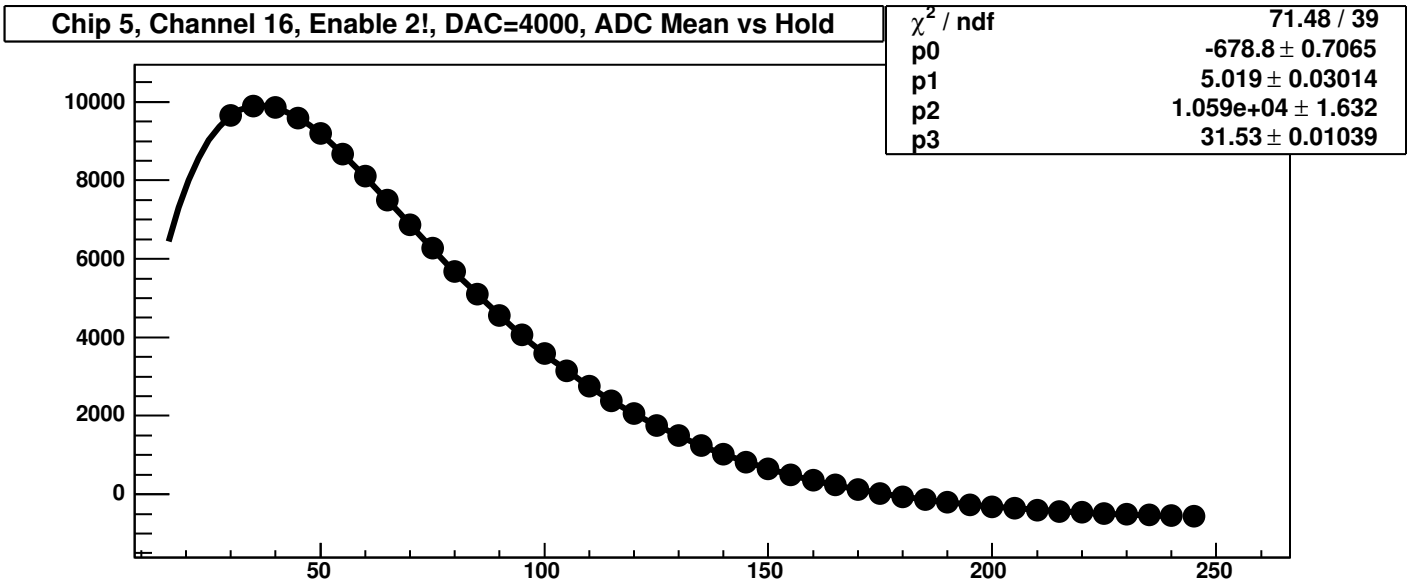


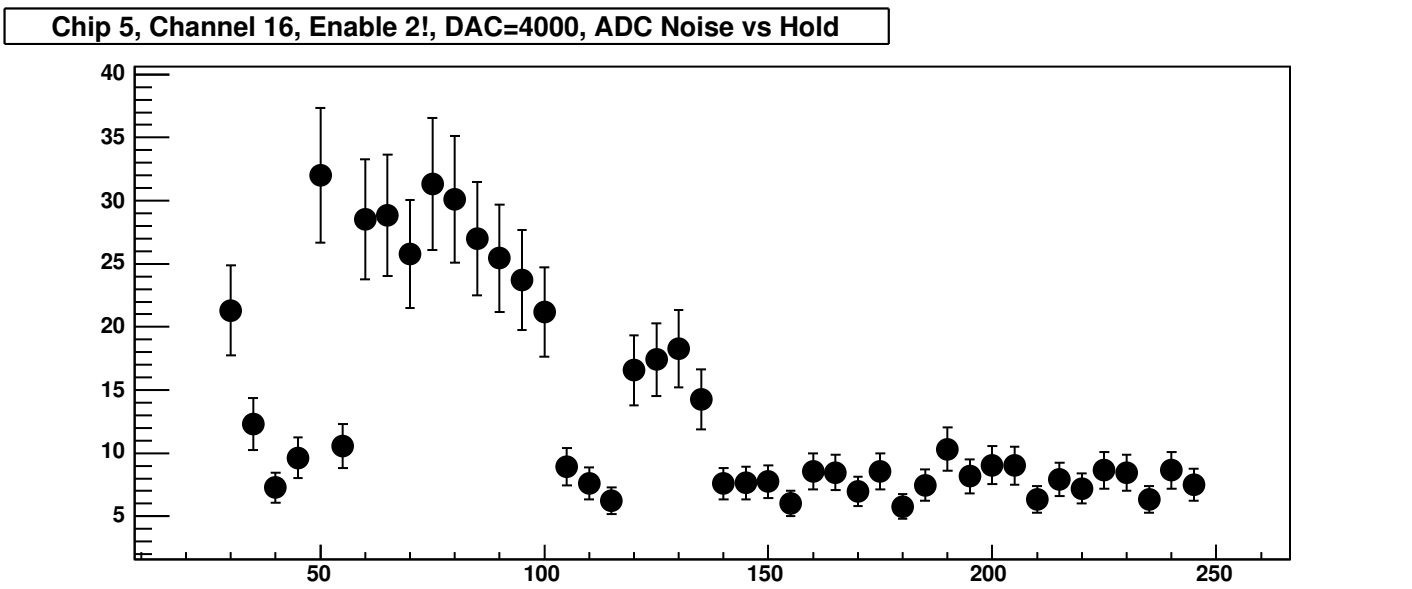
Chip 5, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold

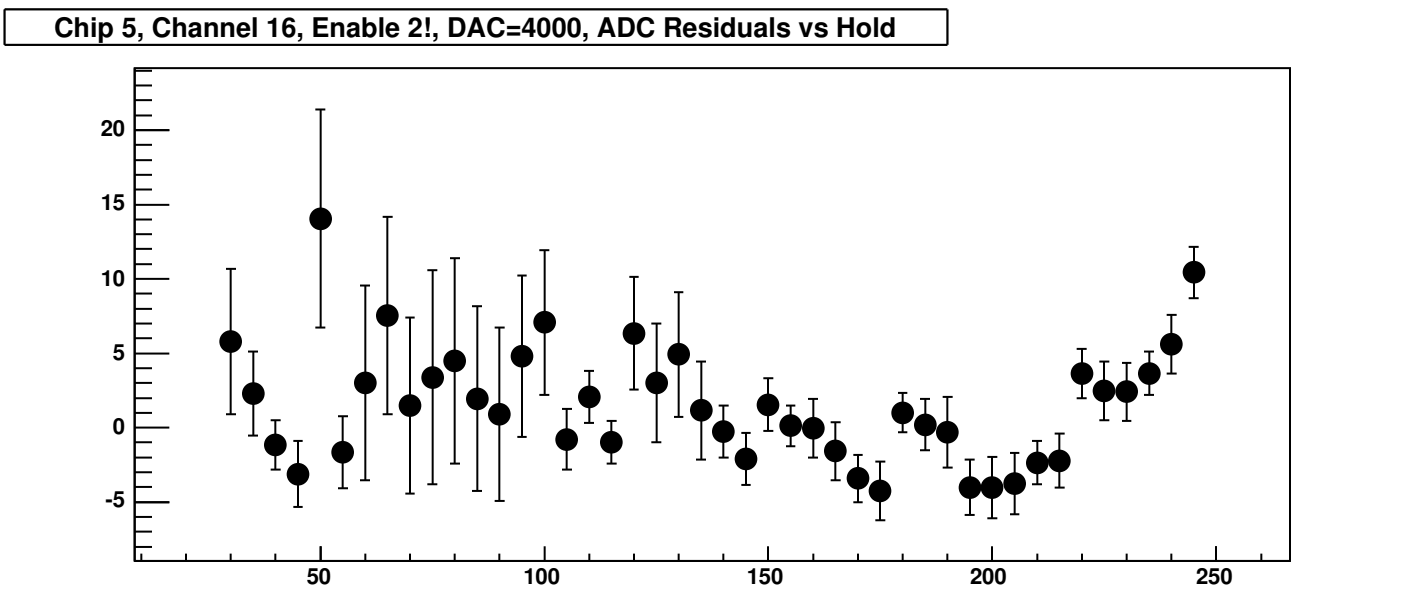


Chip 5, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold

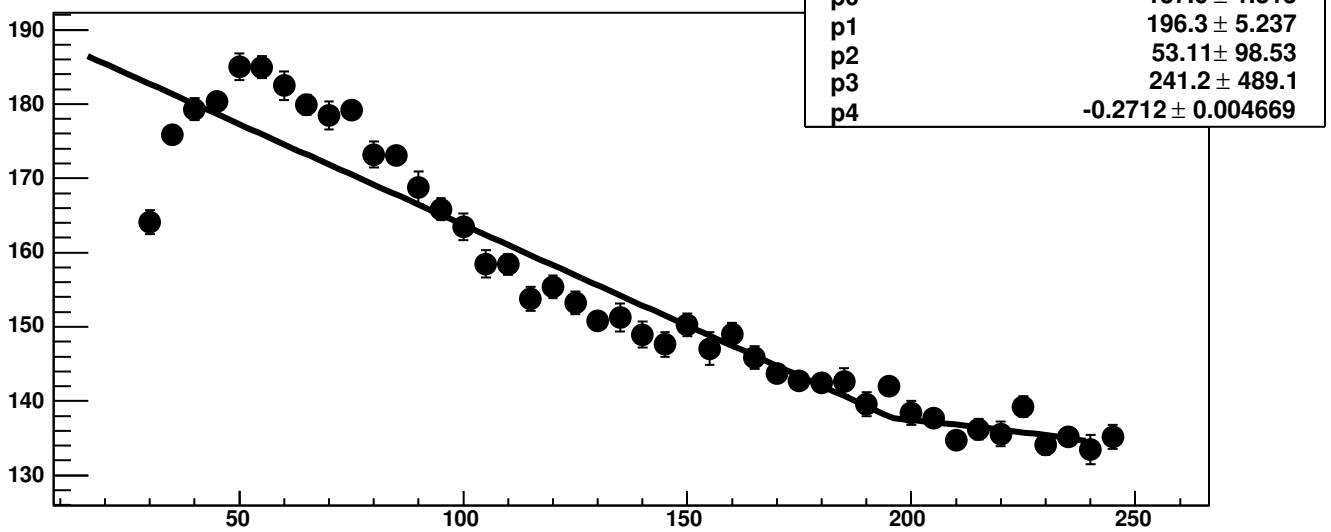



Chip 5, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 71.48 / 39
p0 -678.8 ± 0.7065
p1 5.019 ± 0.03014
p2 1.059e+04 ± 1.632
p3 31.53 ± 0.01039
10000
8000
6000
4000
2000
0
50
100
150
200
250

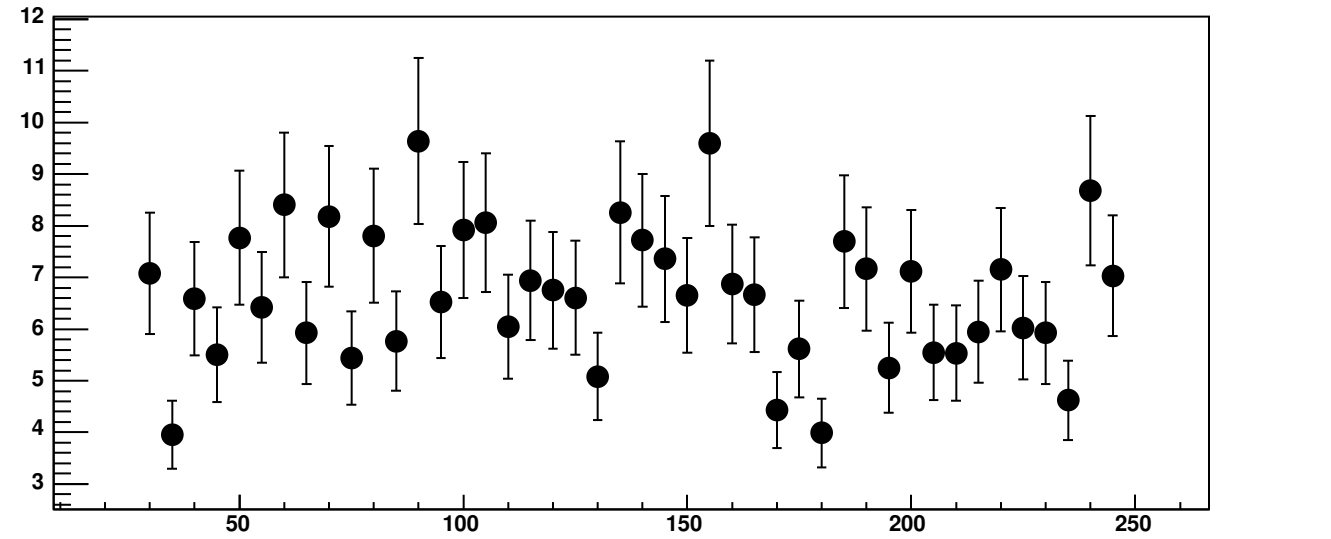

Chip 5, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold
40
35
30
25
20
15
10
5
50
100
150
200
250


Chip 5, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
50
100
150
200
250

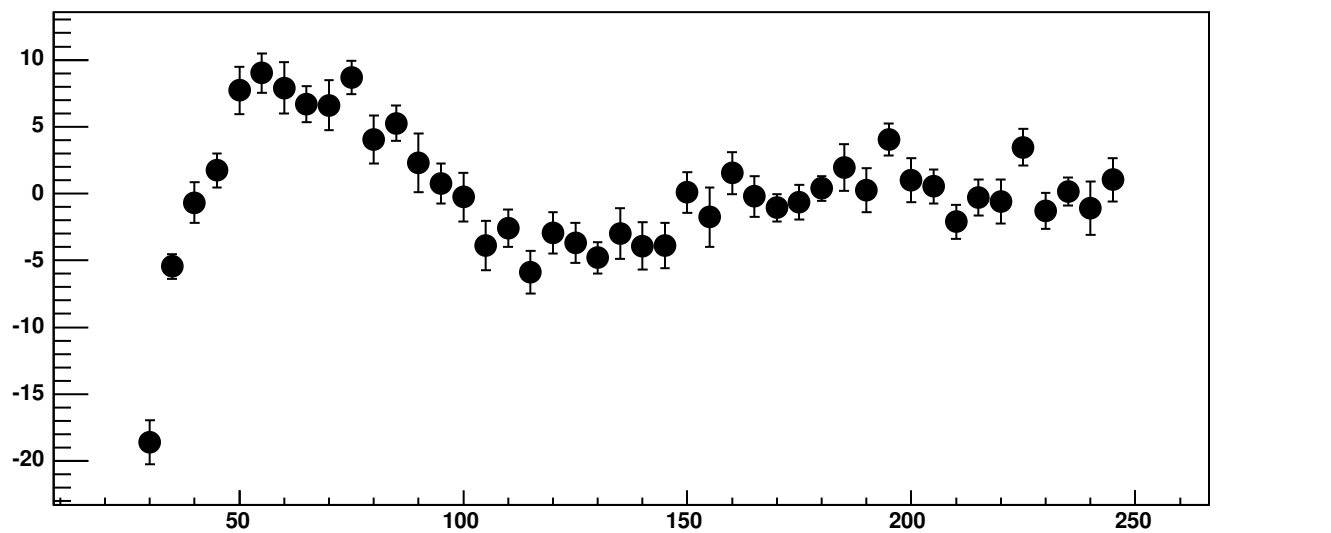
Chip 5, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold



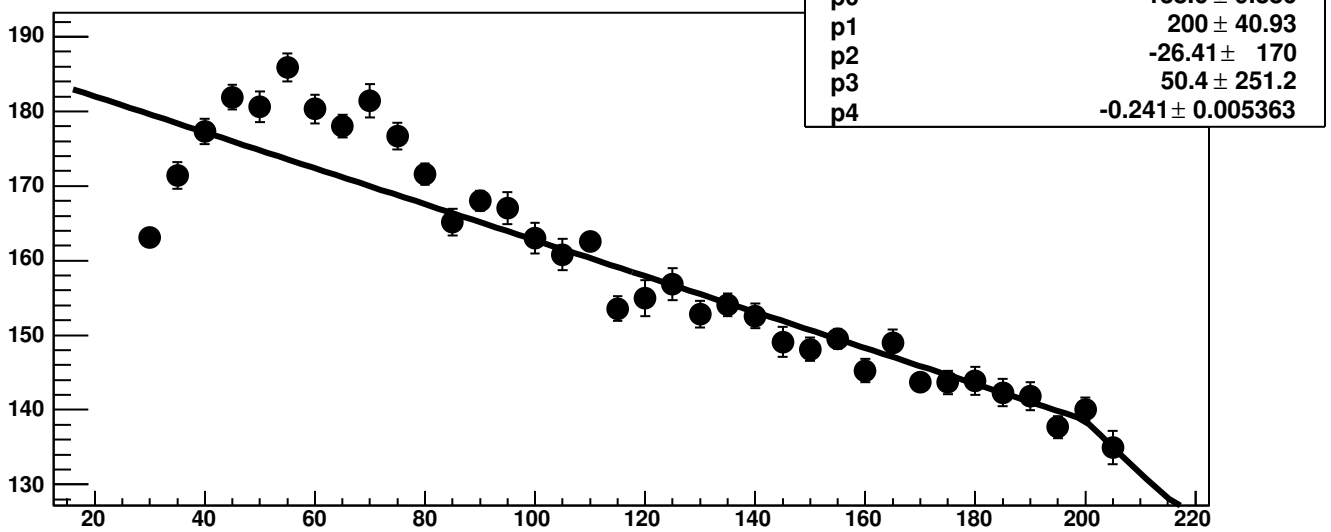
Chip 5, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



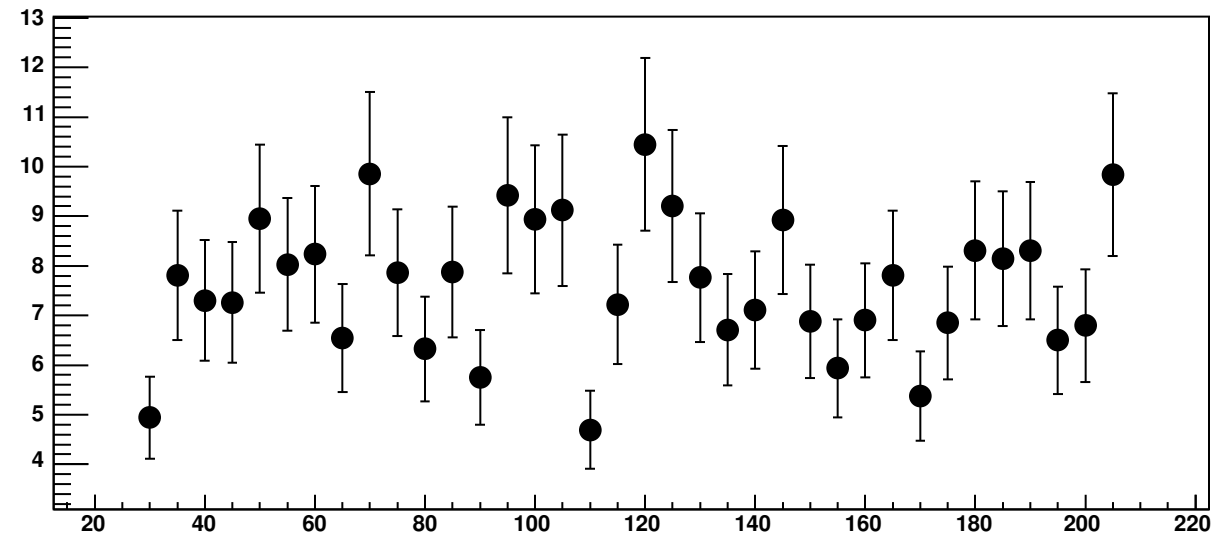
Chip 5, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold



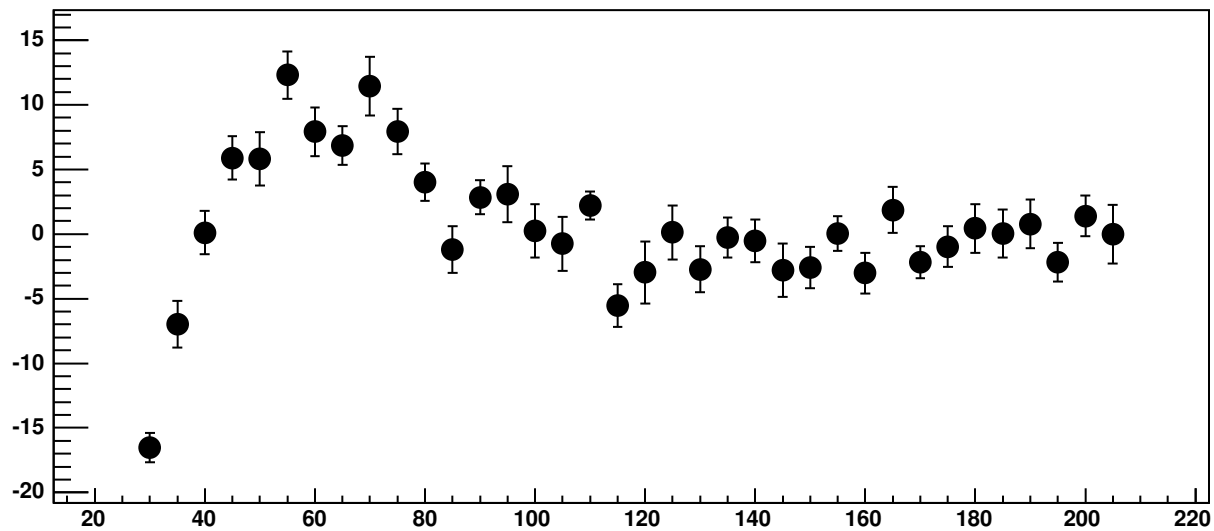
Chip 5, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold



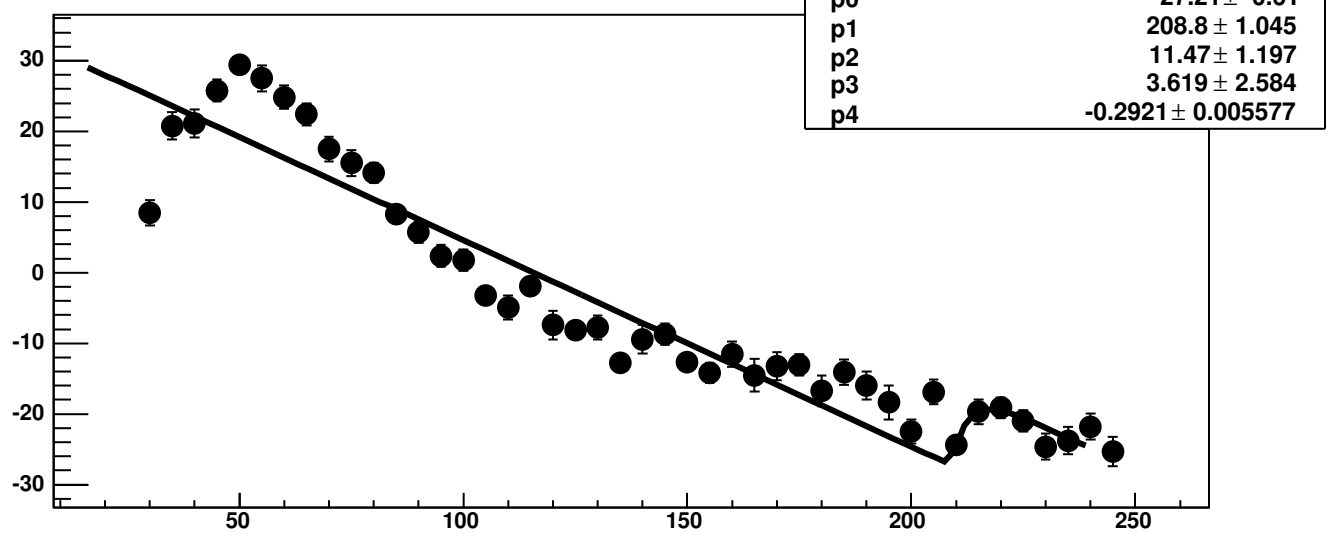
Chip 5, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



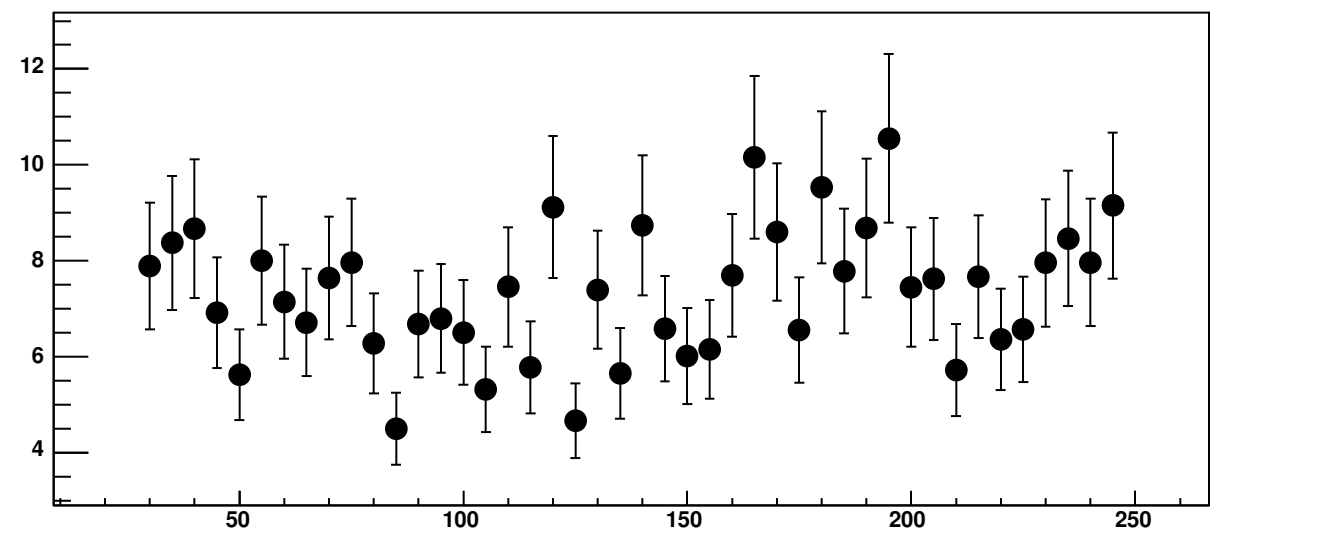
Chip 5, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold



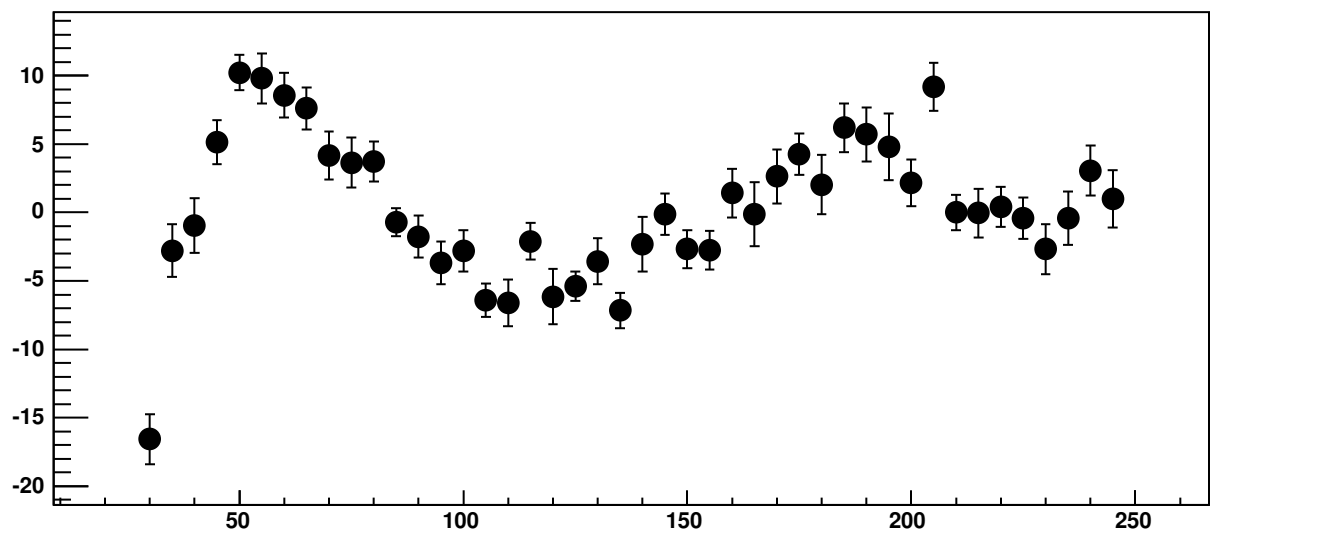
Chip 5, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold

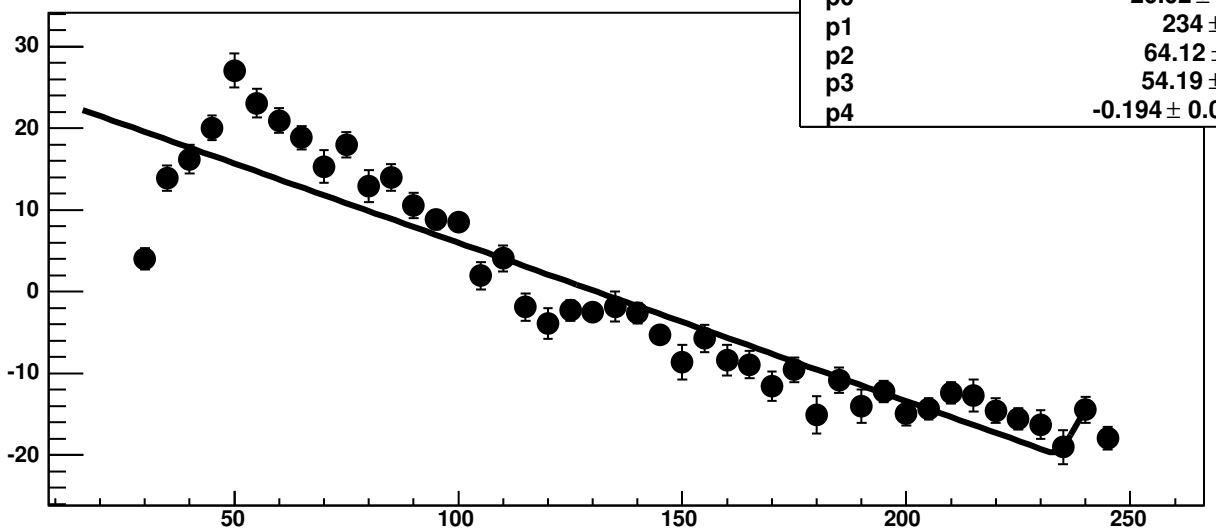


Chip 5, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

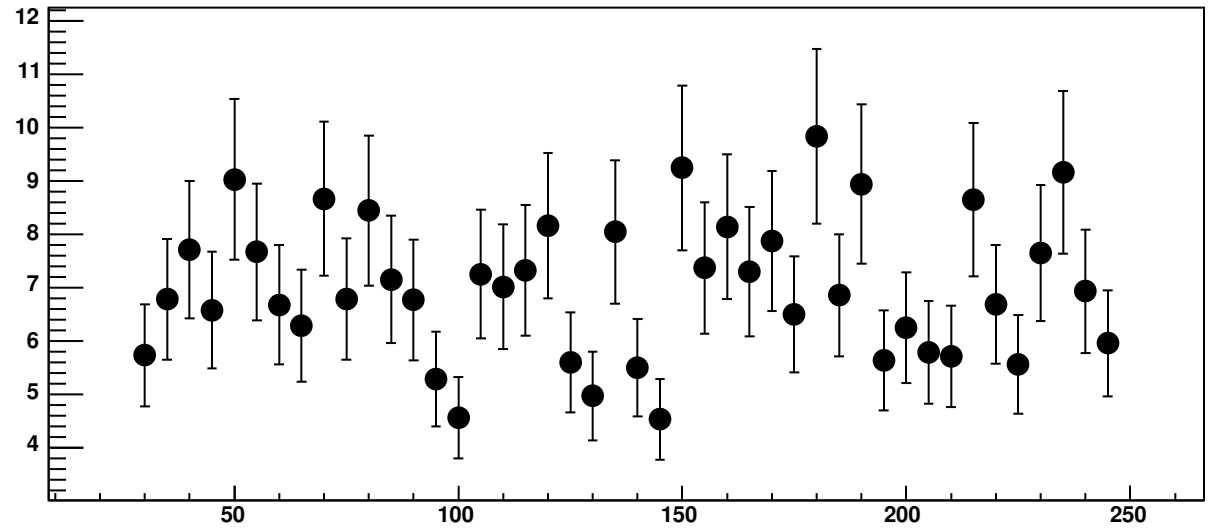


Chip 5, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

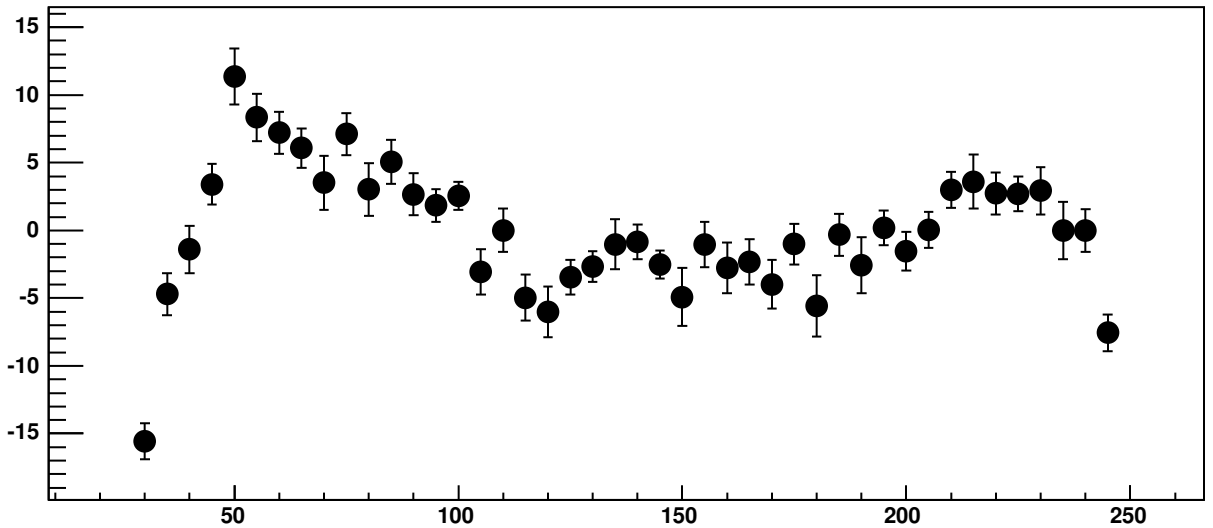
χ^2 / ndf	378.9 / 38
p0	-20.02 ± 0.5829
p1	234 ± 2.208
p2	64.12 ± 226.1
p3	54.19 ± 200.5
p4	-0.194 ± 0.004075



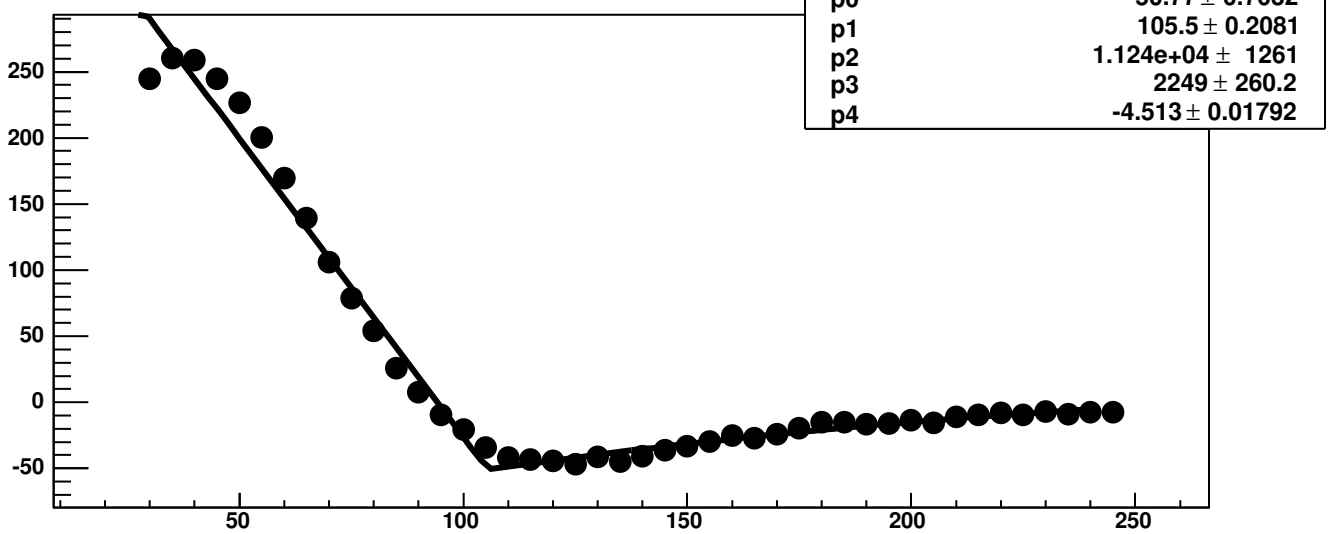
Chip 5, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



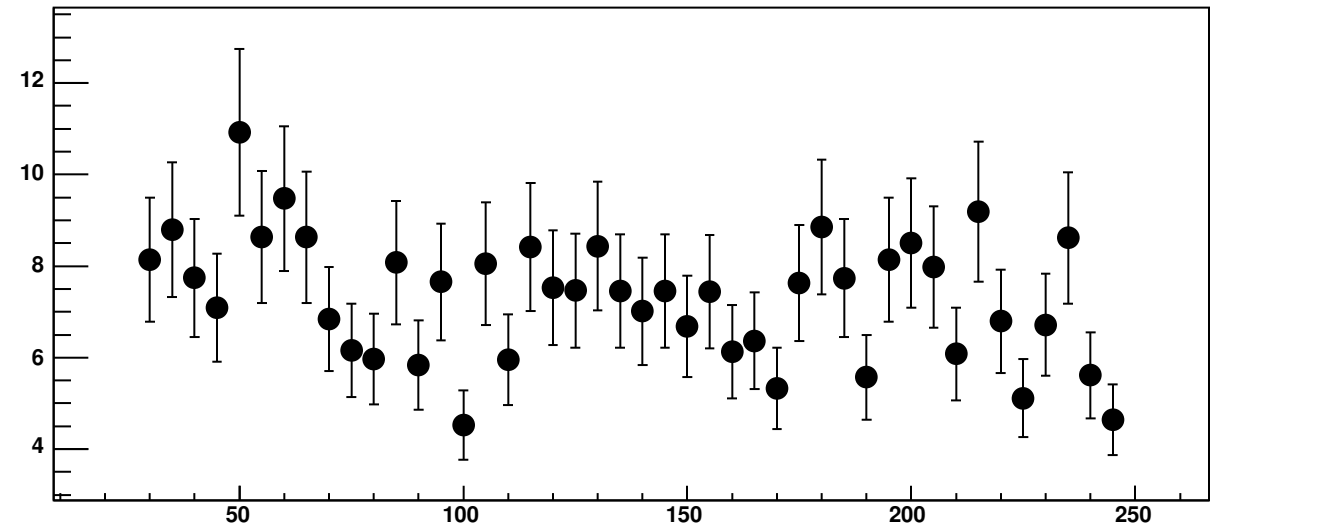
Chip 5, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold



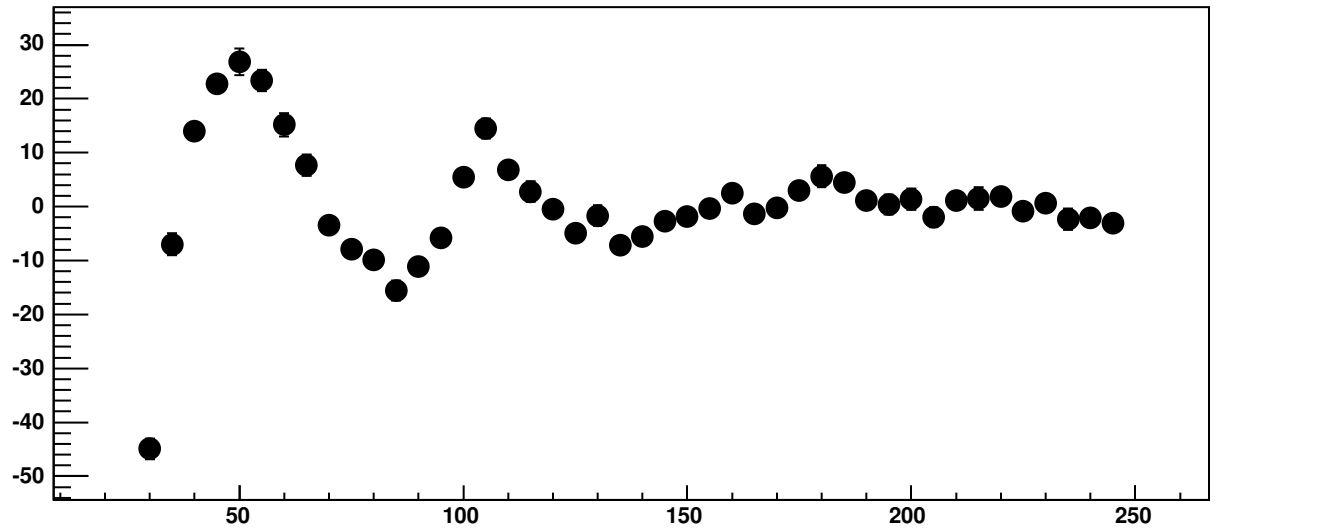
Chip 5, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold



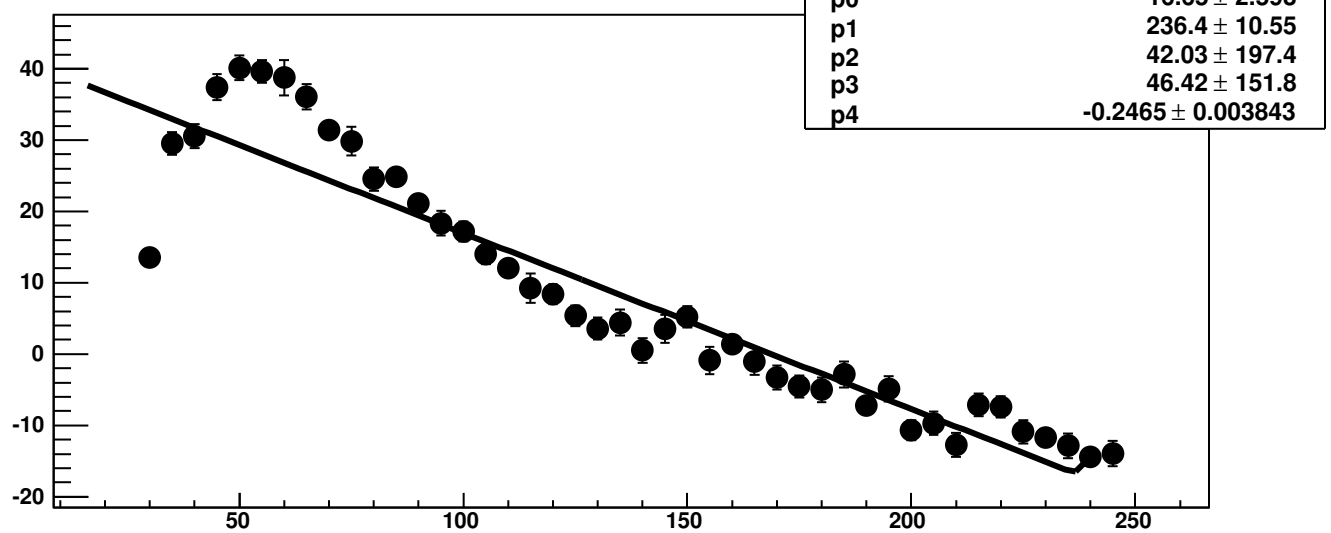
Chip 5, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold



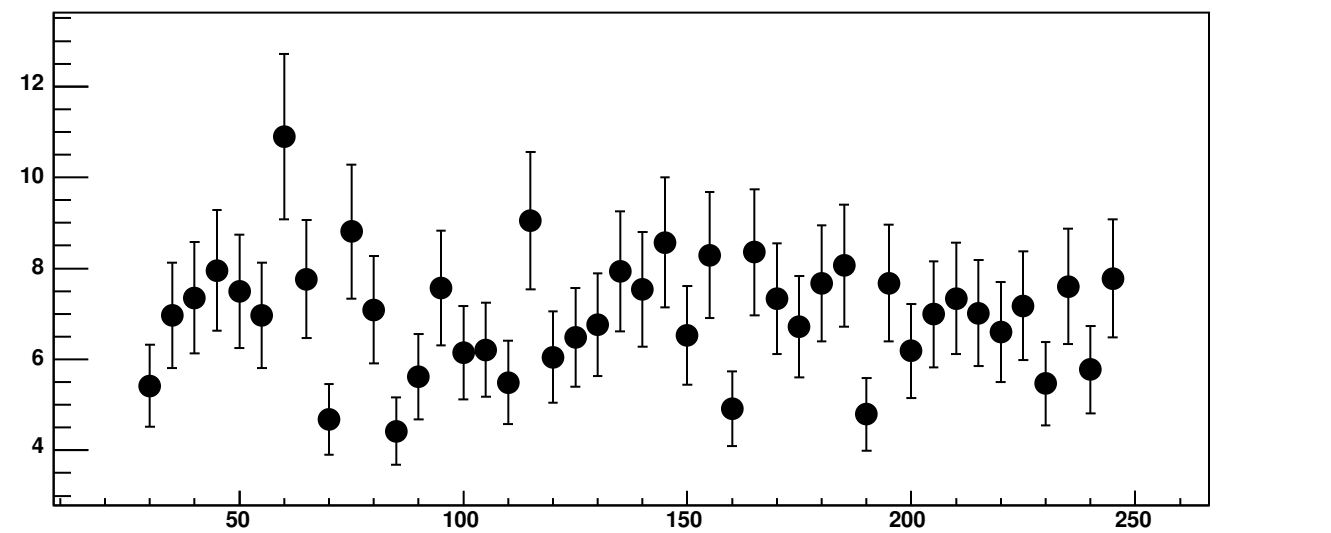
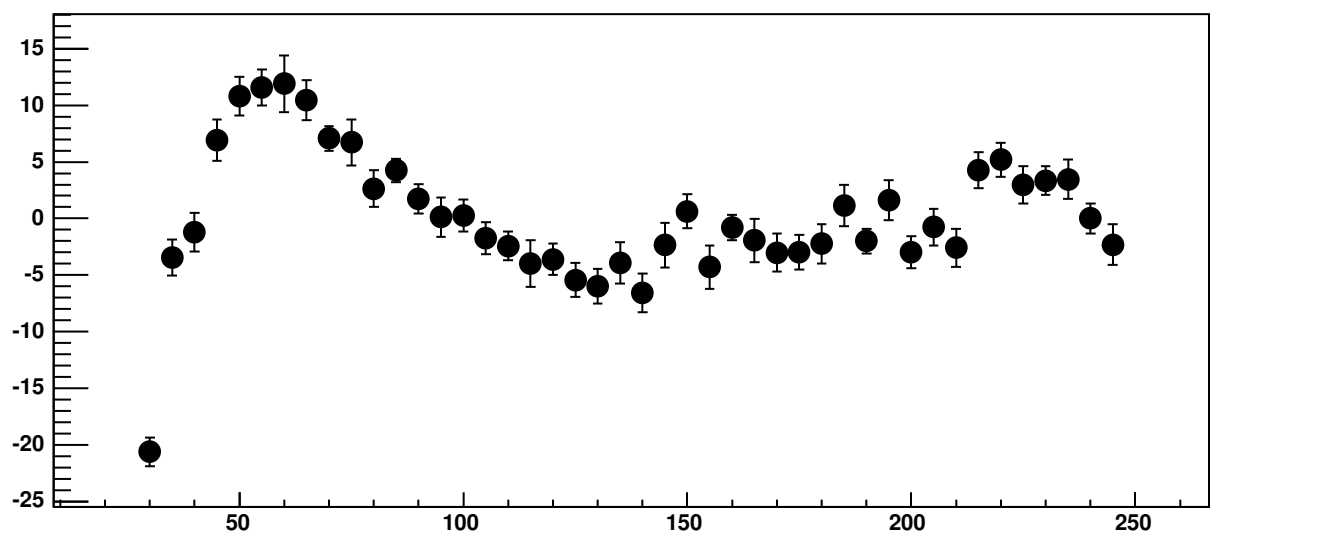
Chip 5, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold



Chip 5, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold