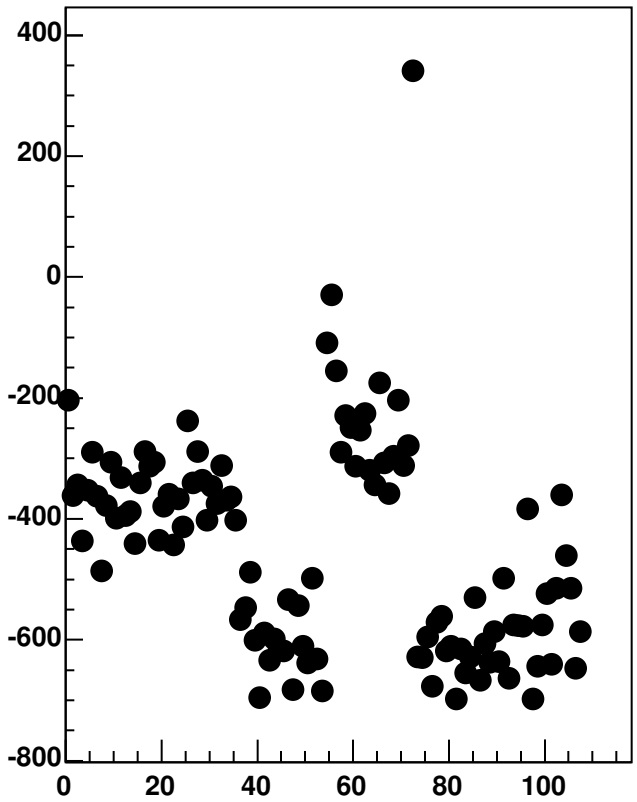
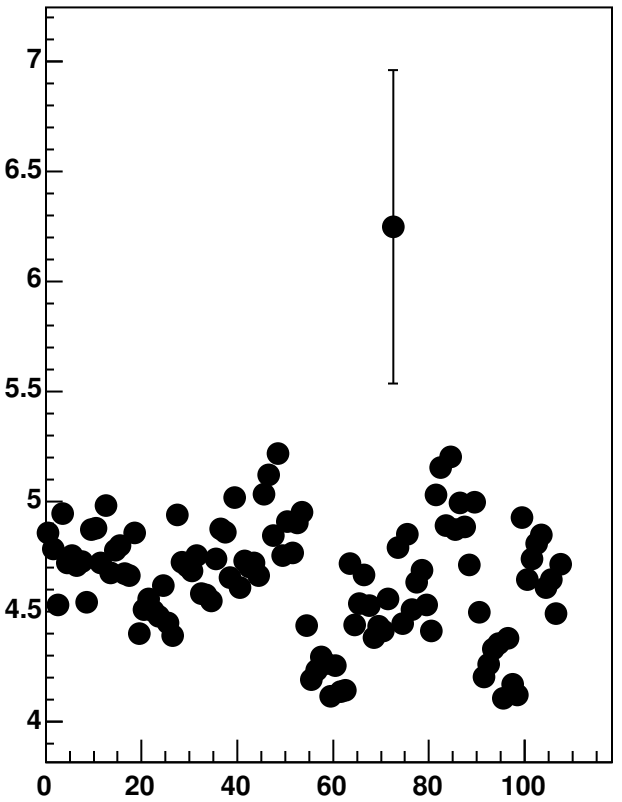
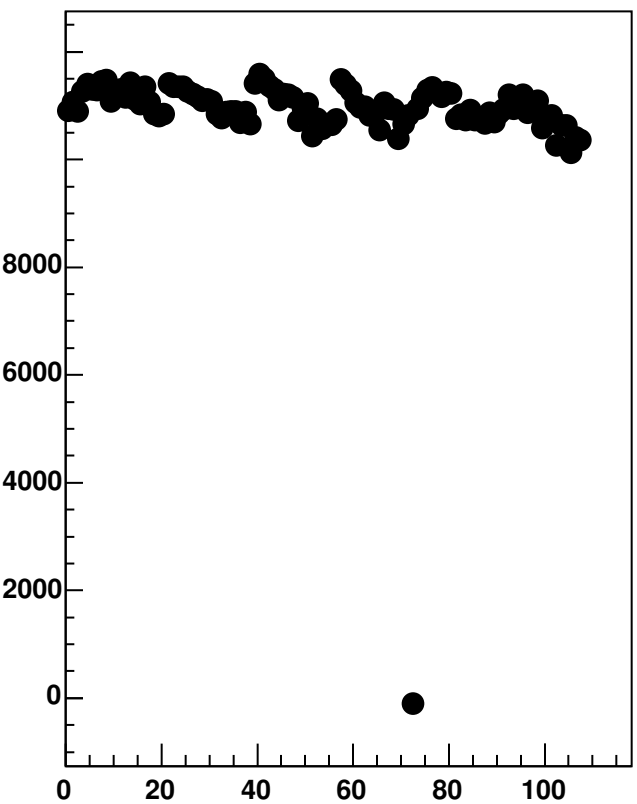
Channel Enabled, DAC=4000, Fit Baseline vs 18*Chip+Chan

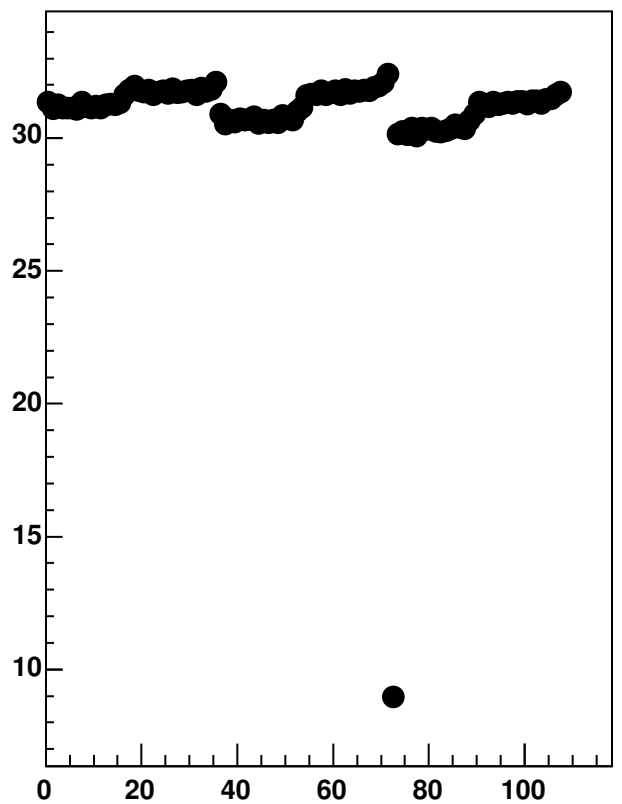


Channel Enabled, DAC=4000, Fit Start Time vs 18*Chip+Chan

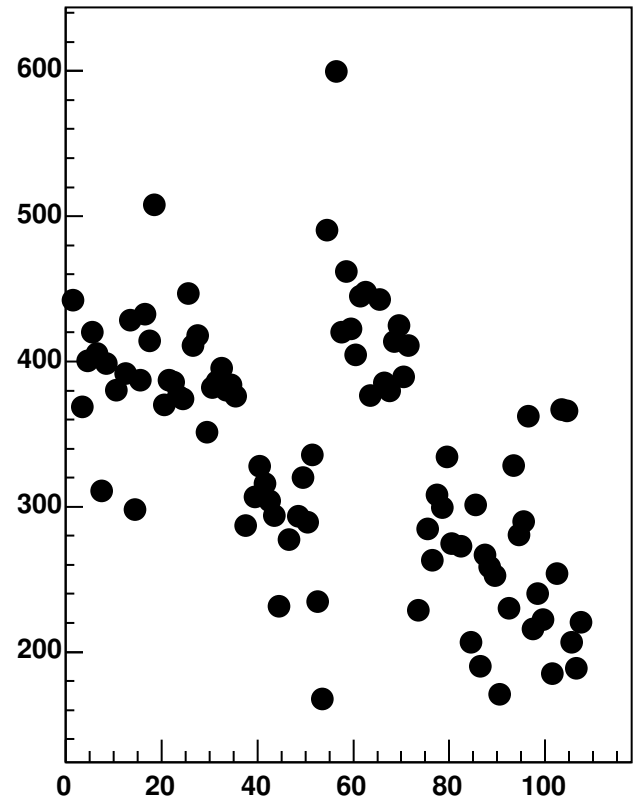


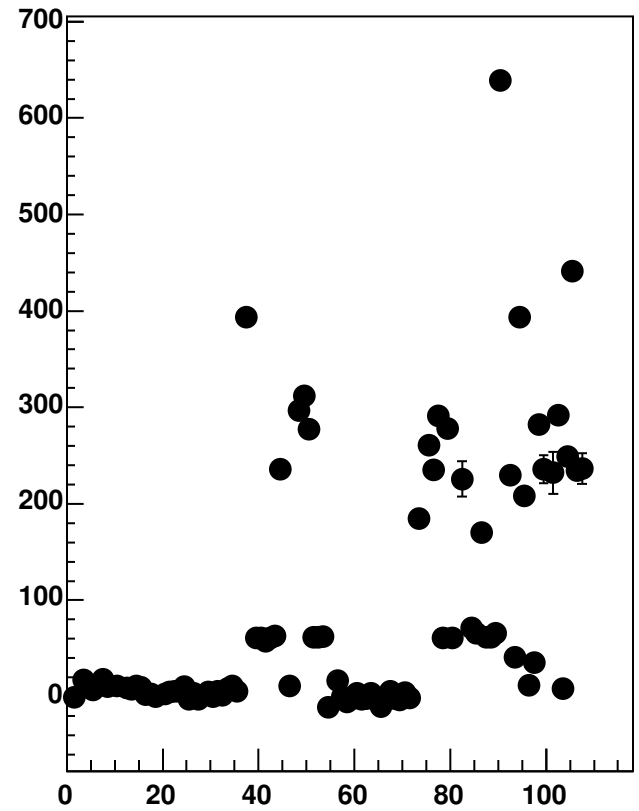
Channel Enabled, DAC=4000, Fit Normalisation vs 18*Chip+Chan



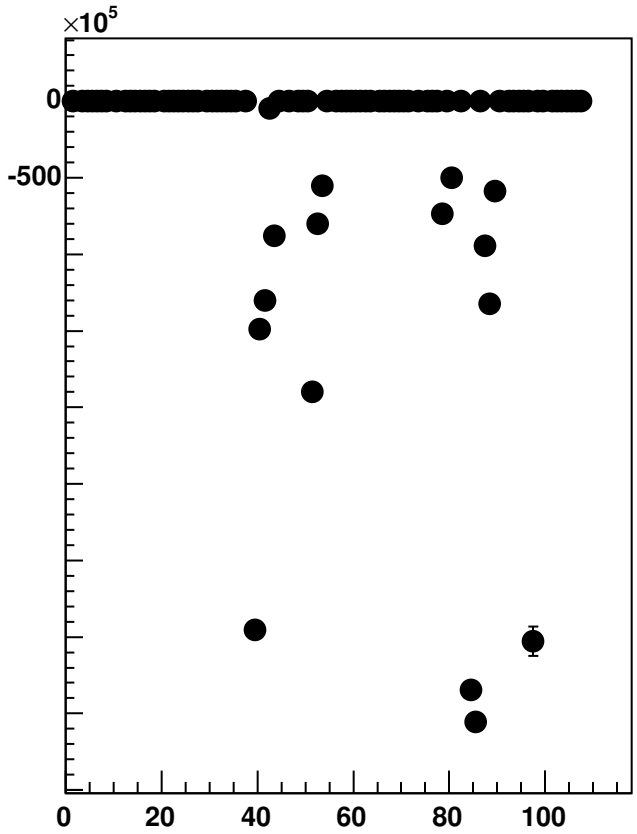


Disabled 0, DAC=4000, Fit Baseline vs 18*Chip+Chan

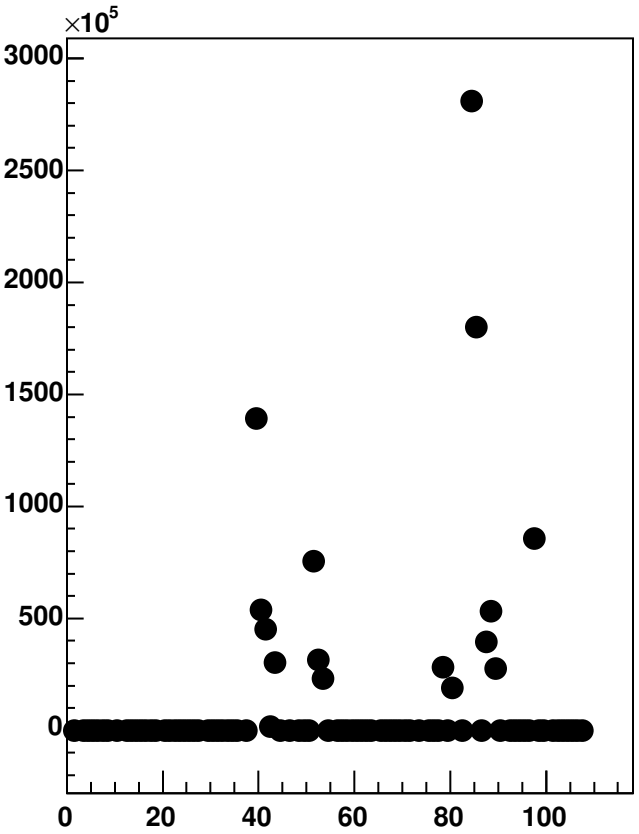




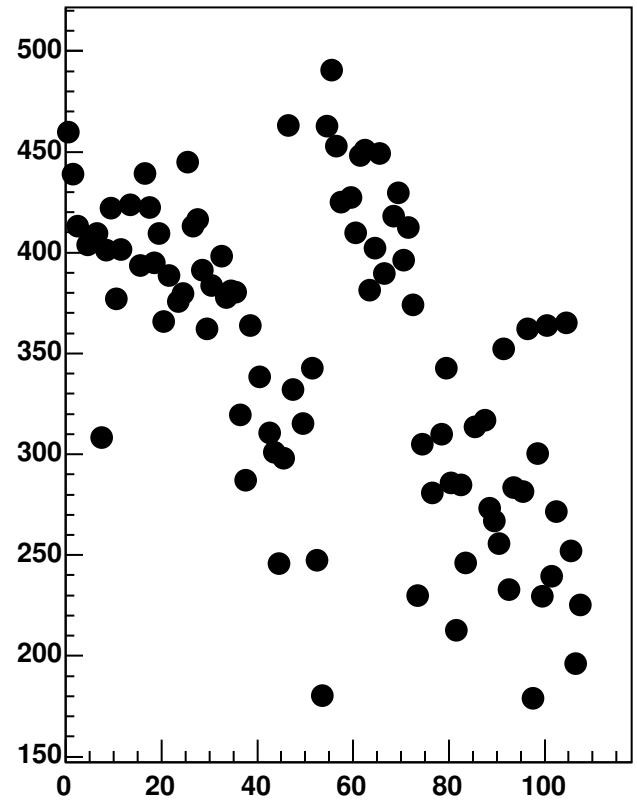
Disabled 0, DAC=4000, Fit Normalisation vs 18*Chip+Chan

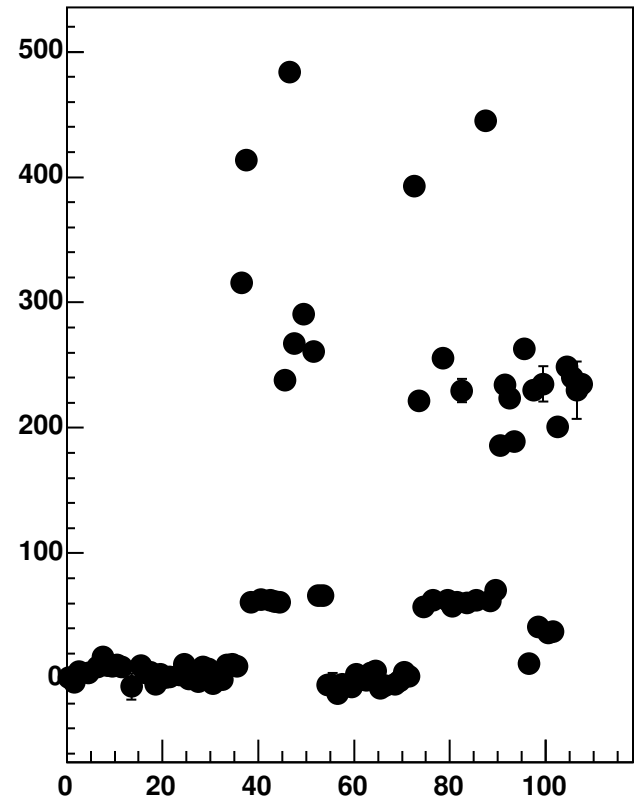


Disabled 0, DAC=4000, Fit Shaping Time vs 18*Chip+Chan

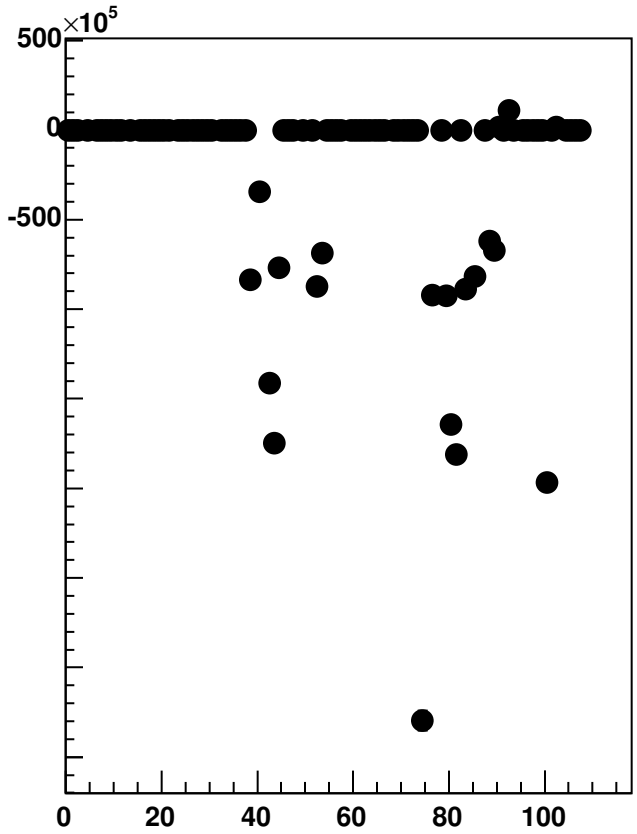


Disabled 1, DAC=4000, Fit Baseline vs 18*Chip+Chan

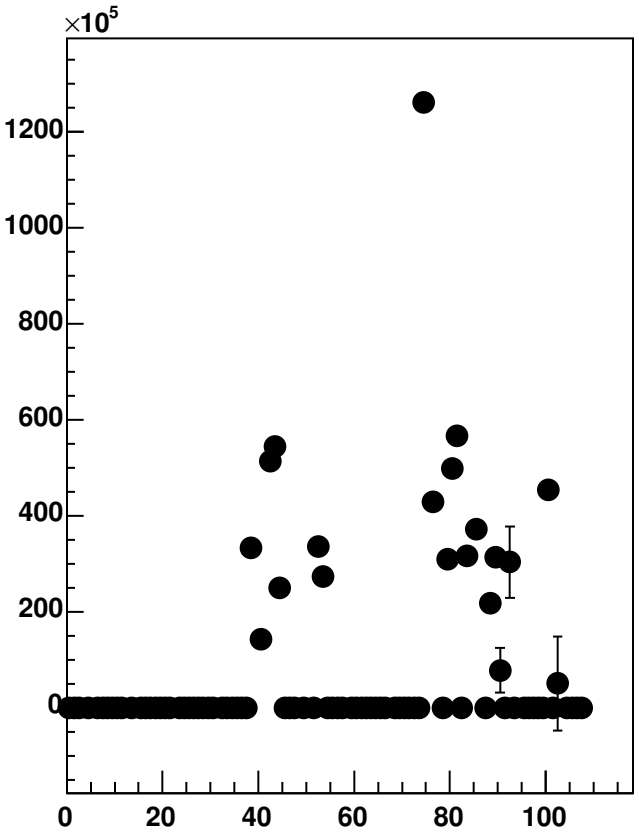




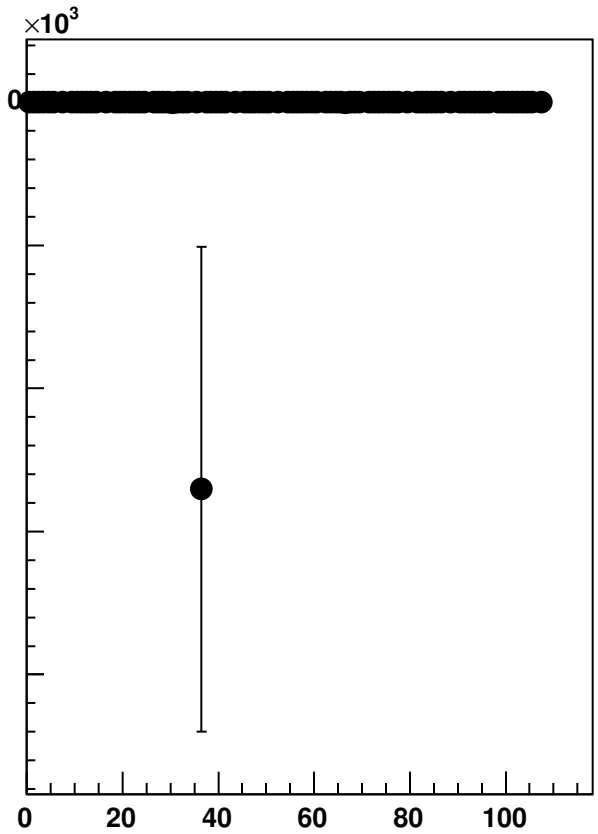
Disabled 1, DAC=4000, Fit Normalisation vs 18*Chip+Chan

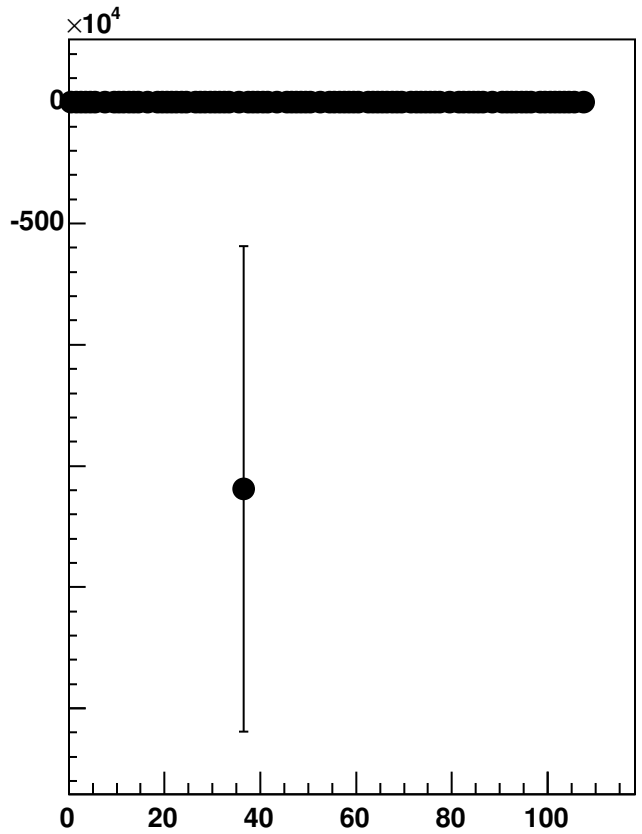


Disabled 1, DAC=4000, Fit Shaping Time vs 18*Chip+Chan

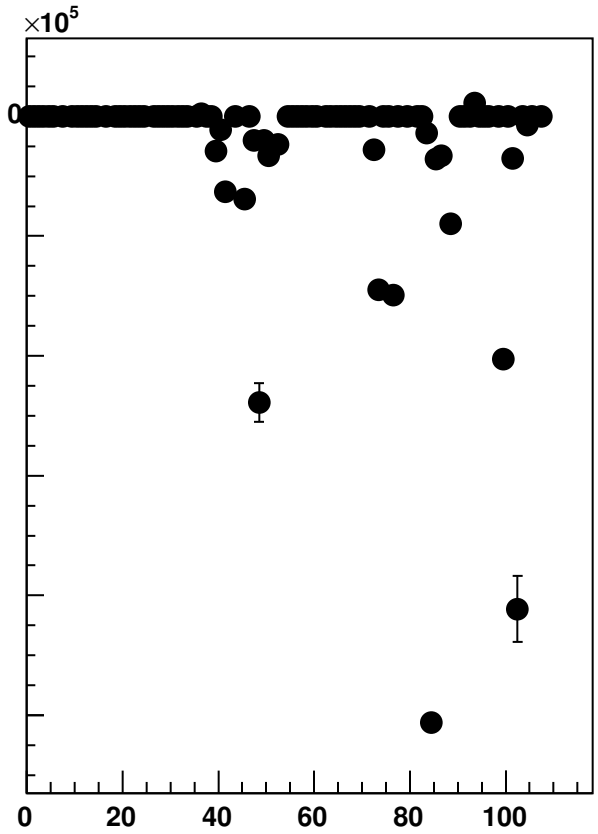


Disabled 2, DAC=4000, Fit Baseline vs 18*Chip+Chan

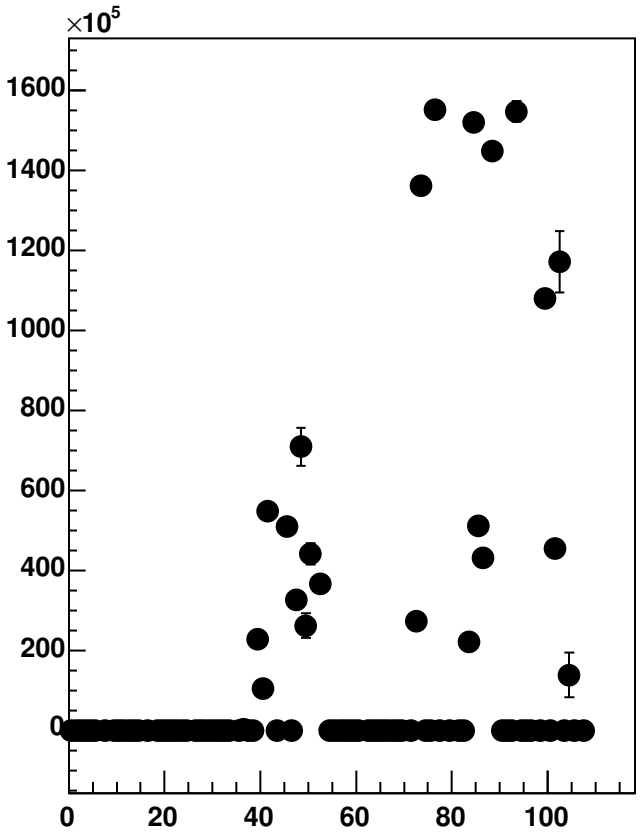




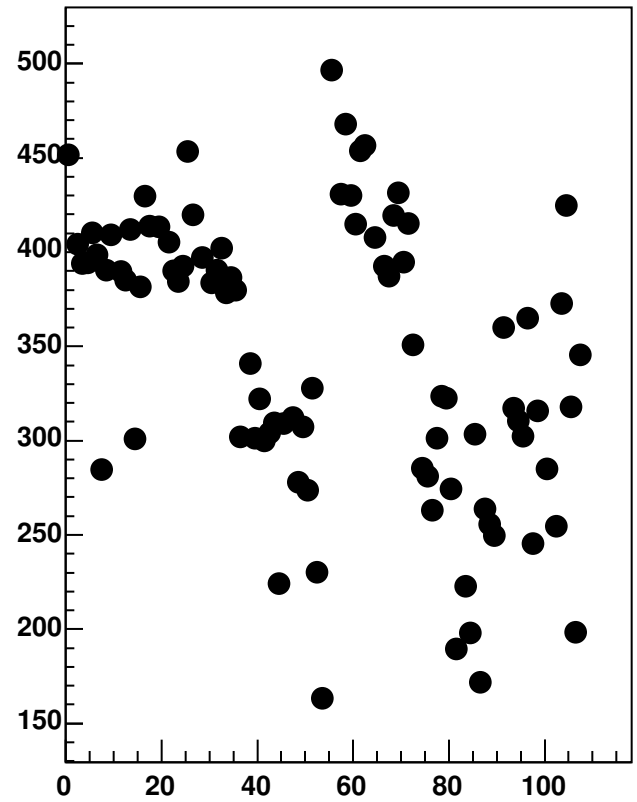
Disabled 2, DAC=4000, Fit Normalisation vs 18*Chip+Chan

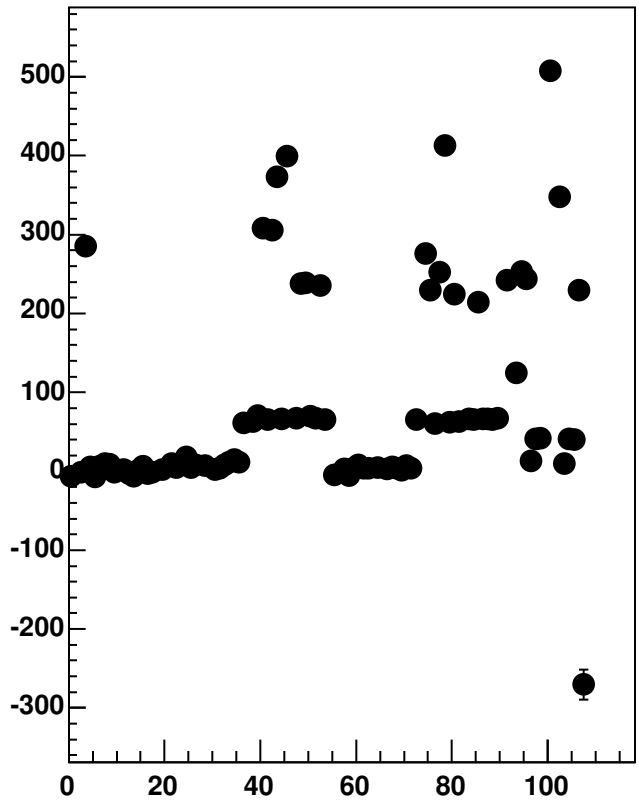


Disabled 2, DAC=4000, Fit Shaping Time vs 18*Chip+Chan

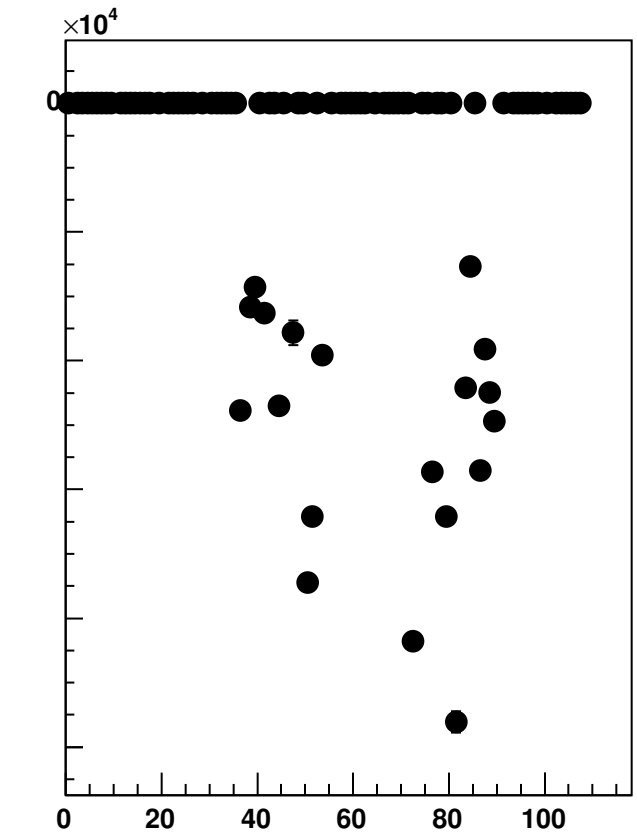


Disabled 3, DAC=4000, Fit Baseline vs 18*Chip+Chan

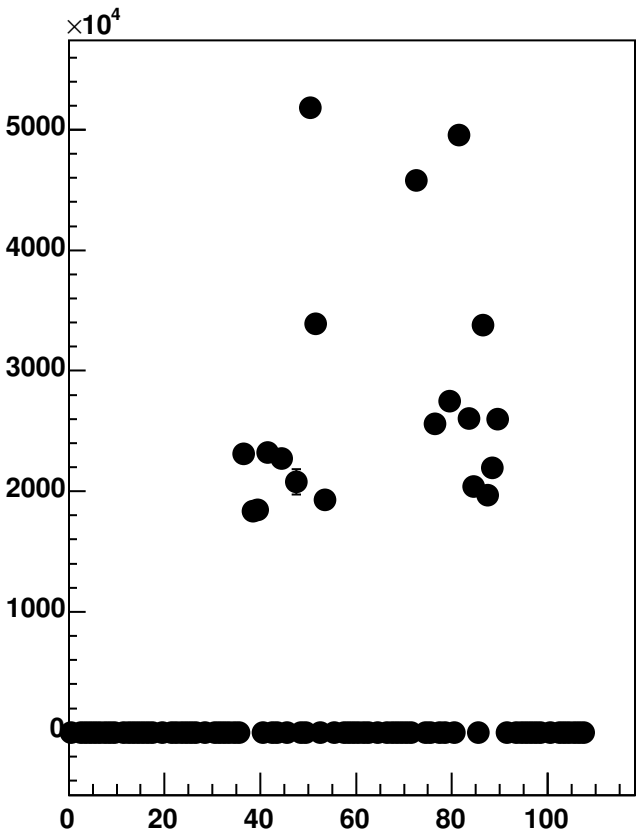




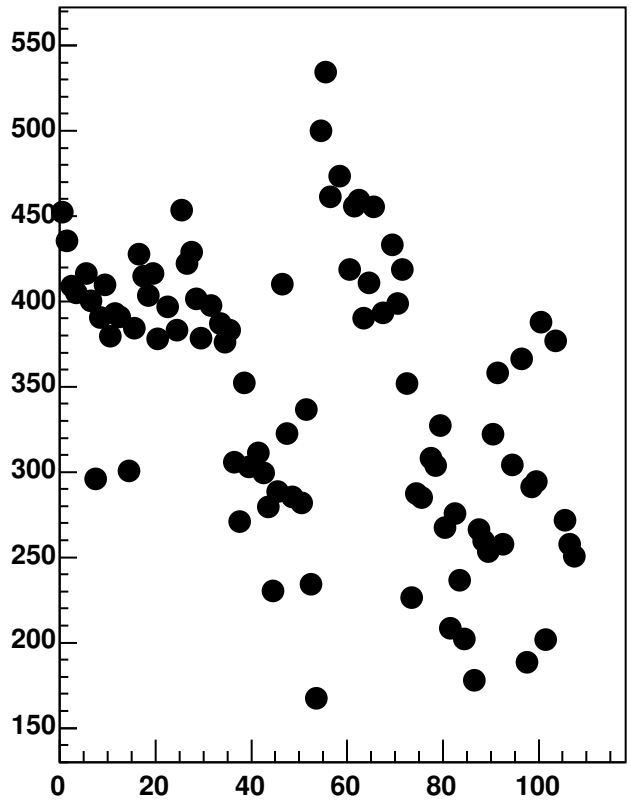
Disabled 3, DAC=4000, Fit Normalisation vs 18*Chip+Chan

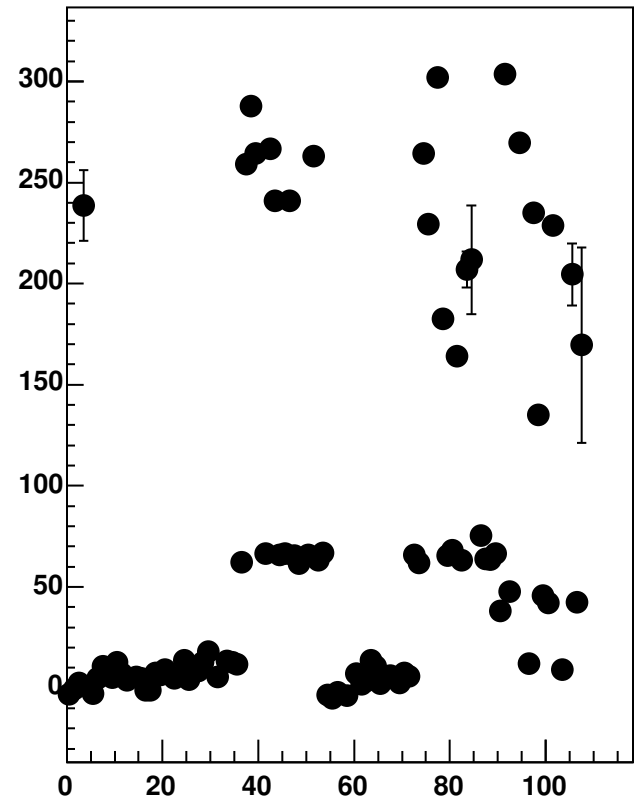


Disabled 3, DAC=4000, Fit Shaping Time vs 18*Chip+Chan

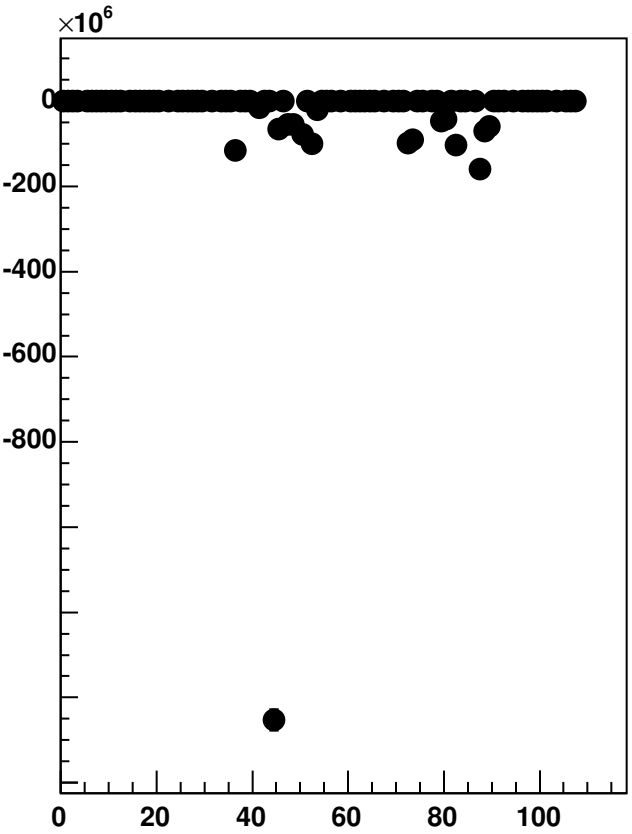


Disabled 4, DAC=4000, Fit Baseline vs 18*Chip+Chan

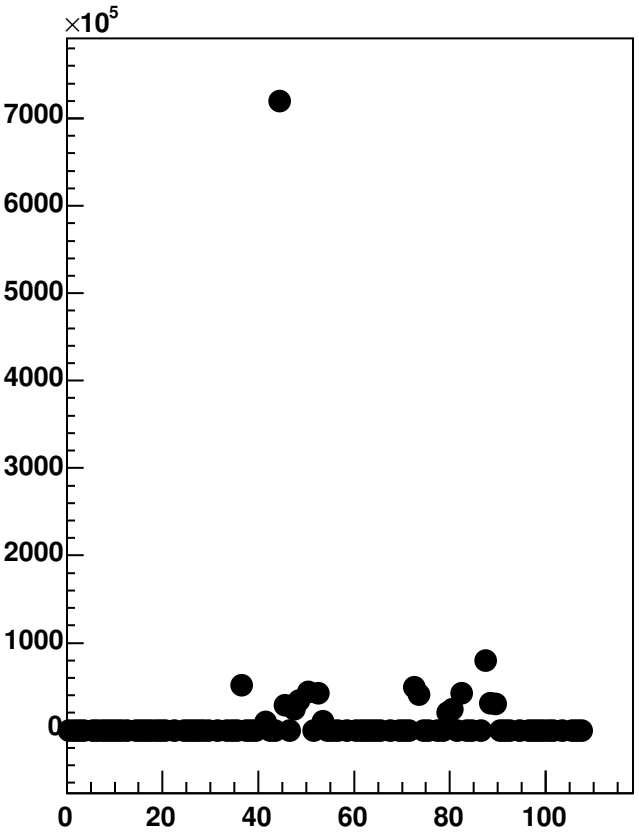




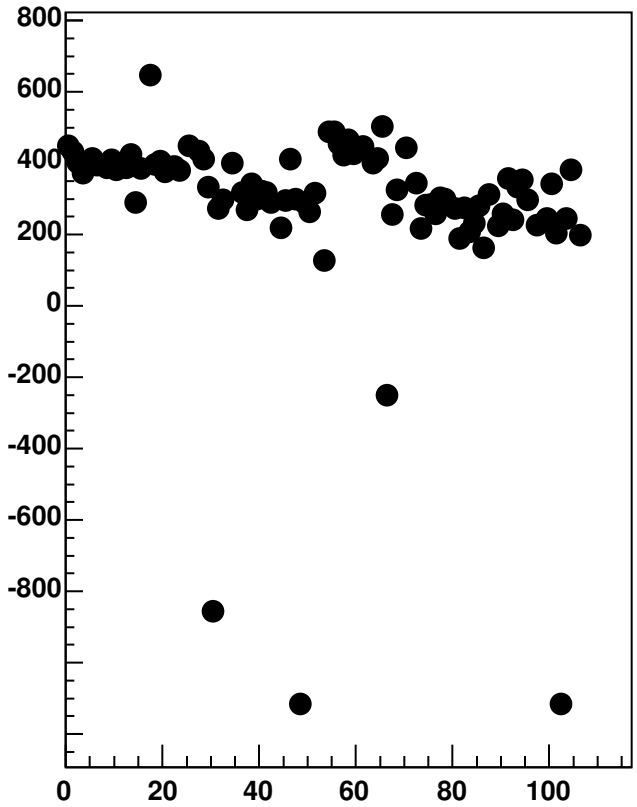
Disabled 4, DAC=4000, Fit Normalisation vs 18*Chip+Chan

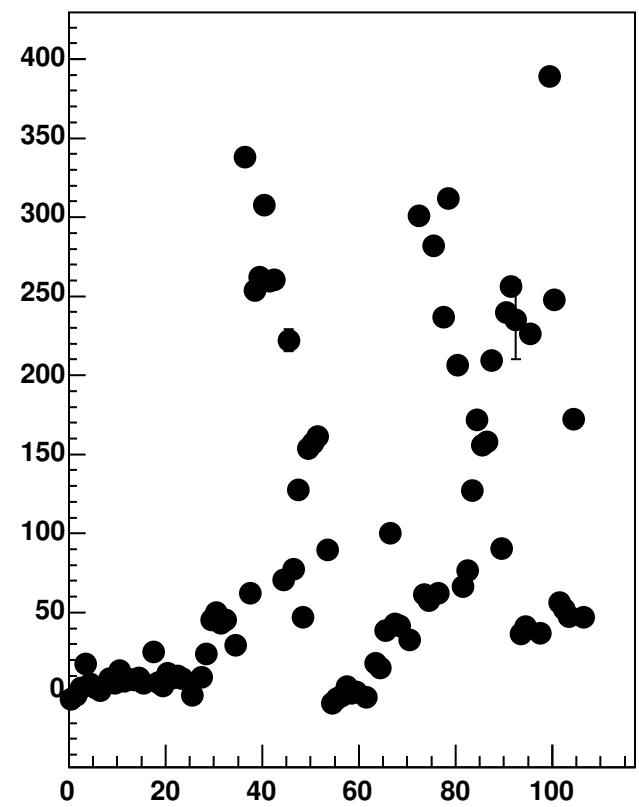


Disabled 4, DAC=4000, Fit Shaping Time vs 18*Chip+Chan



Disabled 5, DAC=4000, Fit Baseline vs 18*Chip+Chan





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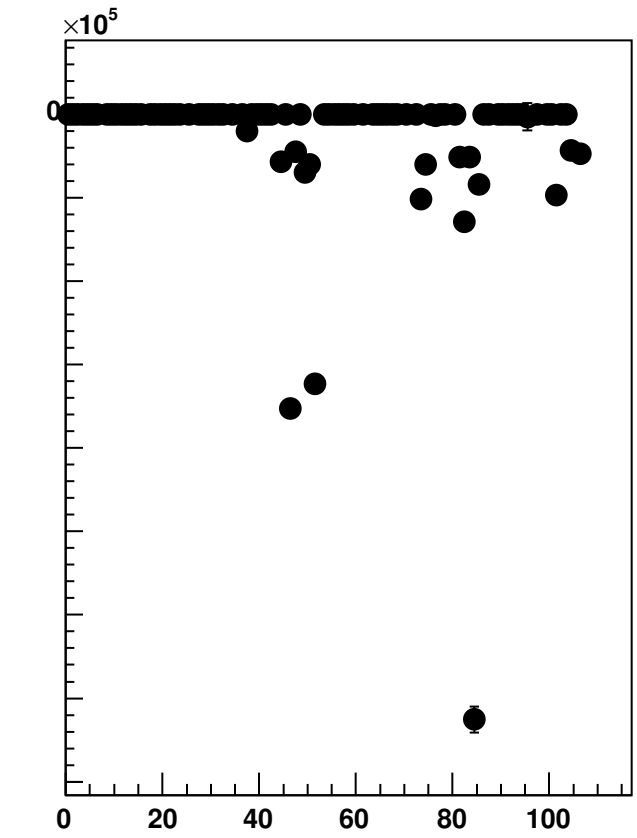
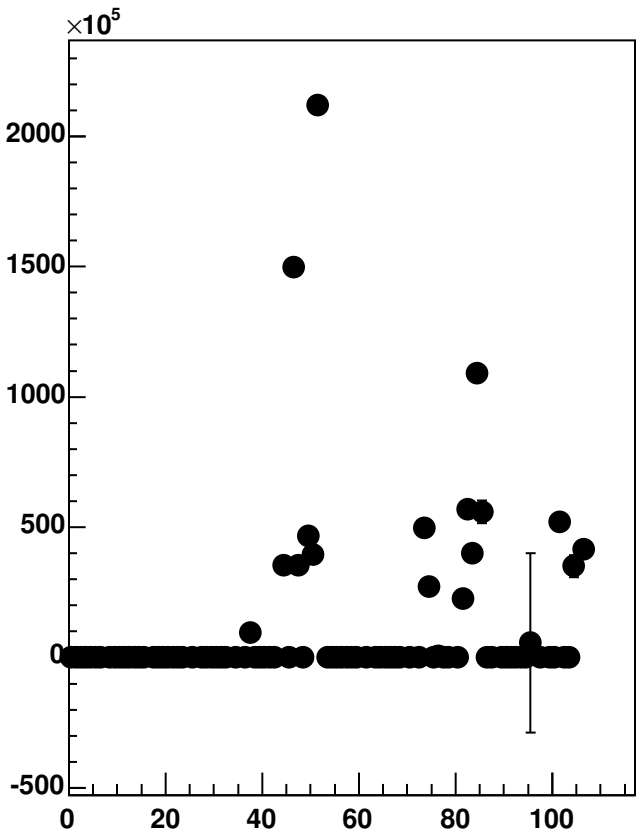
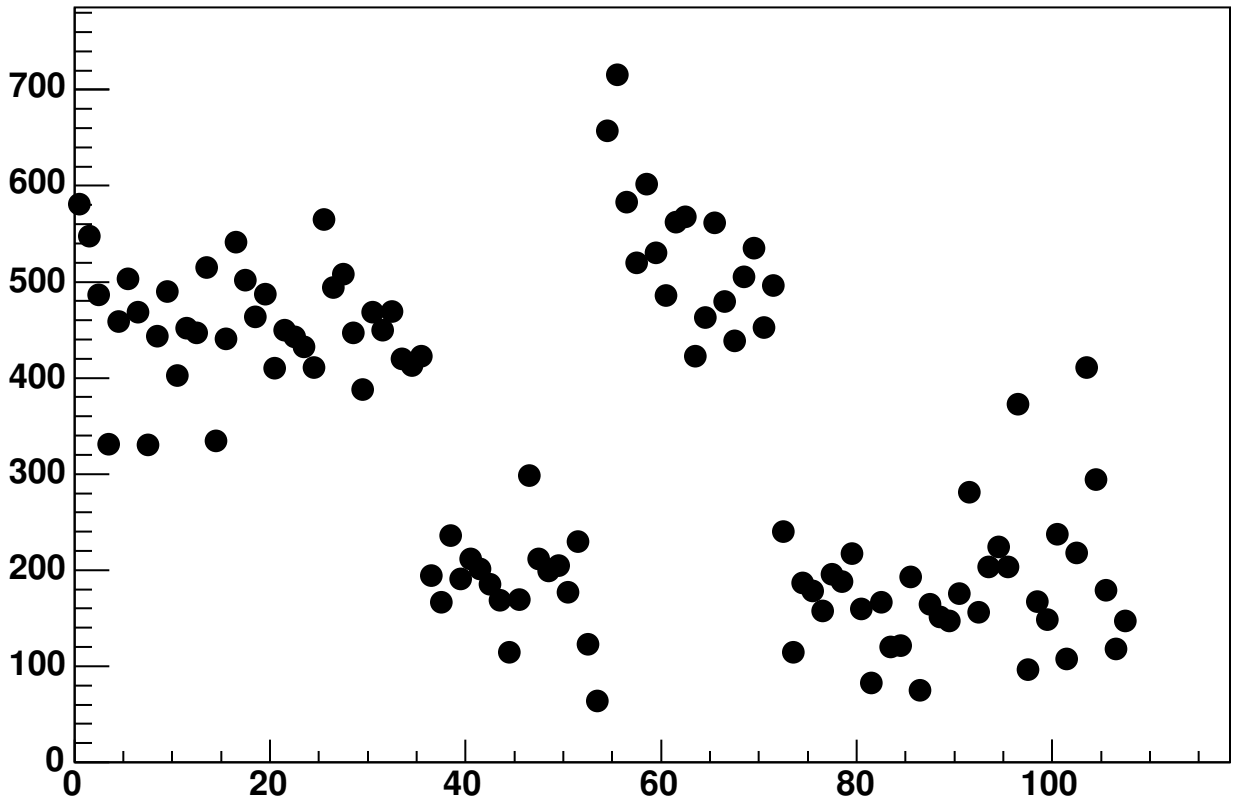


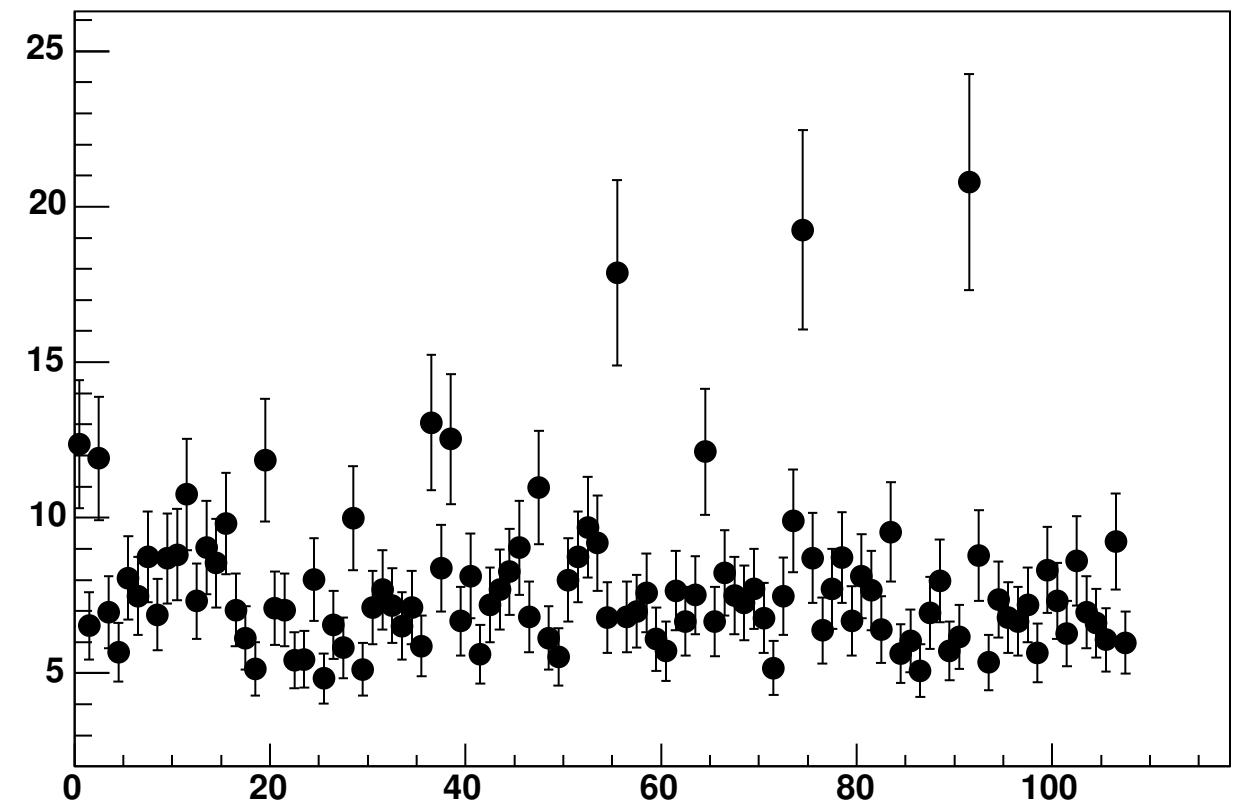
Disabled 5, DAC=4000, Fit Shaping Time vs 18*Chip+Chan



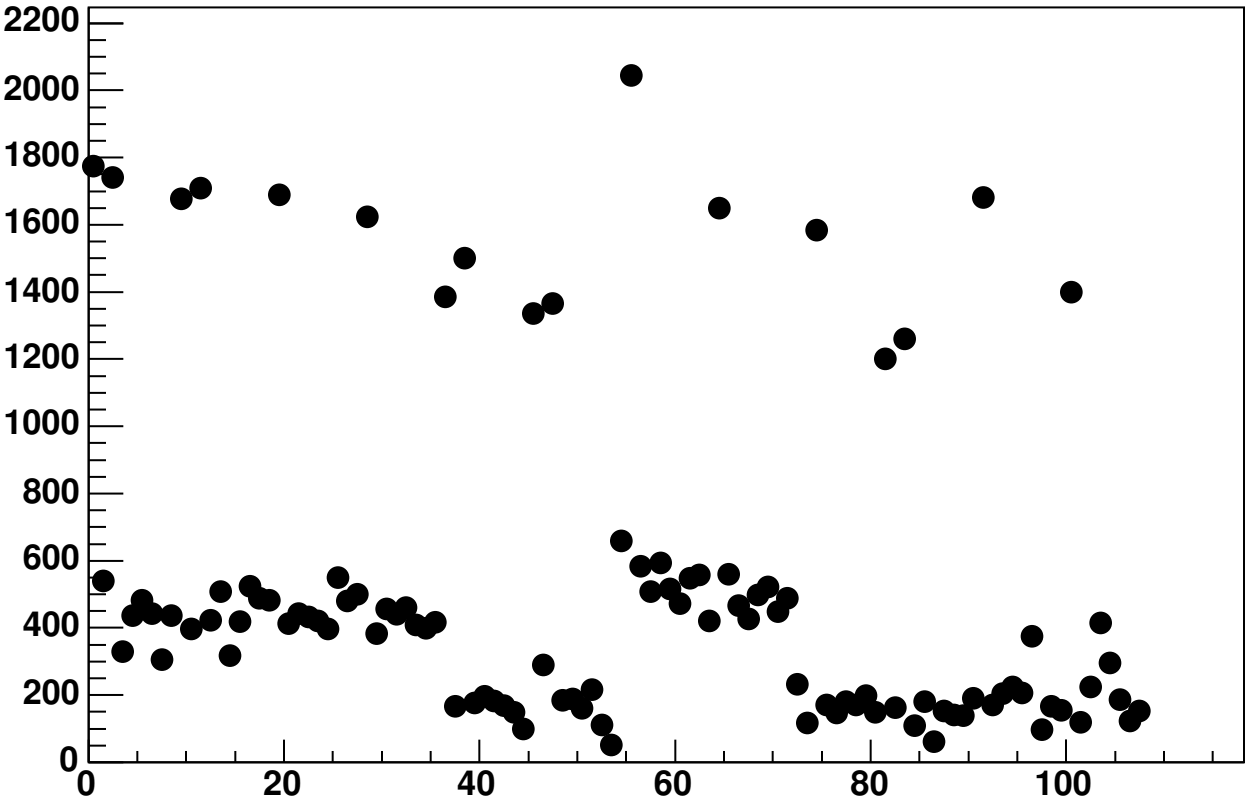
Enable 0, DAC=4000, Hold=0, ADC Mean vs 18*Chip+Chan



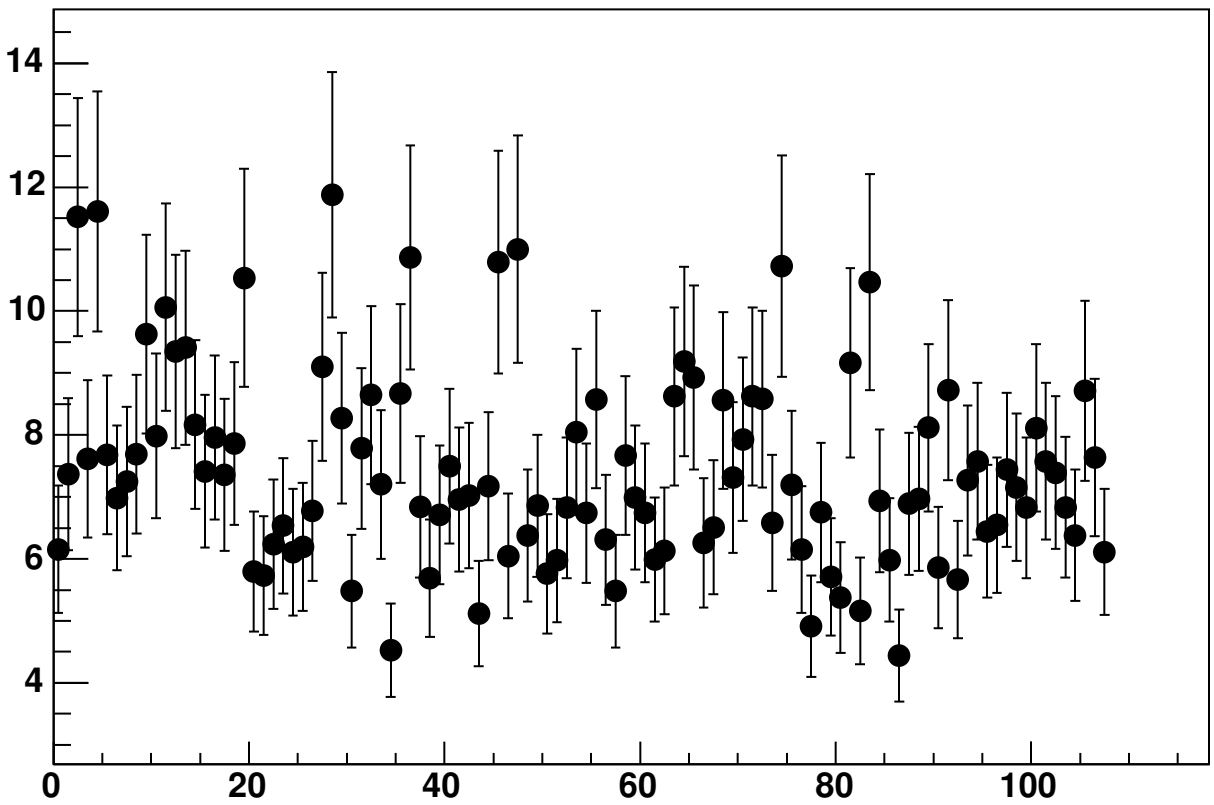
Enable 0, DAC=4000, Hold=0, ADC Noise vs 18*Chip+Chan



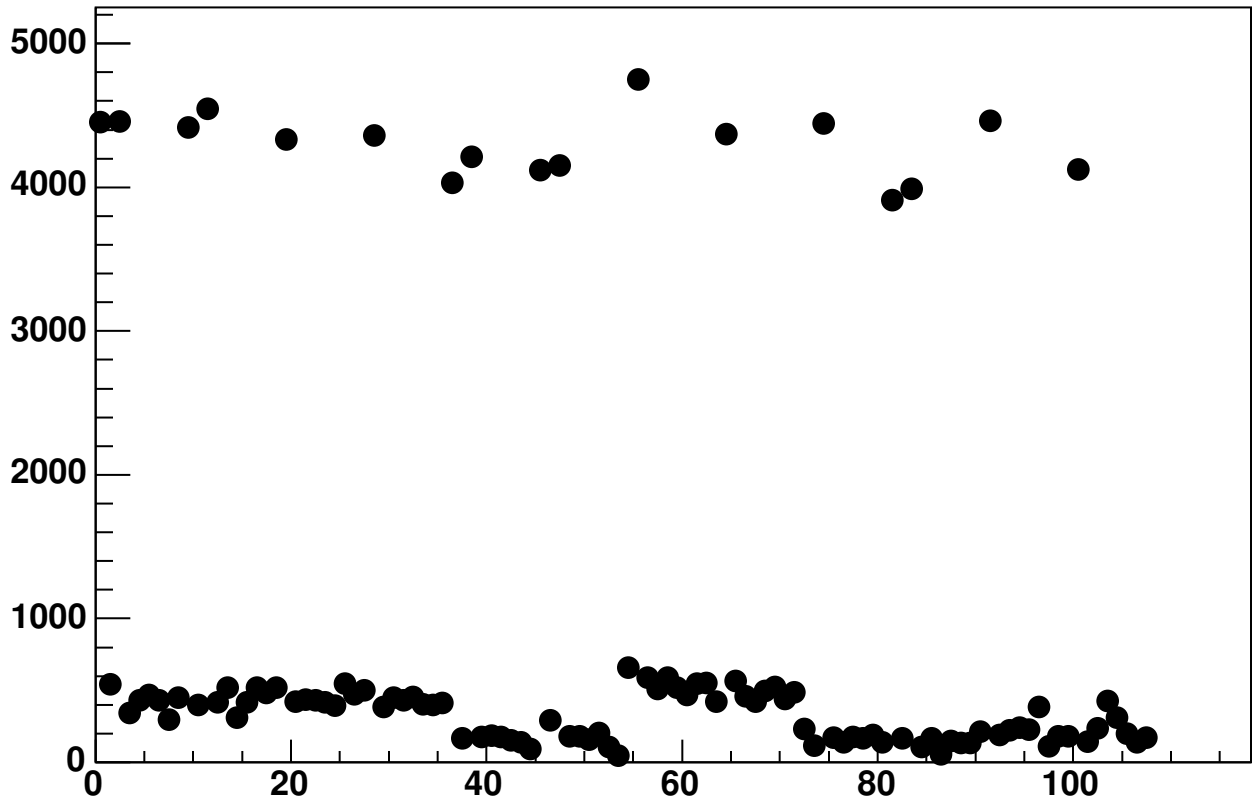
Enable 0, DAC=4000, Hold=5, ADC Mean vs 18*Chip+Chan



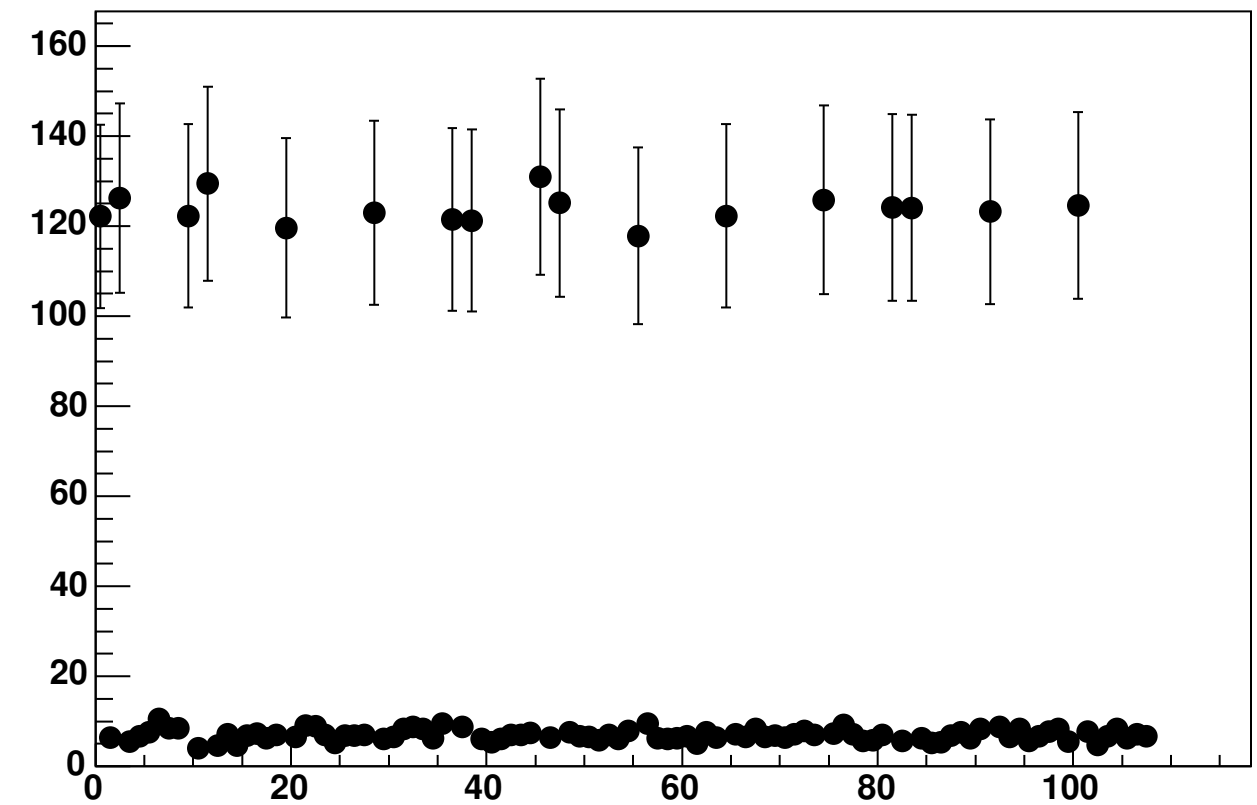
Enable 0, DAC=4000, Hold=5, ADC Noise vs 18*Chip+Chan



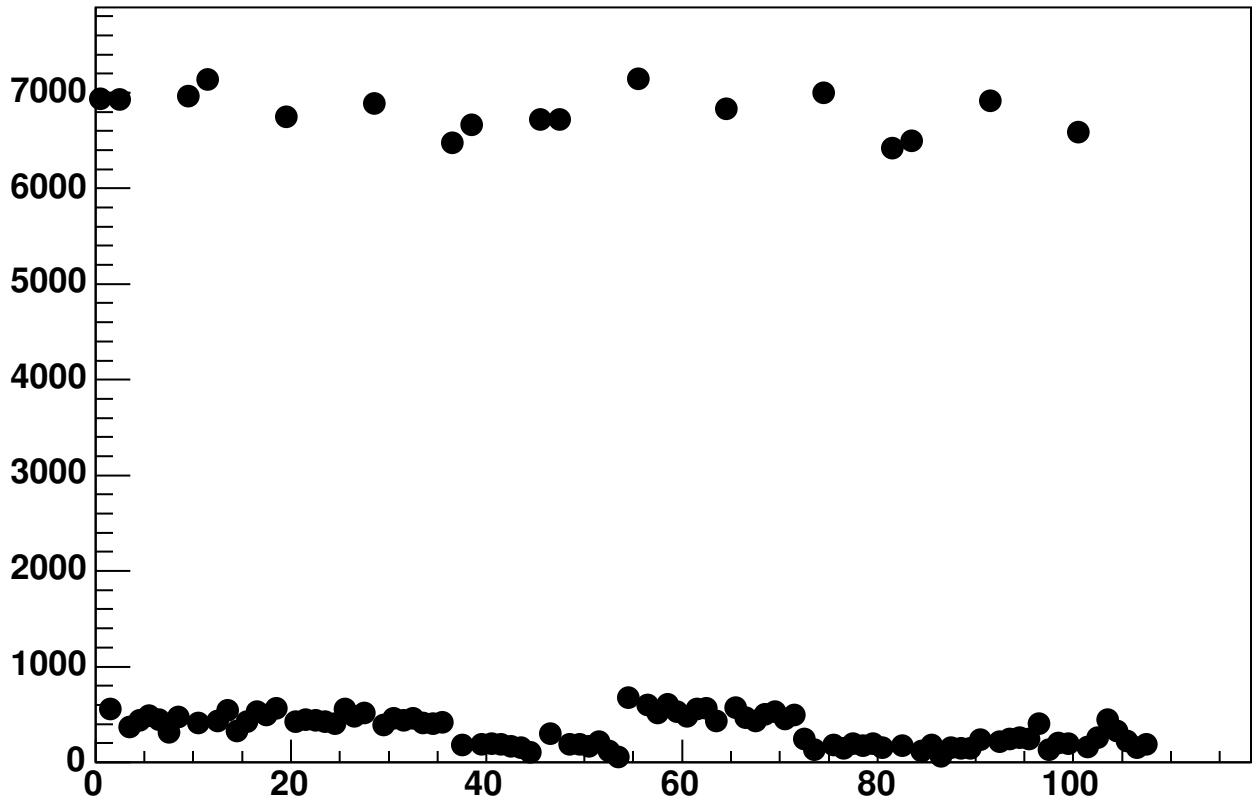
Enable 0, DAC=4000, Hold=10, ADC Mean vs 18*Chip+Chan



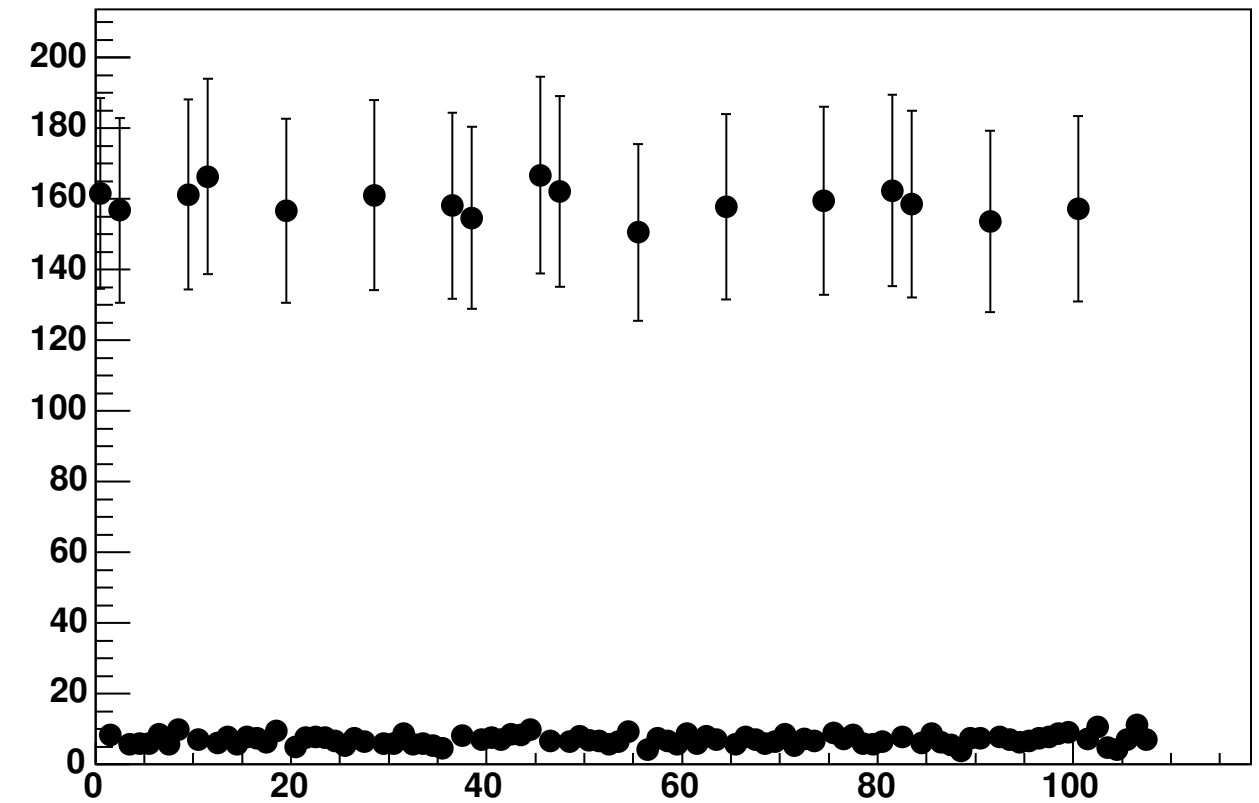
Enable 0, DAC=4000, Hold=10, ADC Noise vs 18*Chip+Chan



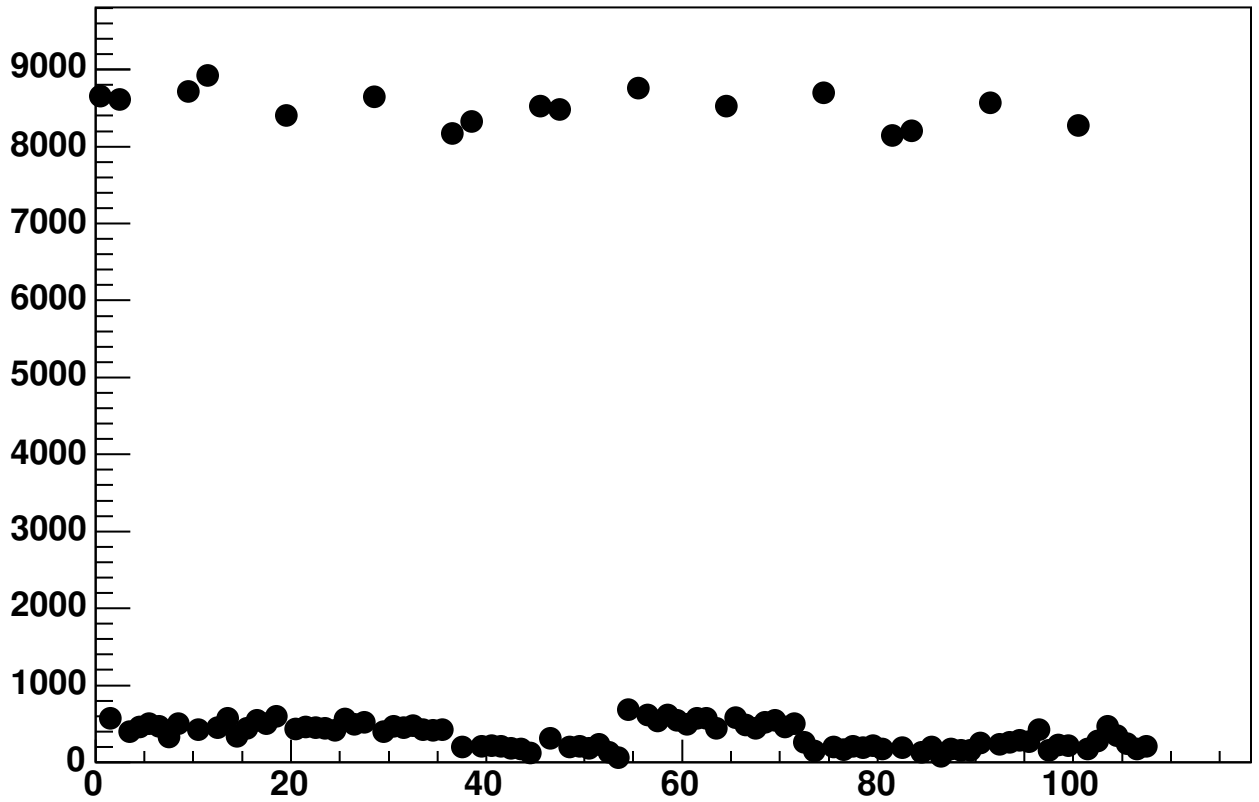
Enable 0, DAC=4000, Hold=15, ADC Mean vs 18*Chip+Chan



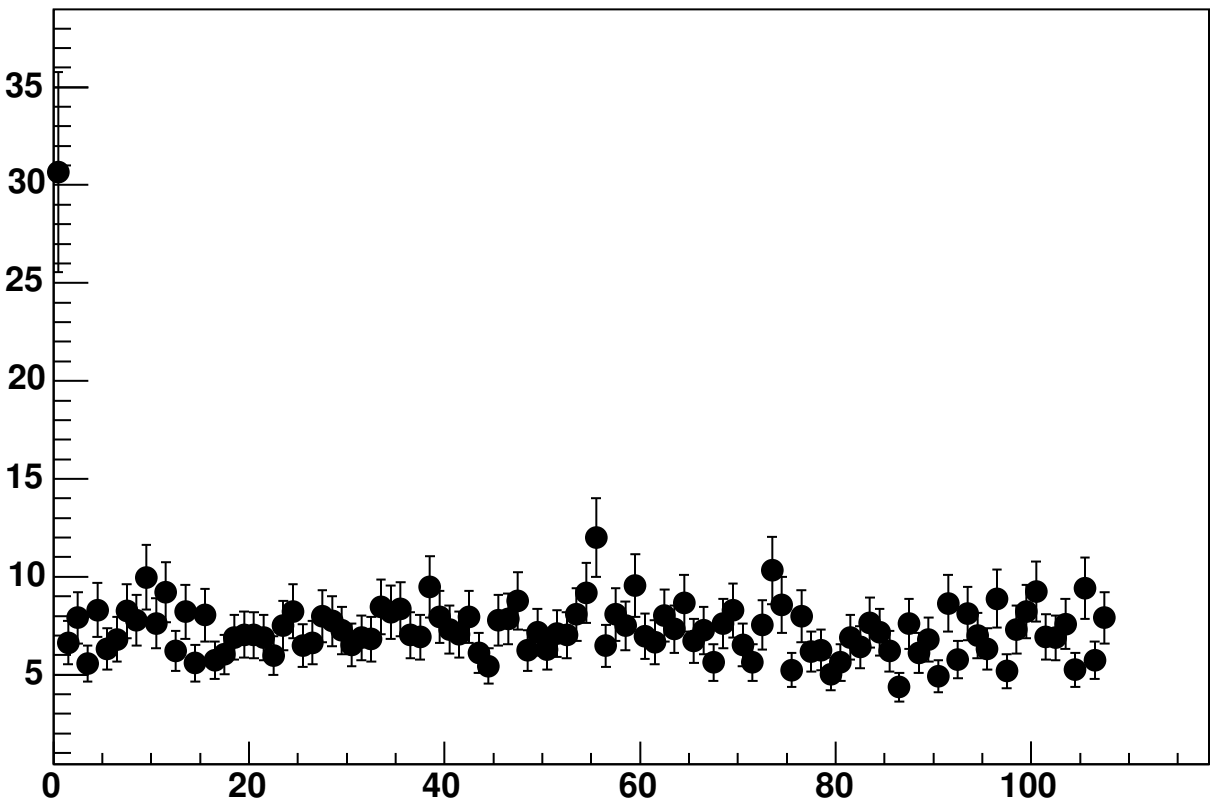
Enable 0, DAC=4000, Hold=15, ADC Noise vs 18*Chip+Chan



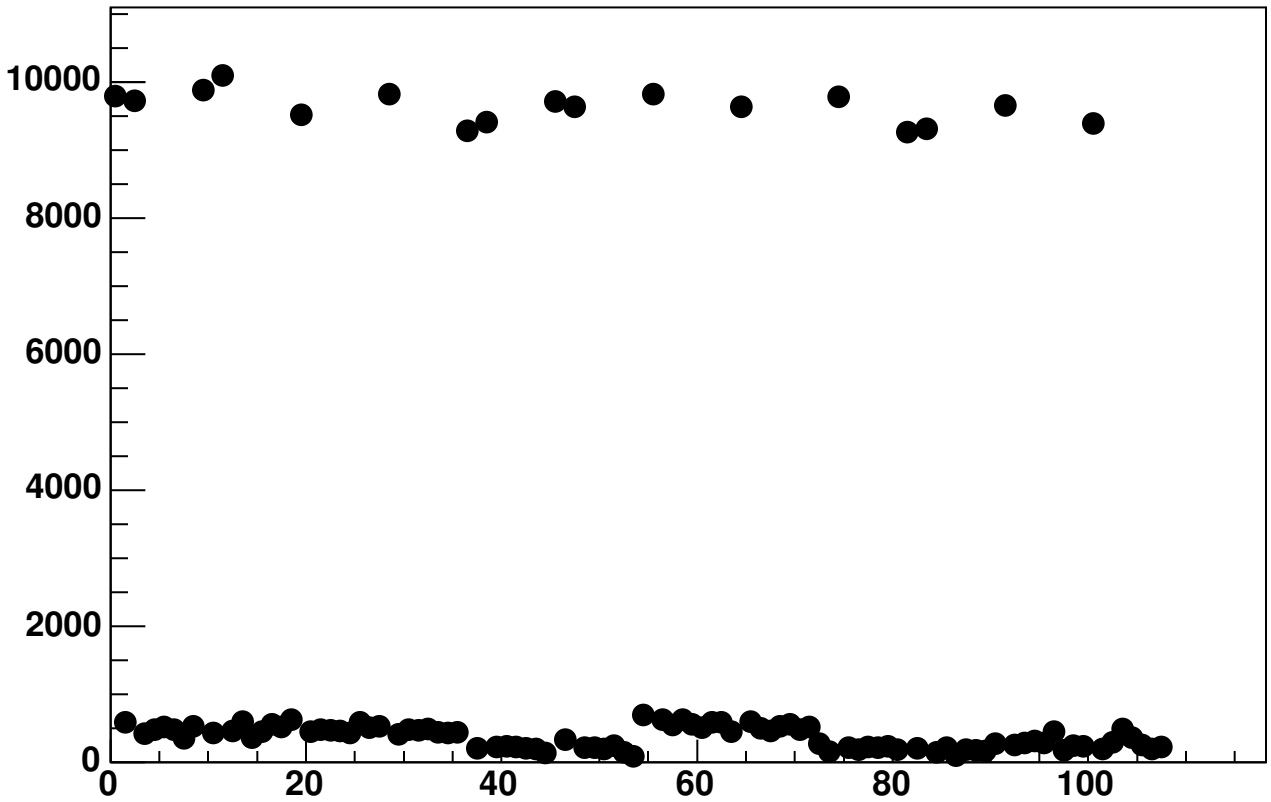
Enable 0, DAC=4000, Hold=20, ADC Mean vs 18*Chip+Chan



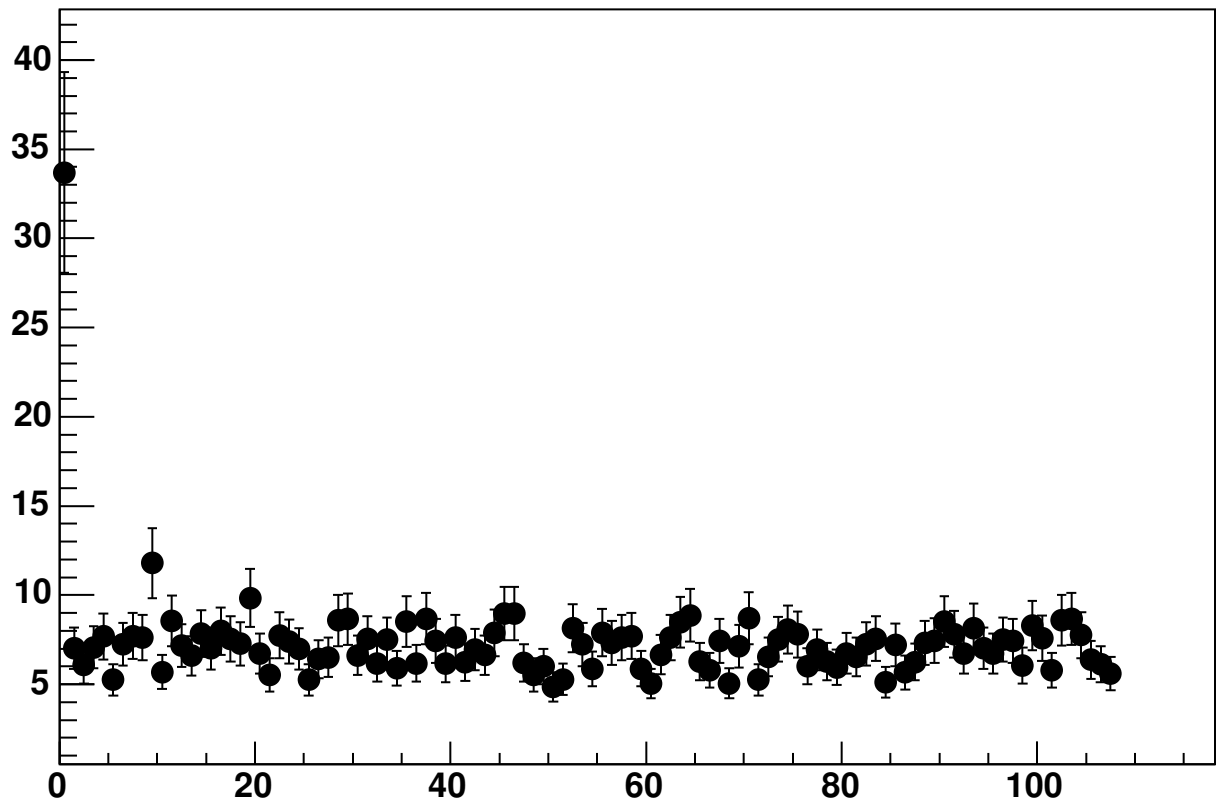
Enable 0, DAC=4000, Hold=20, ADC Noise vs 18*Chip+Chan



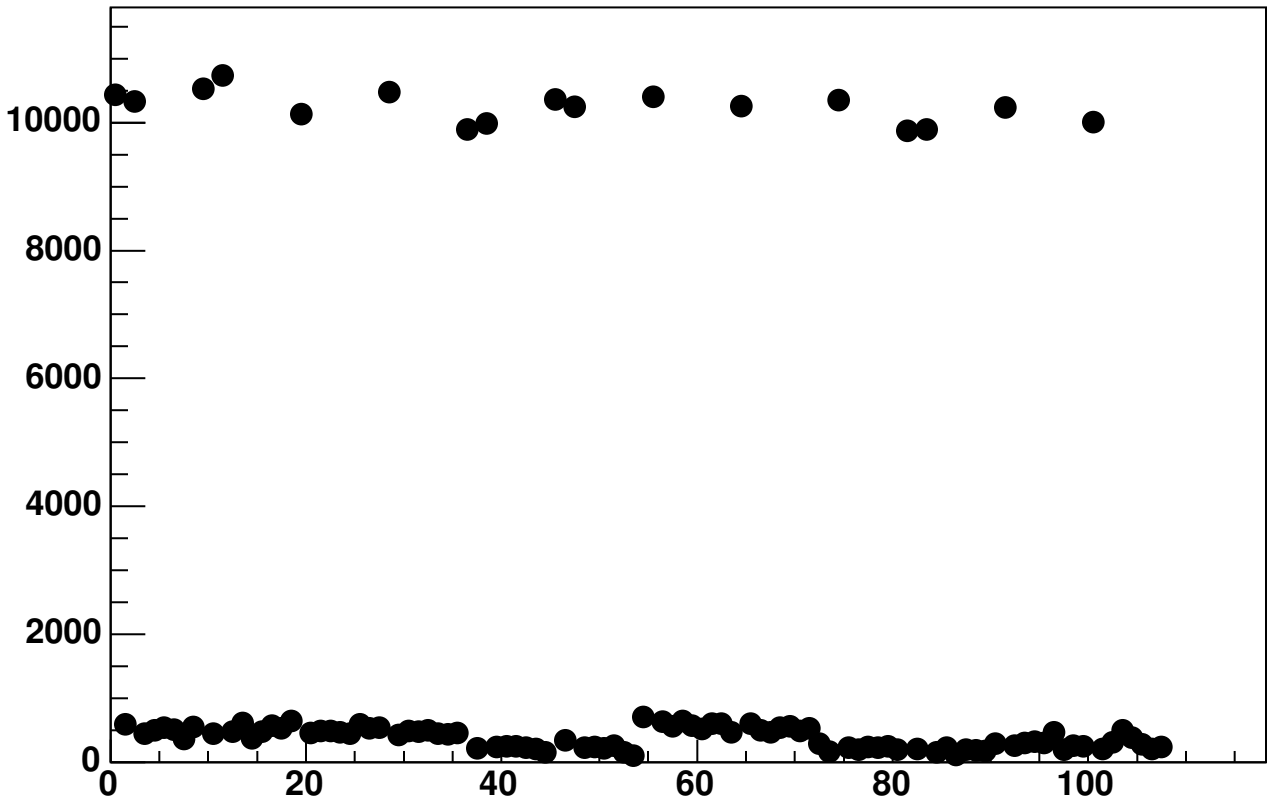
Enable 0, DAC=4000, Hold=25, ADC Mean vs 18*Chip+Chan



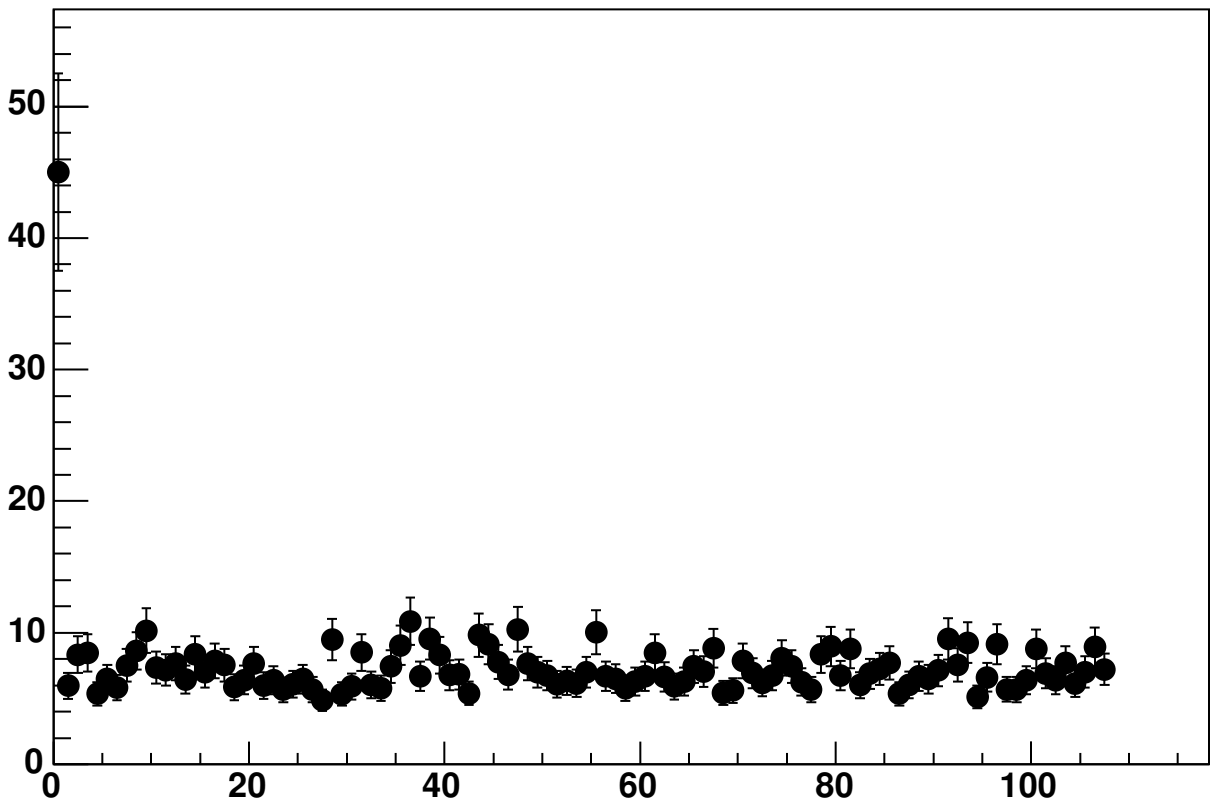
Enable 0, DAC=4000, Hold=25, ADC Noise vs 18*Chip+Chan



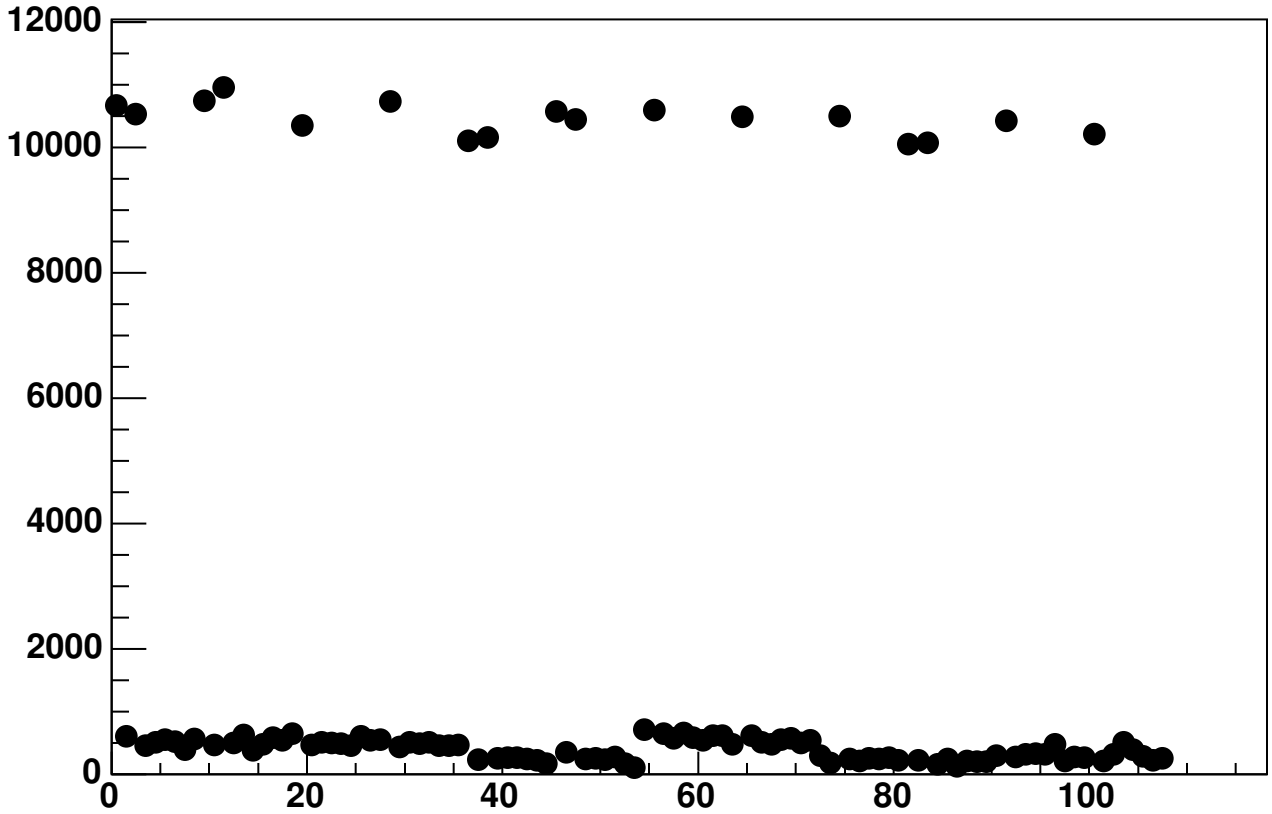
Enable 0, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



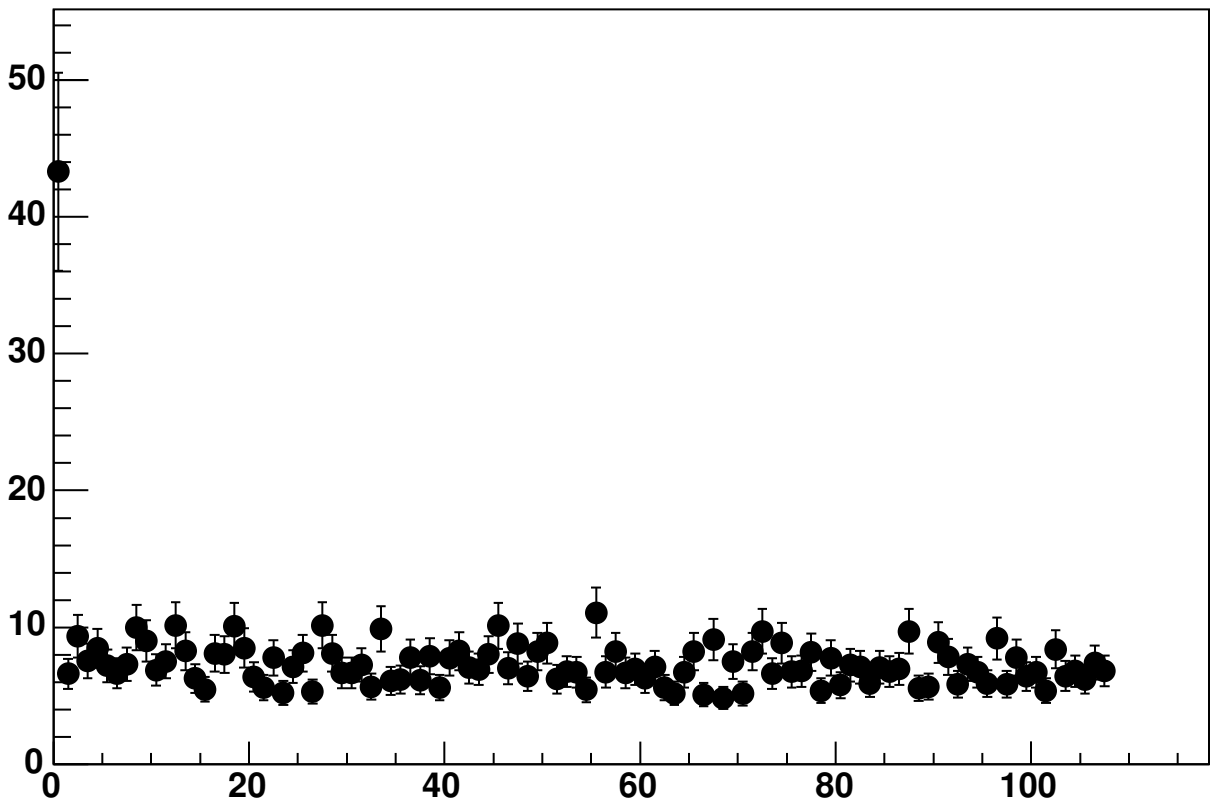
Enable 0, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



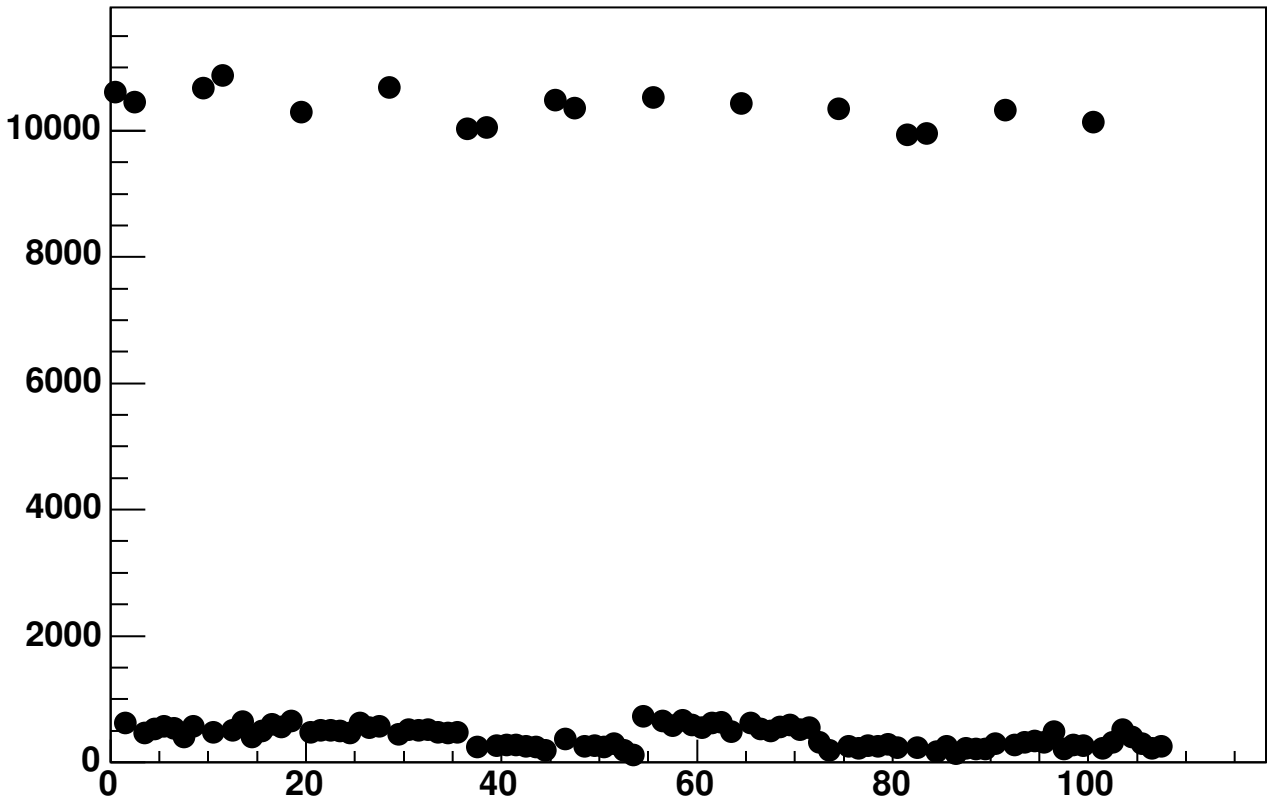
Enable 0, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



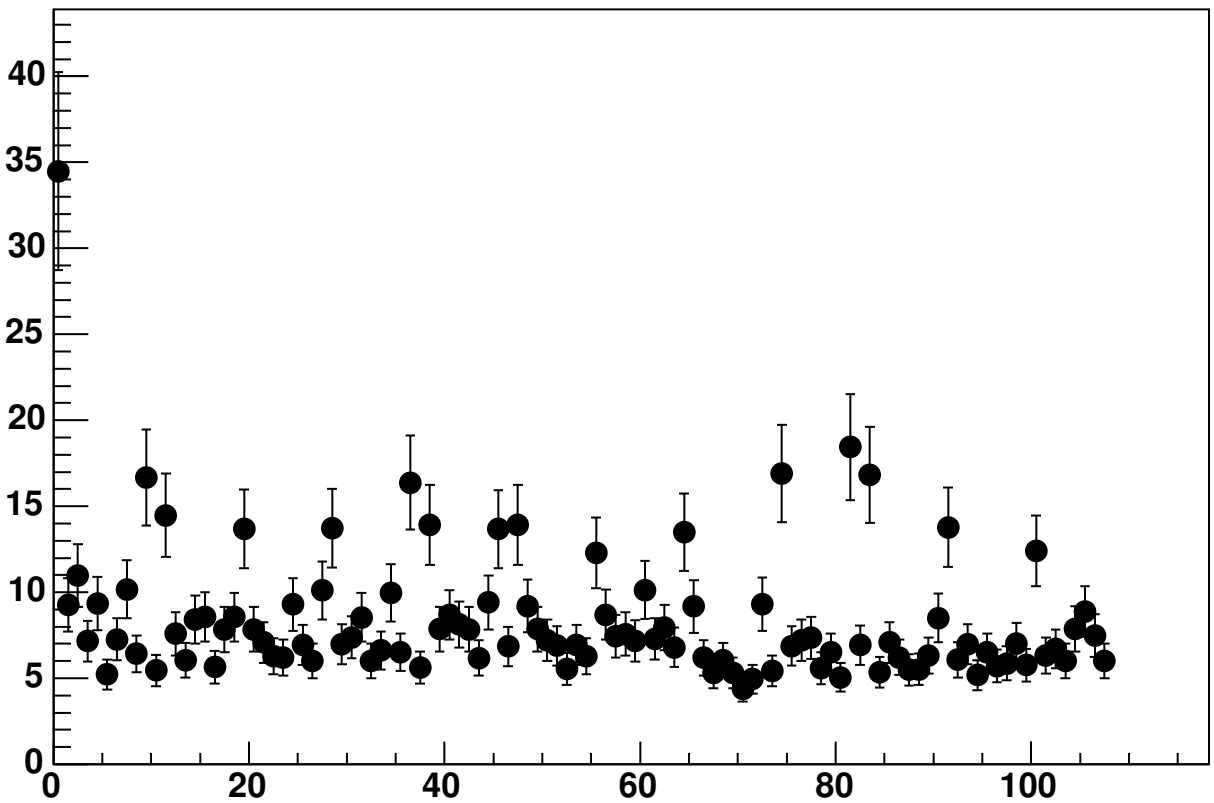
Enable 0, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



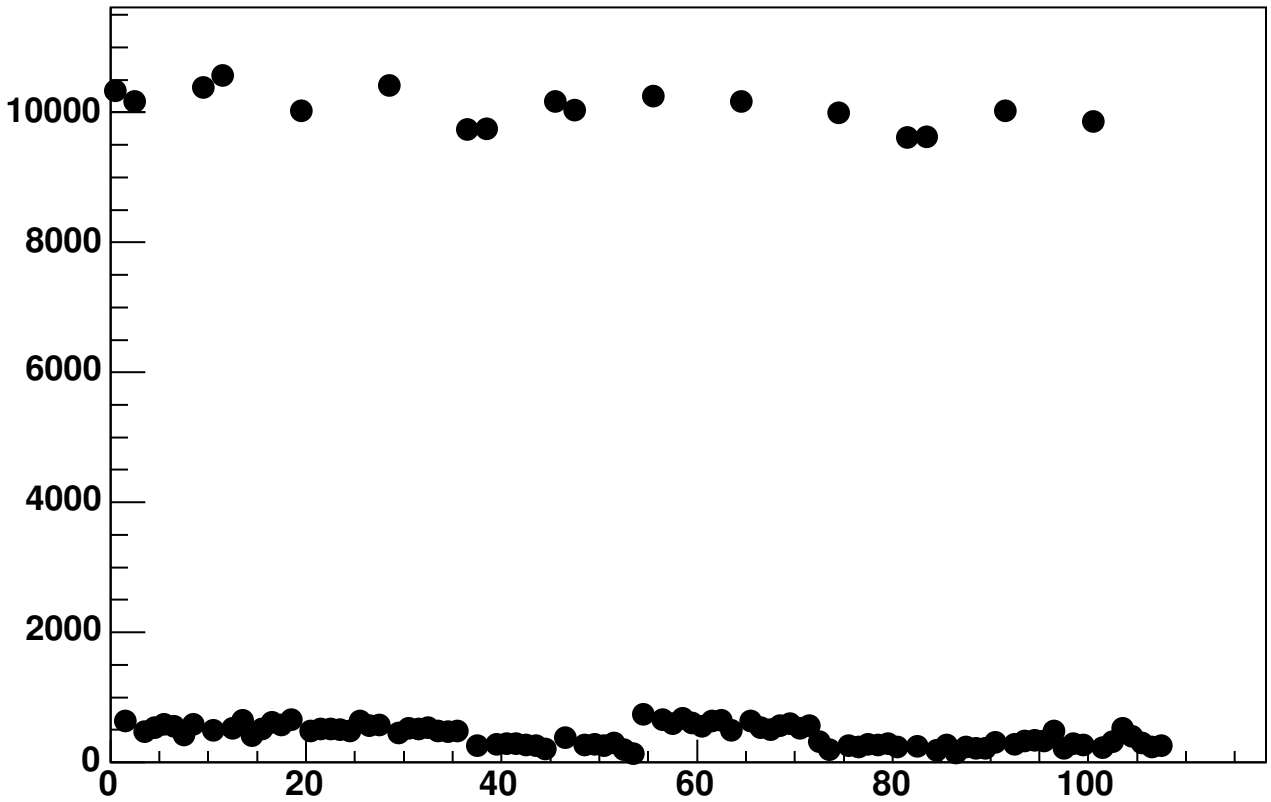
Enable 0, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



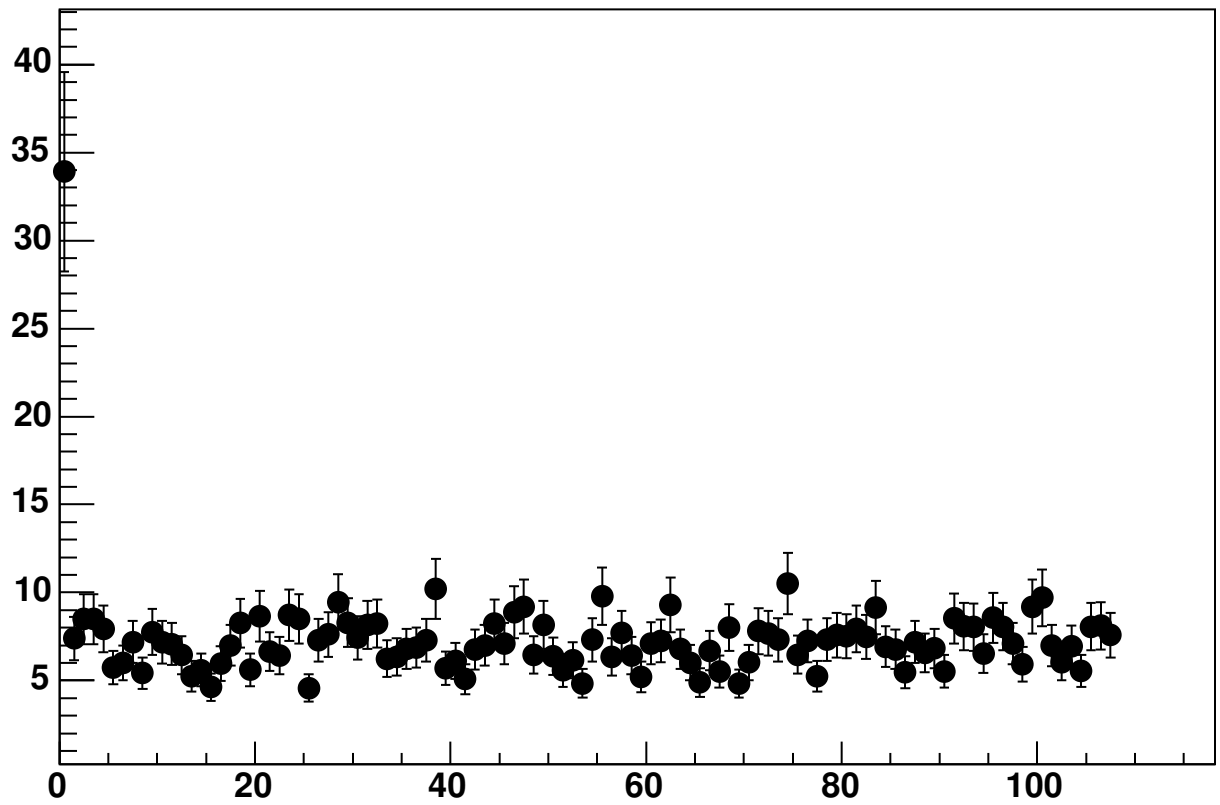
Enable 0, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



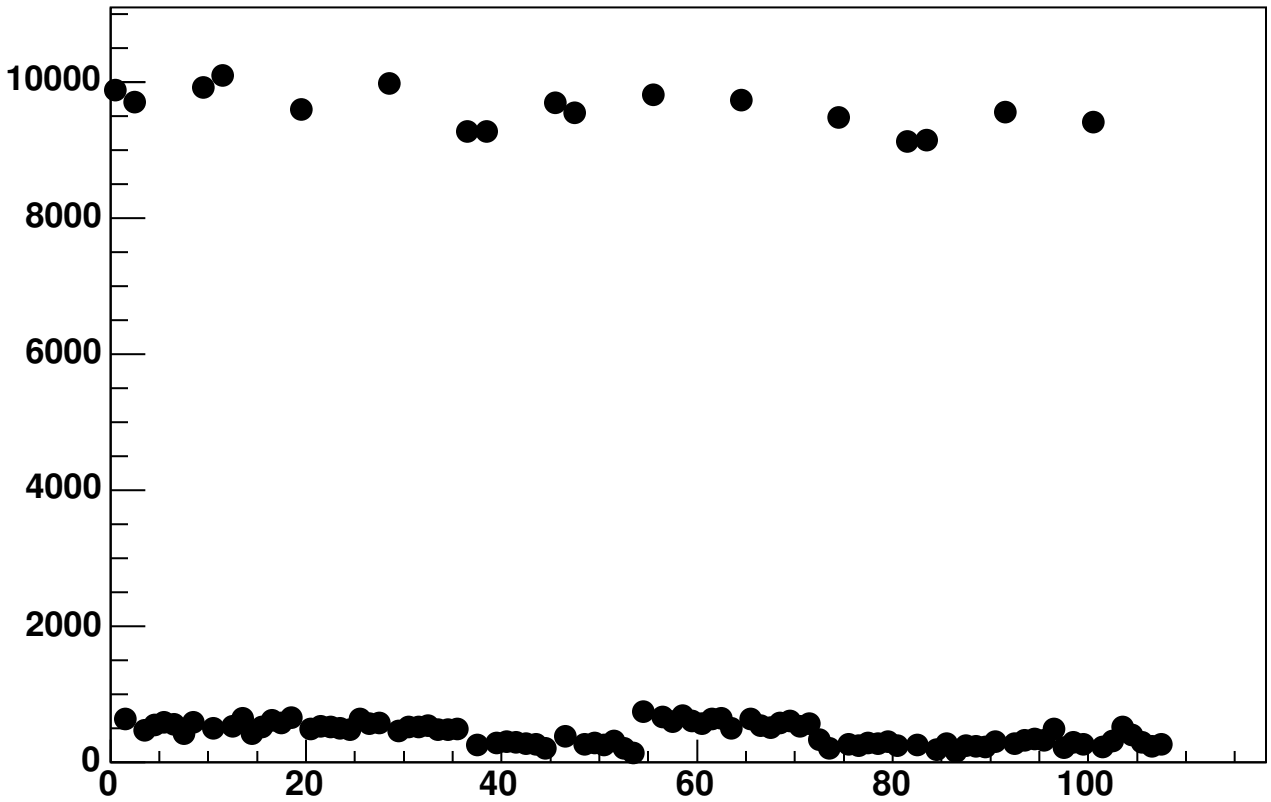
Enable 0, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



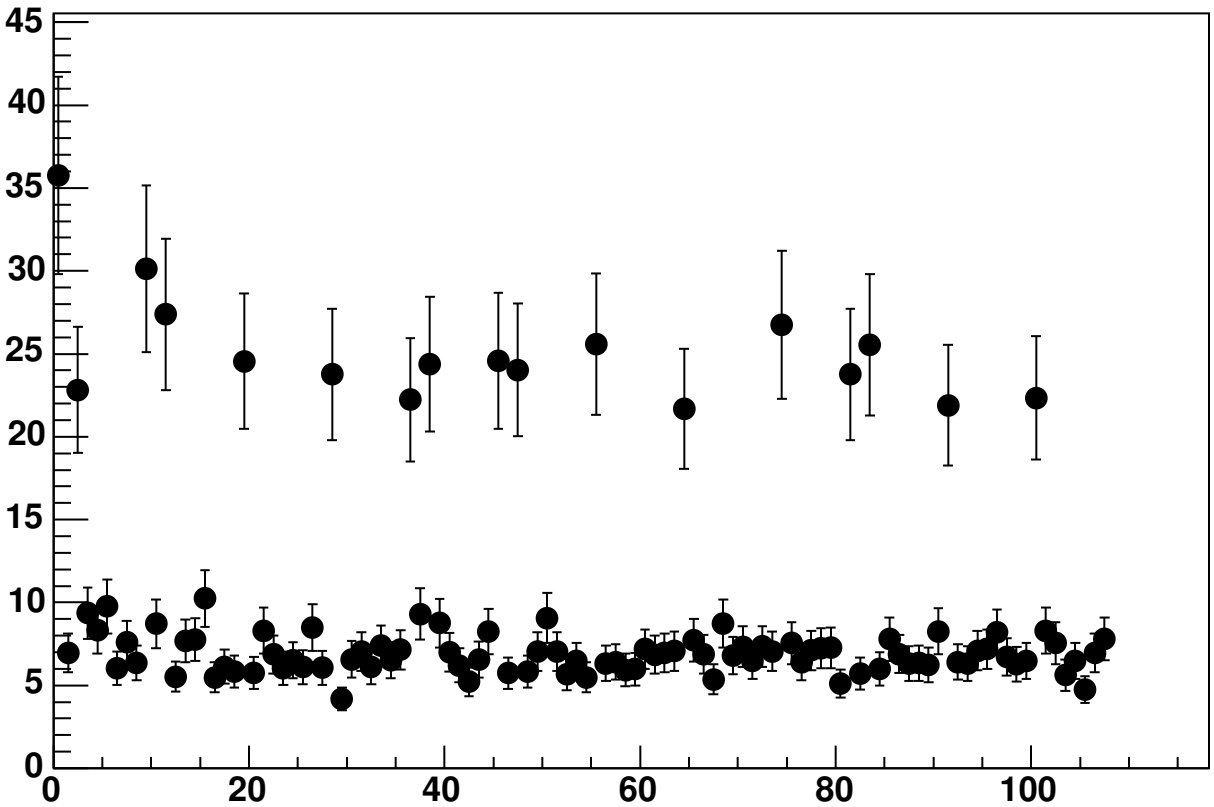
Enable 0, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



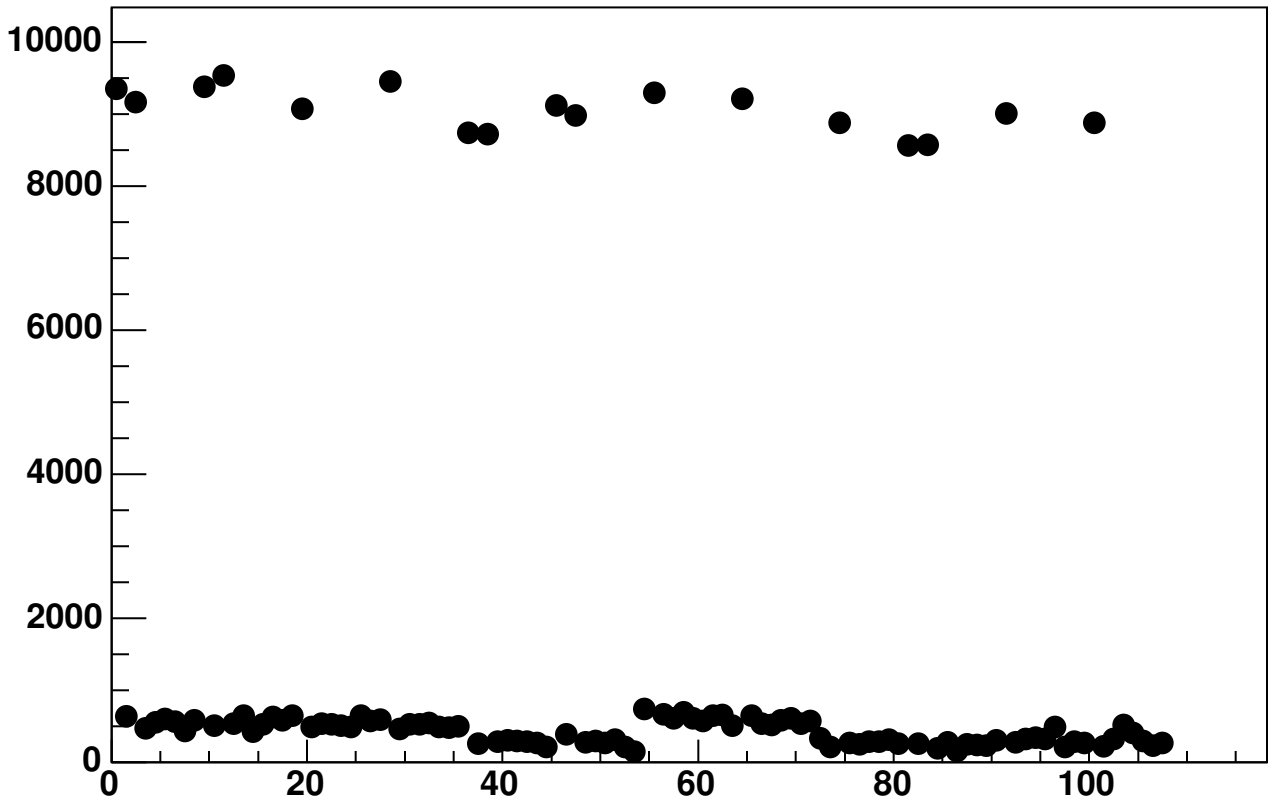
Enable 0, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



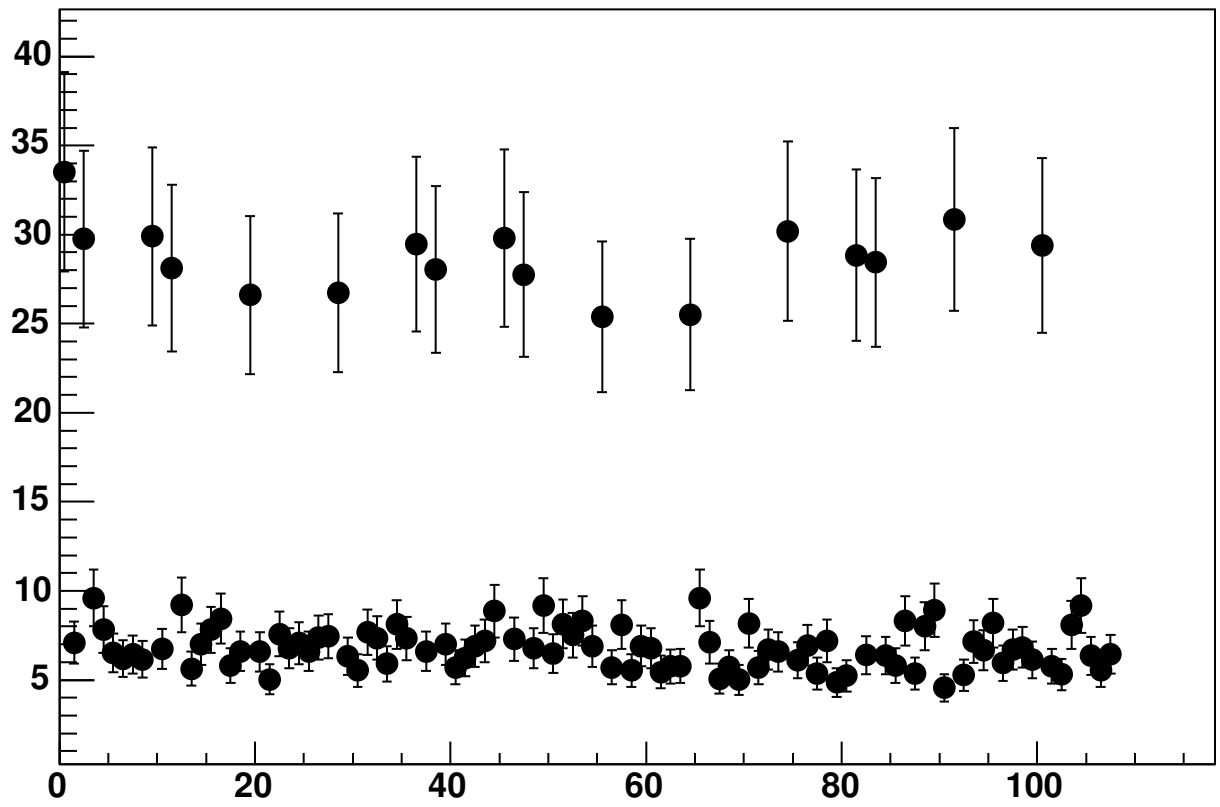
Enable 0, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



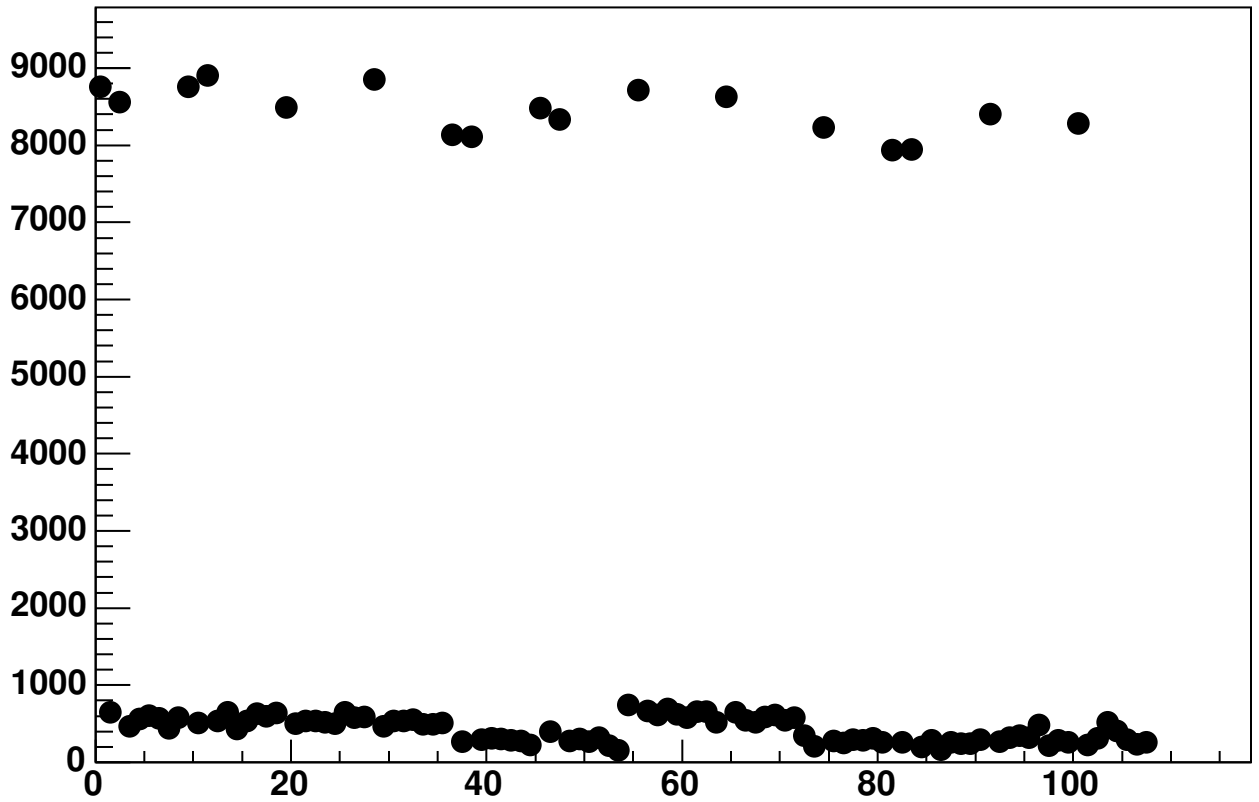
Enable 0, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



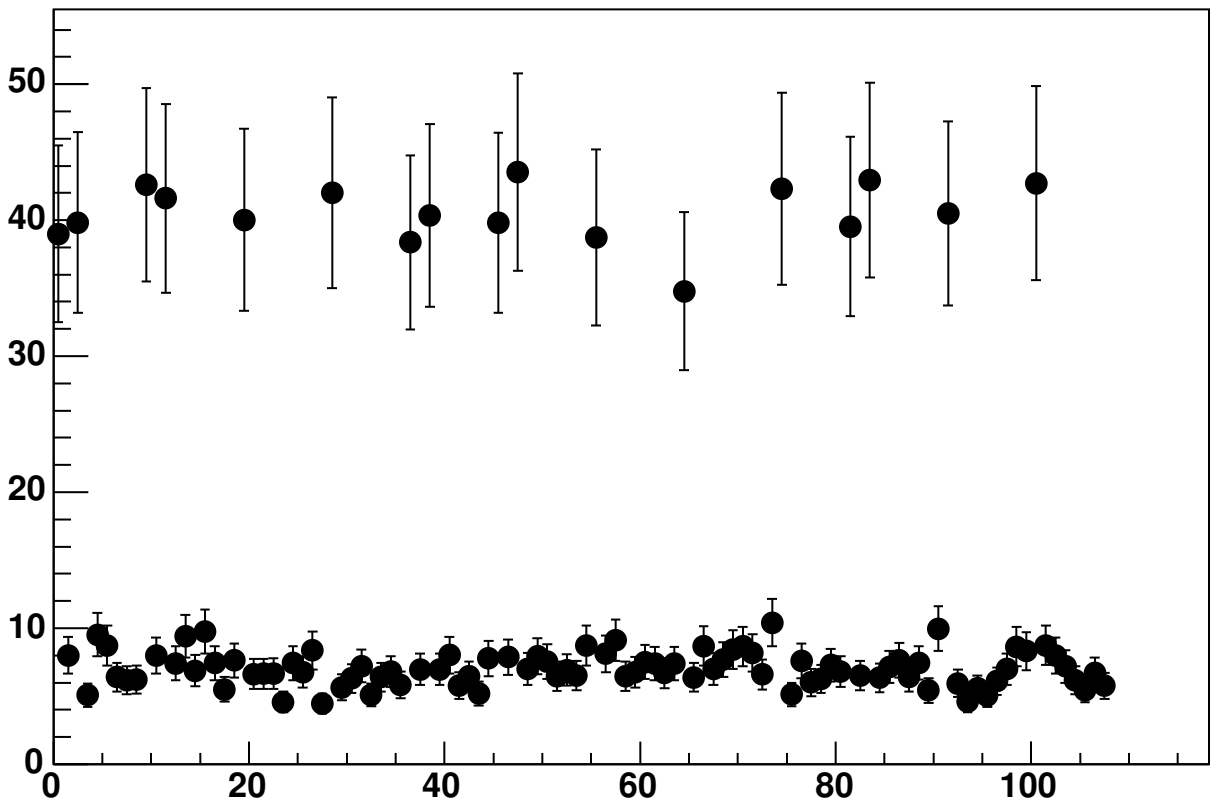
Enable 0, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



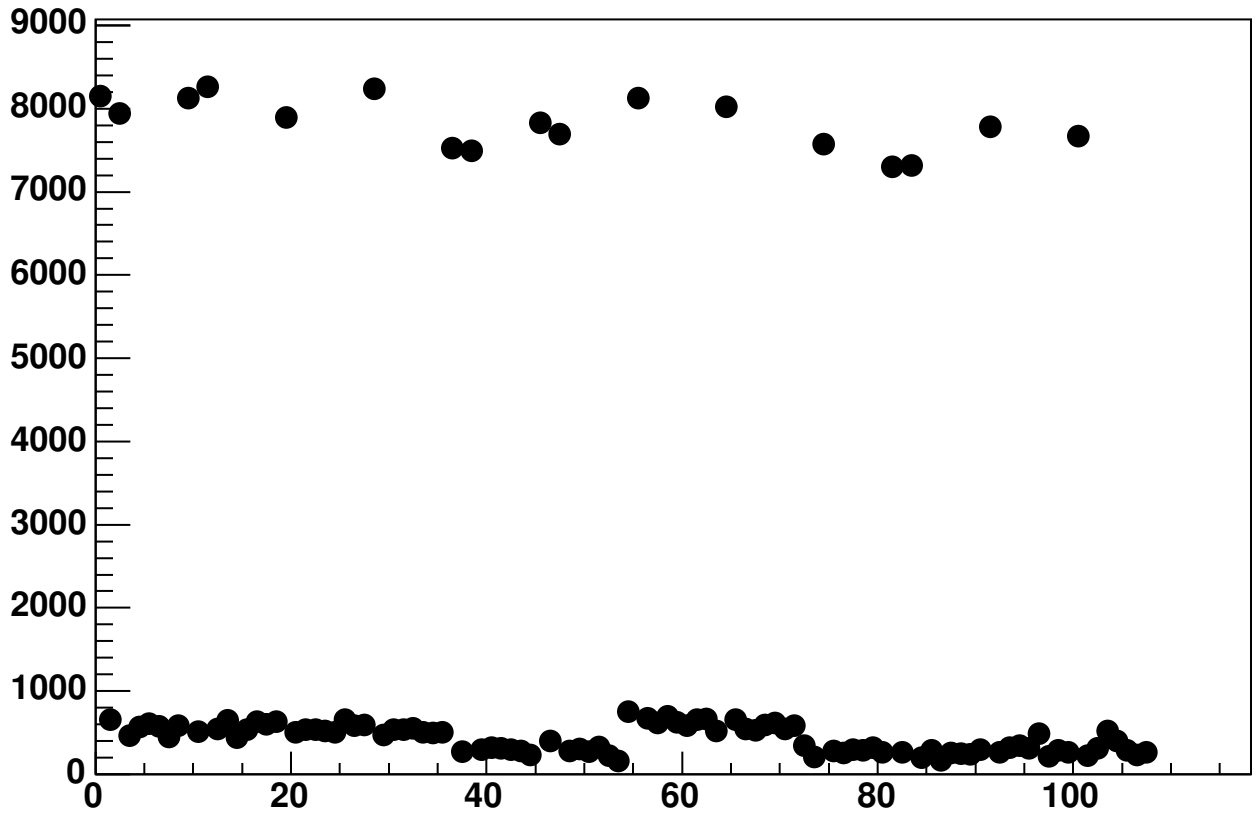
Enable 0, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



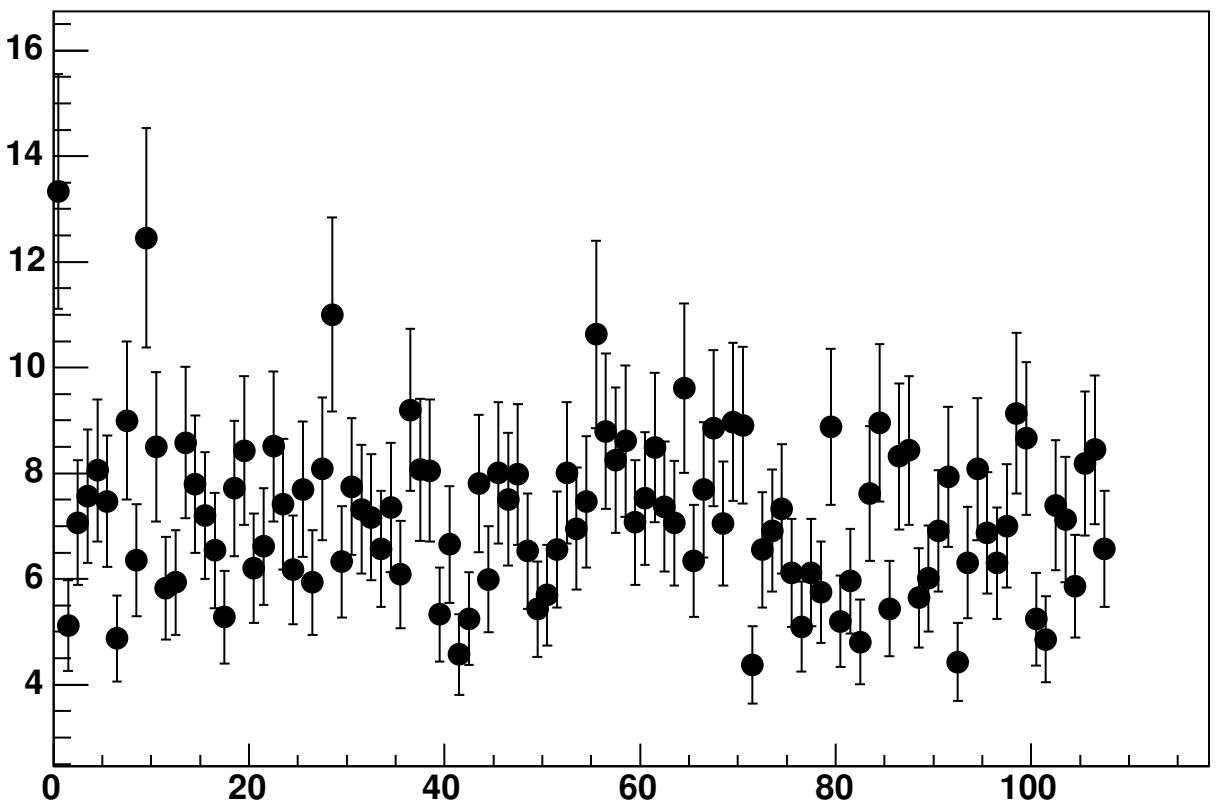
Enable 0, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



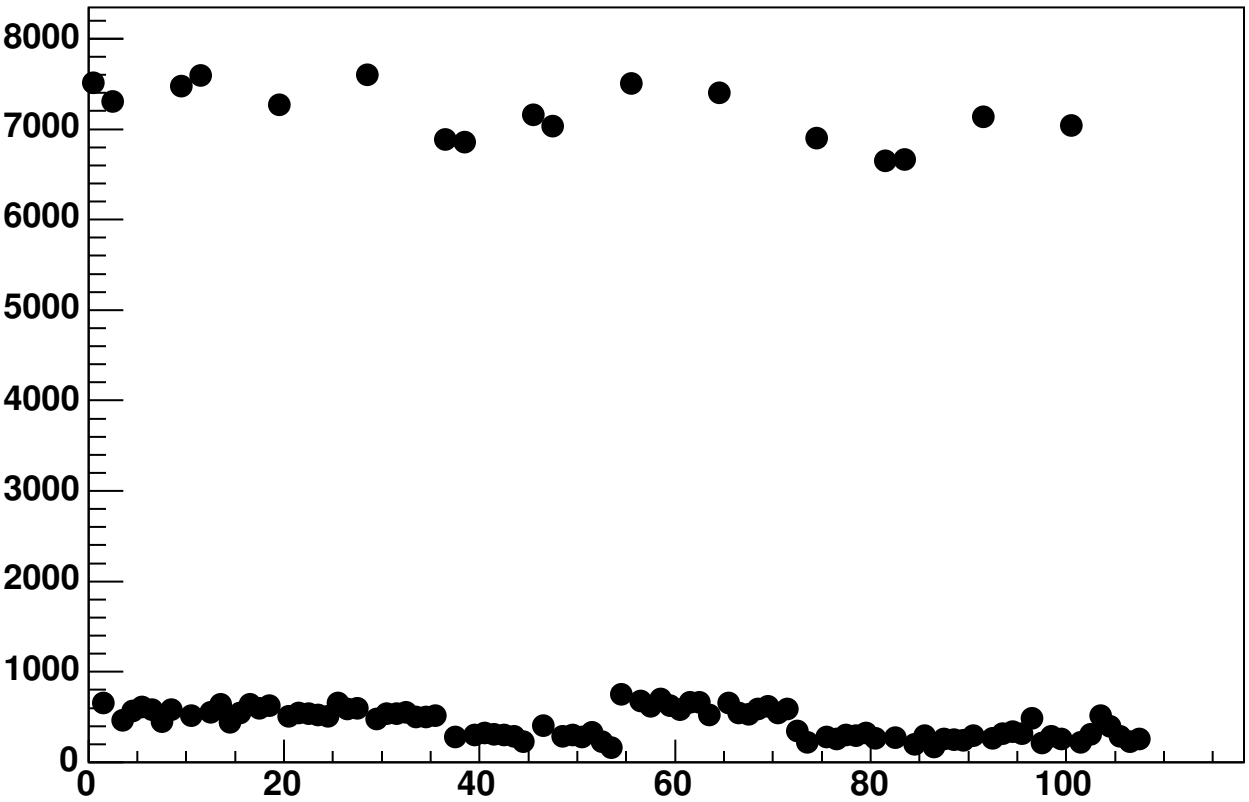
Enable 0, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



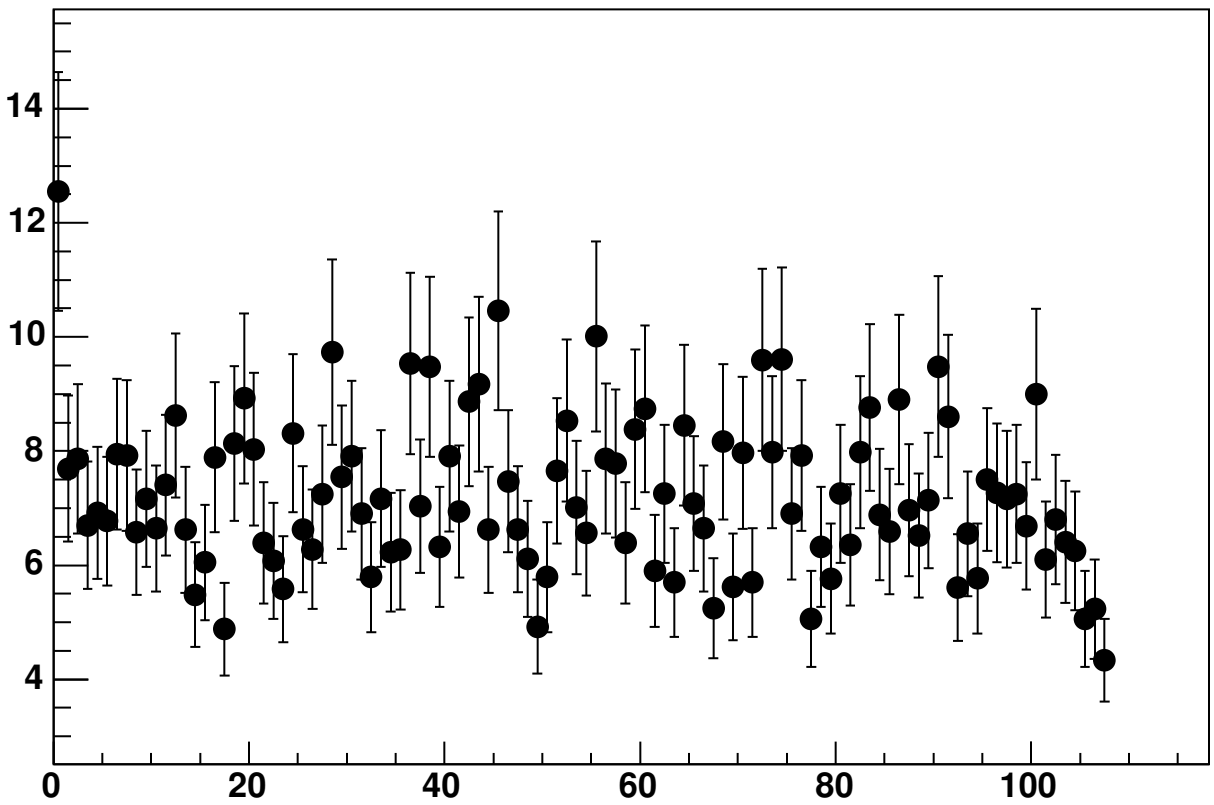
Enable 0, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



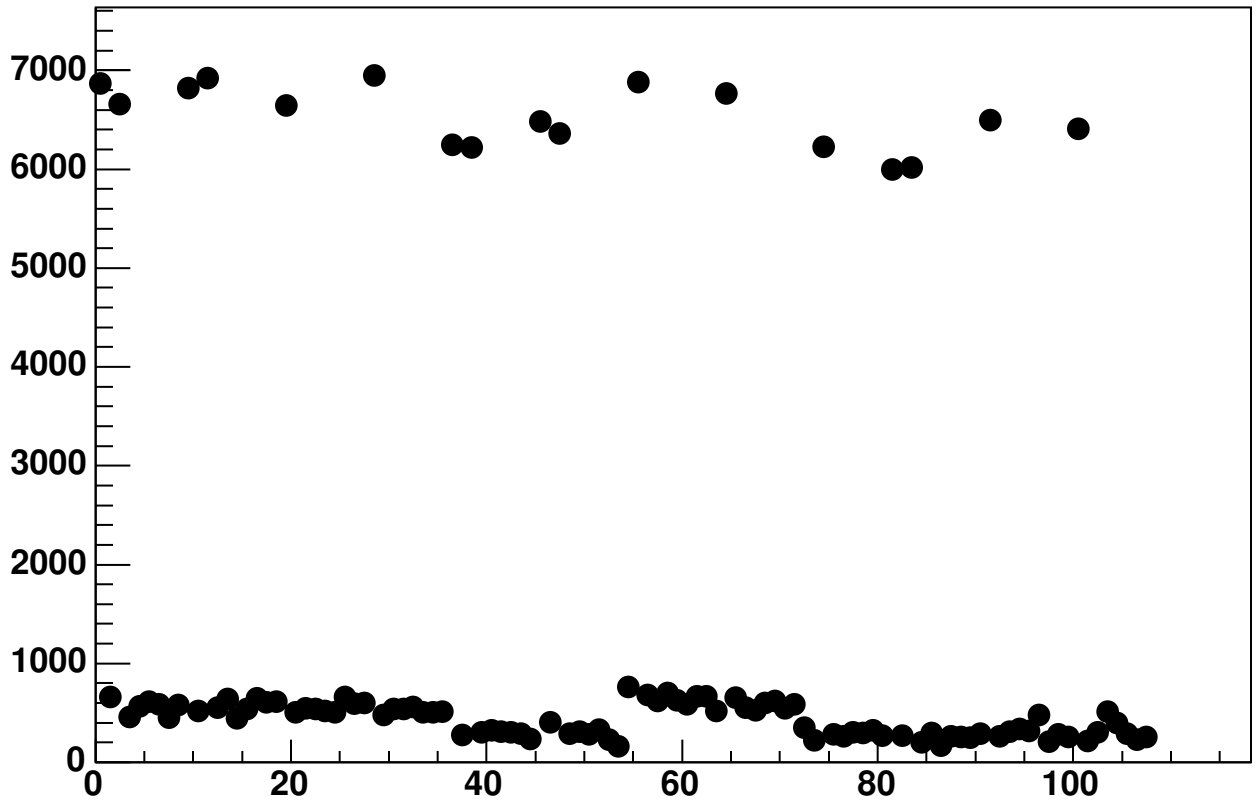
Enable 0, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



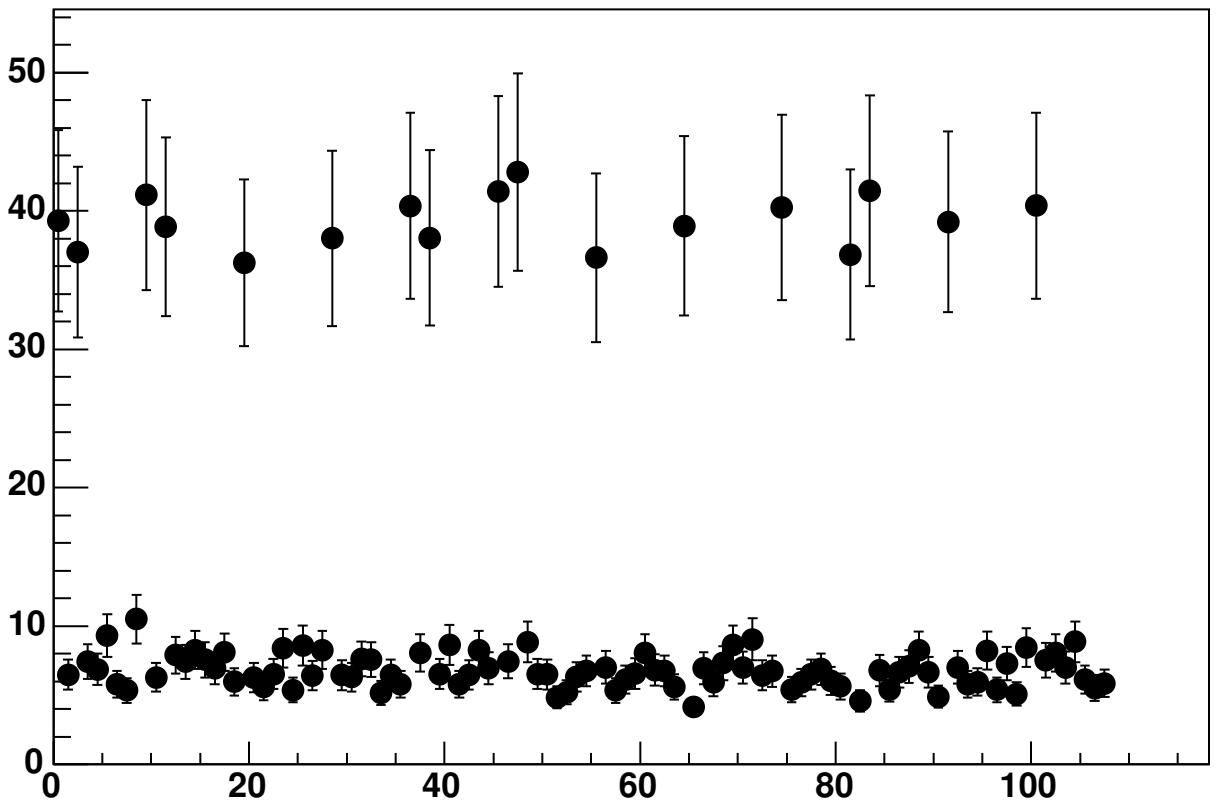
Enable 0, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



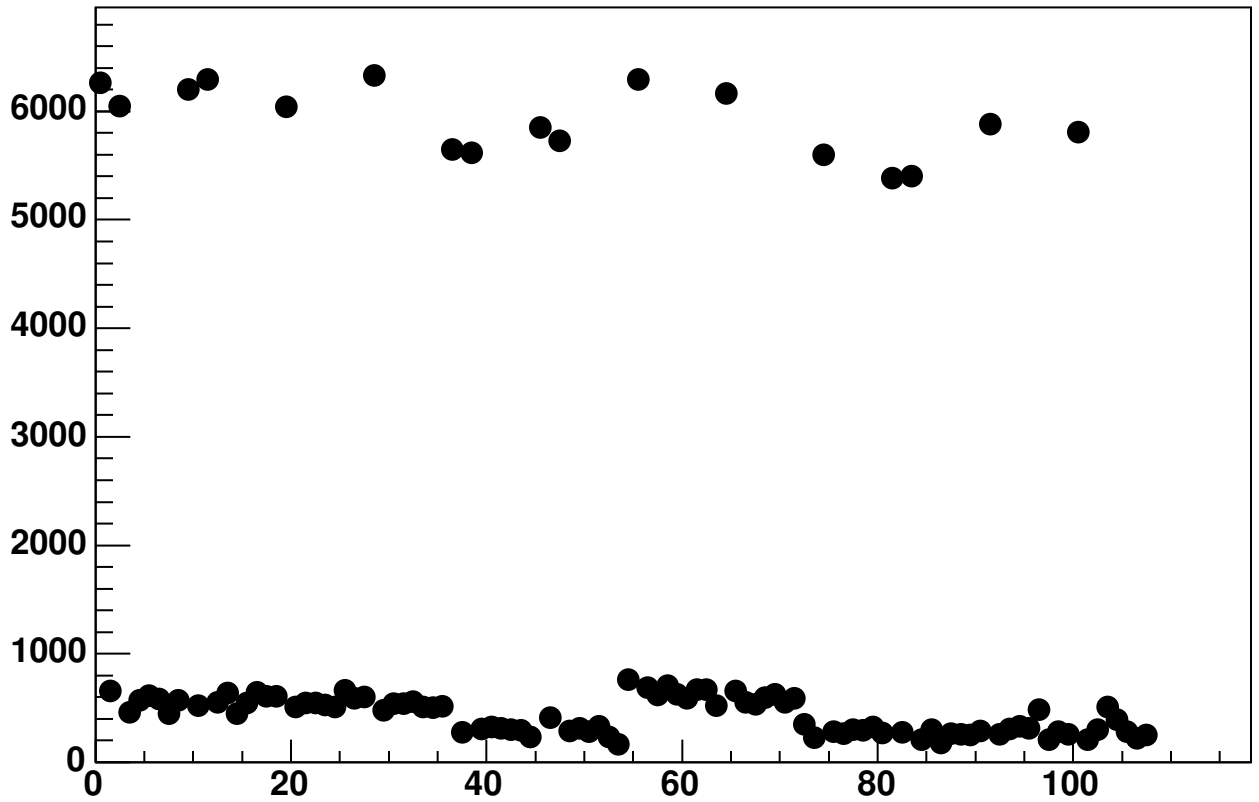
Enable 0, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



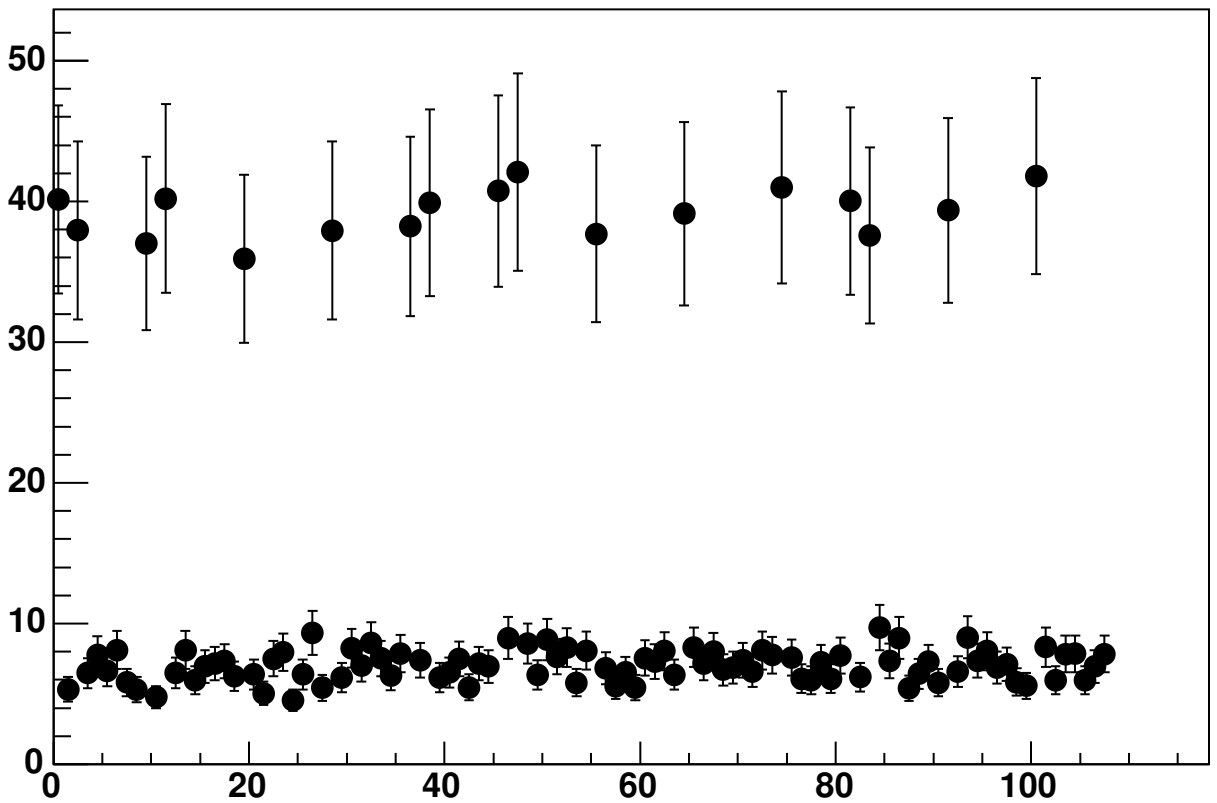
Enable 0, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



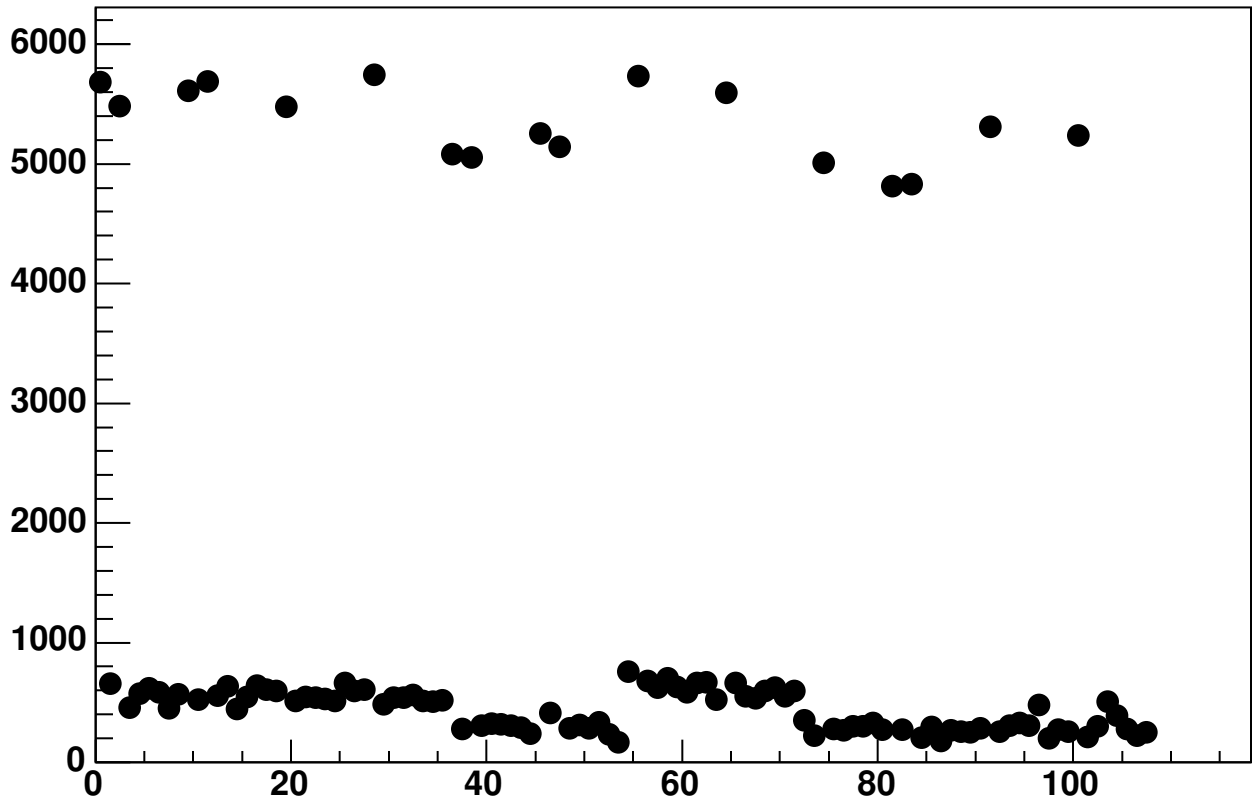
Enable 0, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



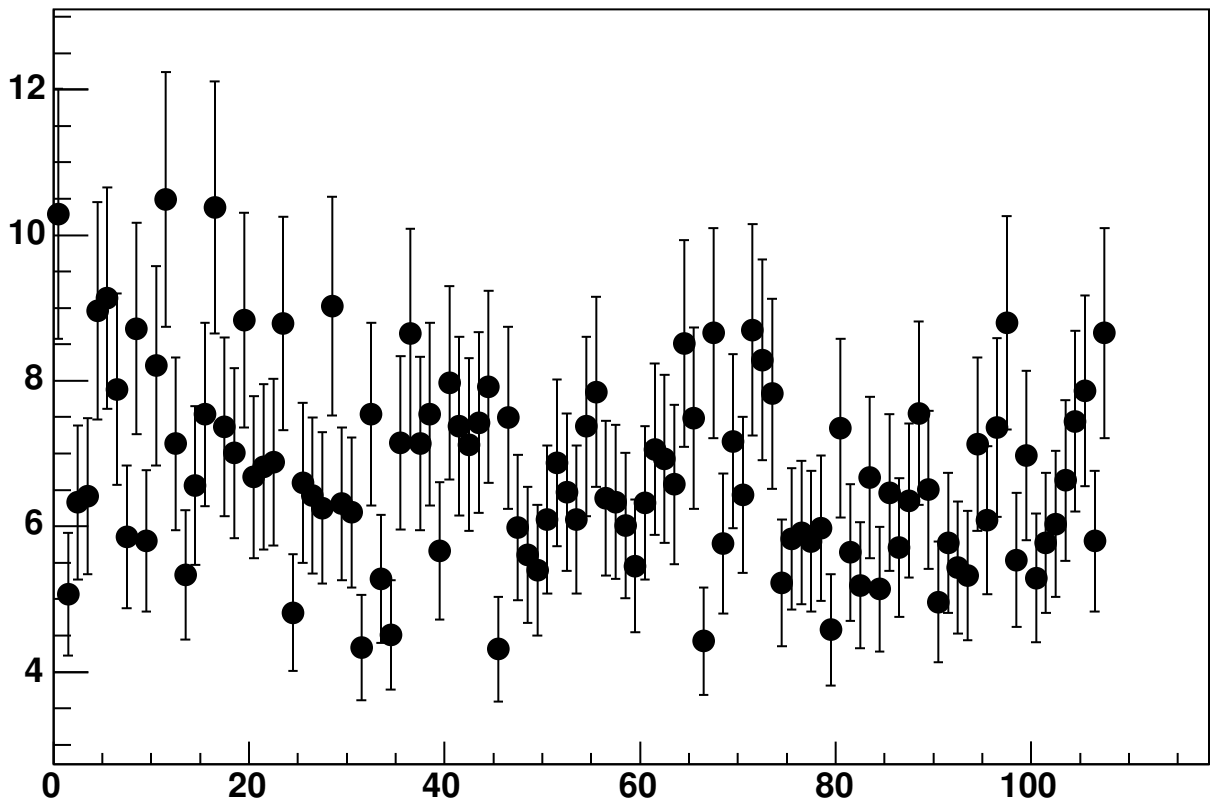
Enable 0, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



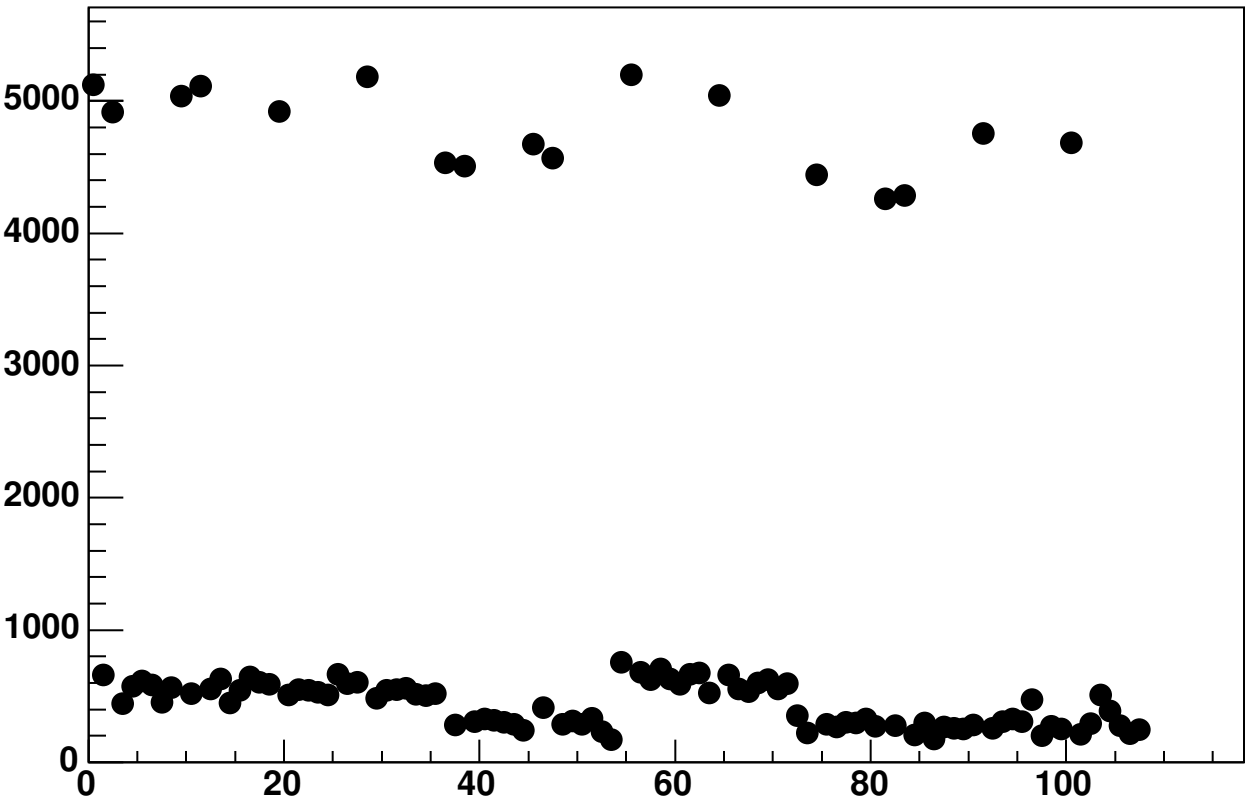
Enable 0, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



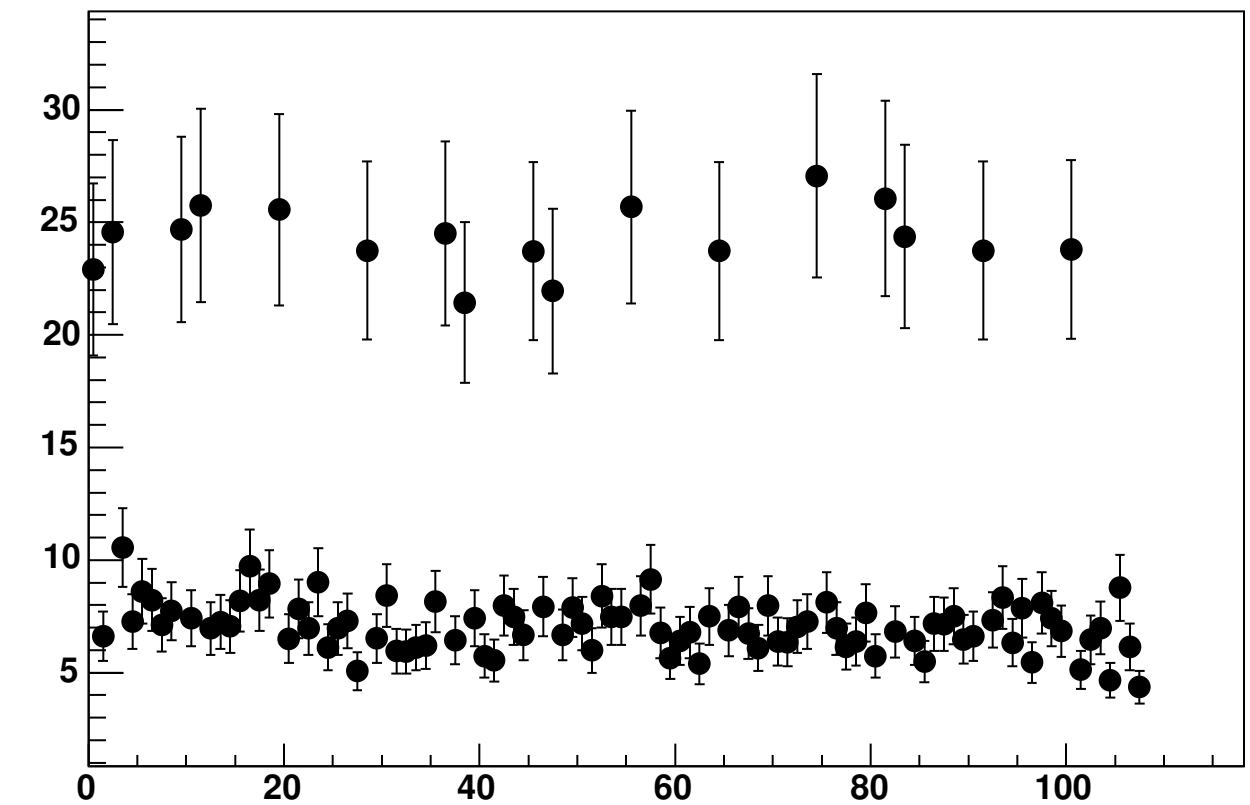
Enable 0, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



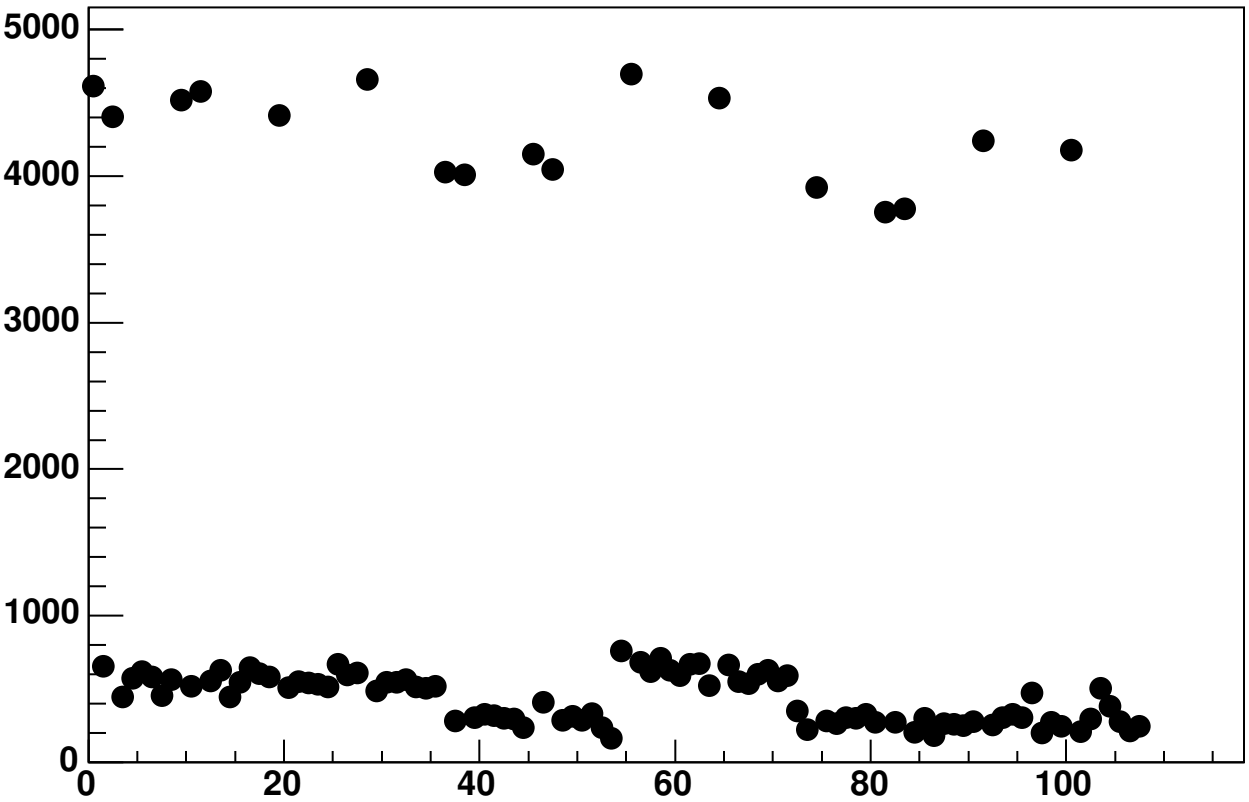
Enable 0, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



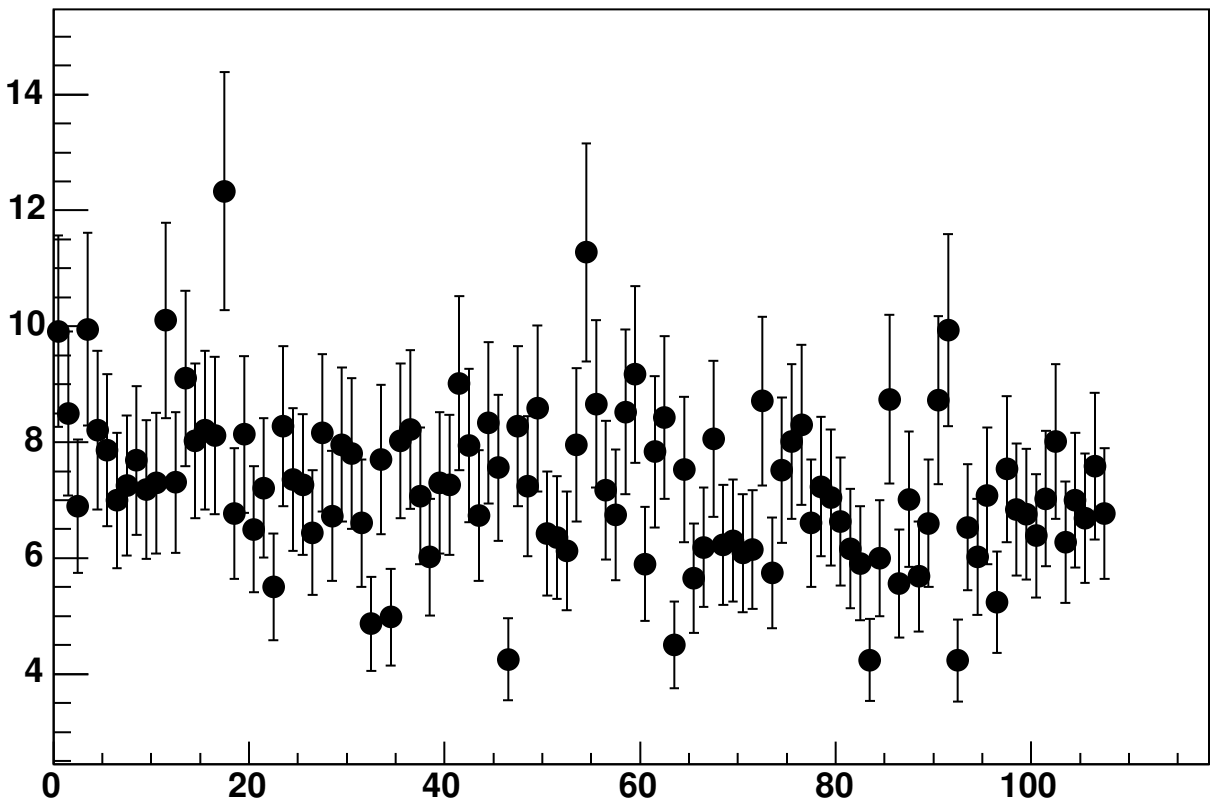
Enable 0, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



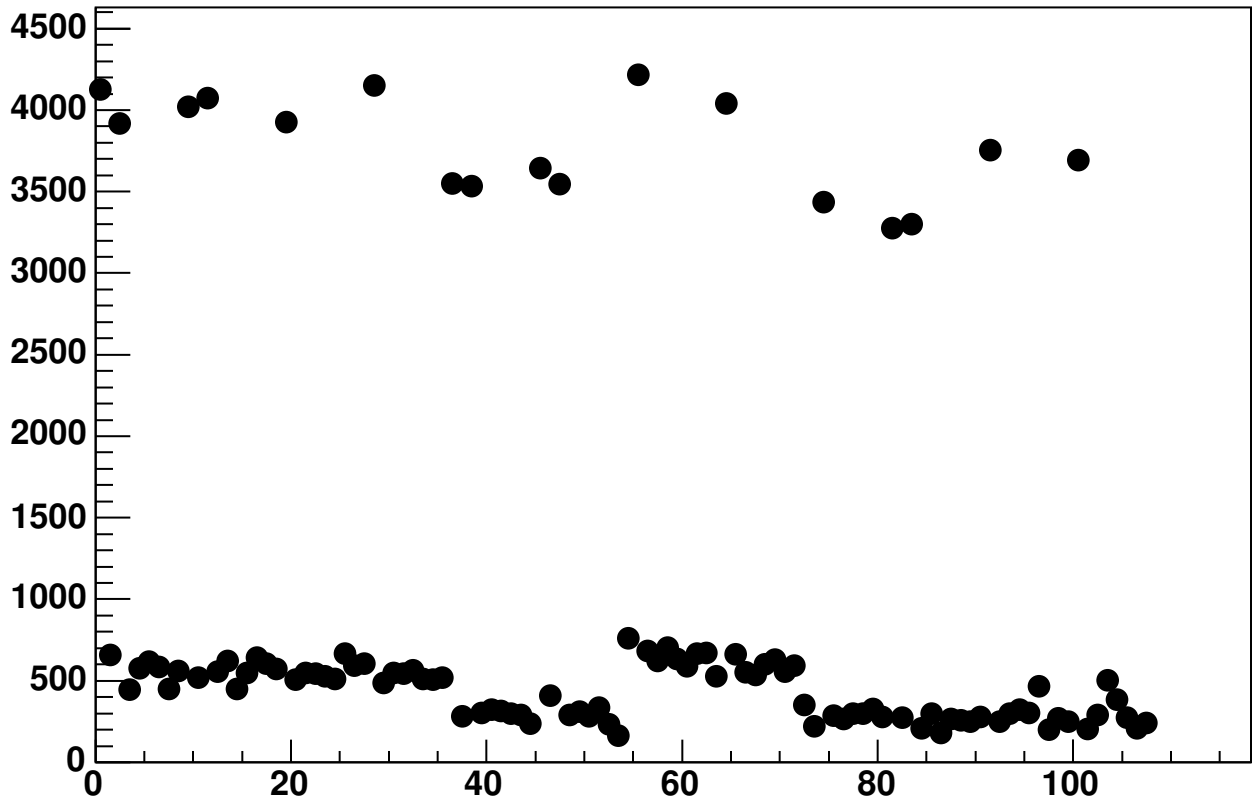
Enable 0, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



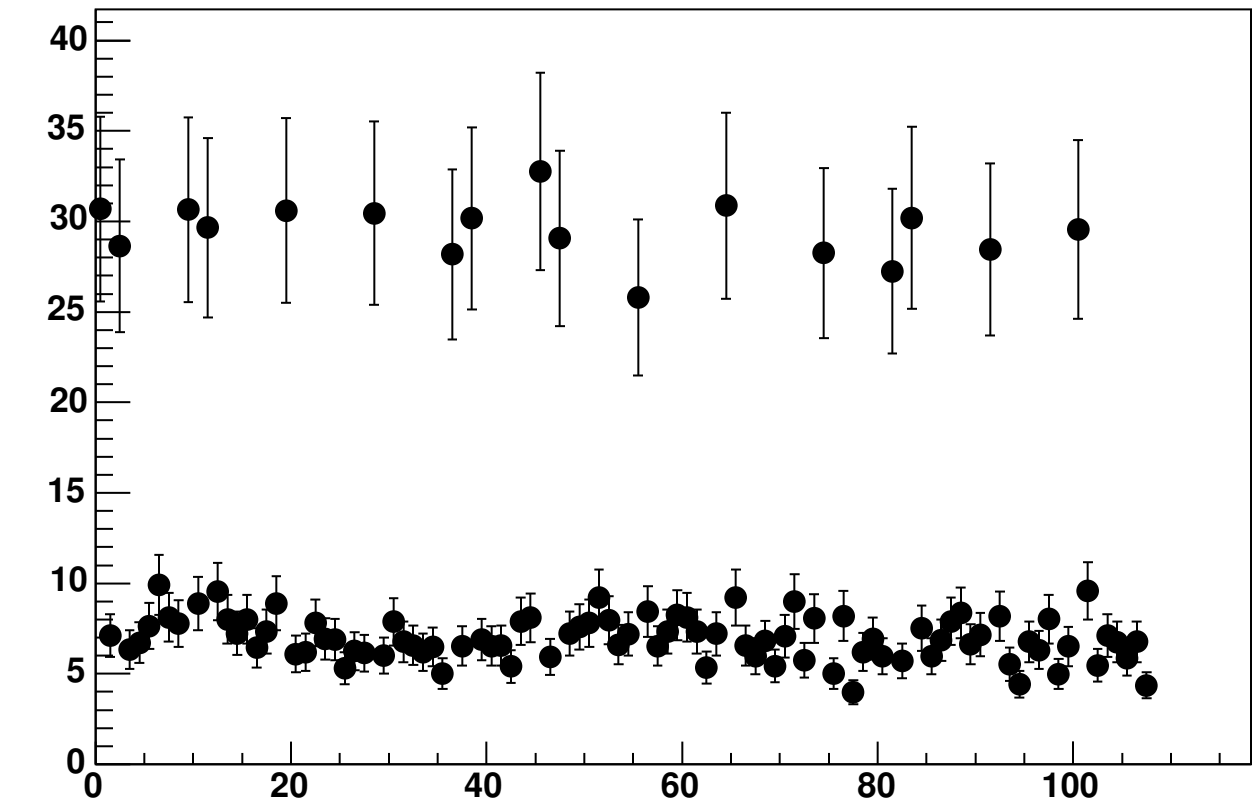
Enable 0, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



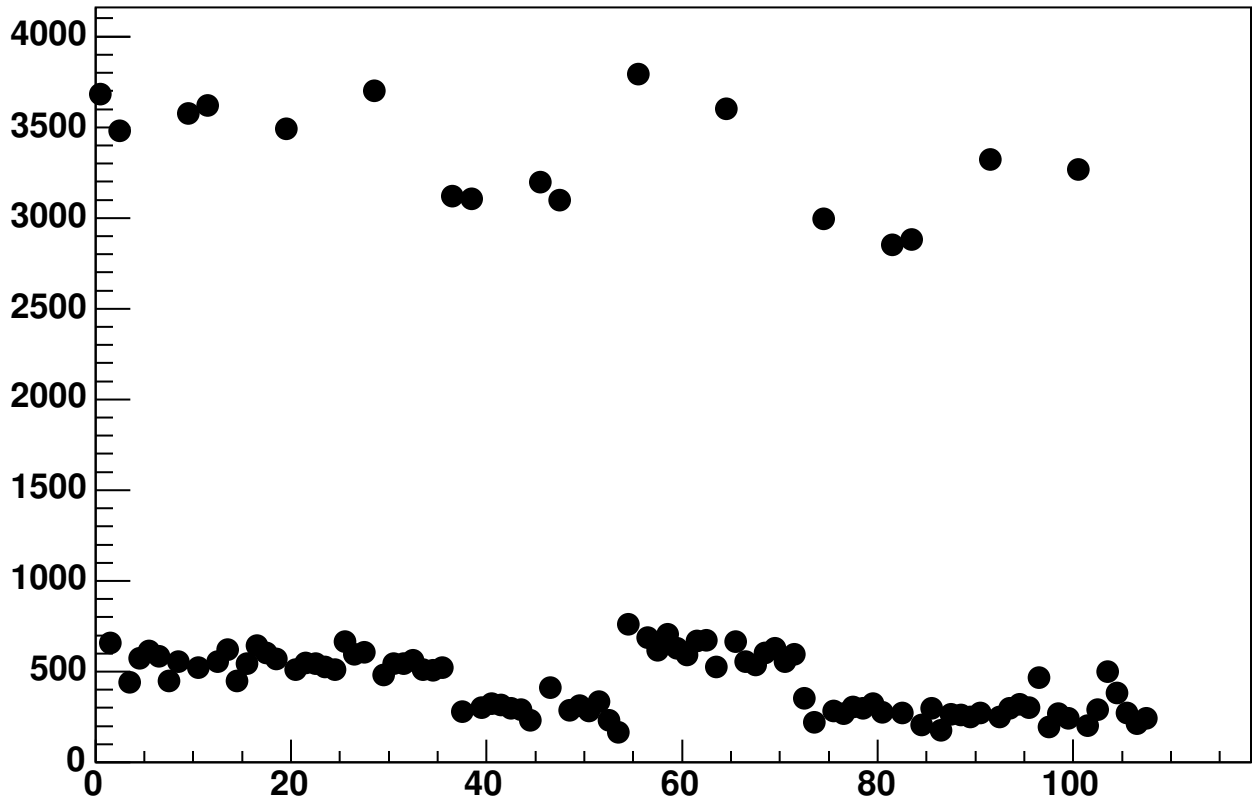
Enable 0, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



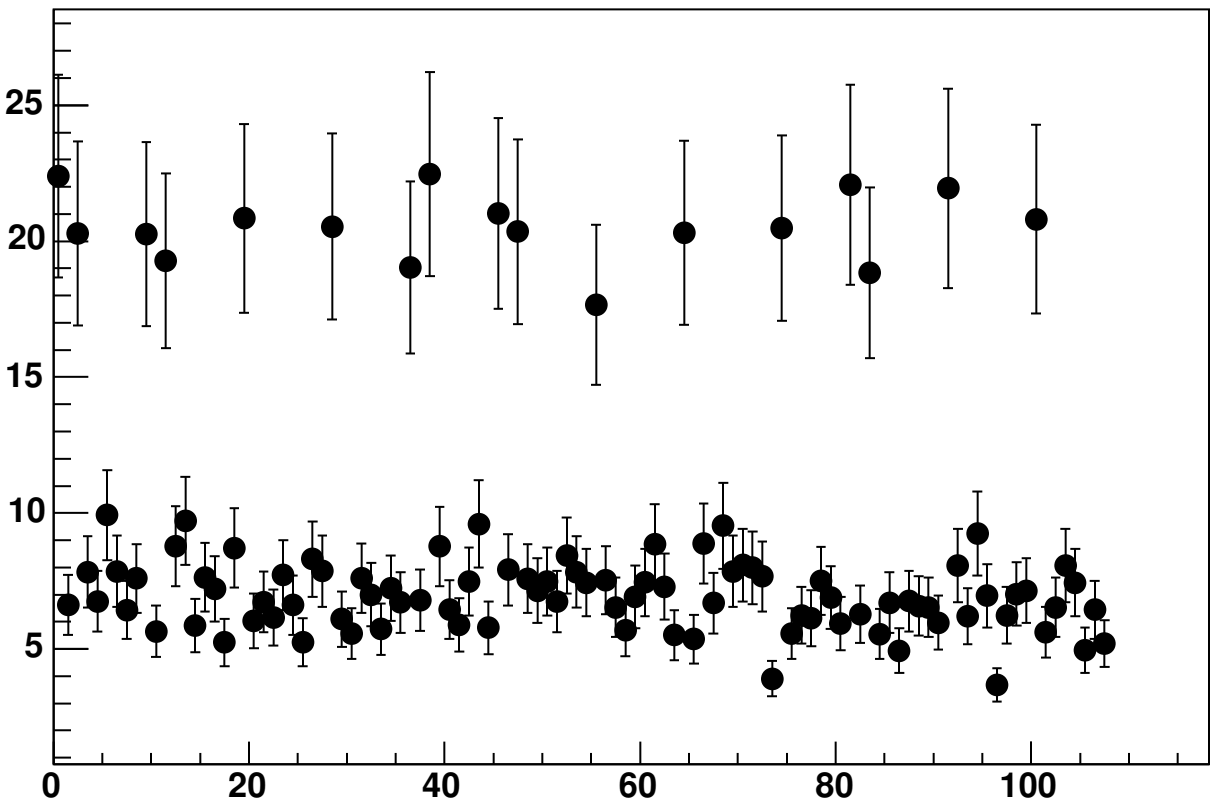
Enable 0, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



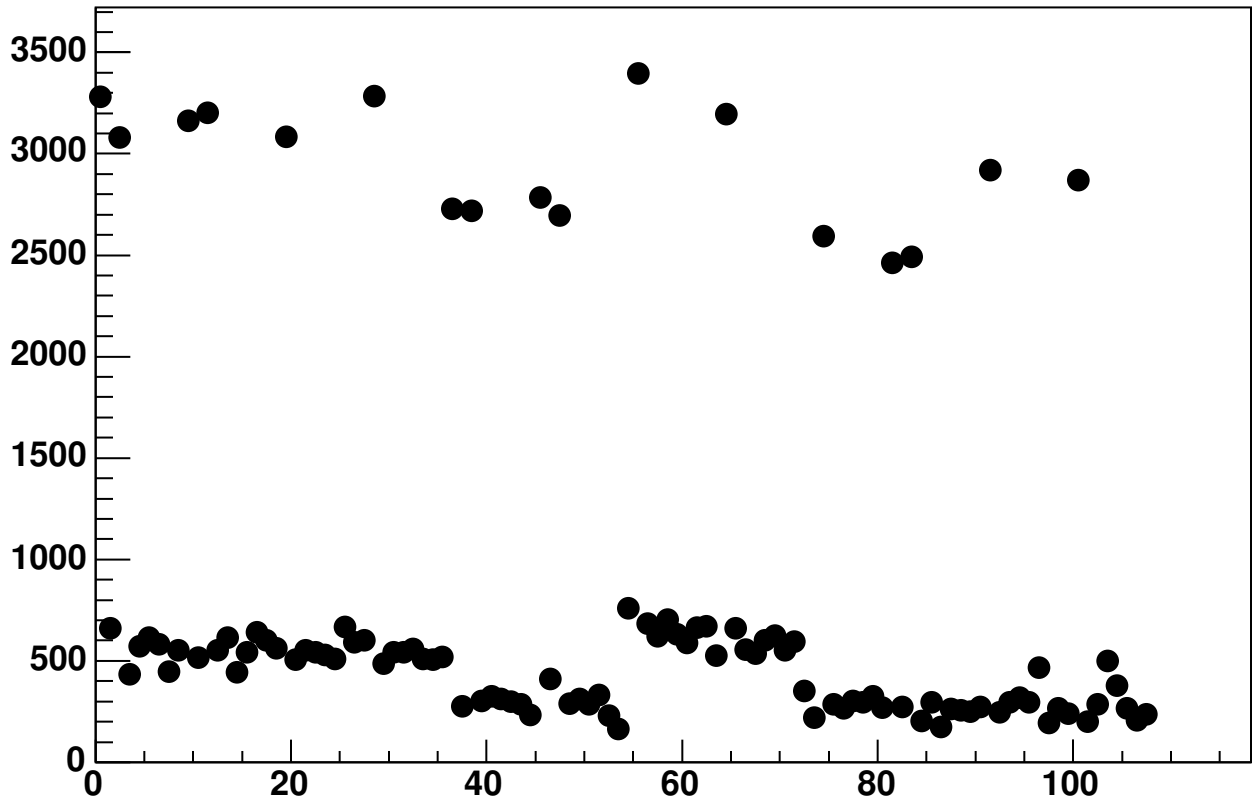
Enable 0, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



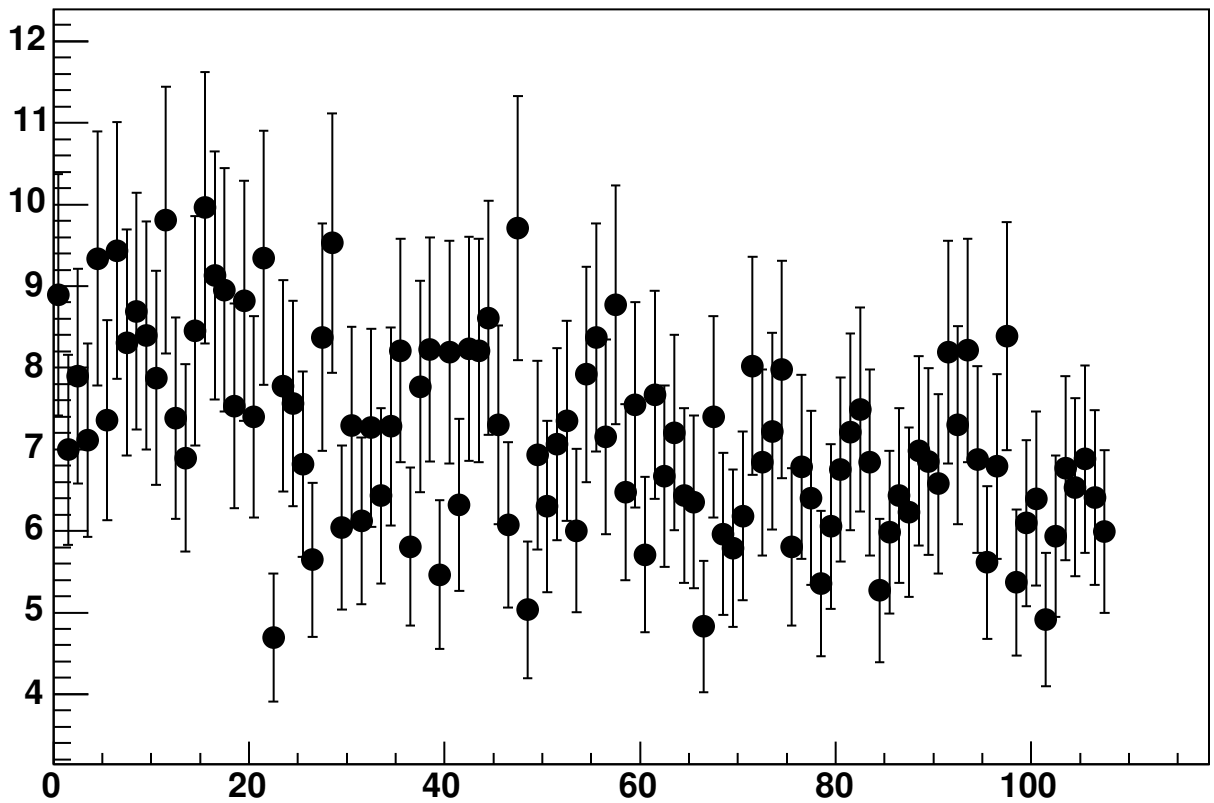
Enable 0, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



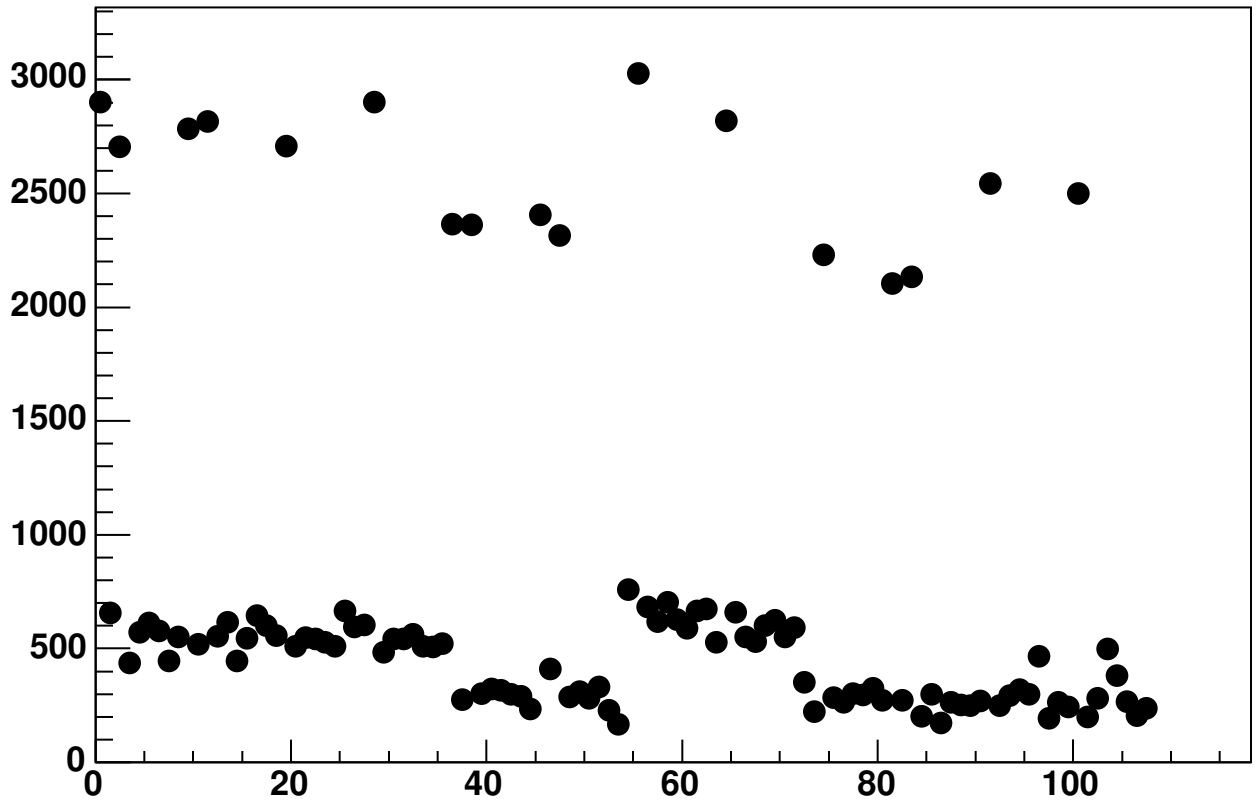
Enable 0, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



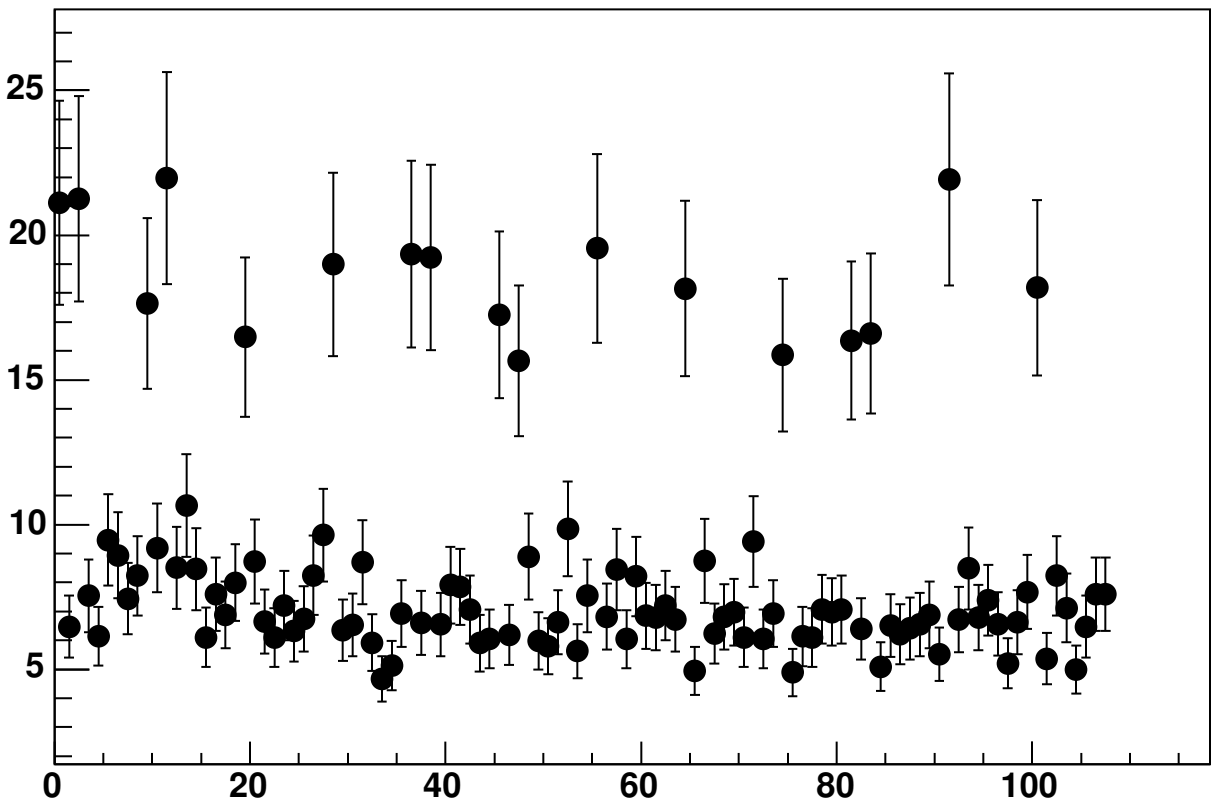
Enable 0, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



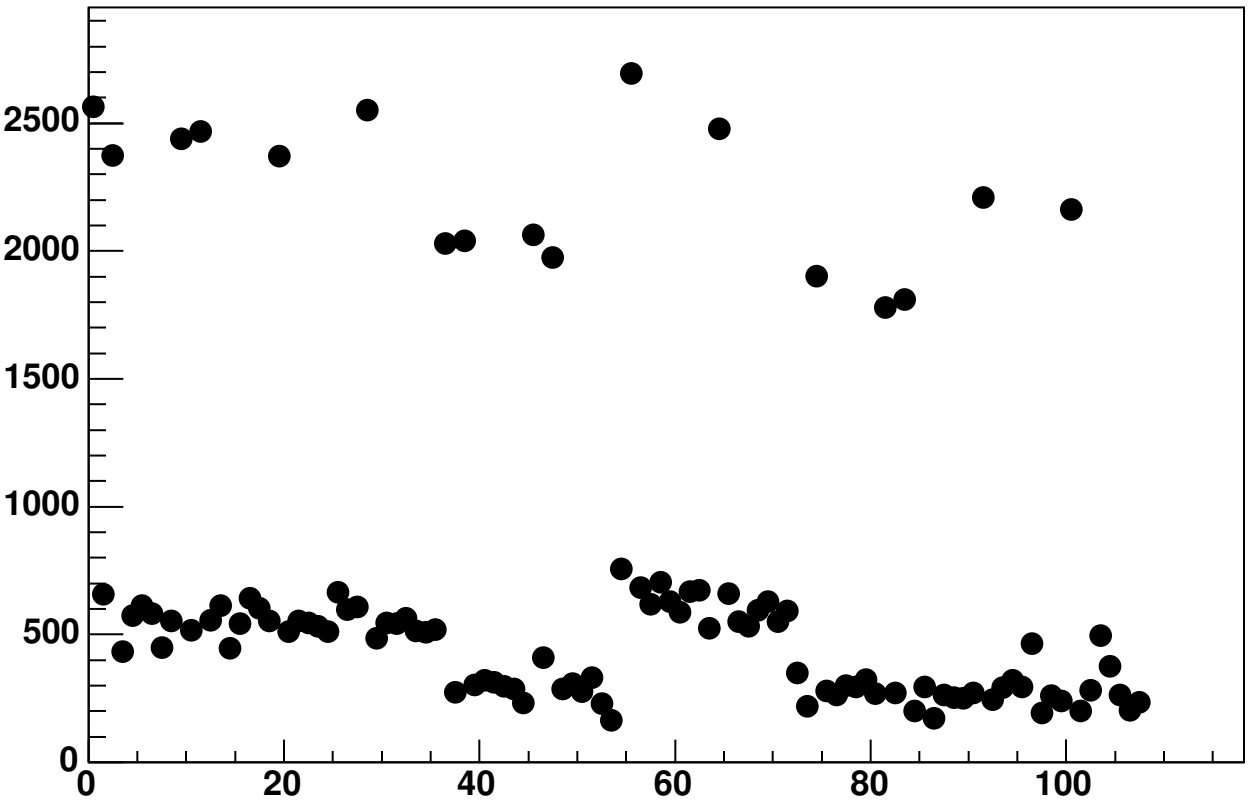
Enable 0, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



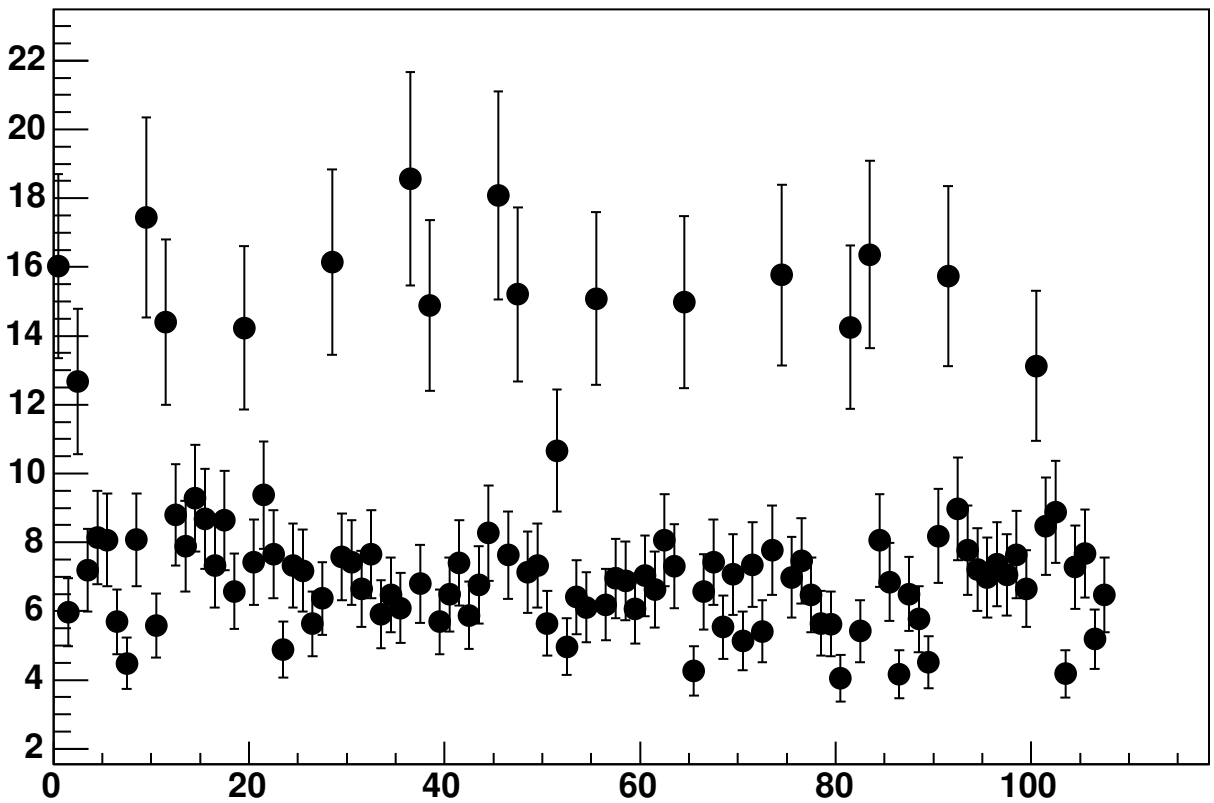
Enable 0, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



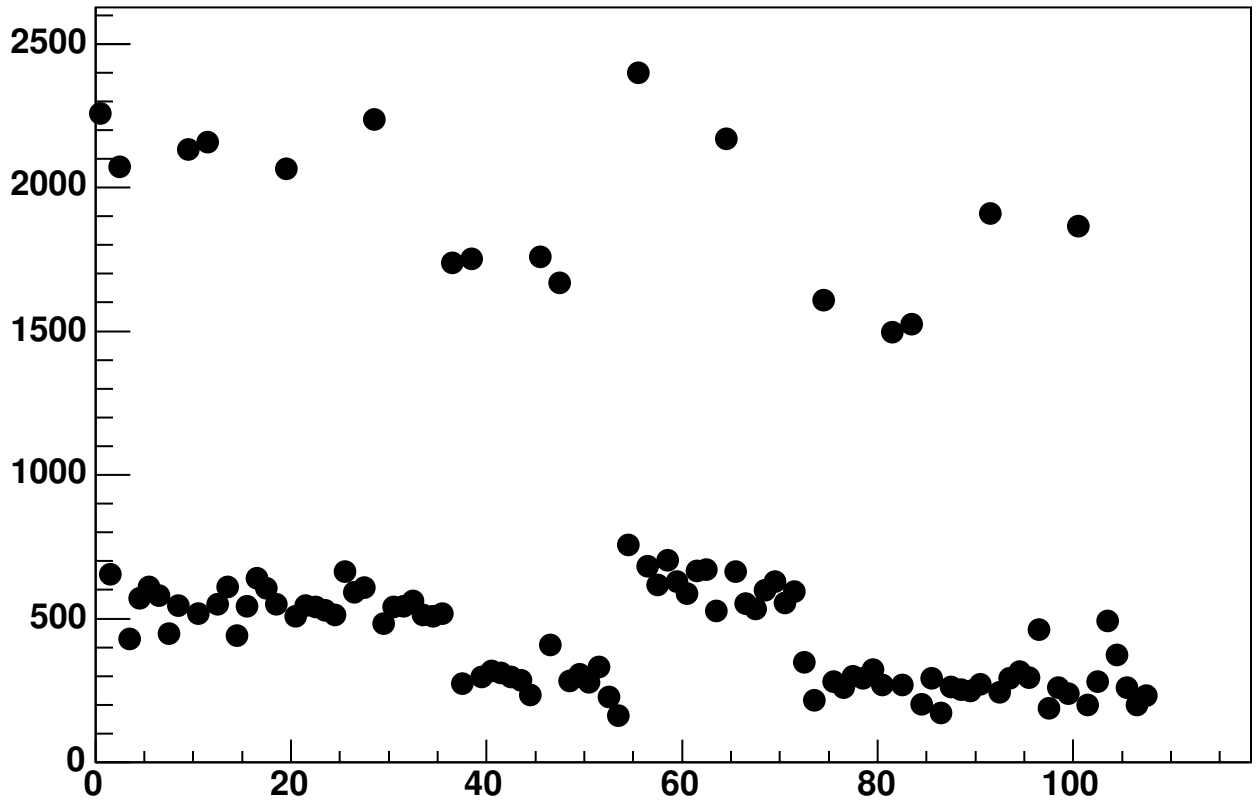
Enable 0, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



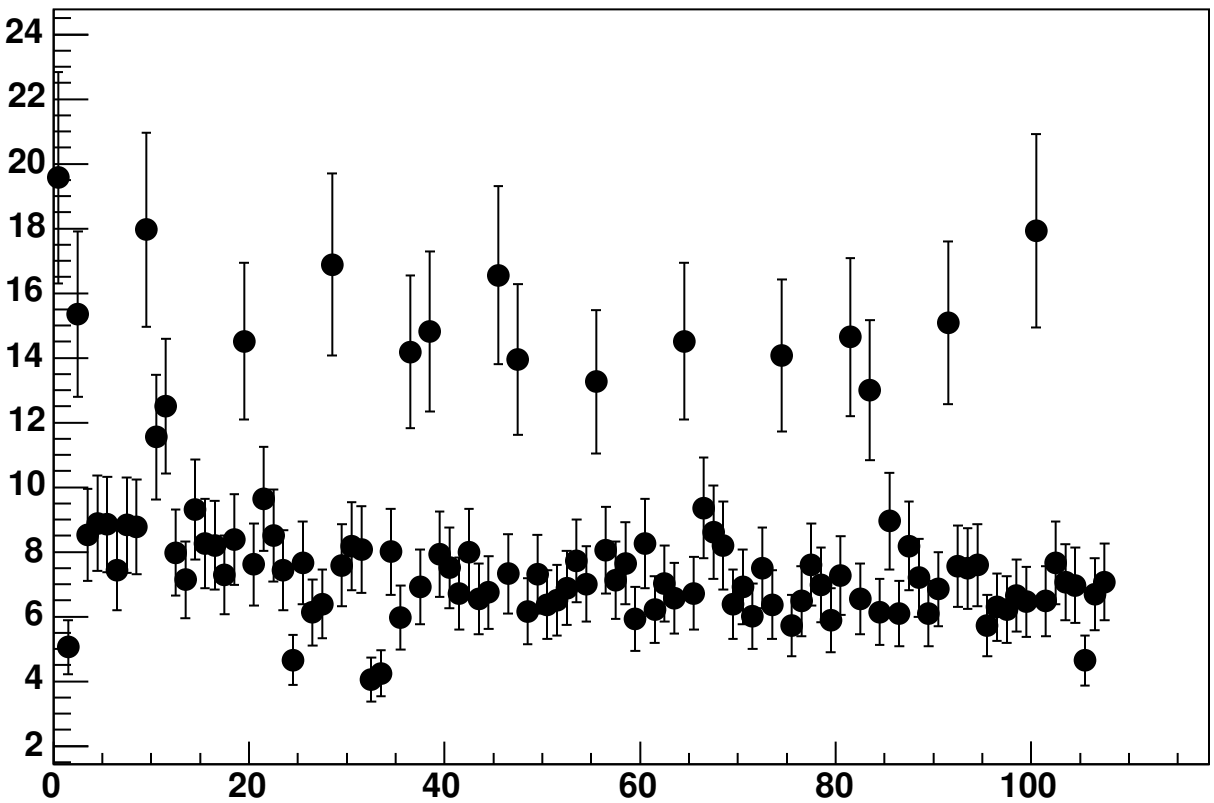
Enable 0, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



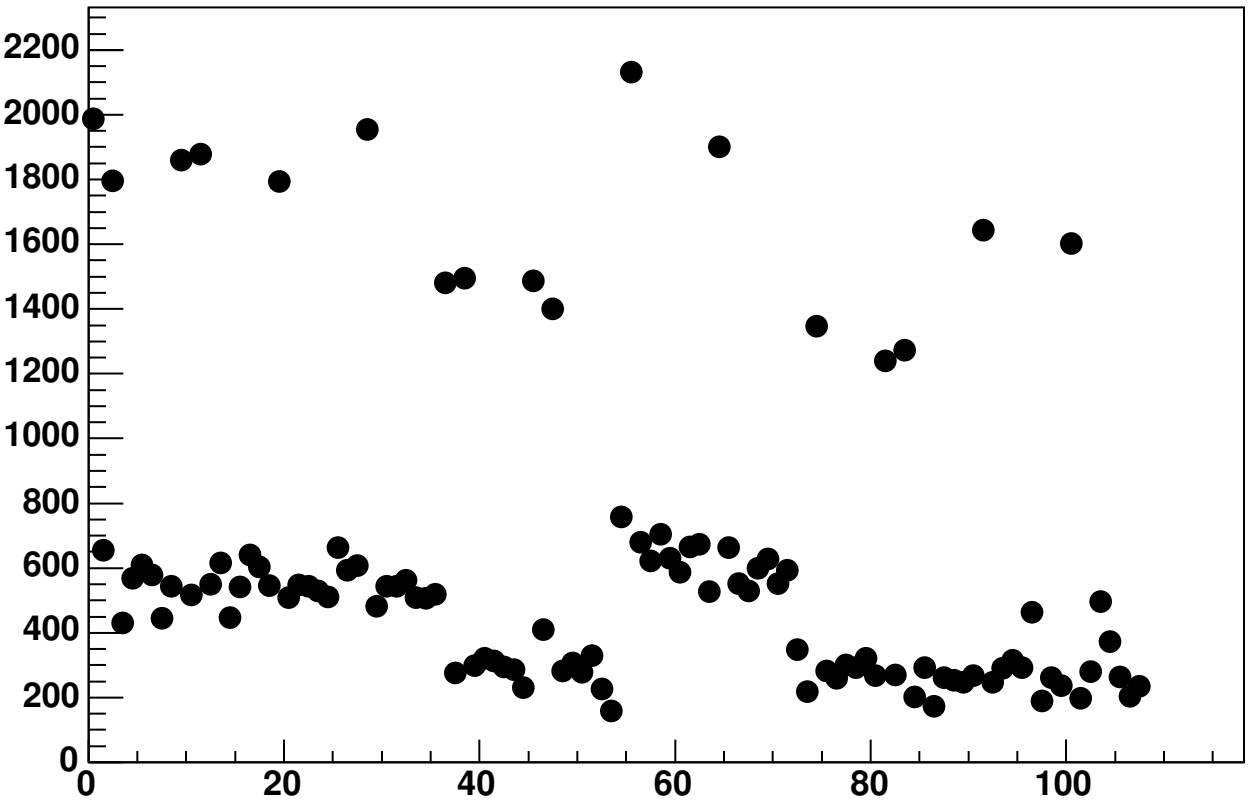
Enable 0, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



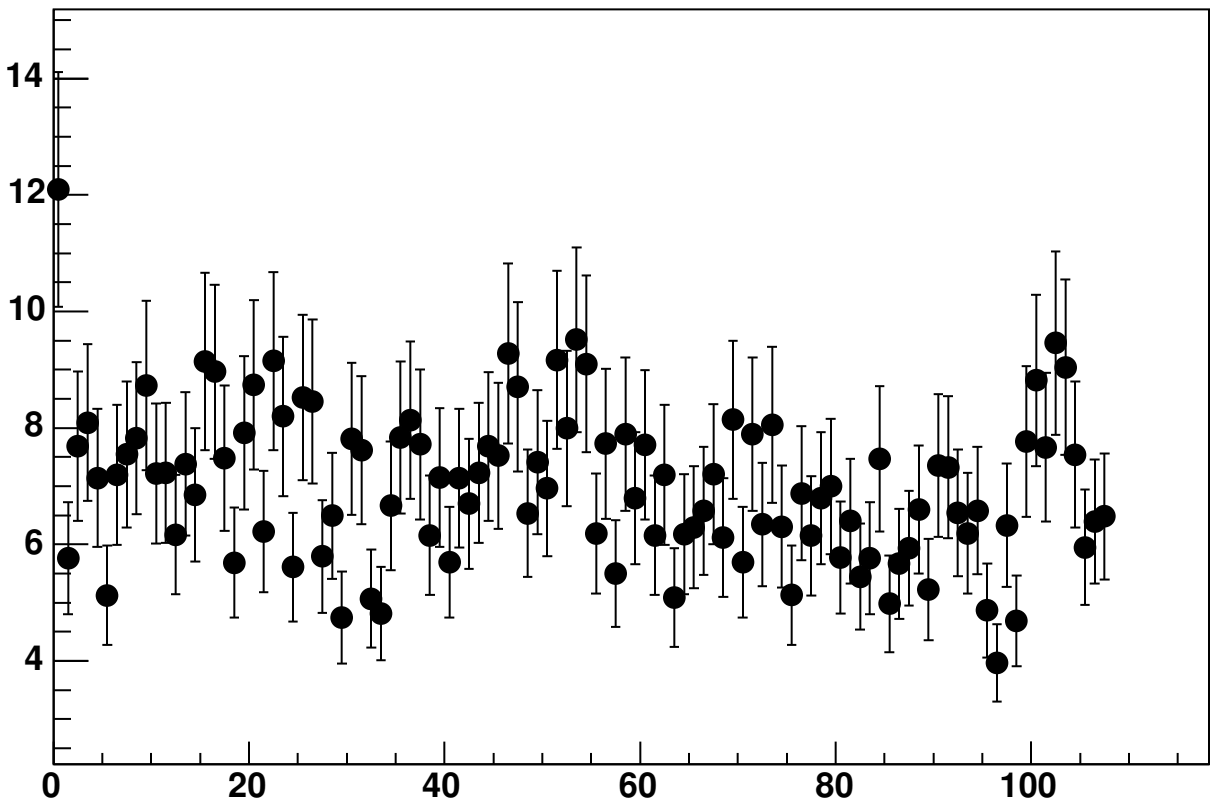
Enable 0, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



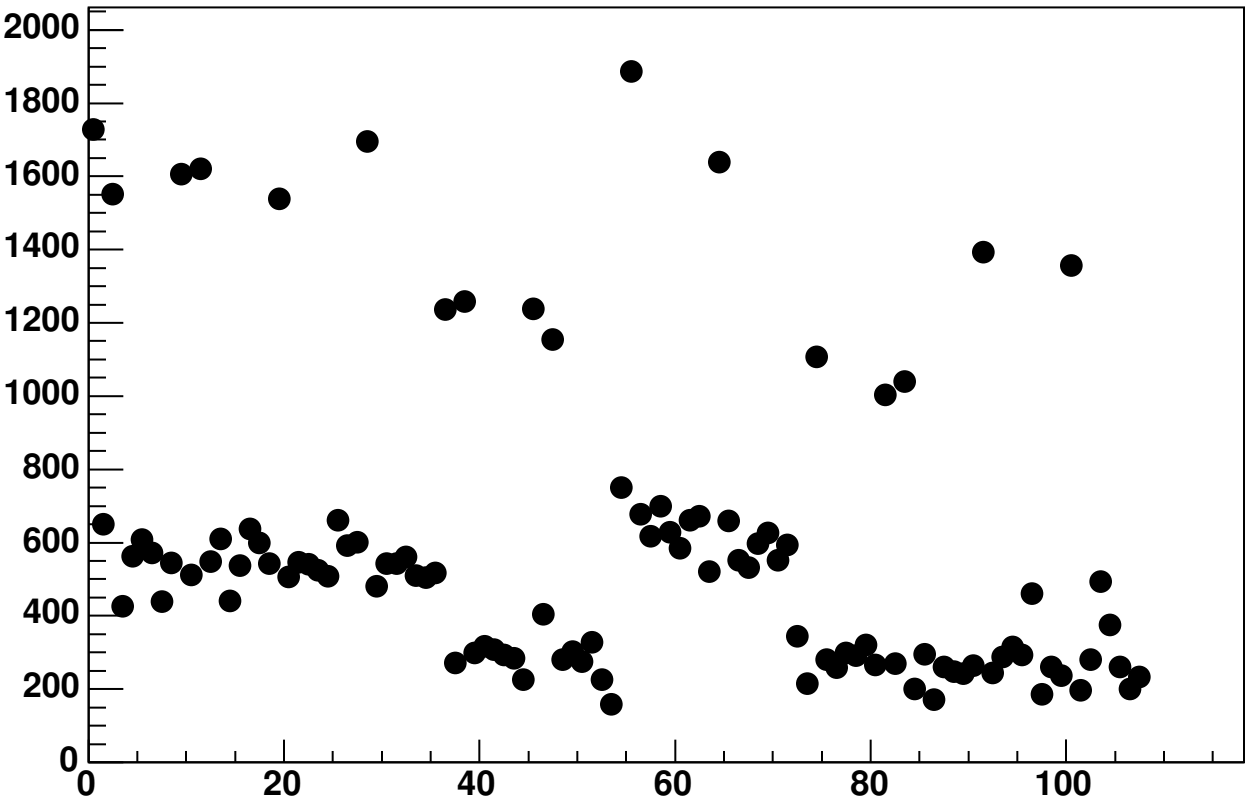
Enable 0, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



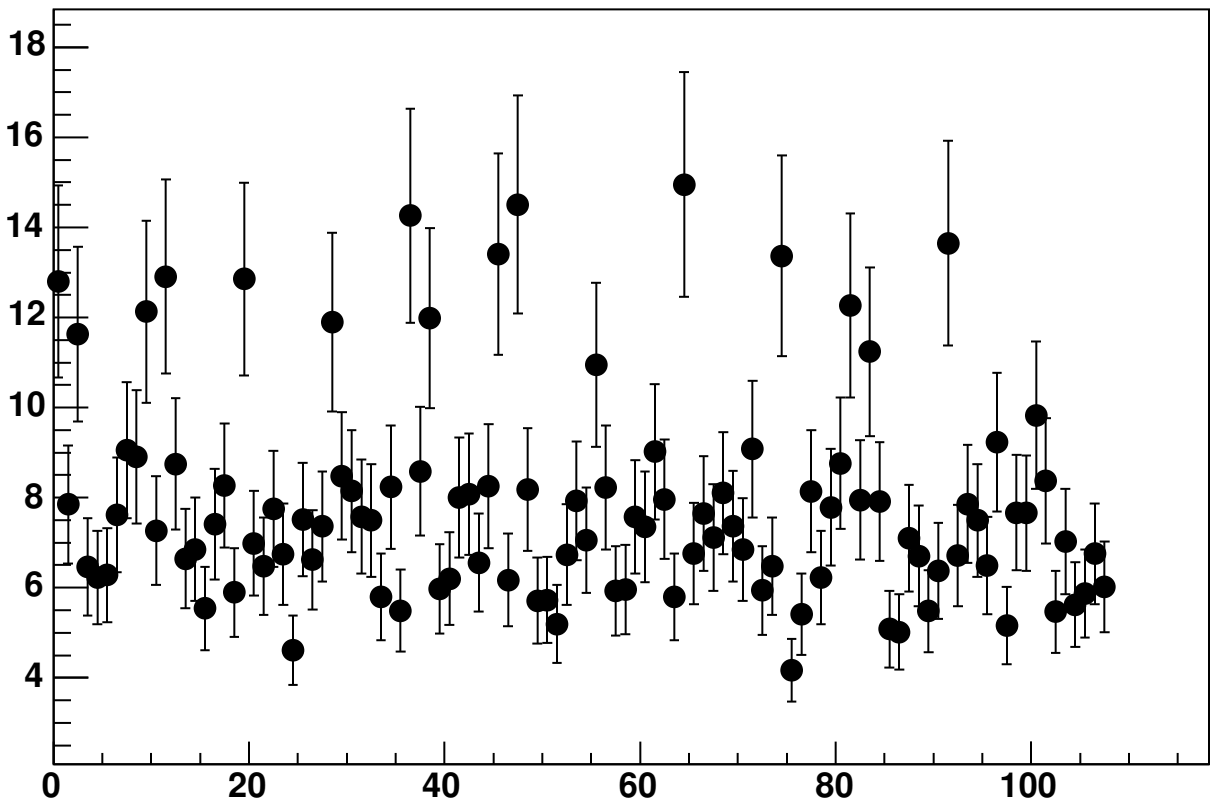
Enable 0, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



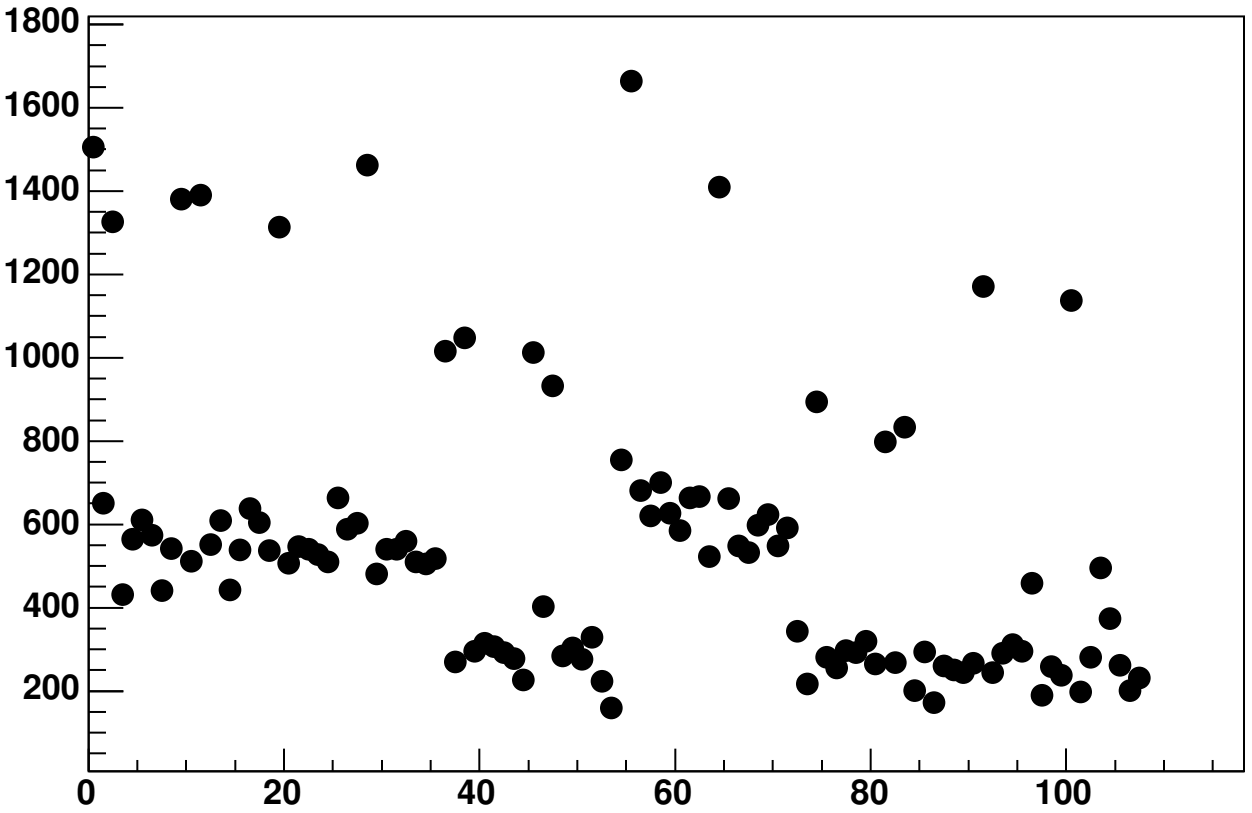
Enable 0, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



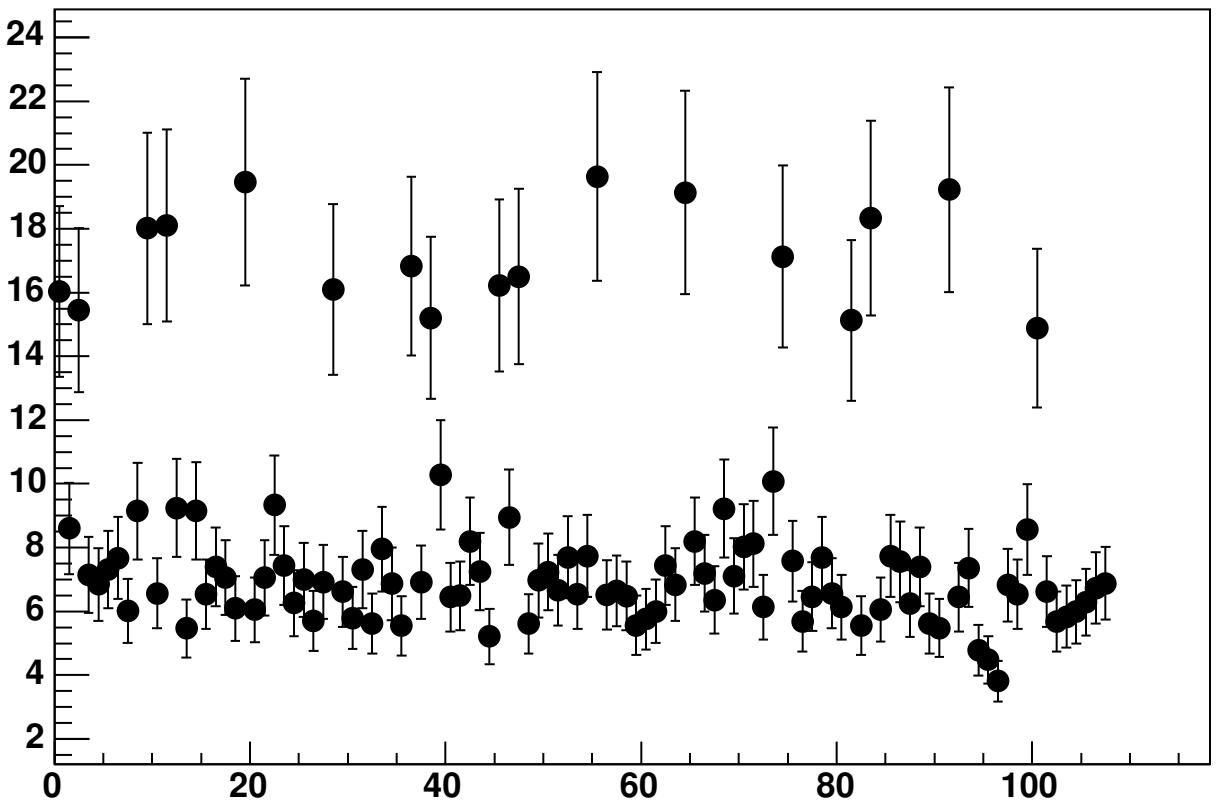
Enable 0, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



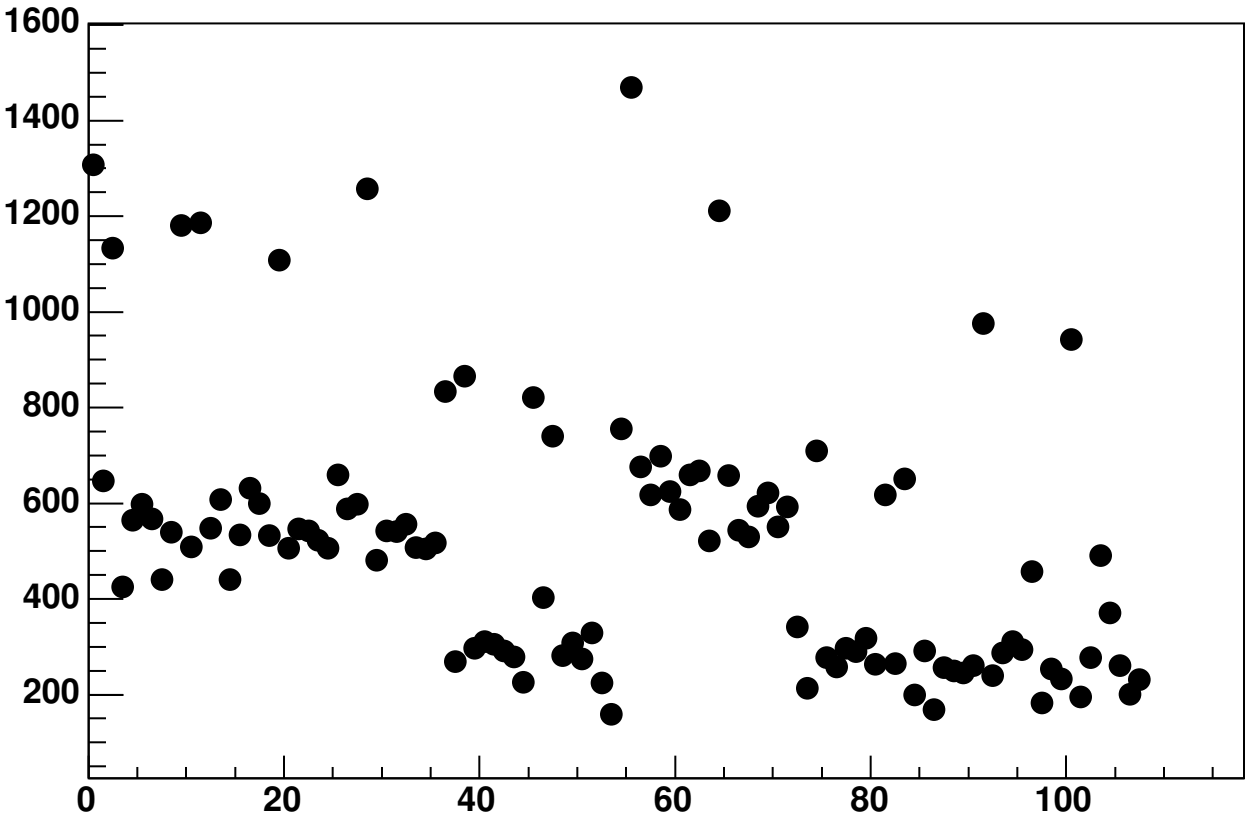
Enable 0, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



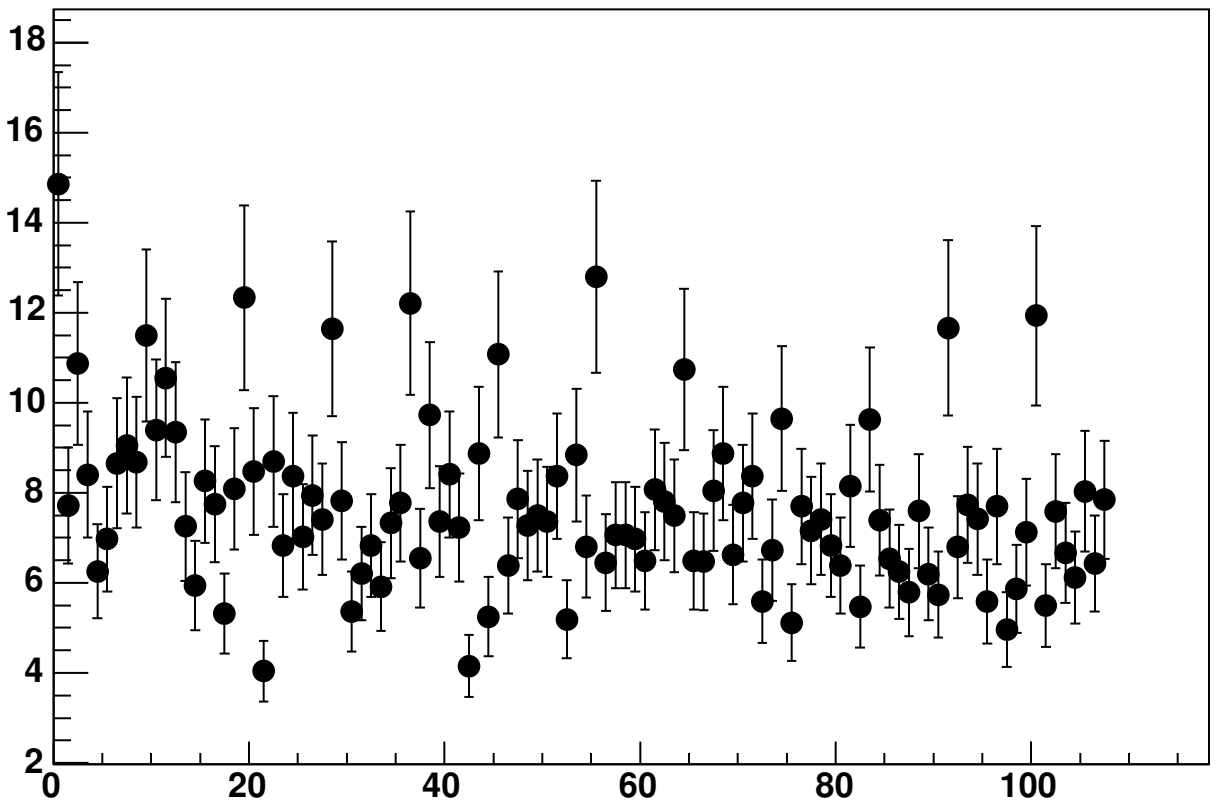
Enable 0, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



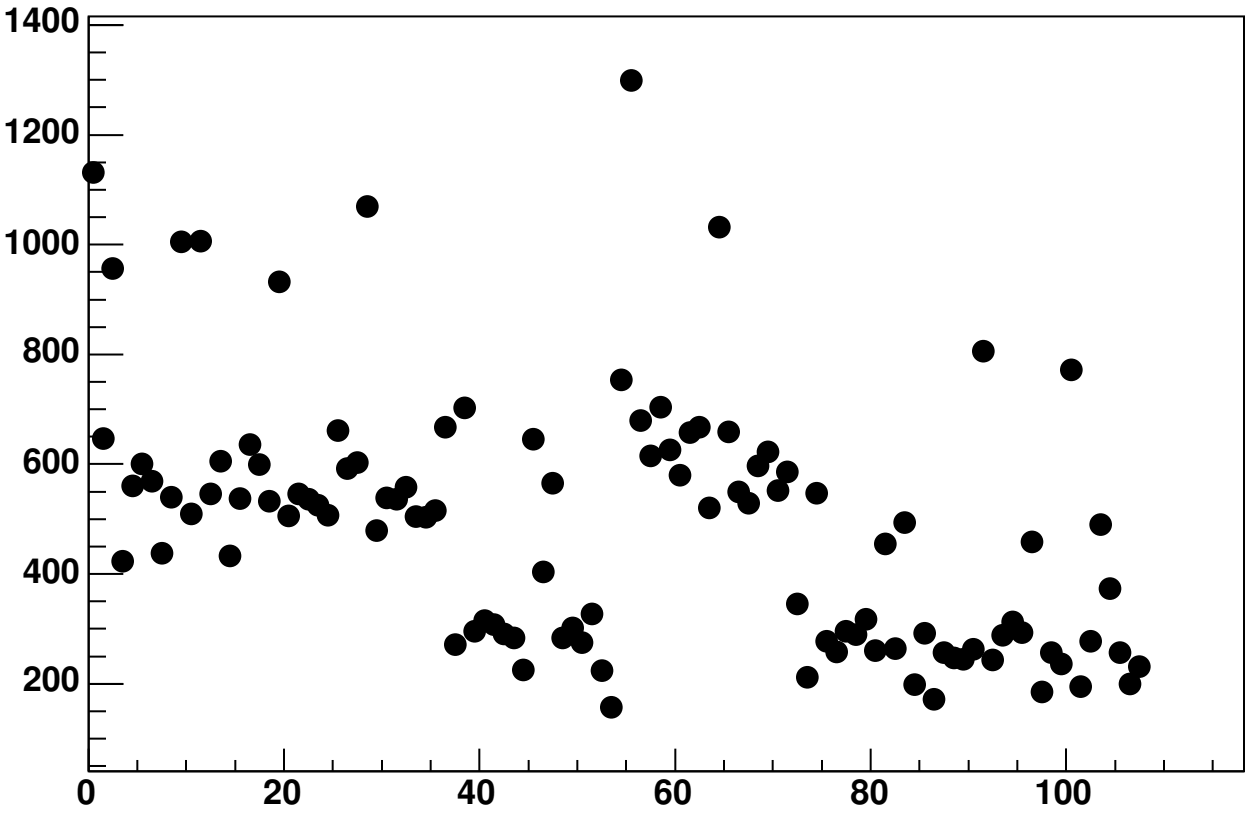
Enable 0, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



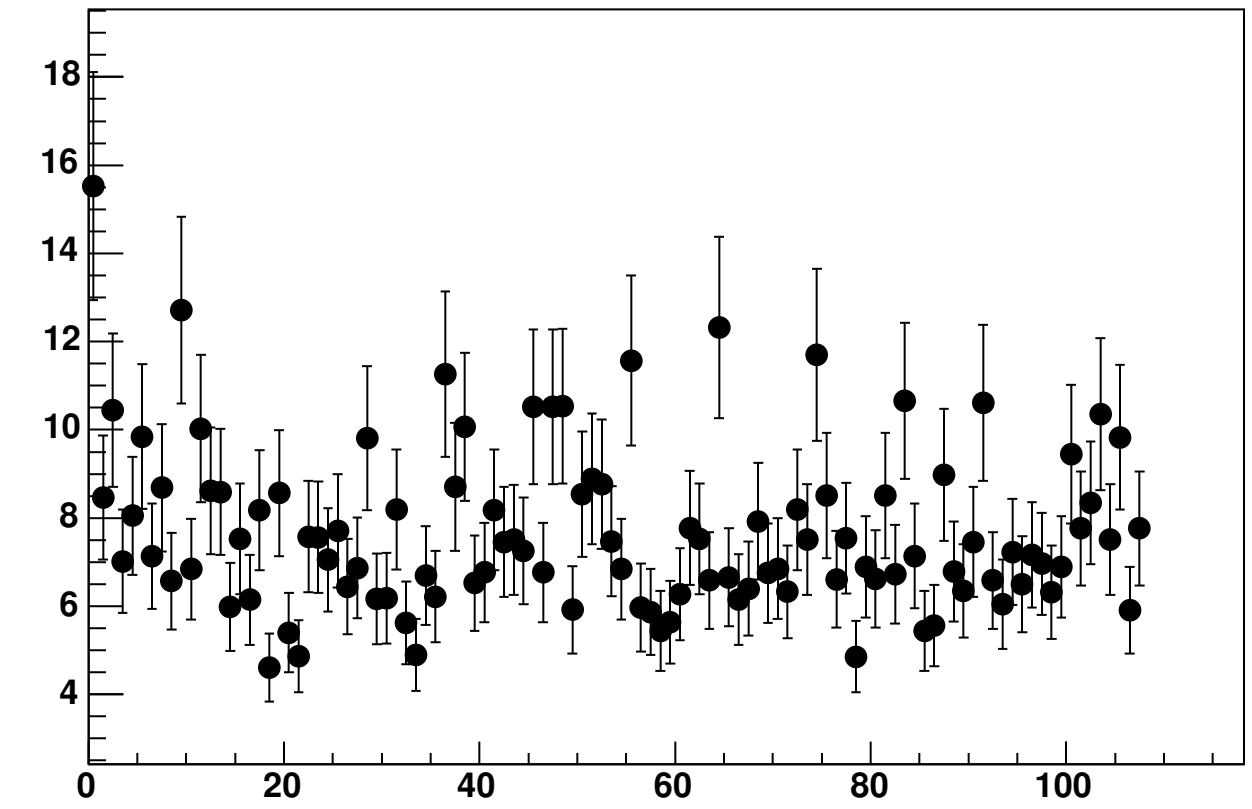
Enable 0, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



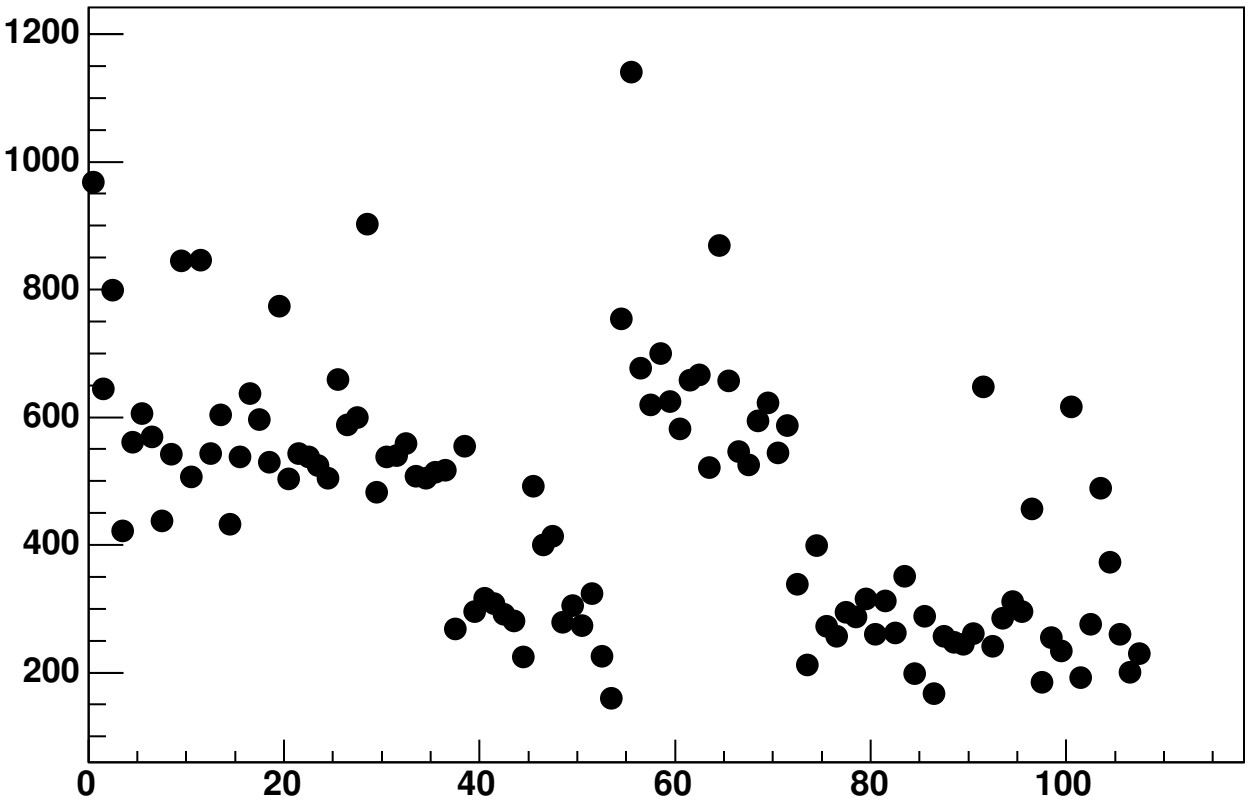
Enable 0, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



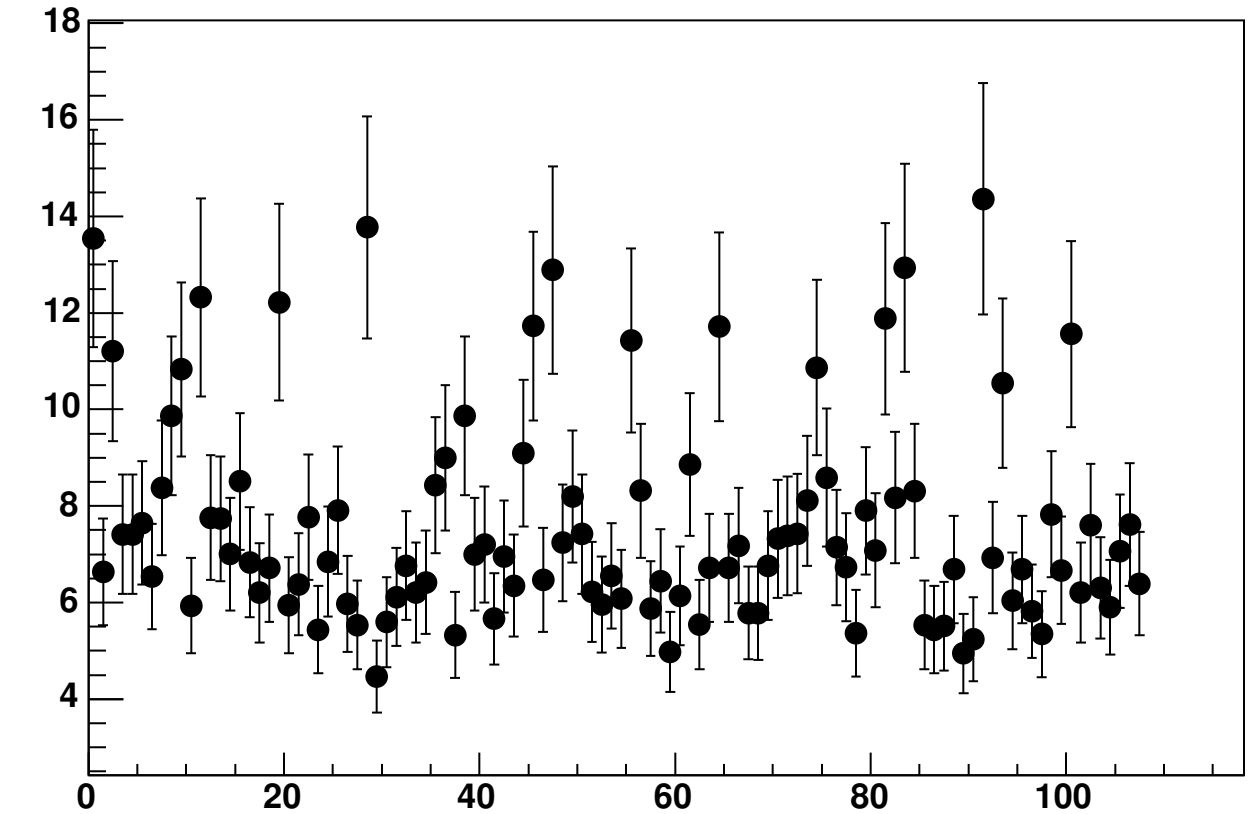
Enable 0, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



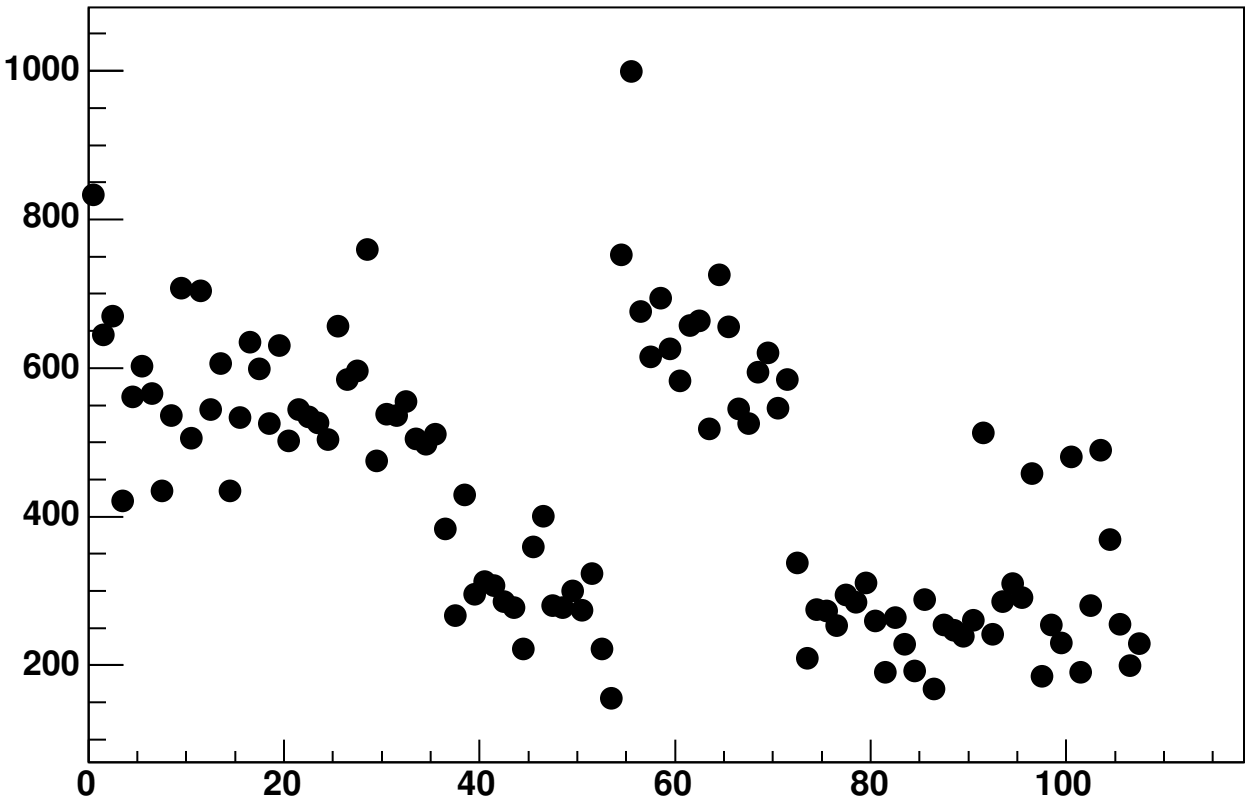
Enable 0, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



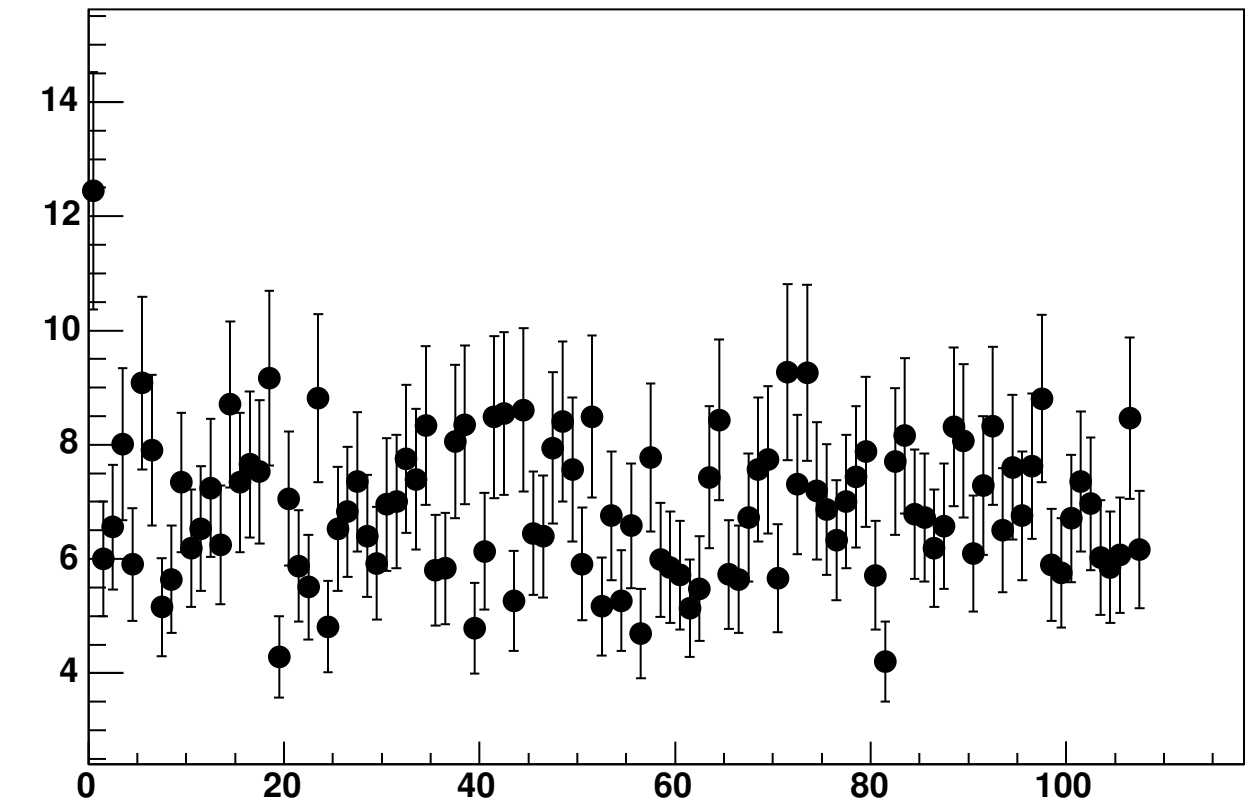
Enable 0, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



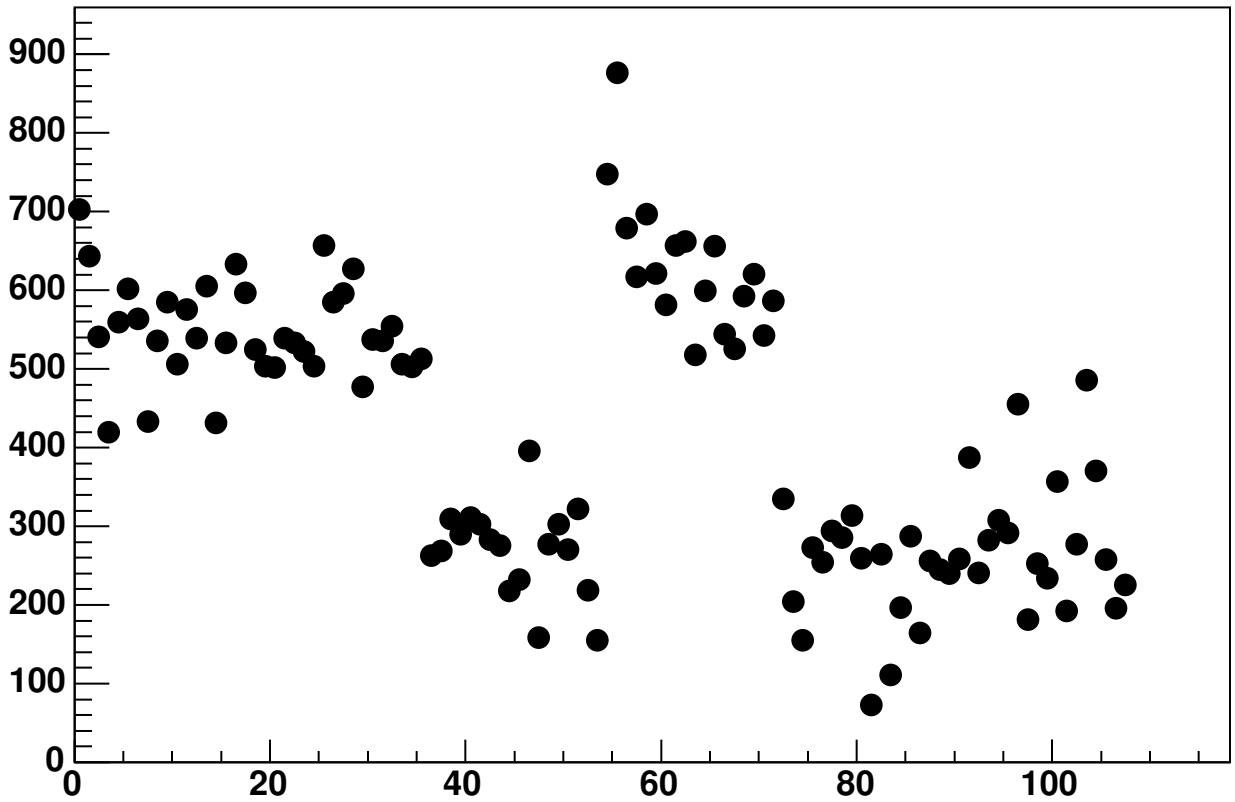
Enable 0, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



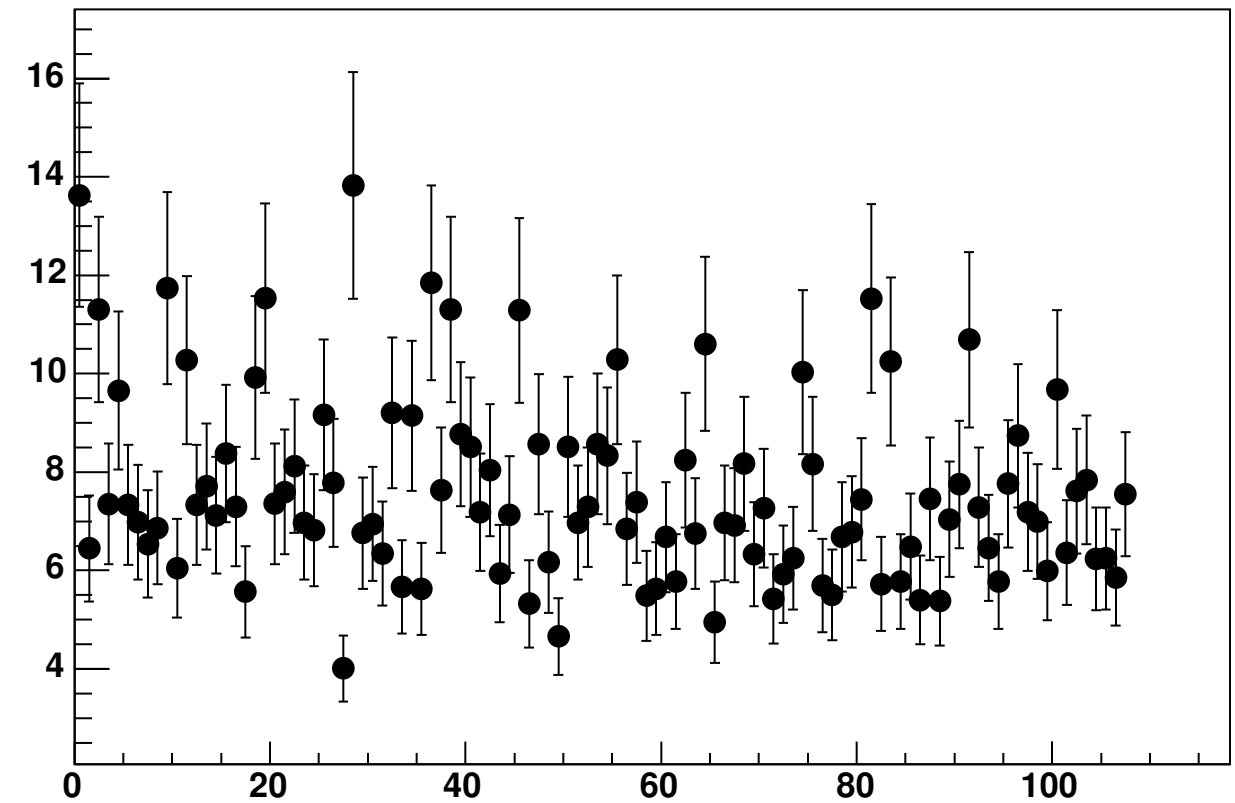
Enable 0, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



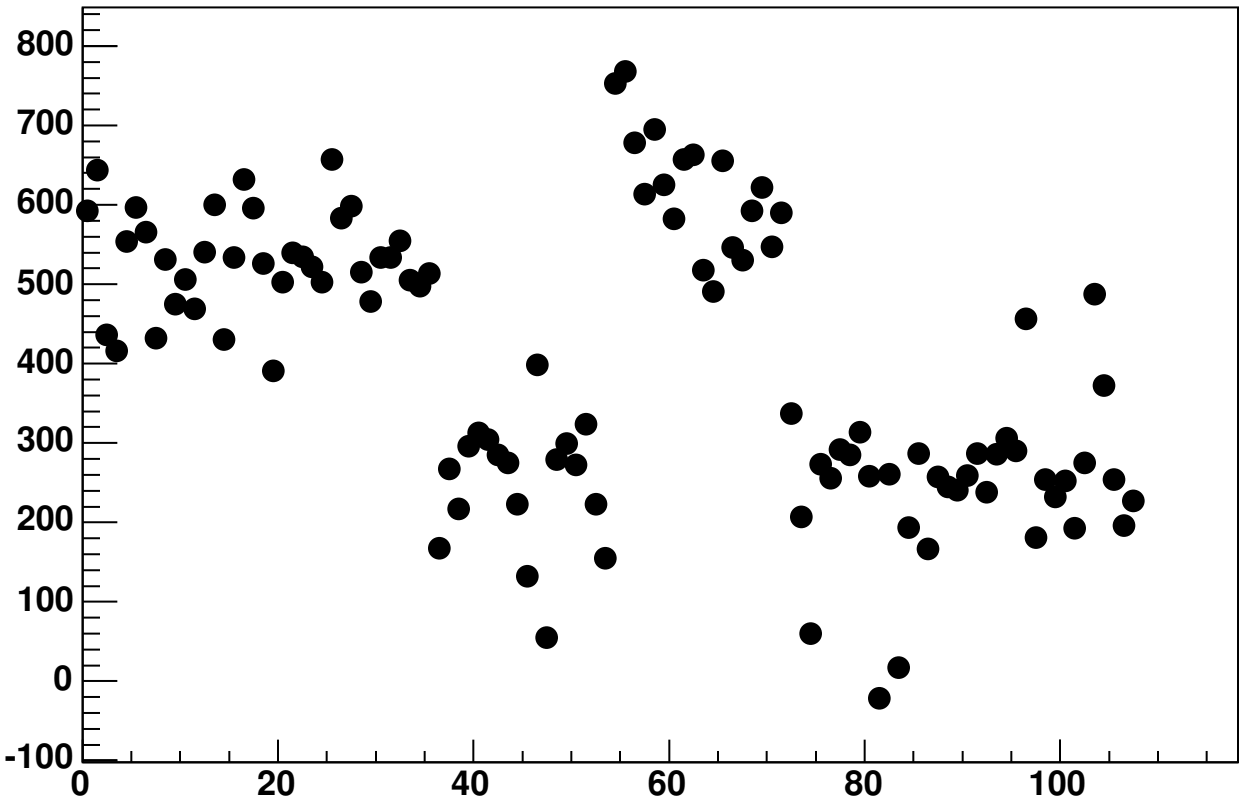
Enable 0, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



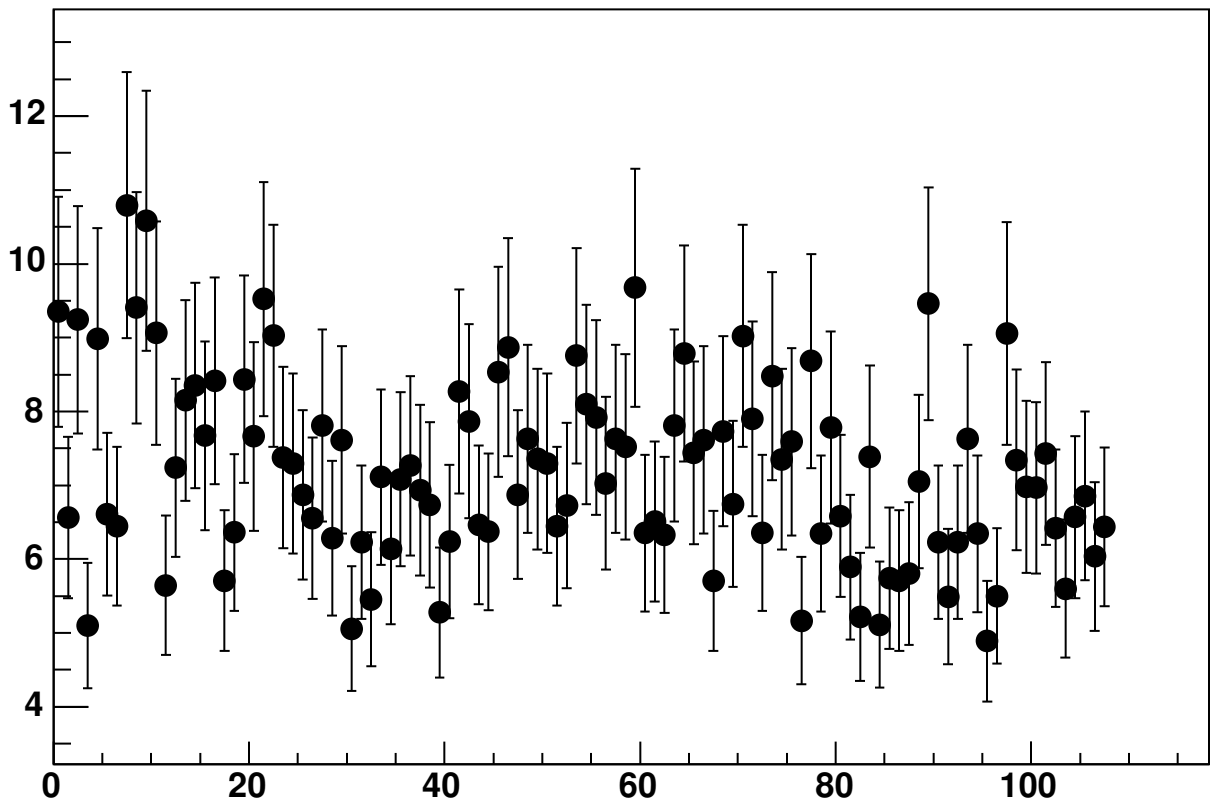
Enable 0, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



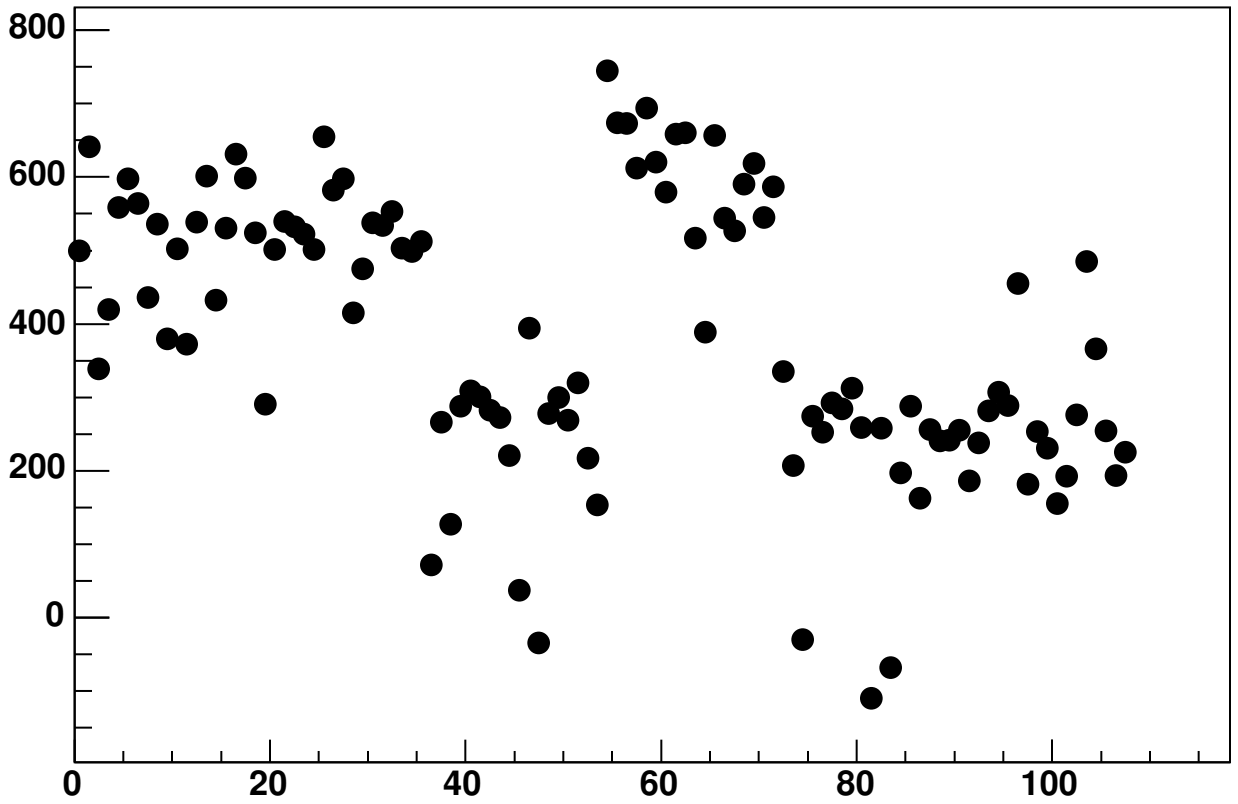
Enable 0, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



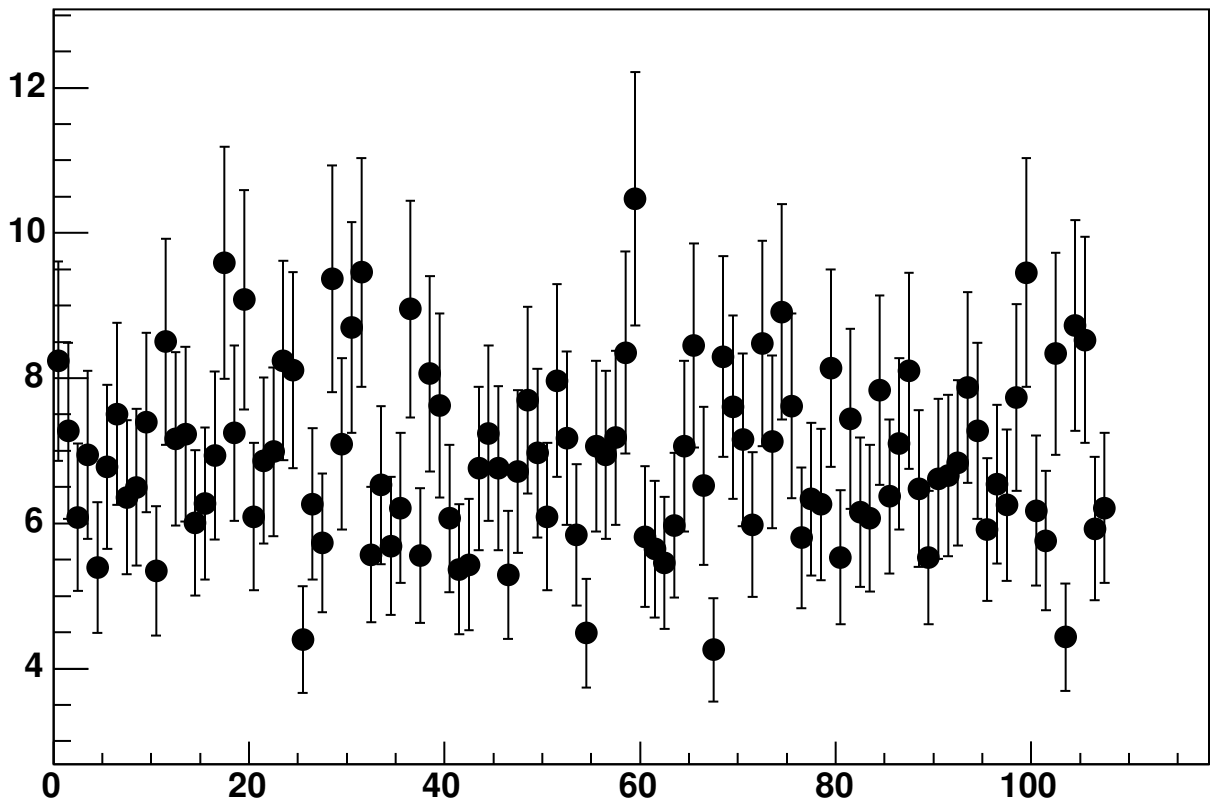
Enable 0, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



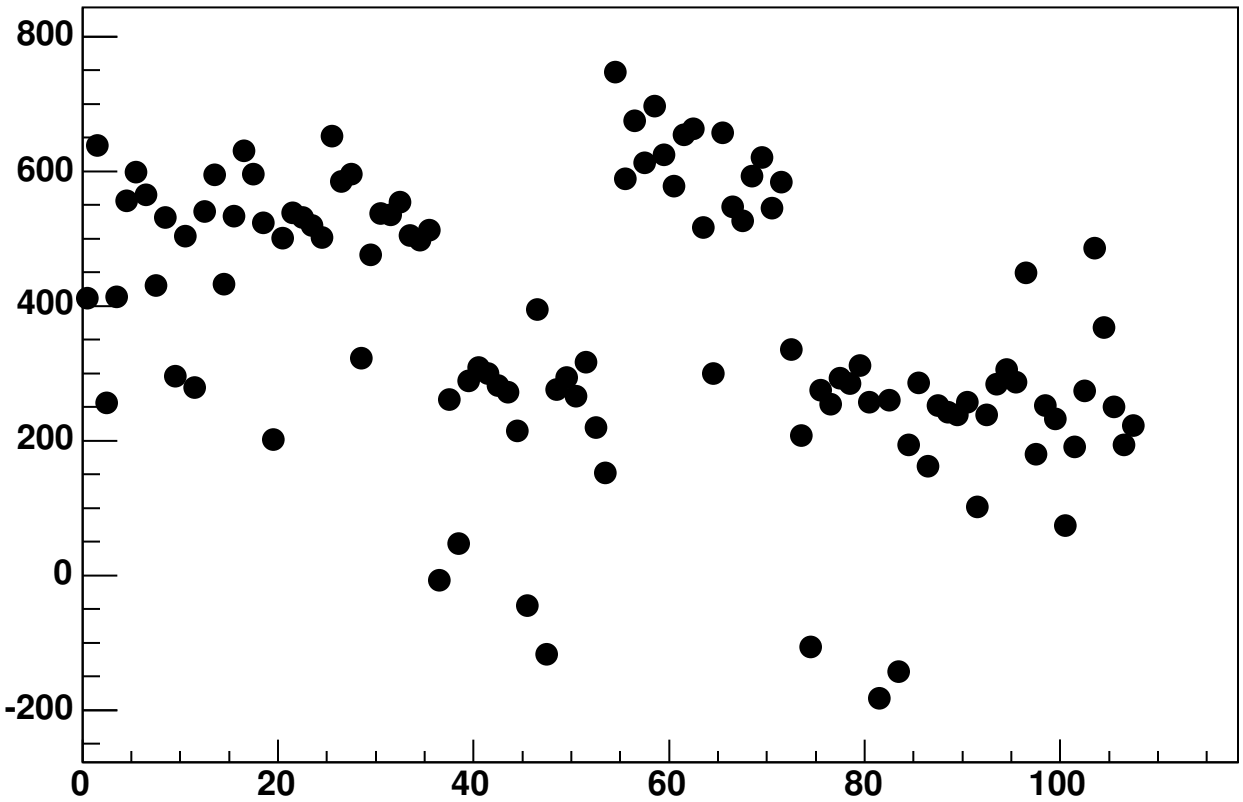
Enable 0, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



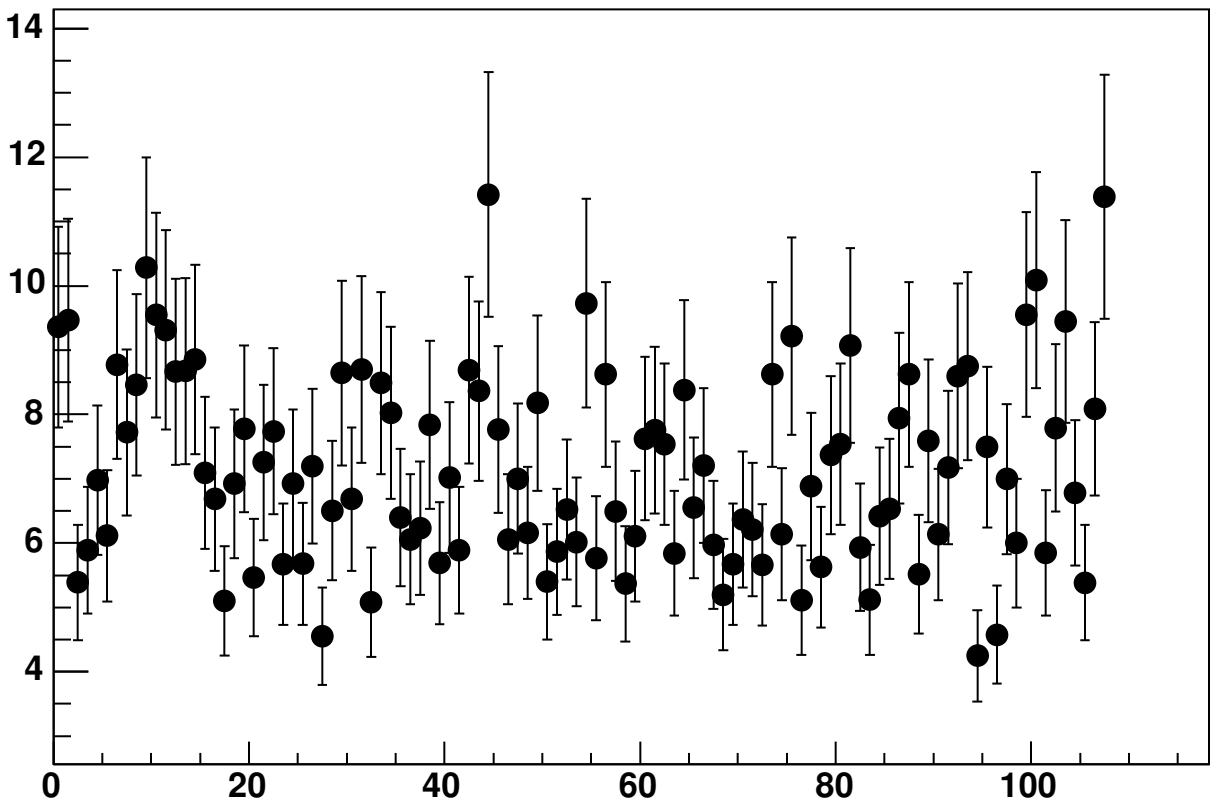
Enable 0, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



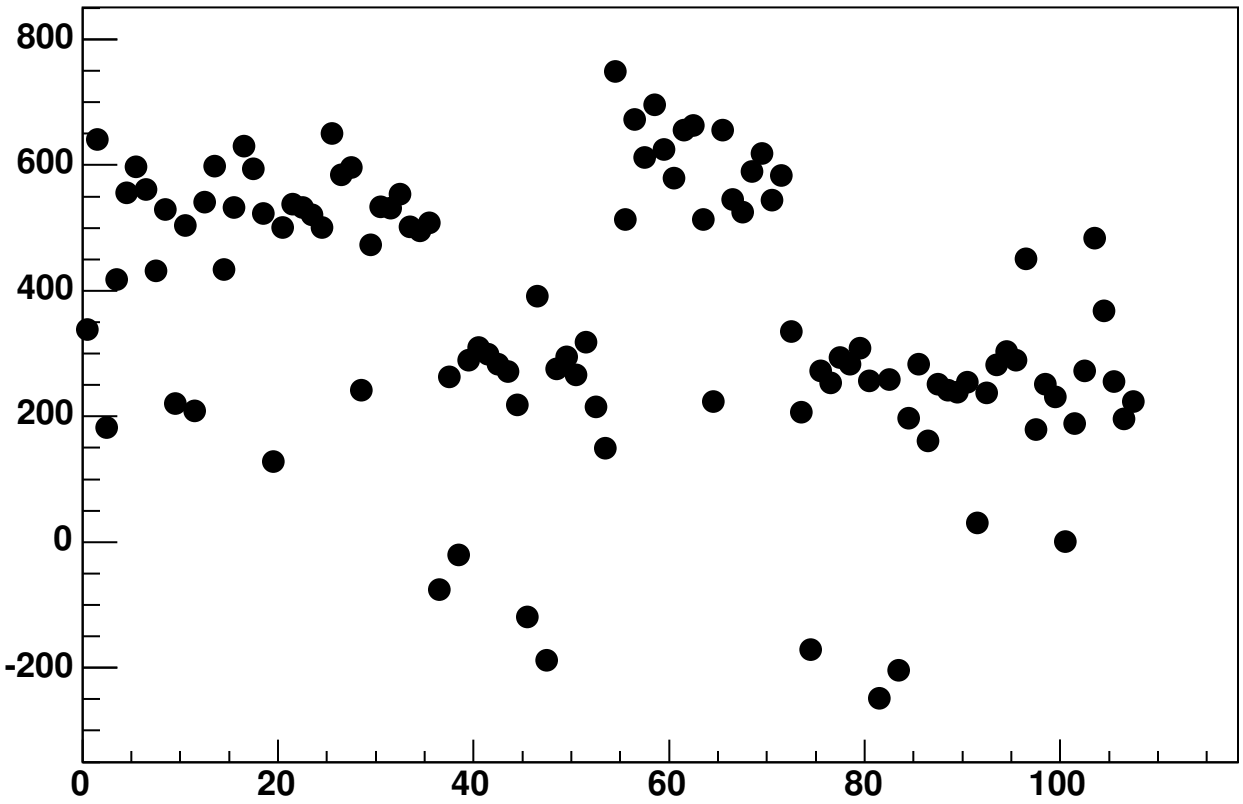
Enable 0, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



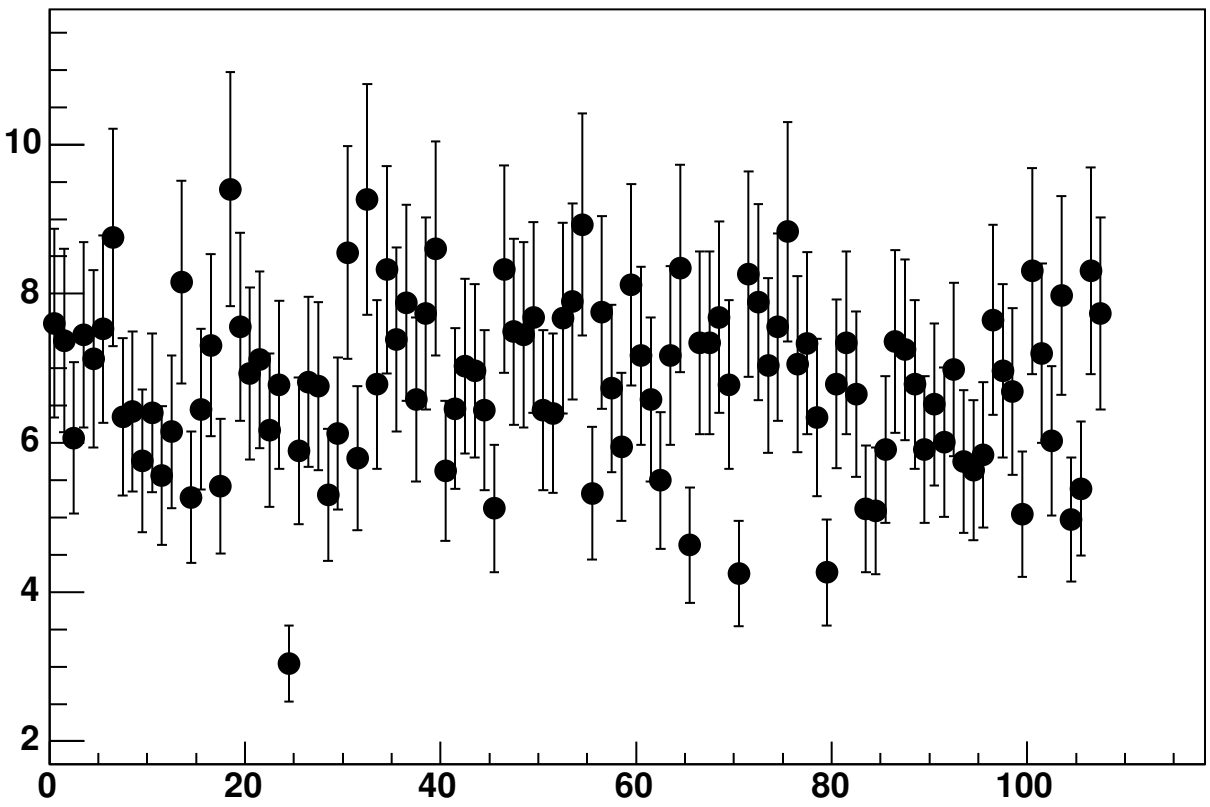
Enable 0, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



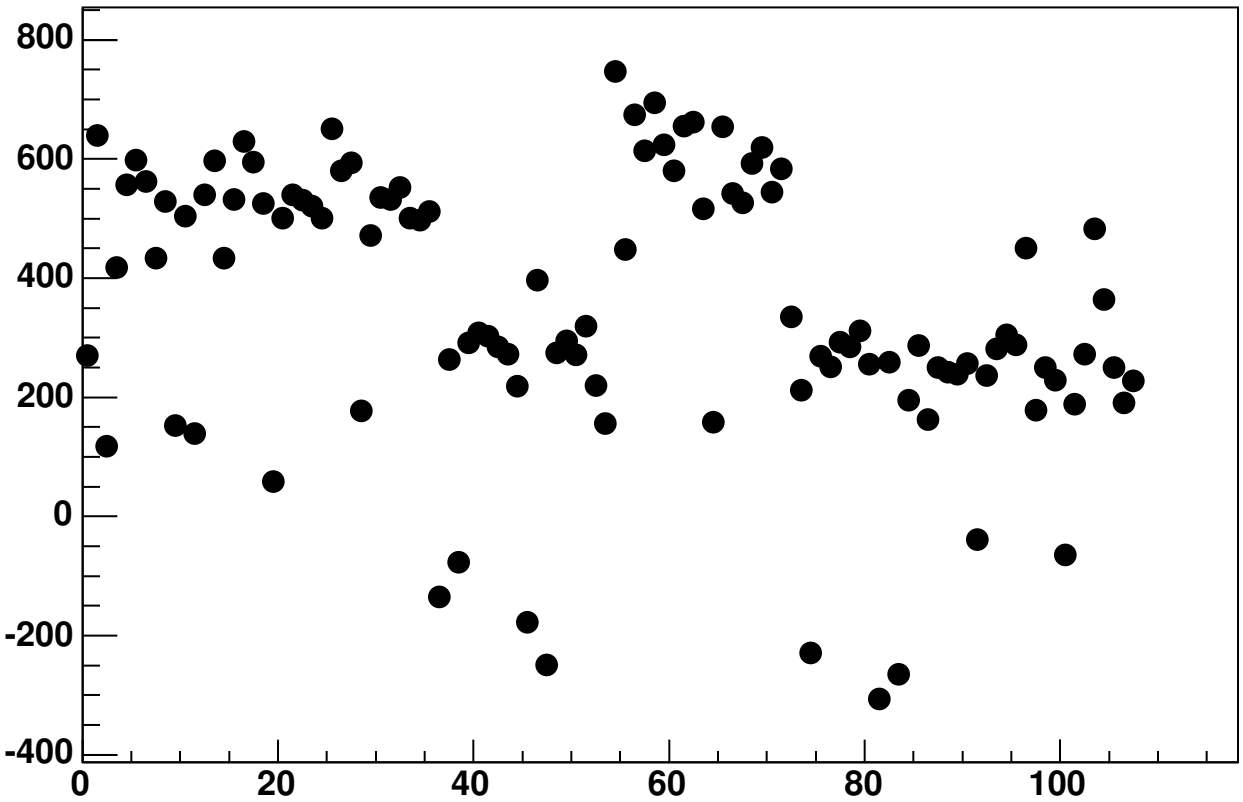
Enable 0, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



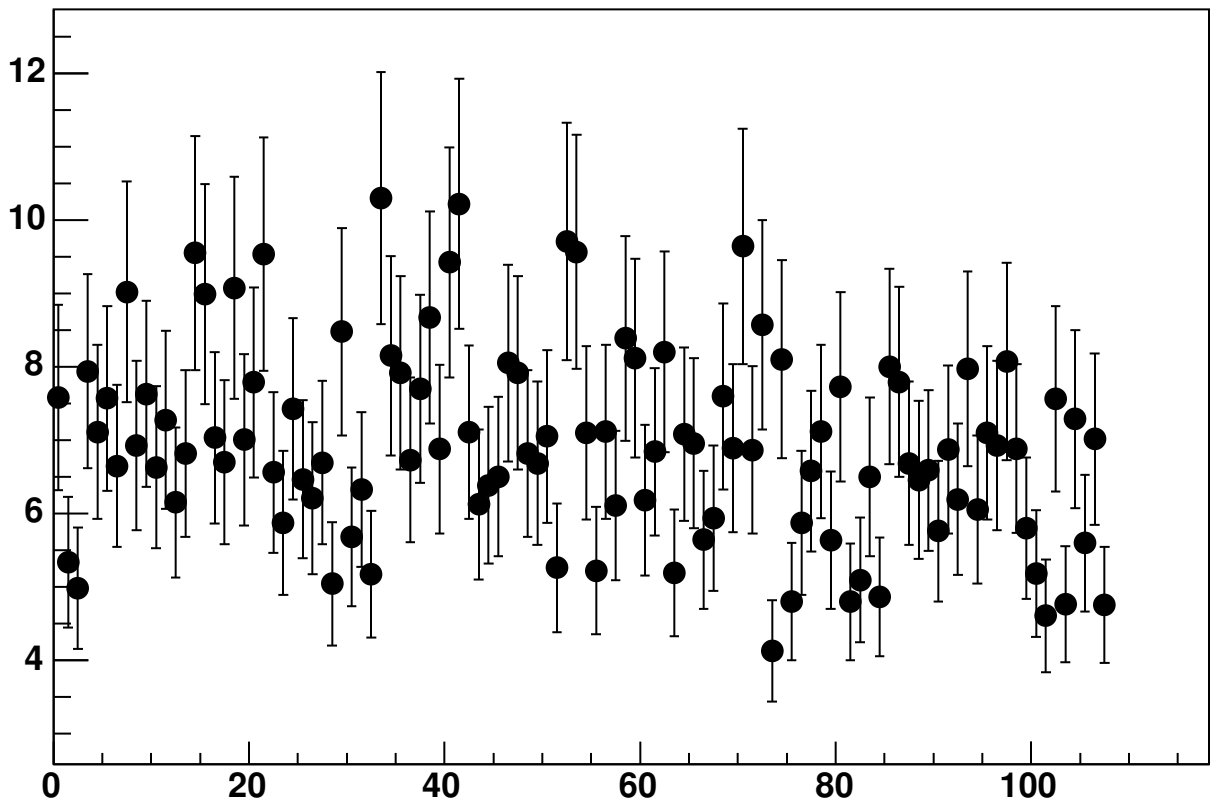
Enable 0, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



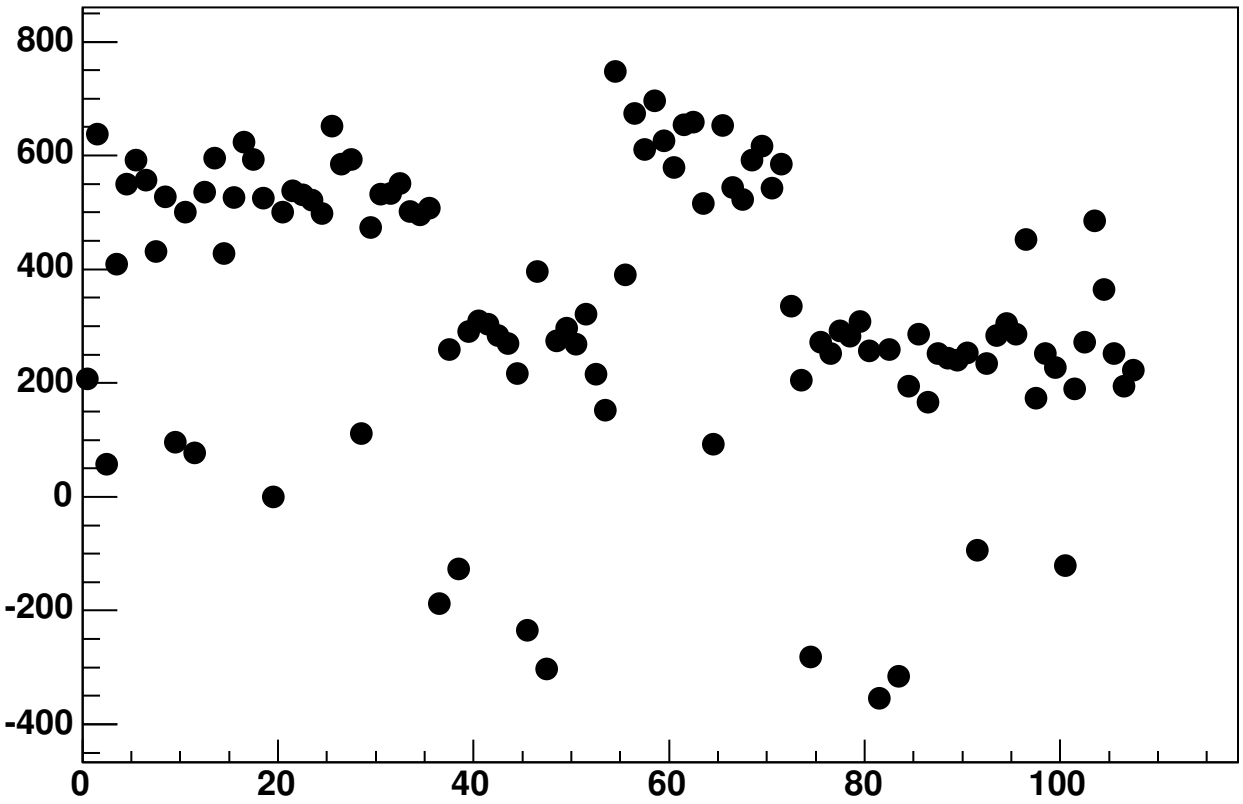
Enable 0, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



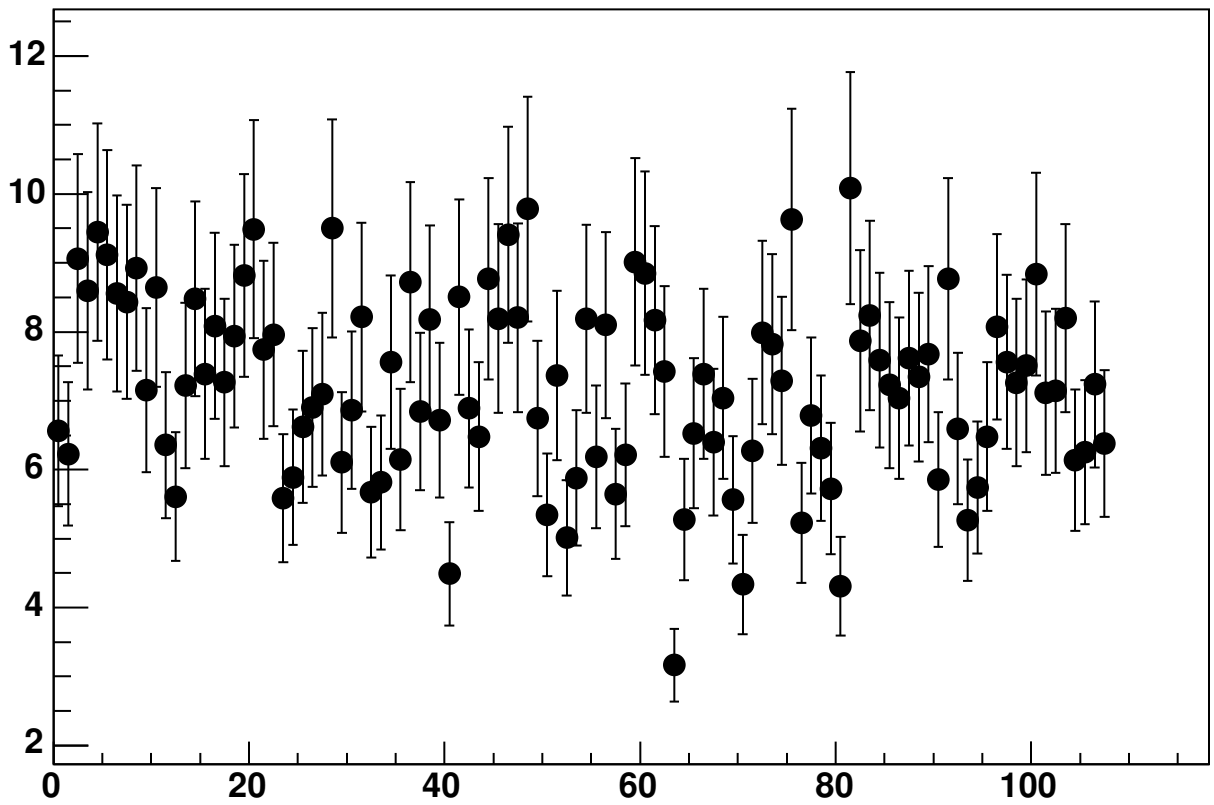
Enable 0, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



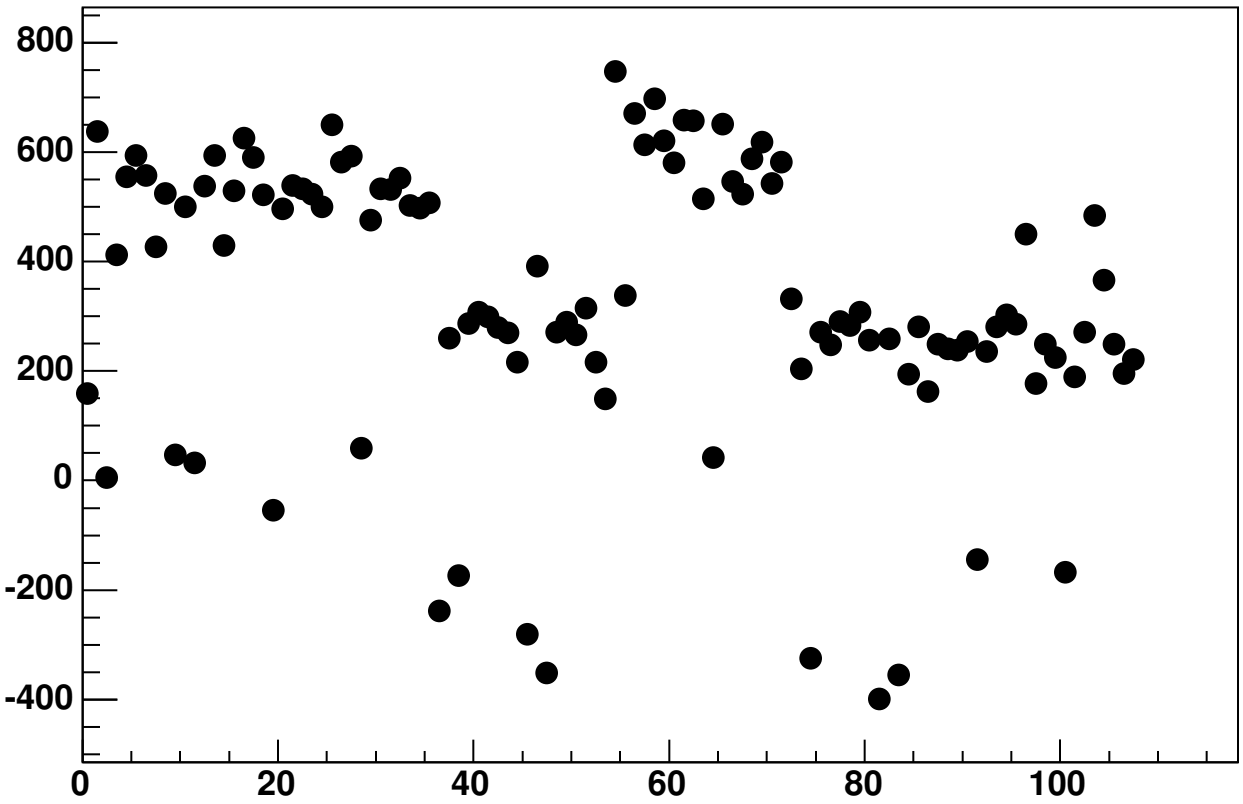
Enable 0, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



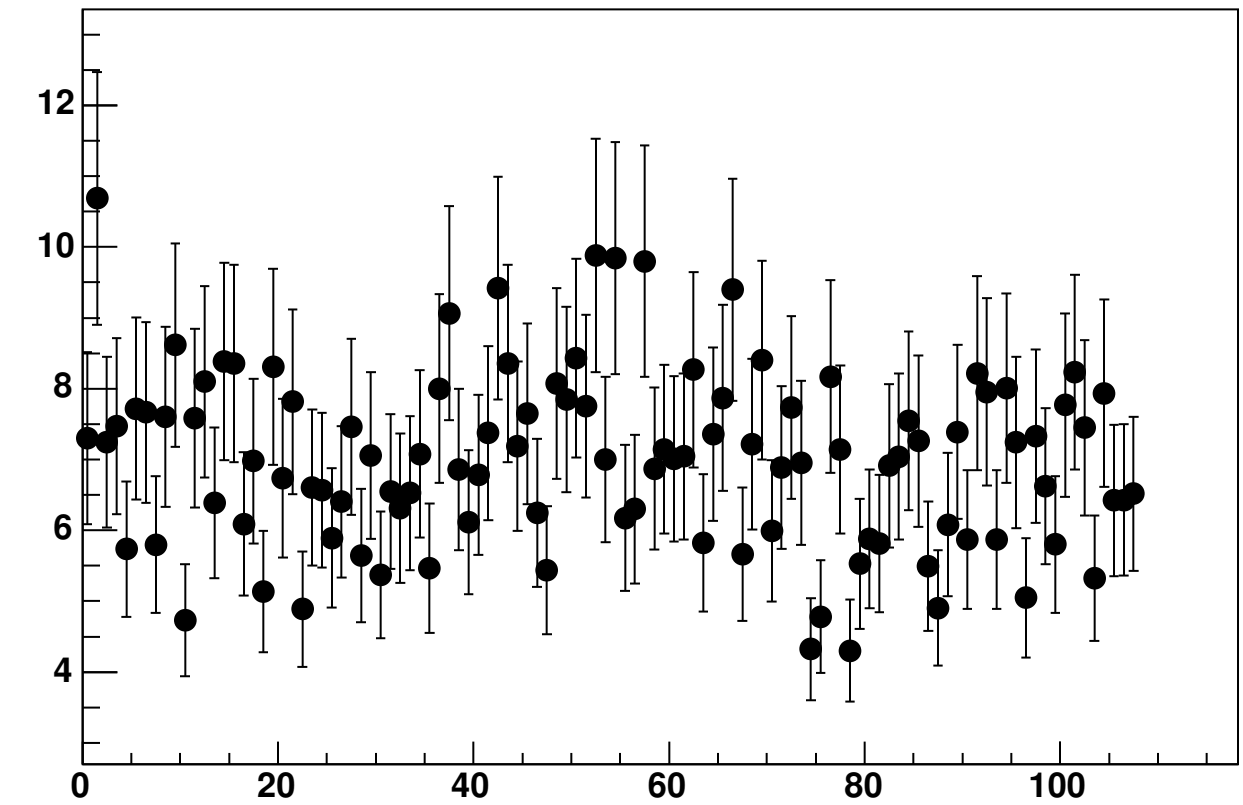
Enable 0, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



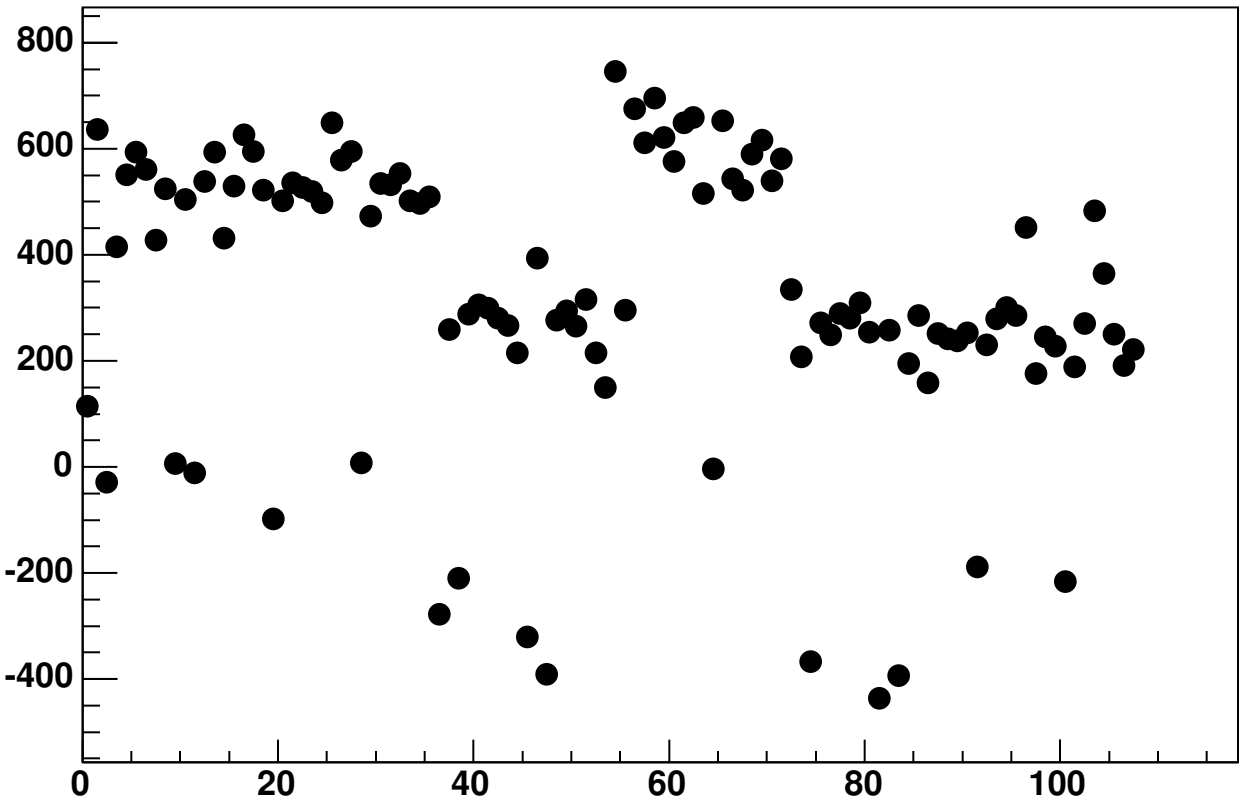
Enable 0, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



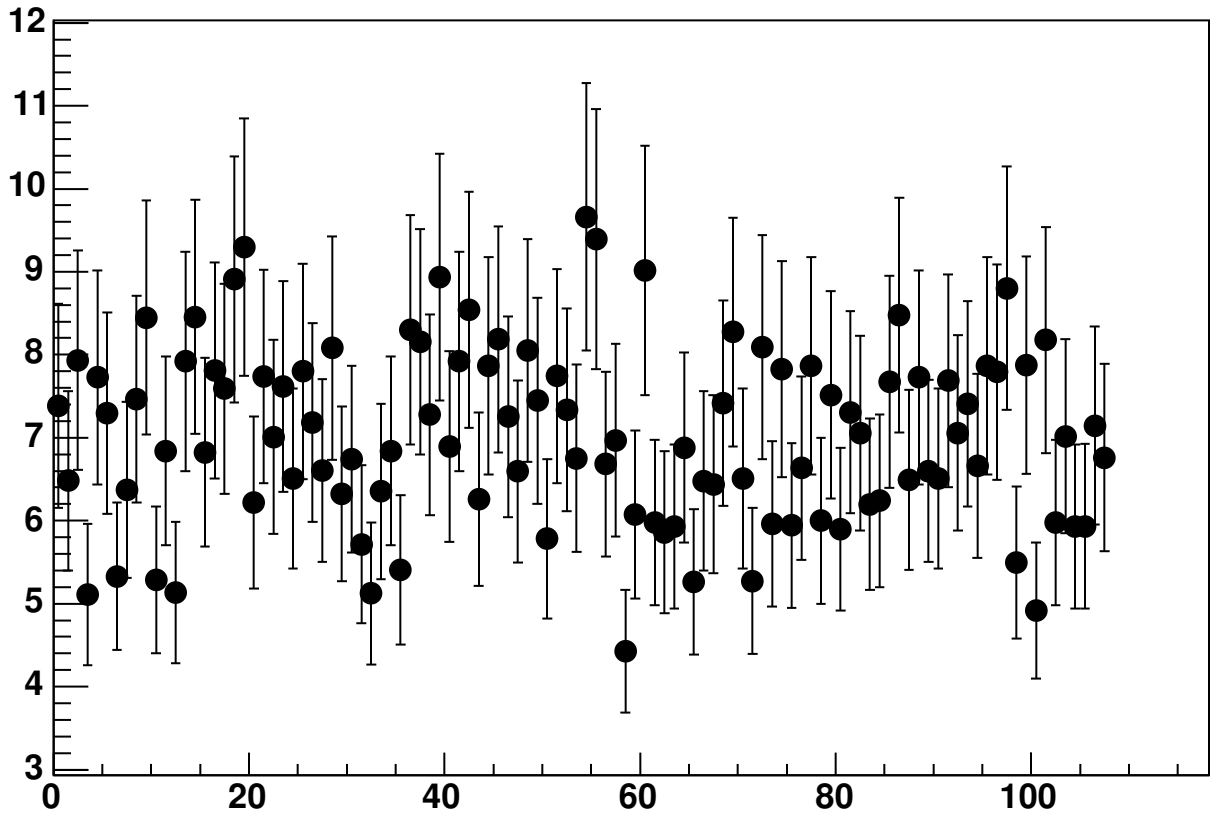
Enable 0, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



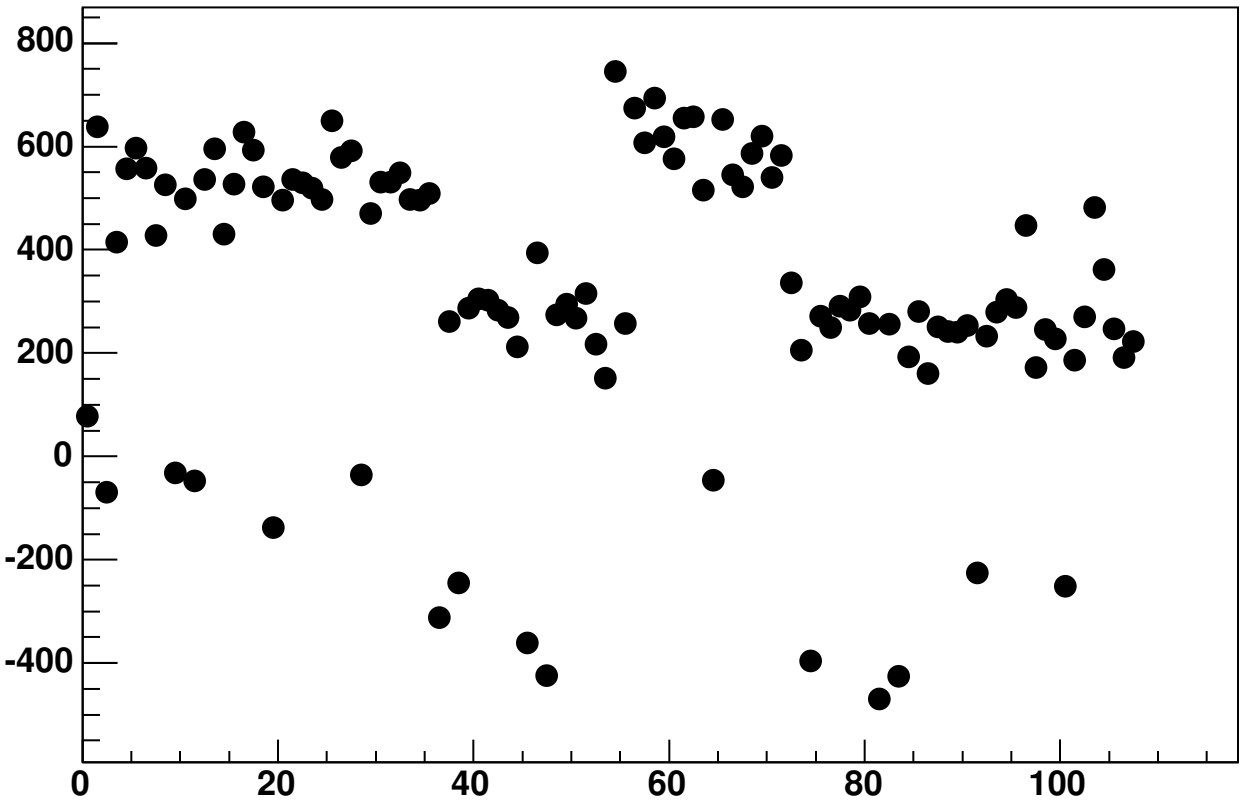
Enable 0, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



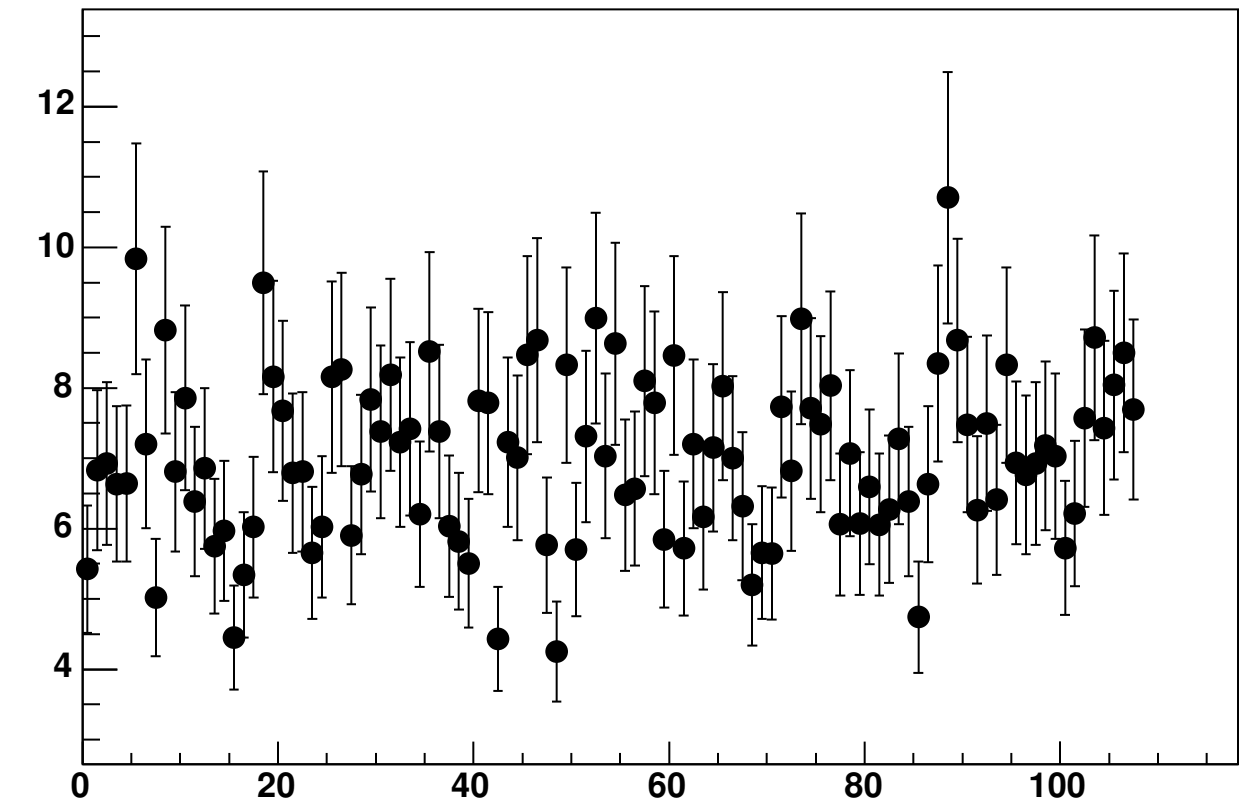
Enable 0, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



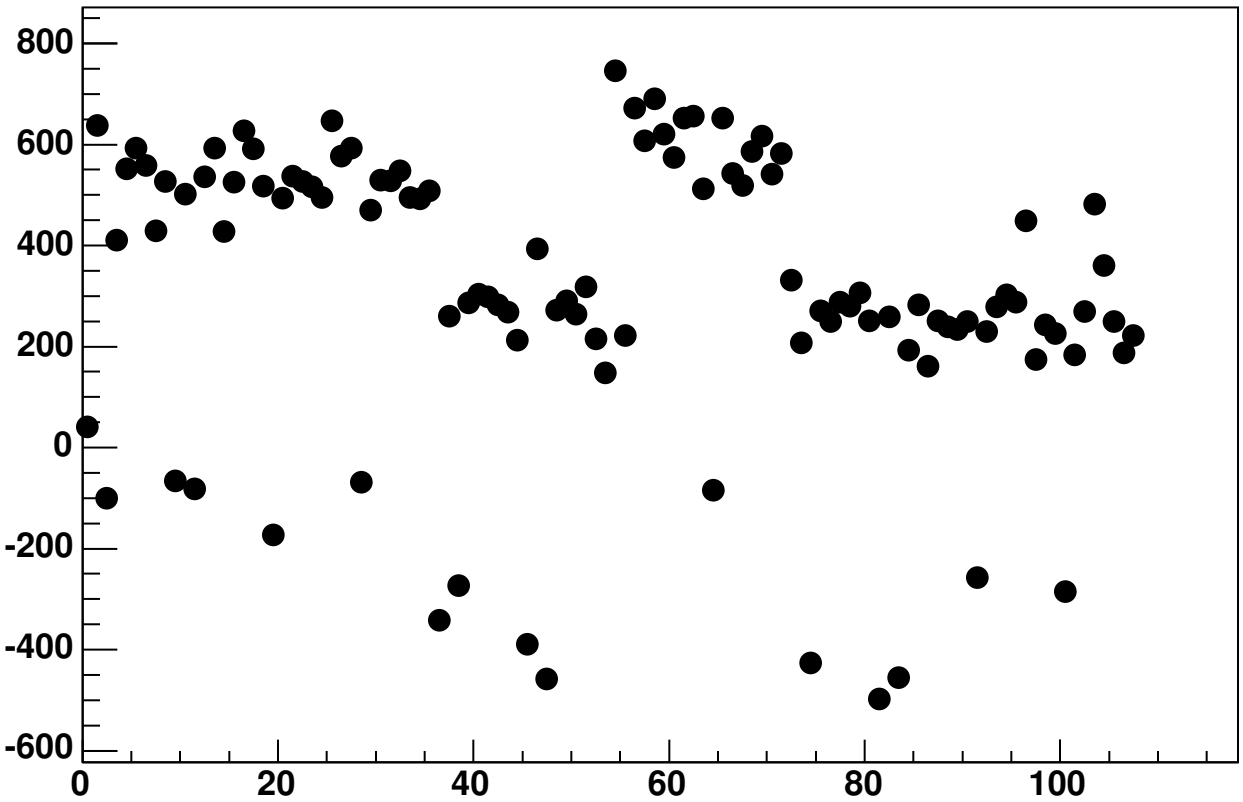
Enable 0, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



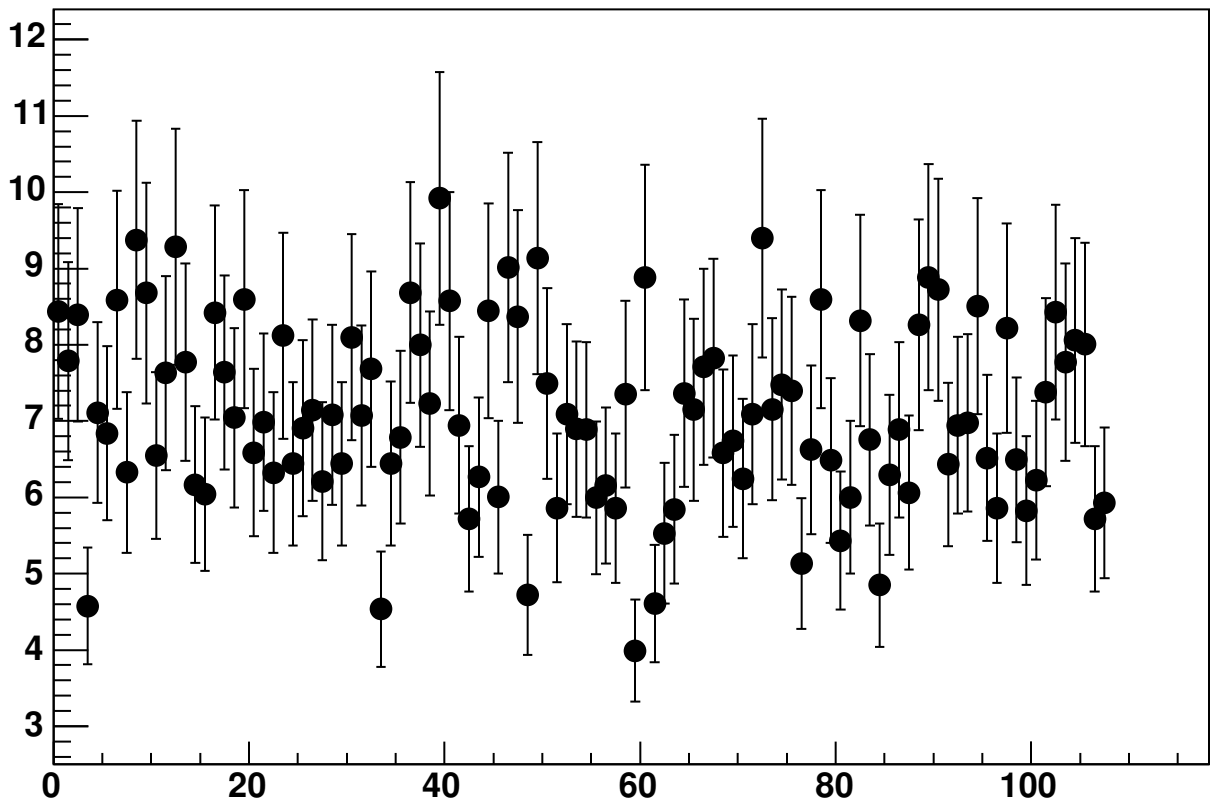
Enable 0, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



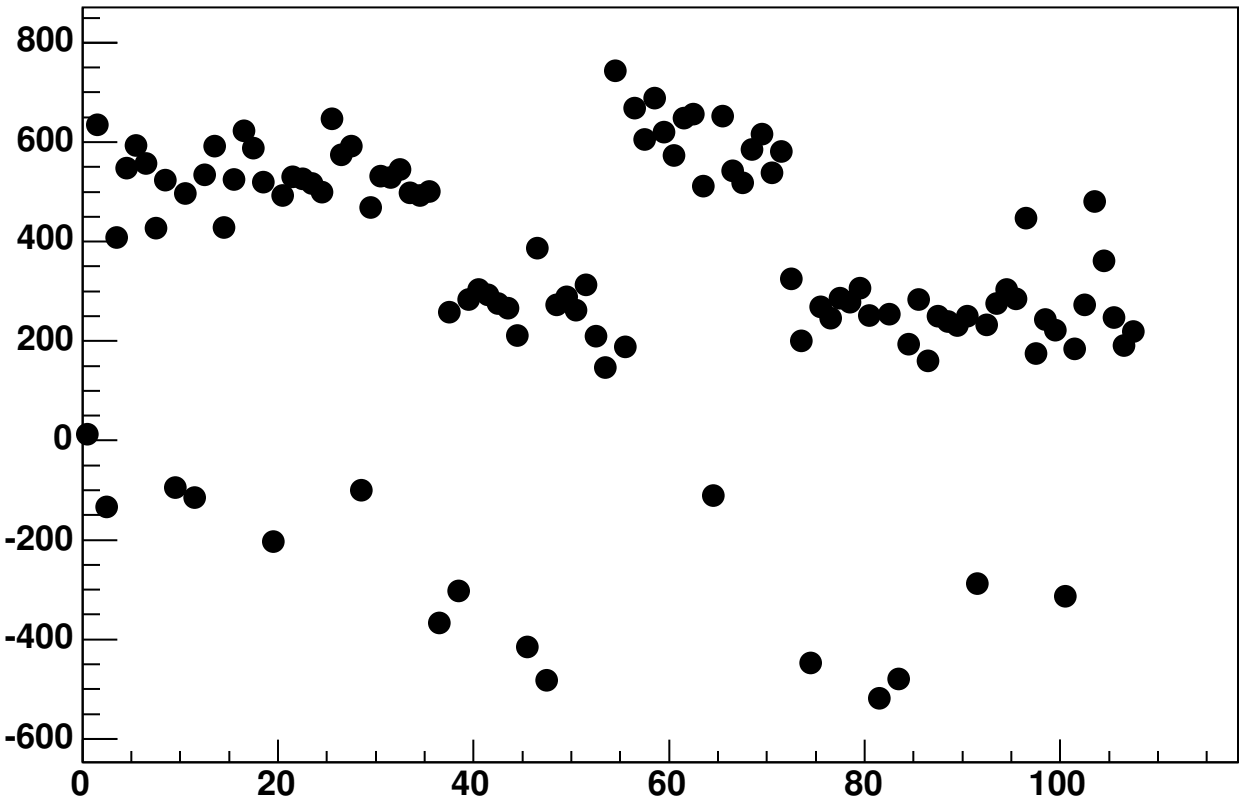
Enable 0, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



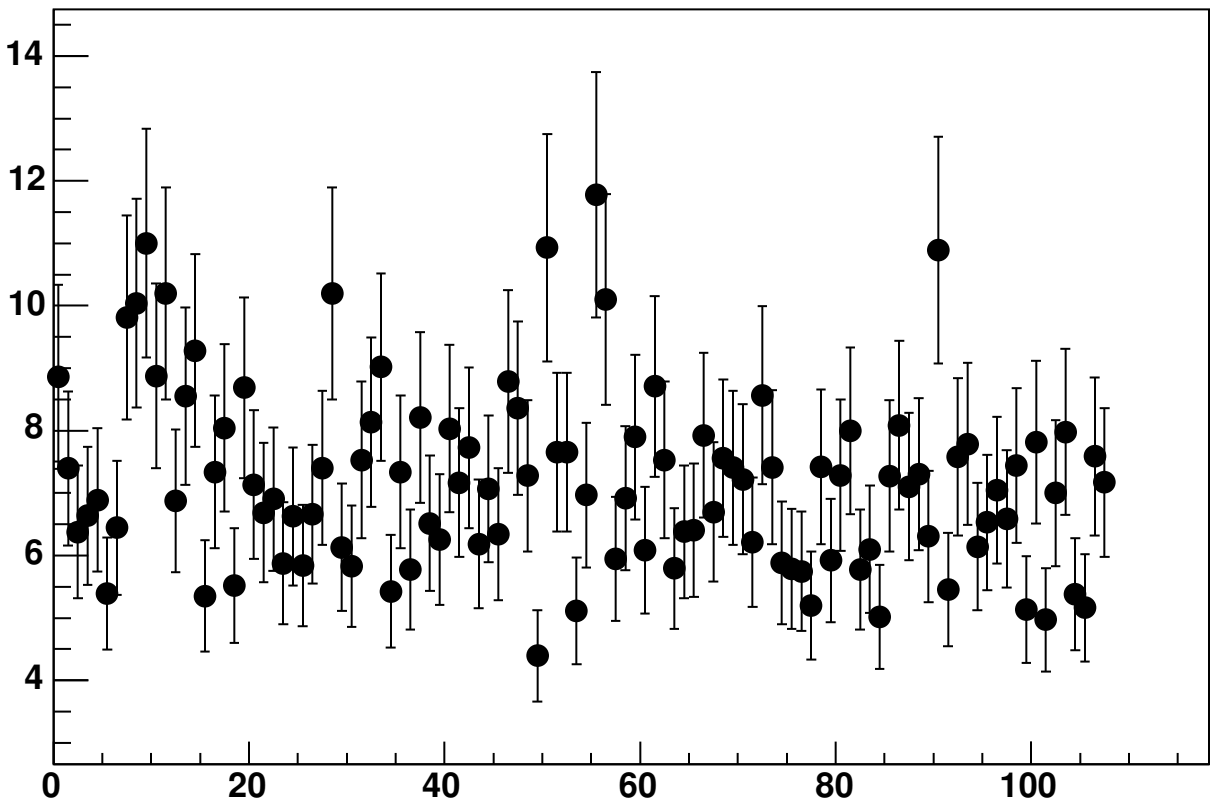
Enable 0, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



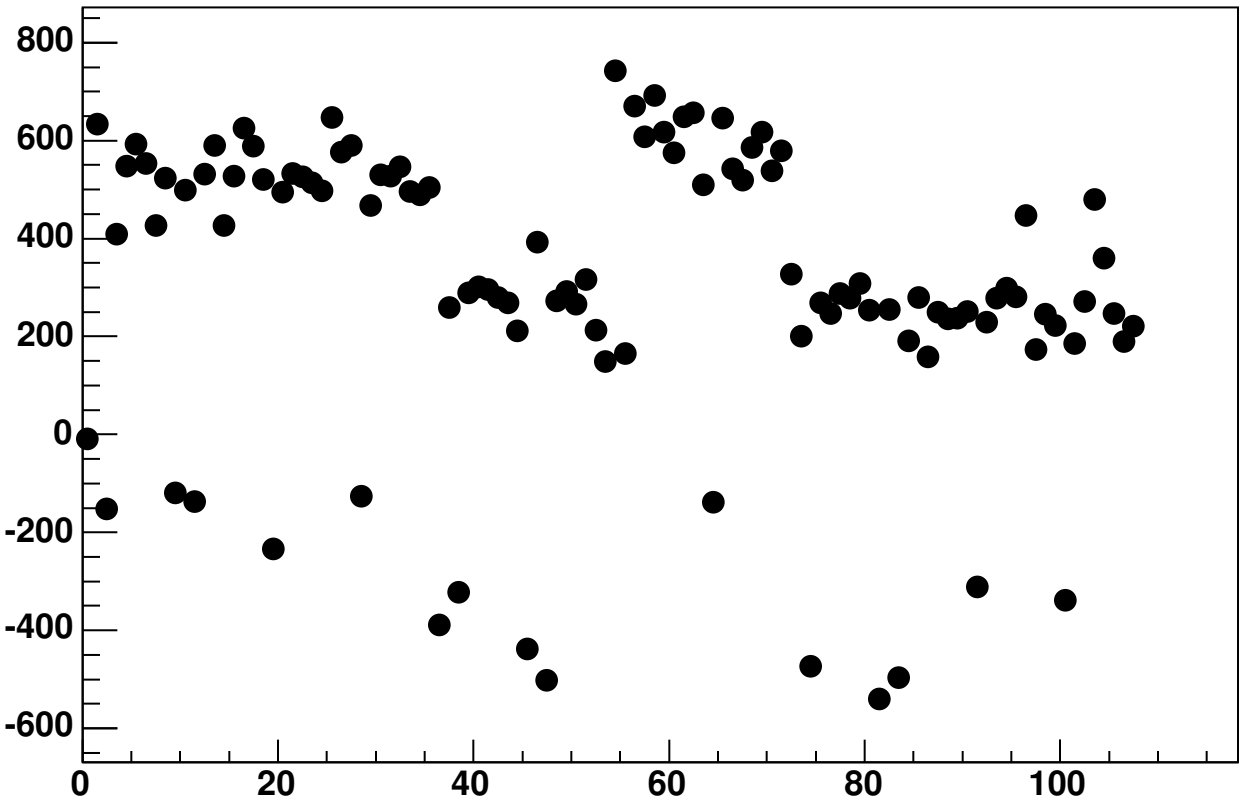
Enable 0, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



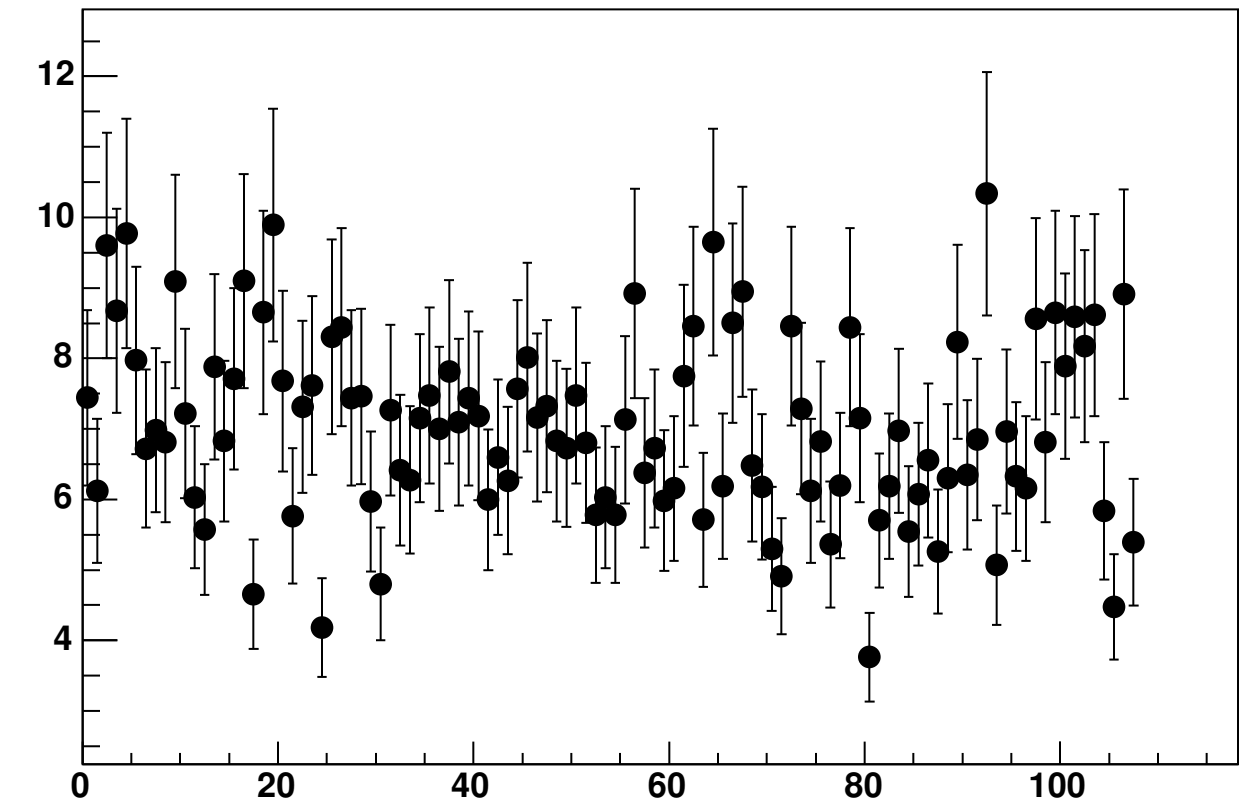
Enable 0, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



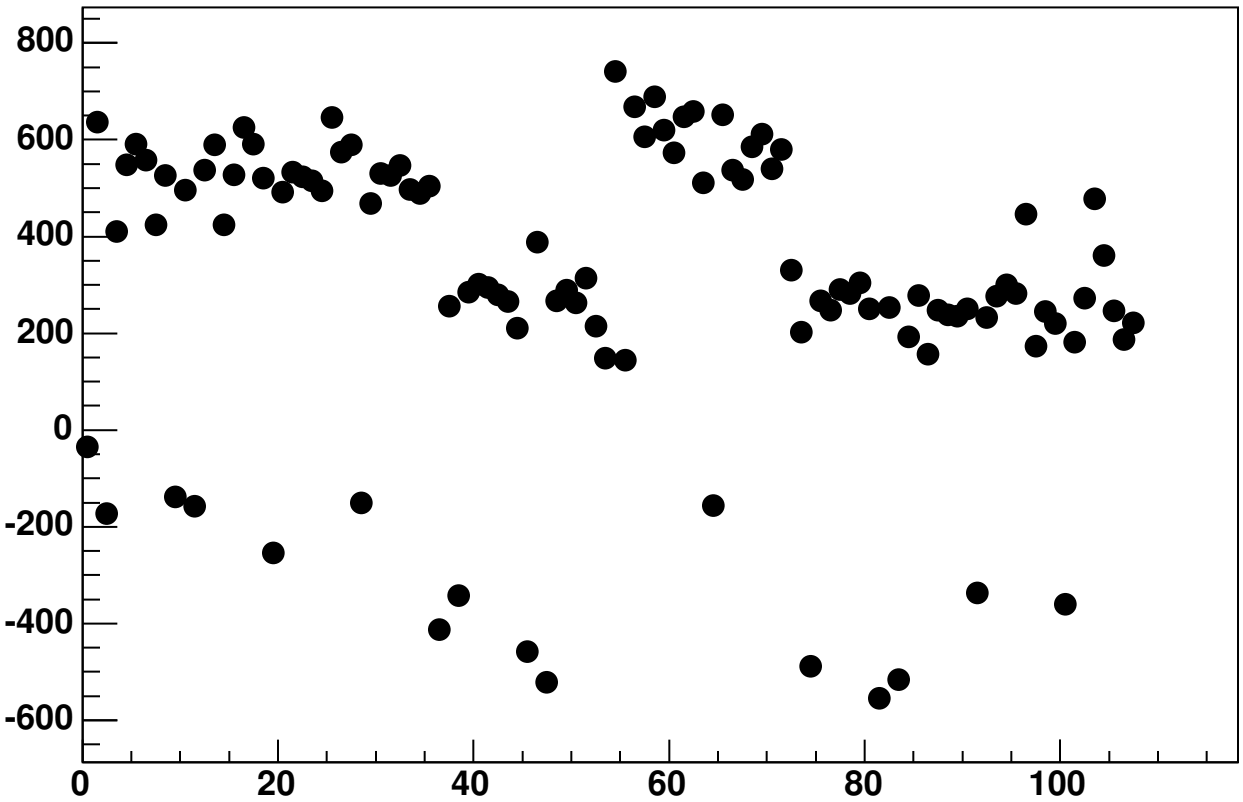
Enable 0, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



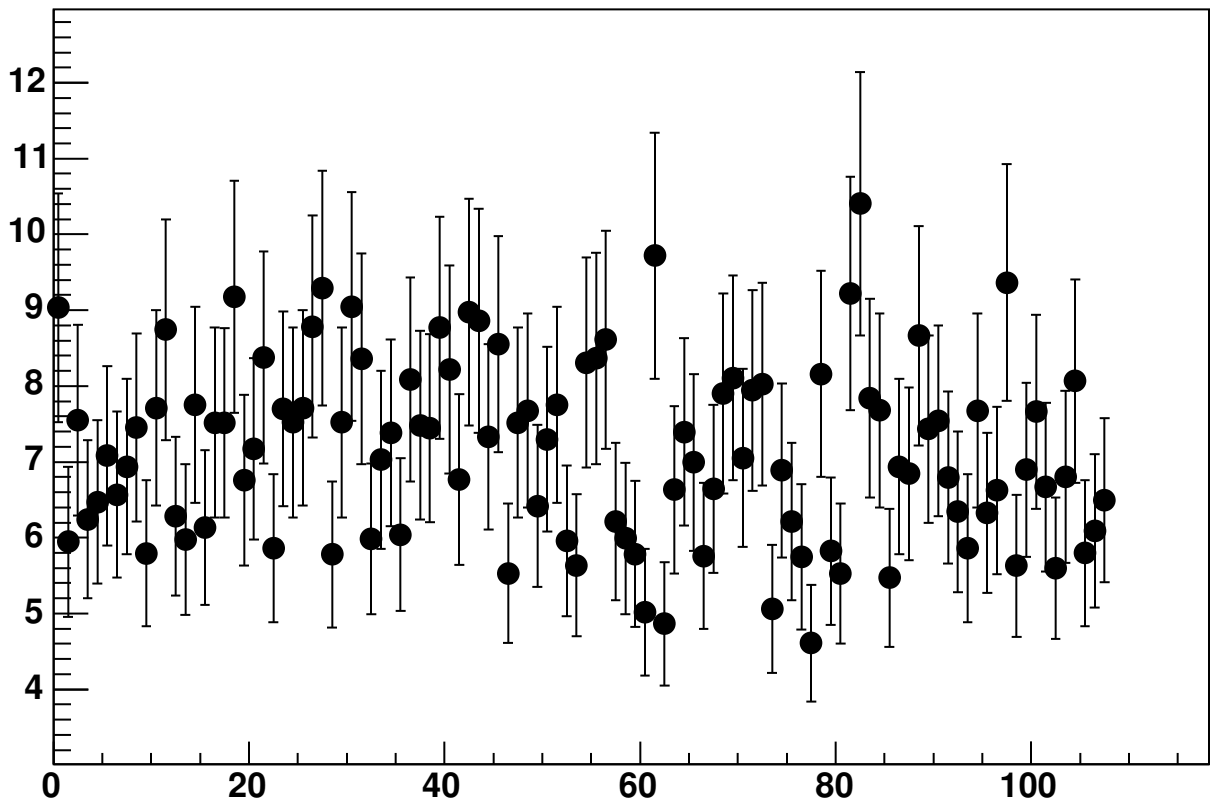
Enable 0, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



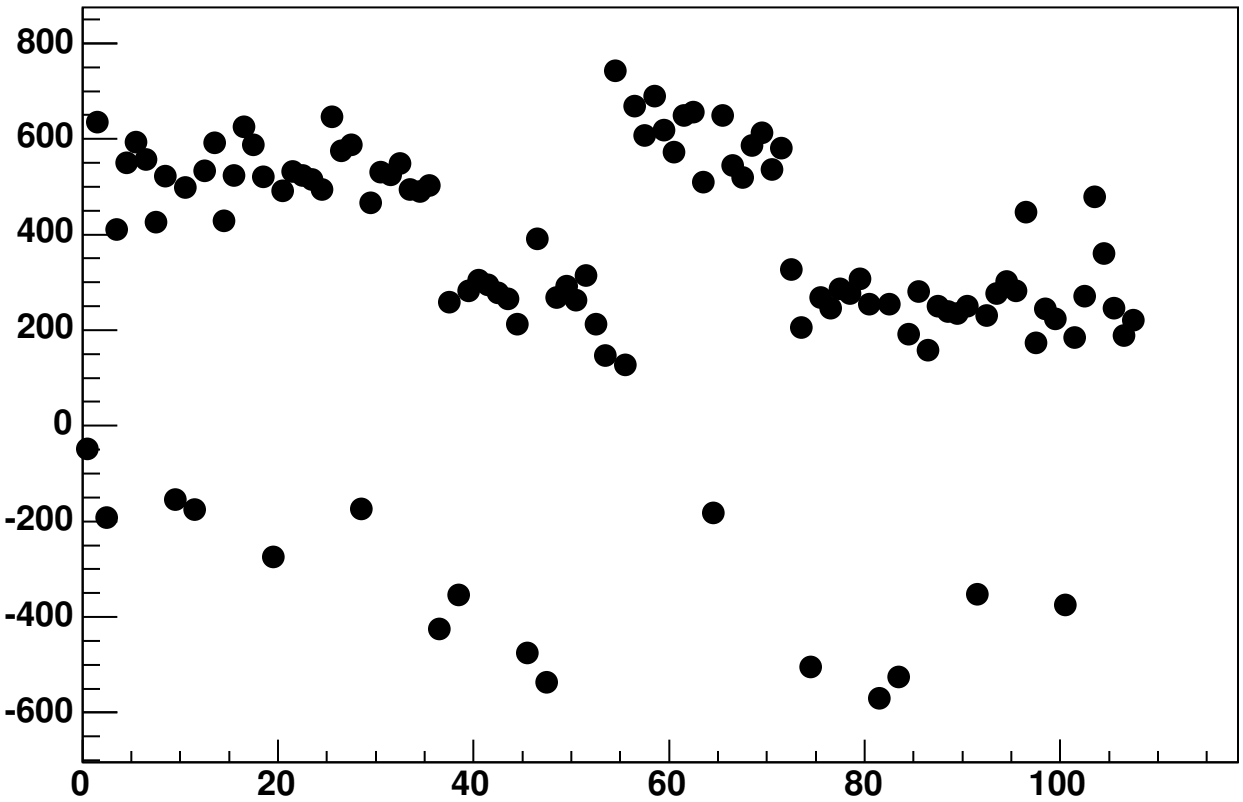
Enable 0, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



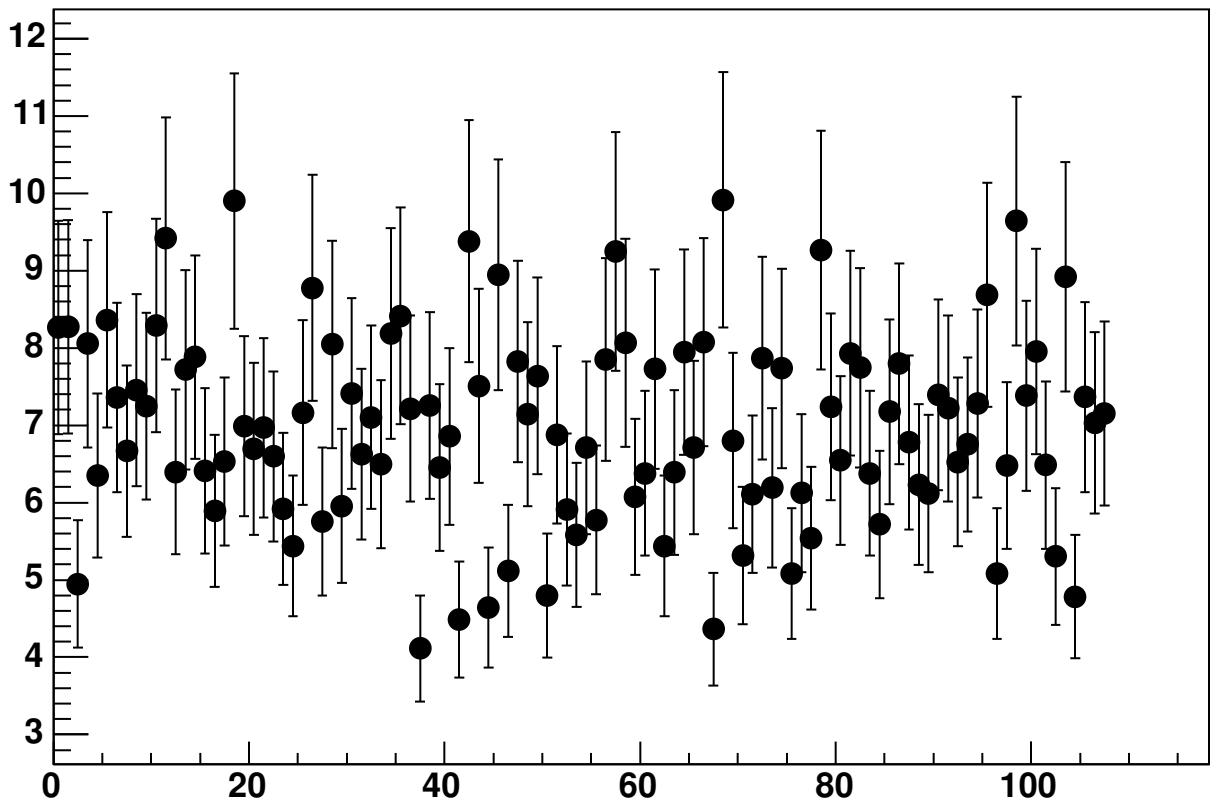
Enable 0, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



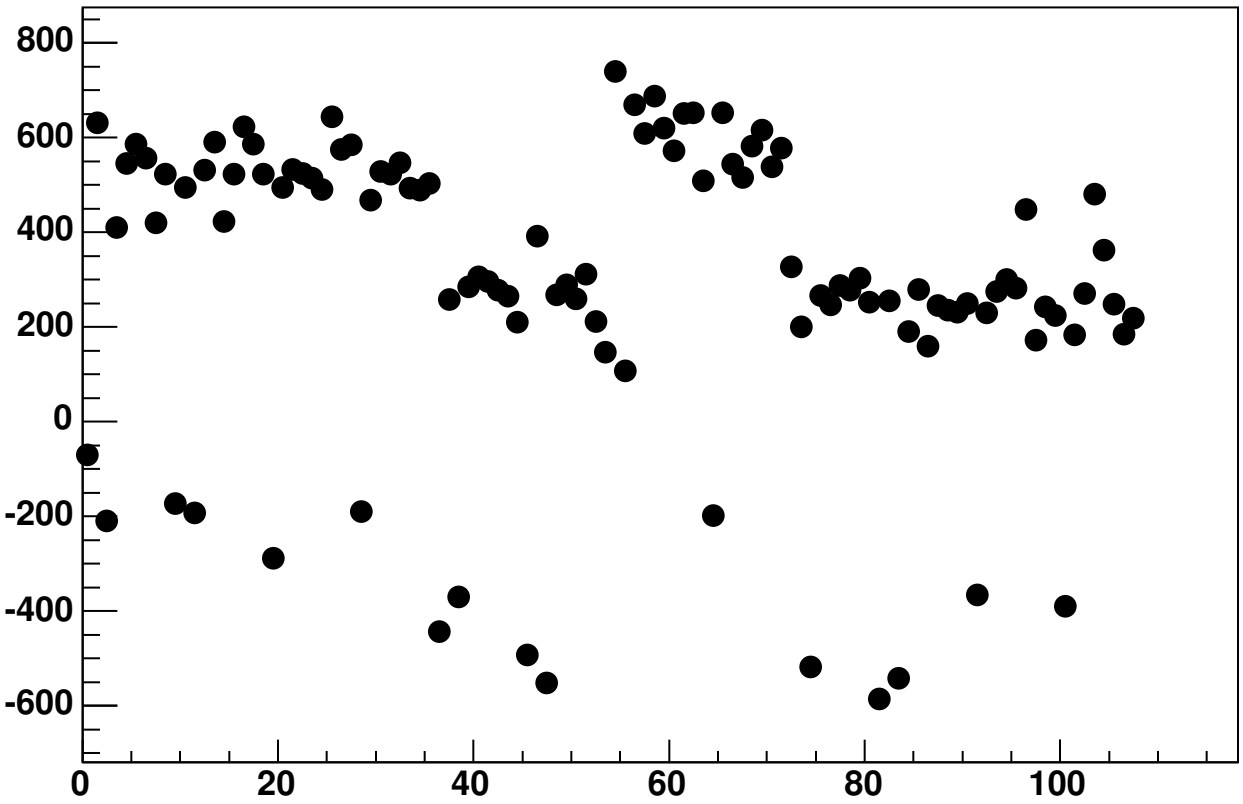
Enable 0, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



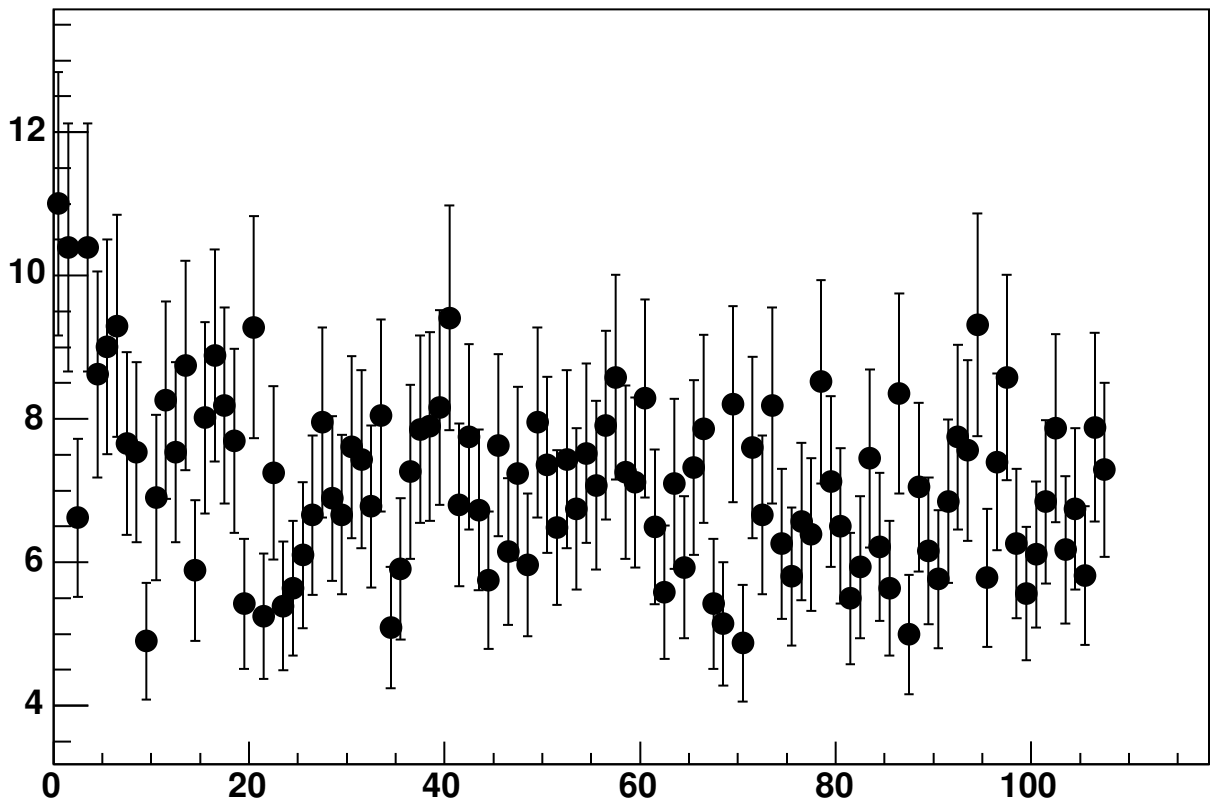
Enable 0, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



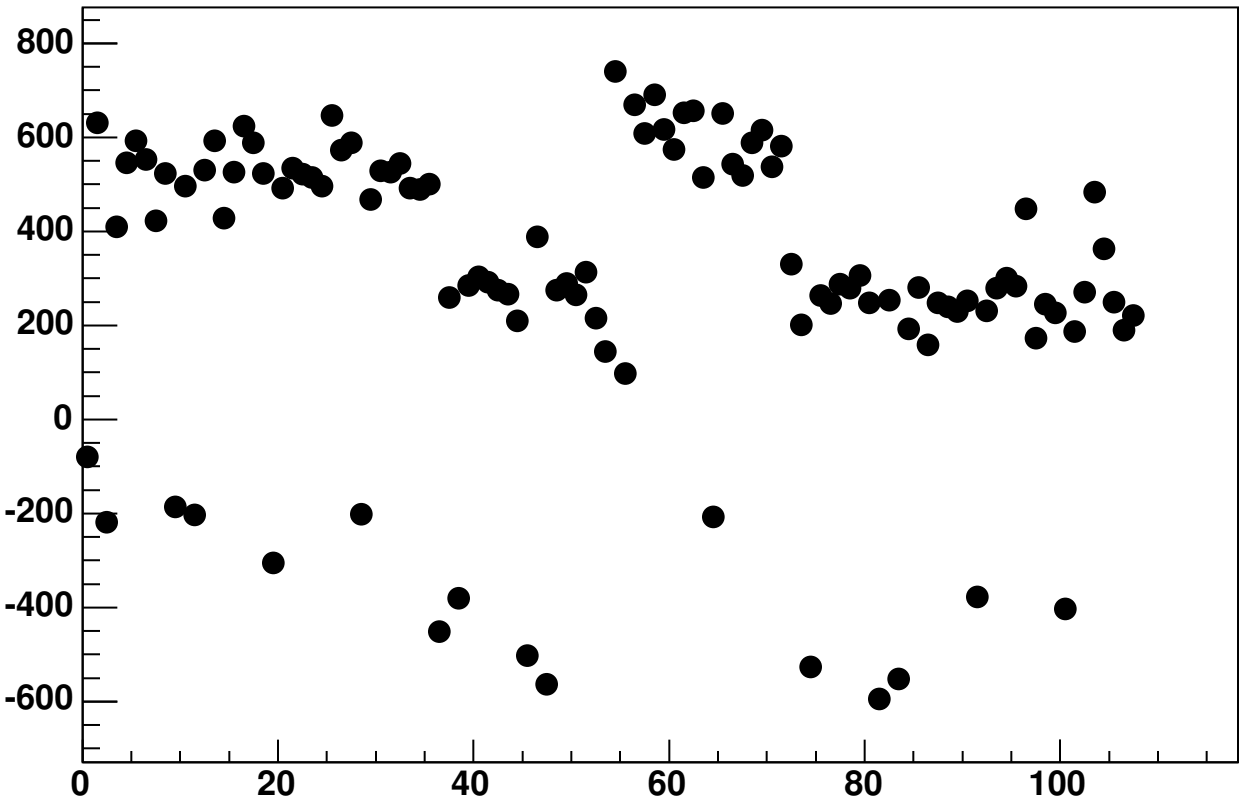
Enable 0, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



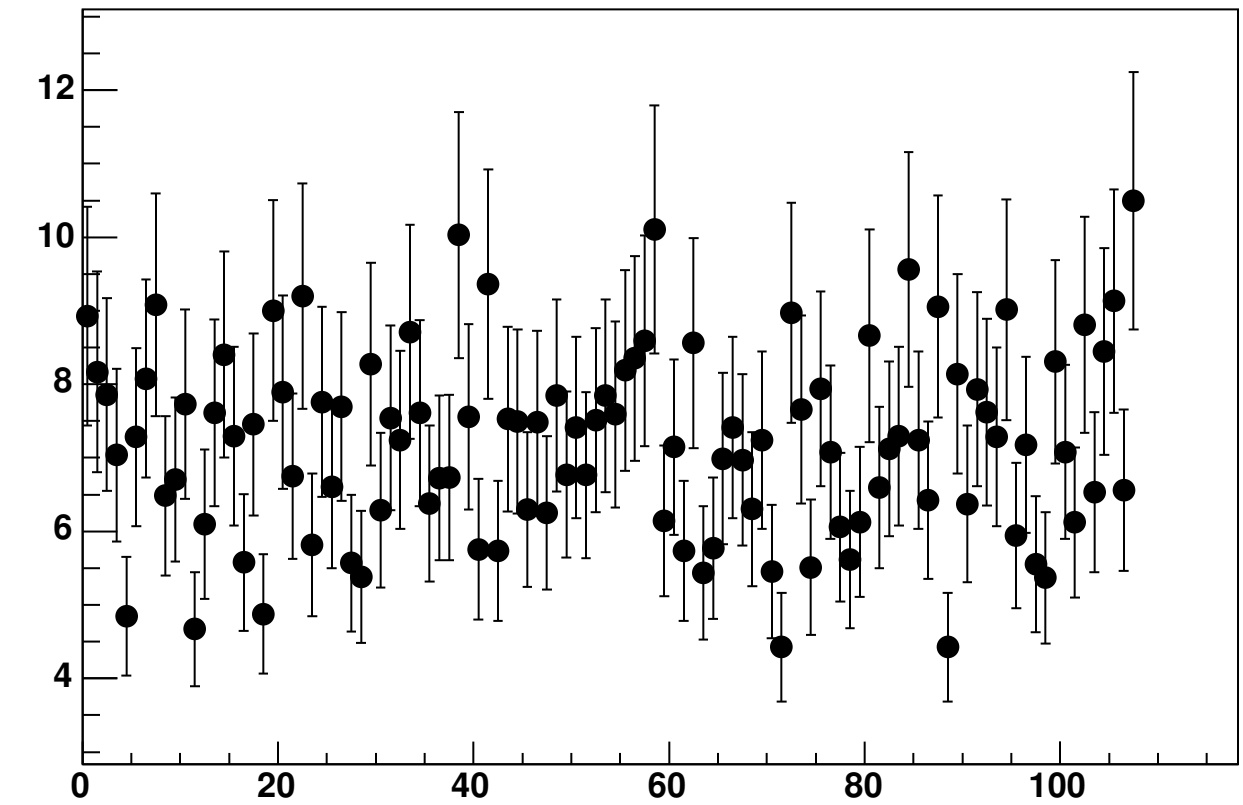
Enable 0, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan



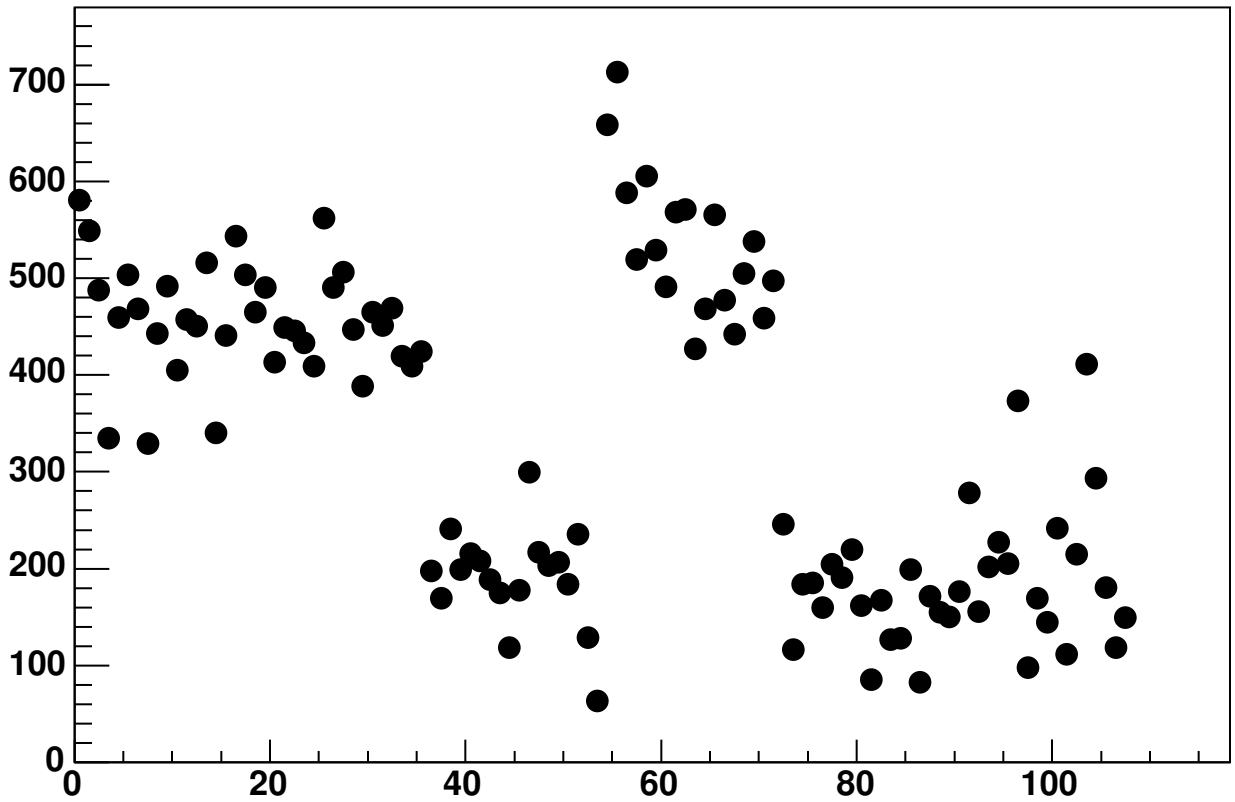
Enable 0, DAC=4000, Hold=245, ADC Mean vs 18*Chip+Chan



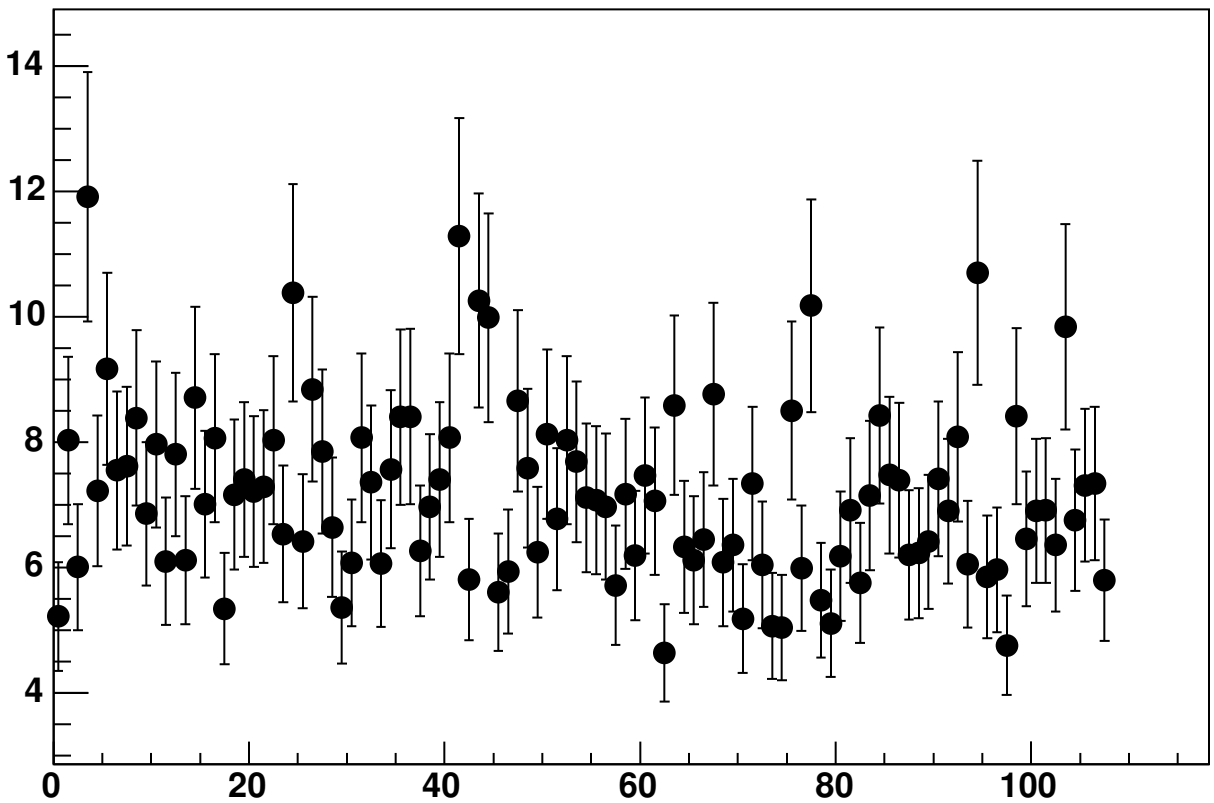
Enable 0, DAC=4000, Hold=245, ADC Noise vs 18*Chip+Chan



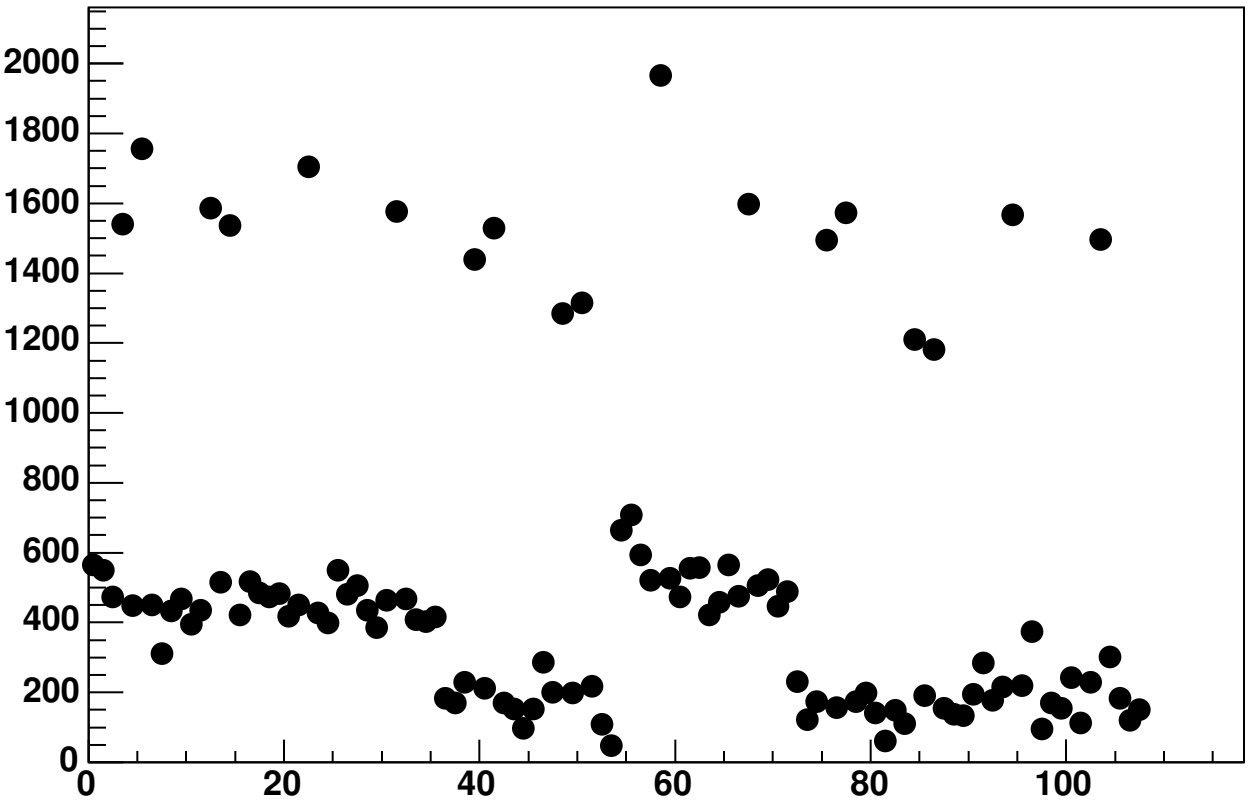
Enable 1, DAC=4000, Hold=0, ADC Mean vs 18*Chip+Chan



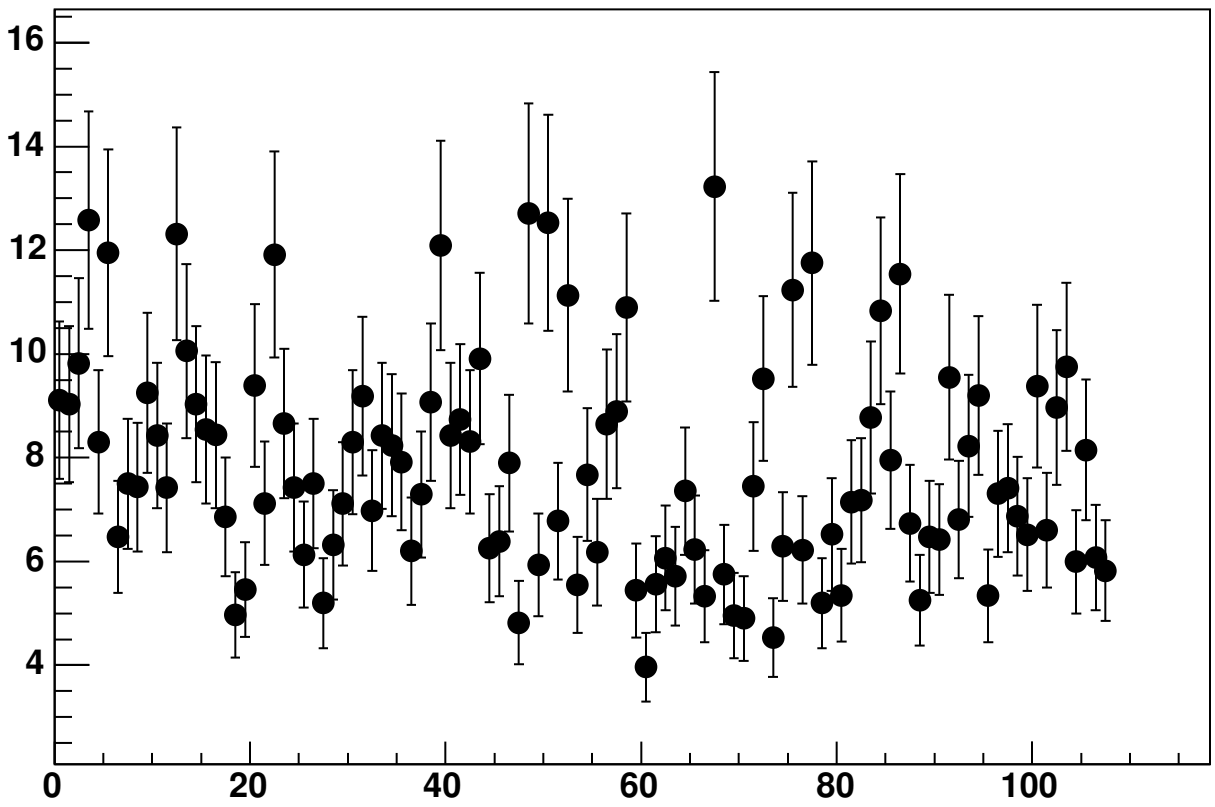
Enable 1, DAC=4000, Hold=0, ADC Noise vs 18*Chip+Chan



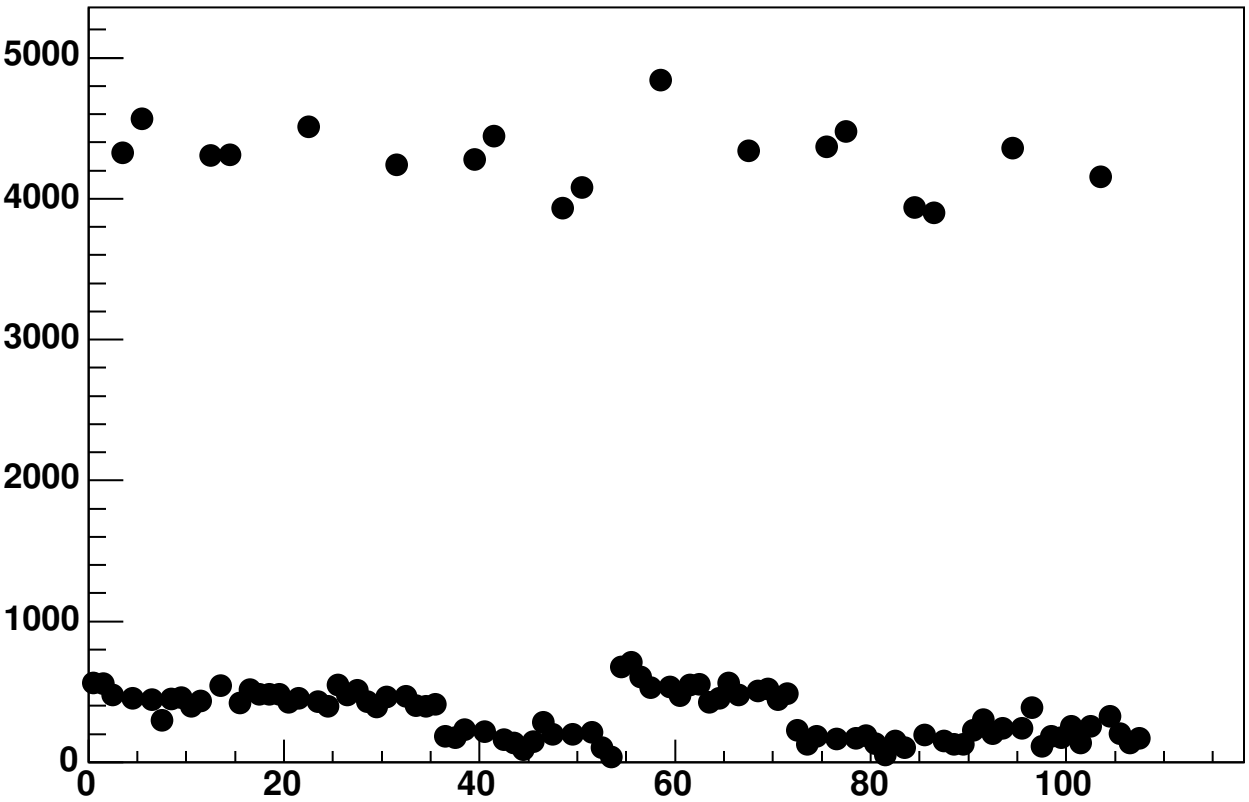
Enable 1, DAC=4000, Hold=5, ADC Mean vs 18*Chip+Chan



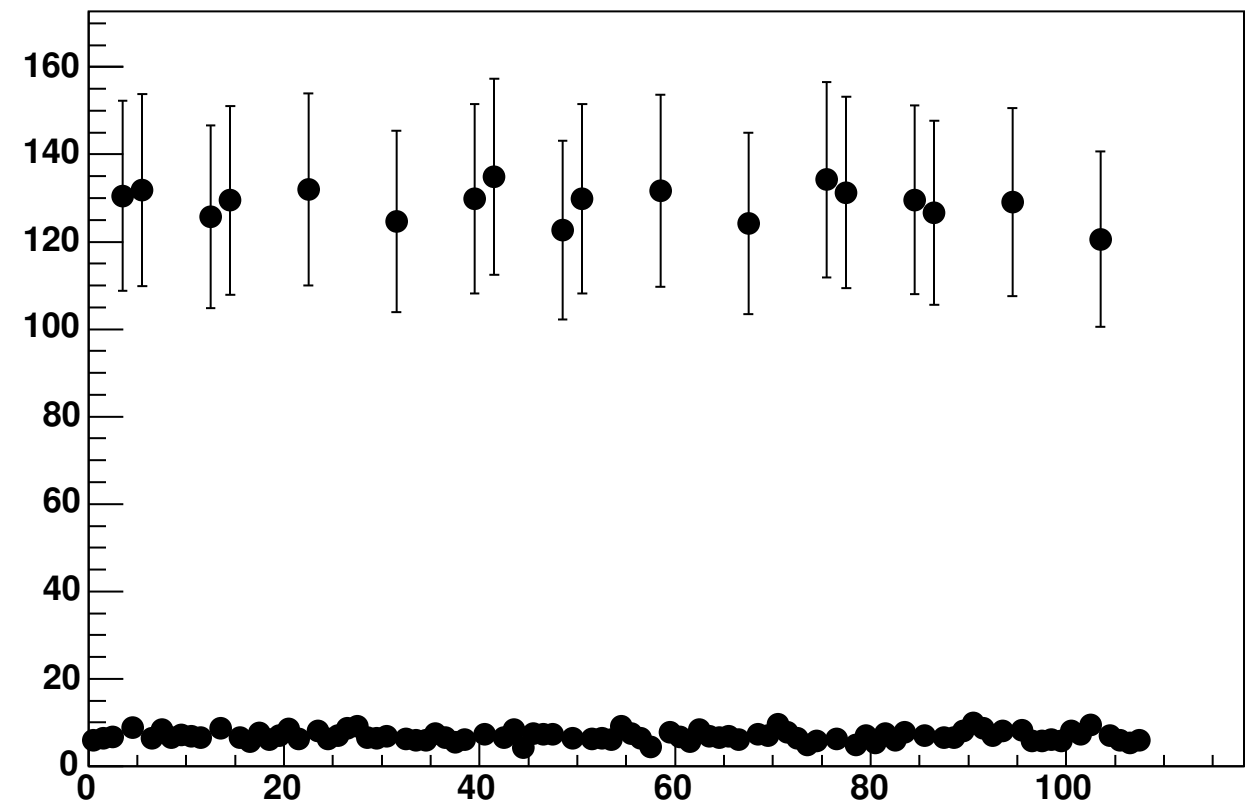
Enable 1, DAC=4000, Hold=5, ADC Noise vs 18*Chip+Chan



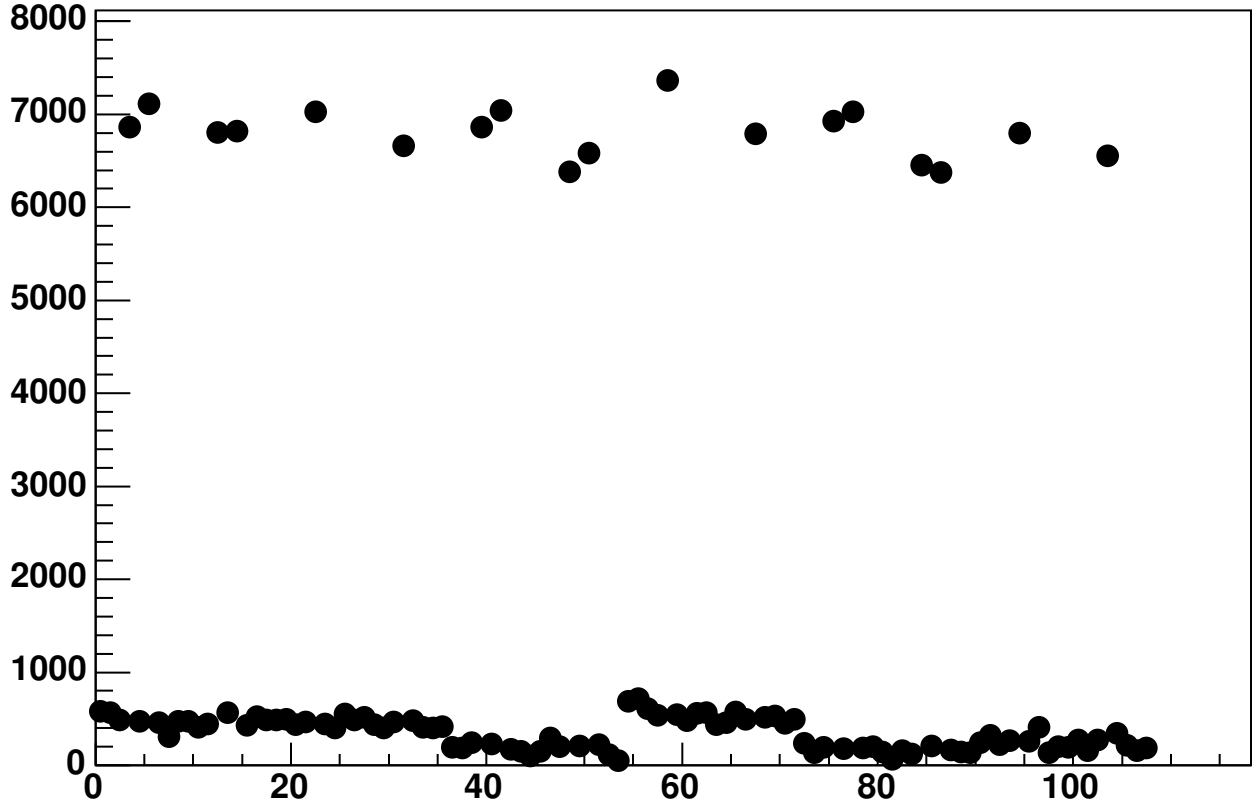
Enable 1, DAC=4000, Hold=10, ADC Mean vs 18*Chip+Chan



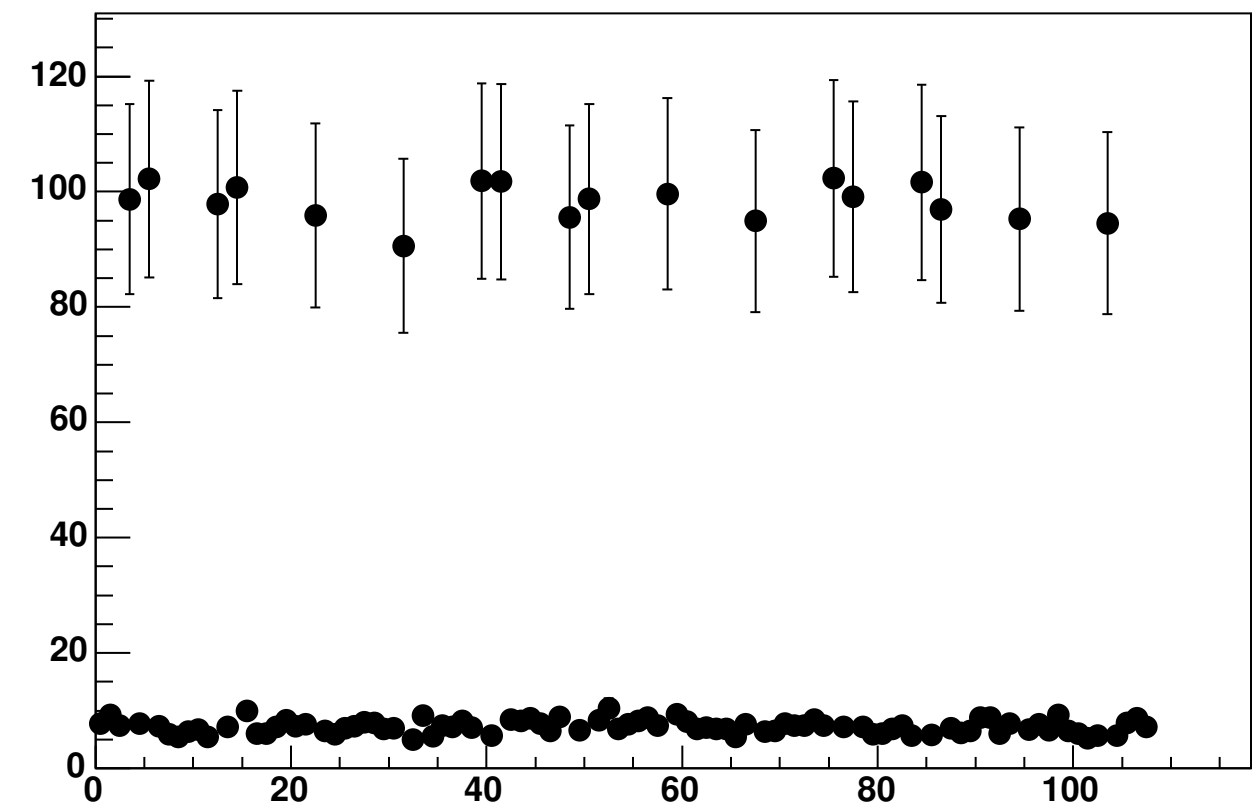
Enable 1, DAC=4000, Hold=10, ADC Noise vs 18*Chip+Chan



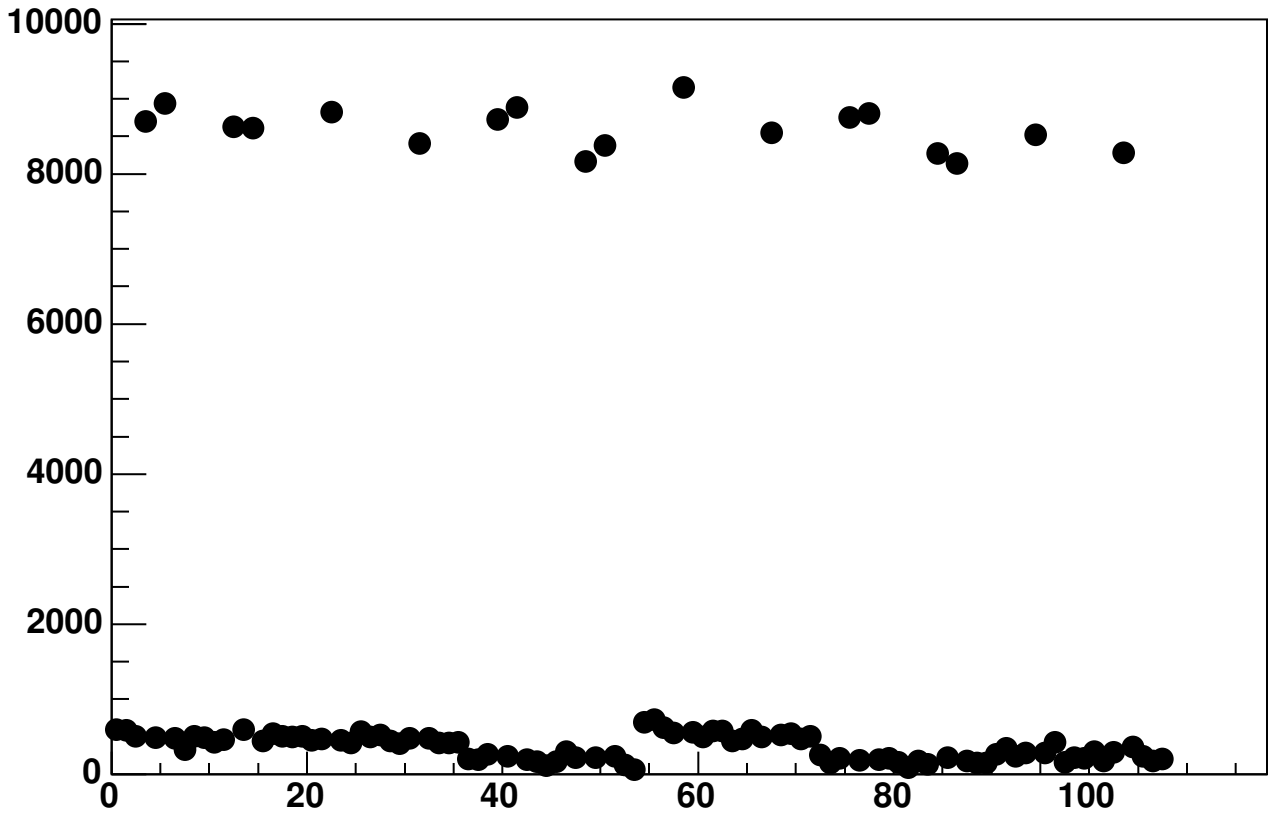
Enable 1, DAC=4000, Hold=15, ADC Mean vs 18*Chip+Chan



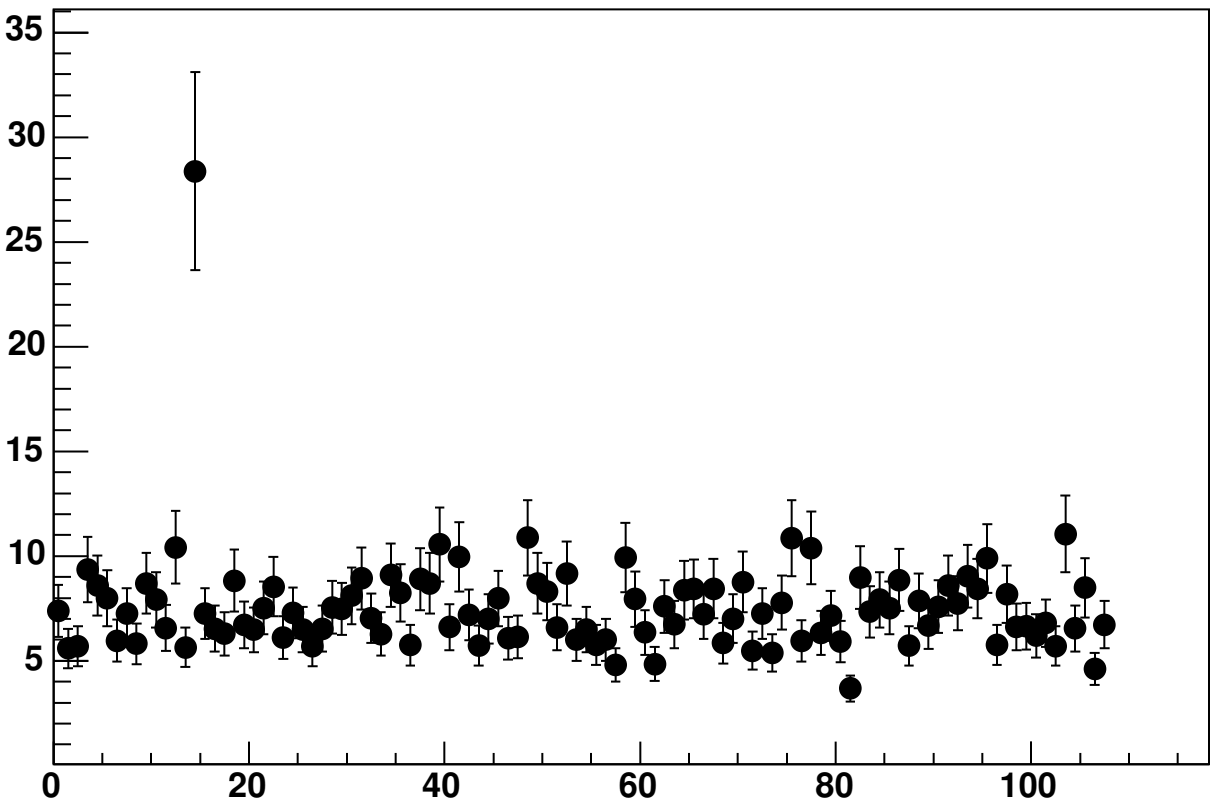
Enable 1, DAC=4000, Hold=15, ADC Noise vs 18*Chip+Chan



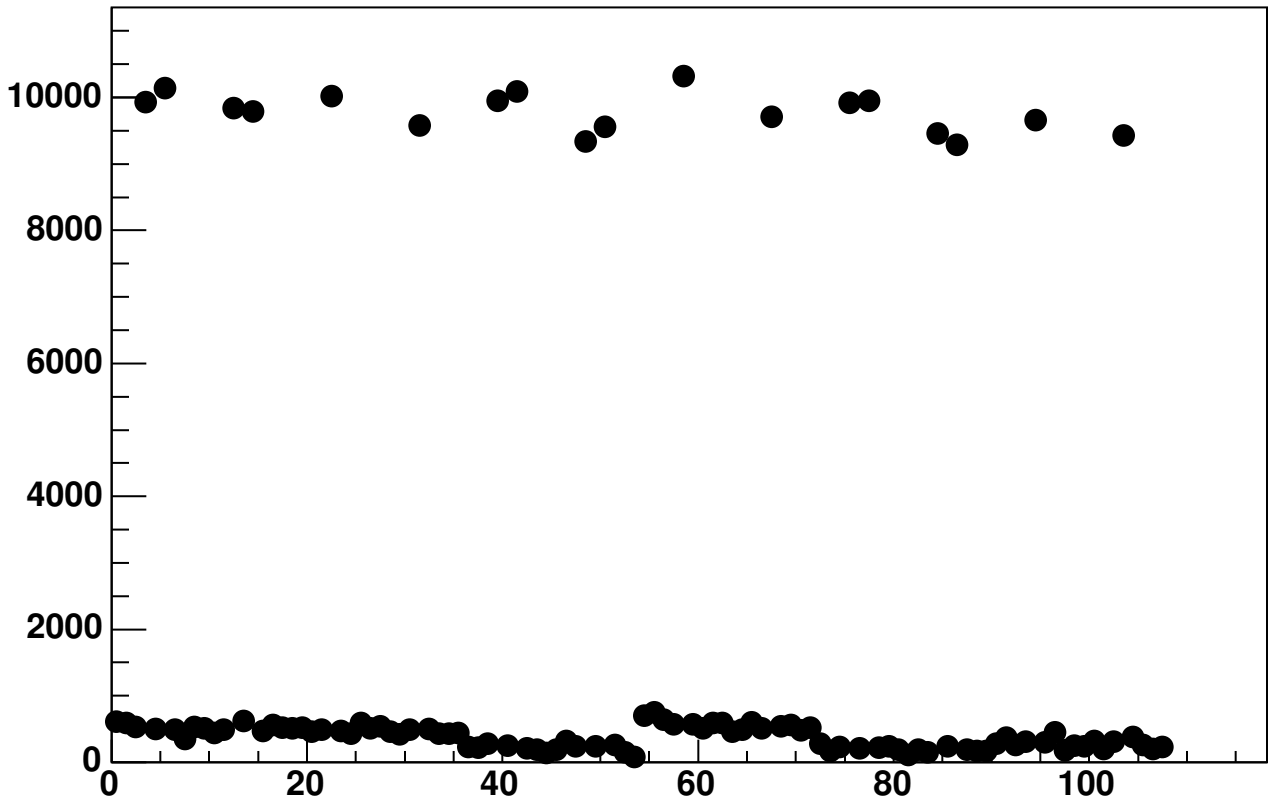
Enable 1, DAC=4000, Hold=20, ADC Mean vs 18*Chip+Chan



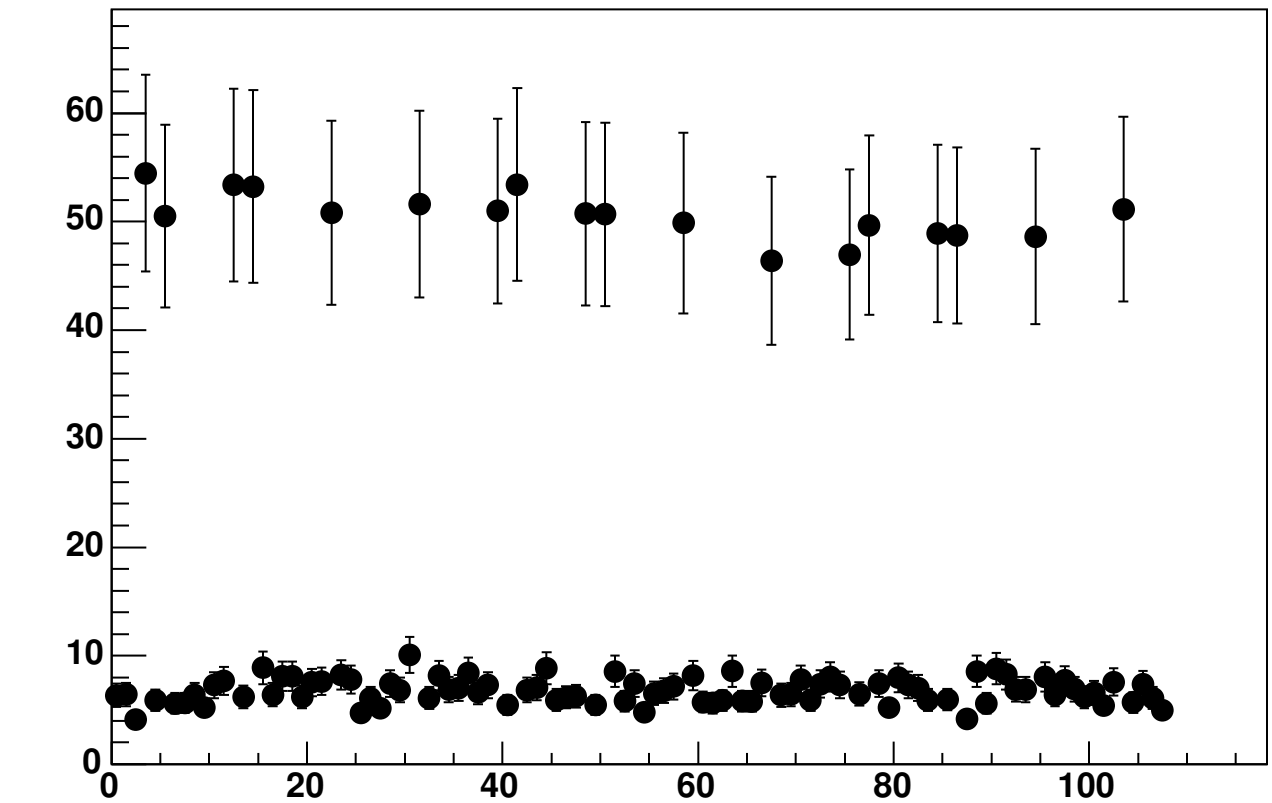
Enable 1, DAC=4000, Hold=20, ADC Noise vs 18*Chip+Chan



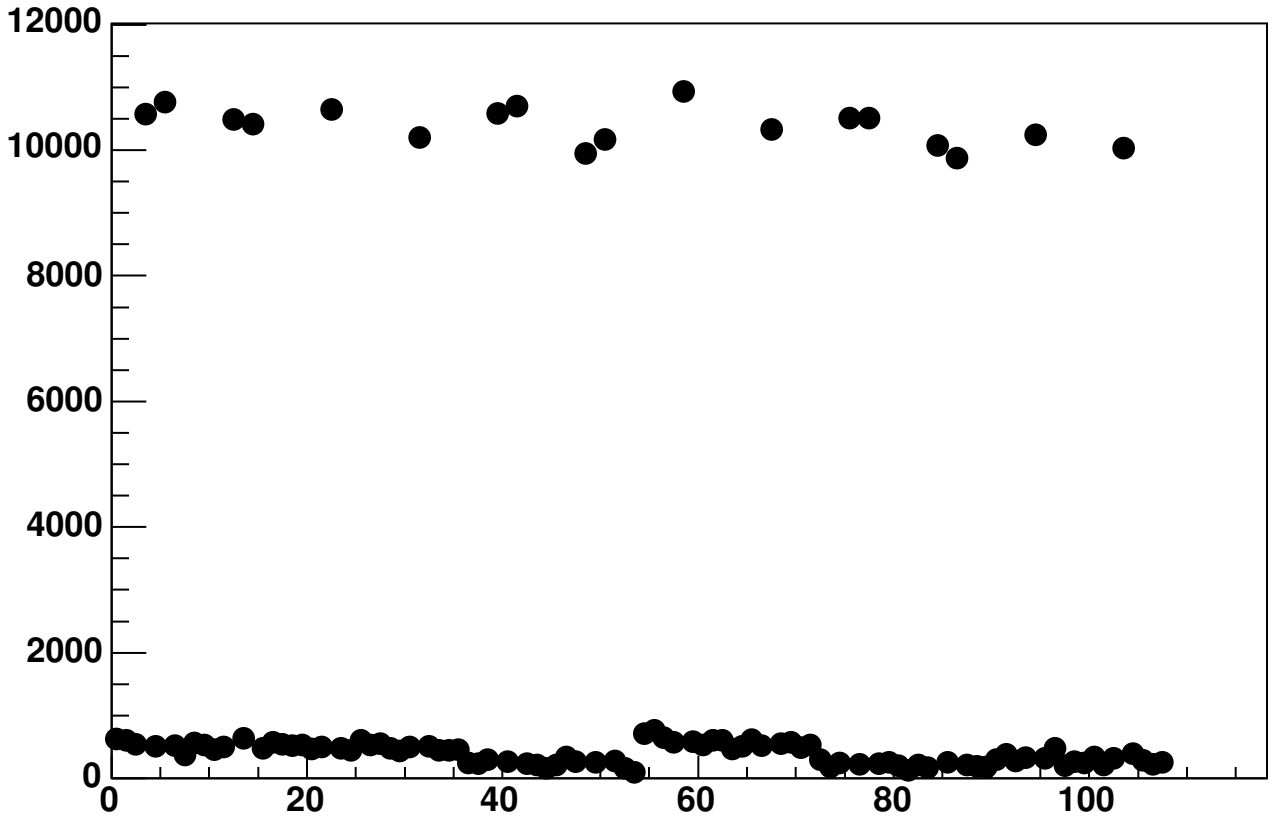
Enable 1, DAC=4000, Hold=25, ADC Mean vs 18*Chip+Chan



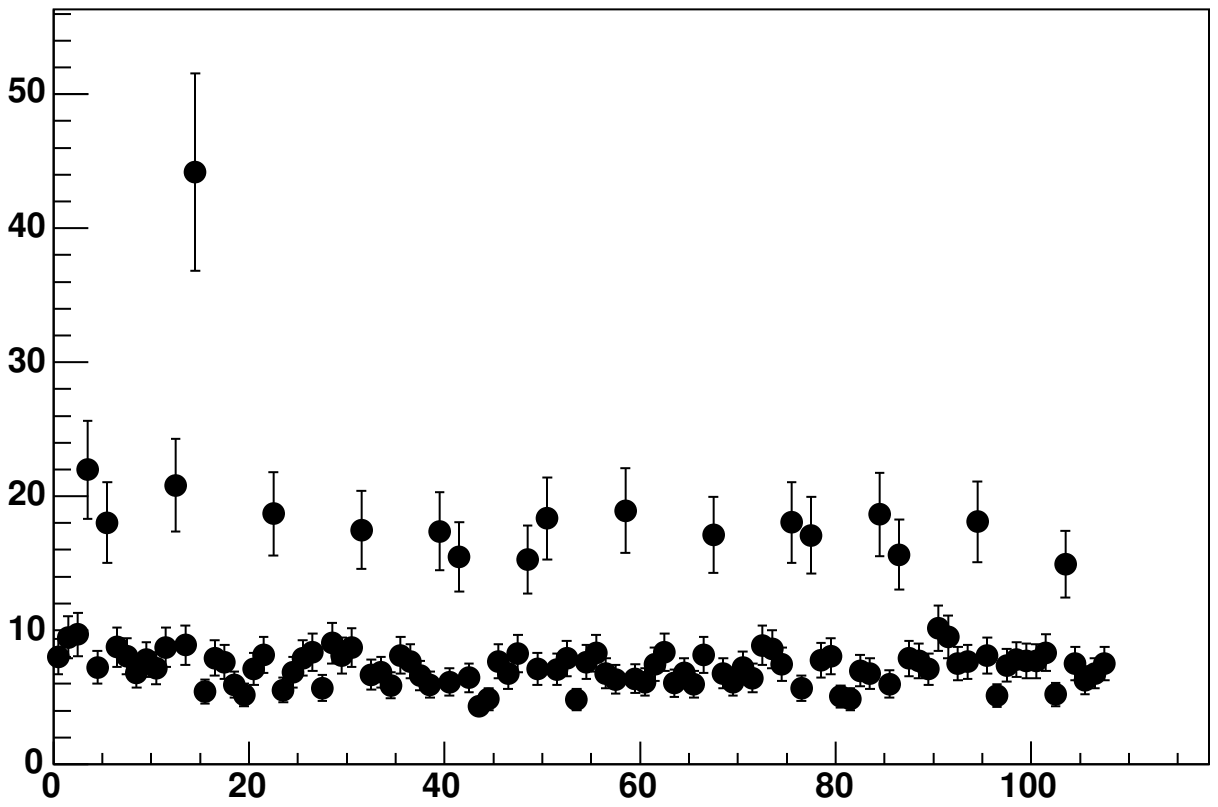
Enable 1, DAC=4000, Hold=25, ADC Noise vs 18*Chip+Chan



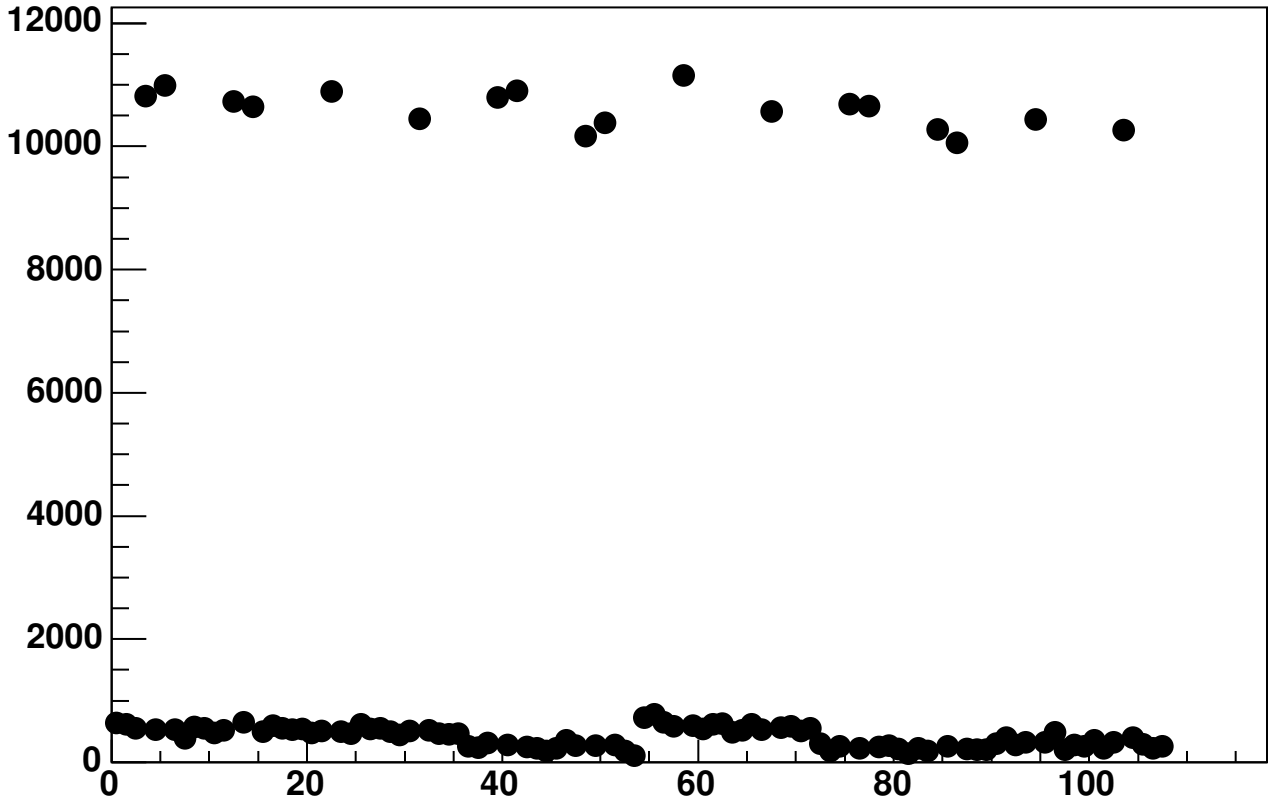
Enable 1, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



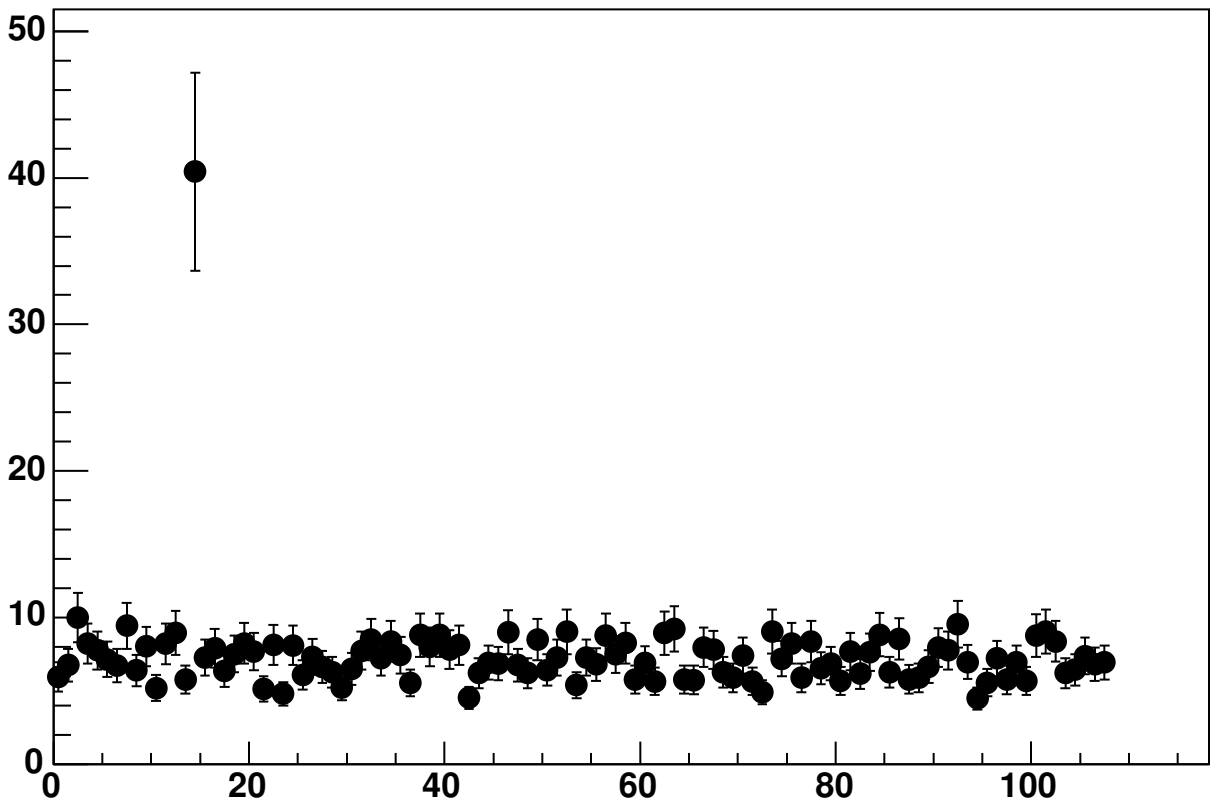
Enable 1, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



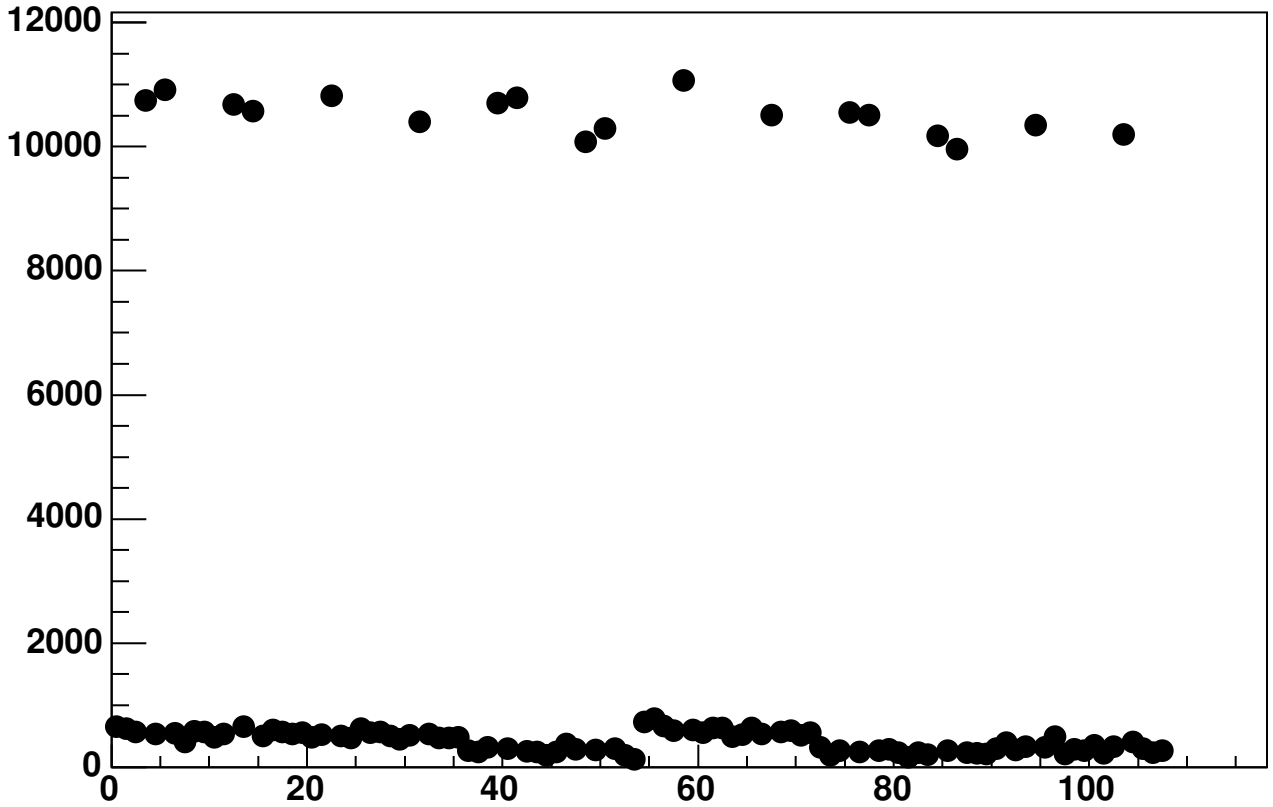
Enable 1, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



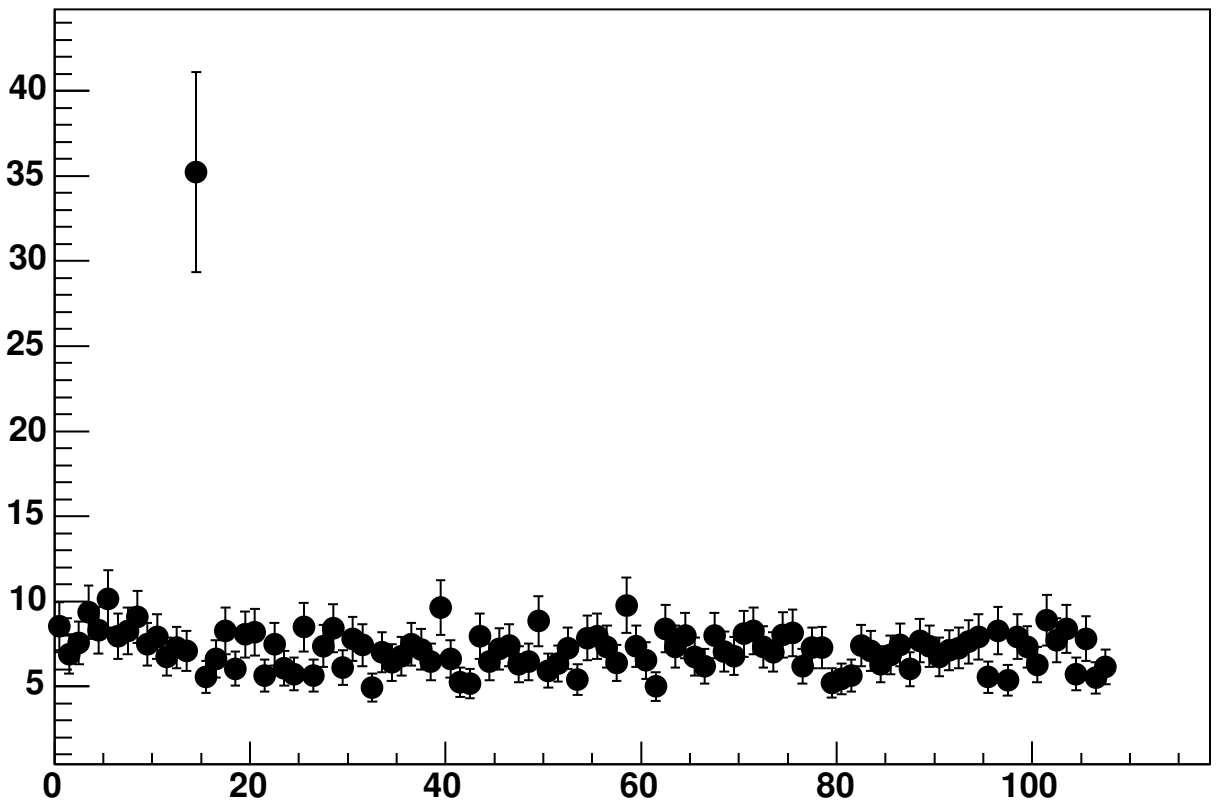
Enable 1, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



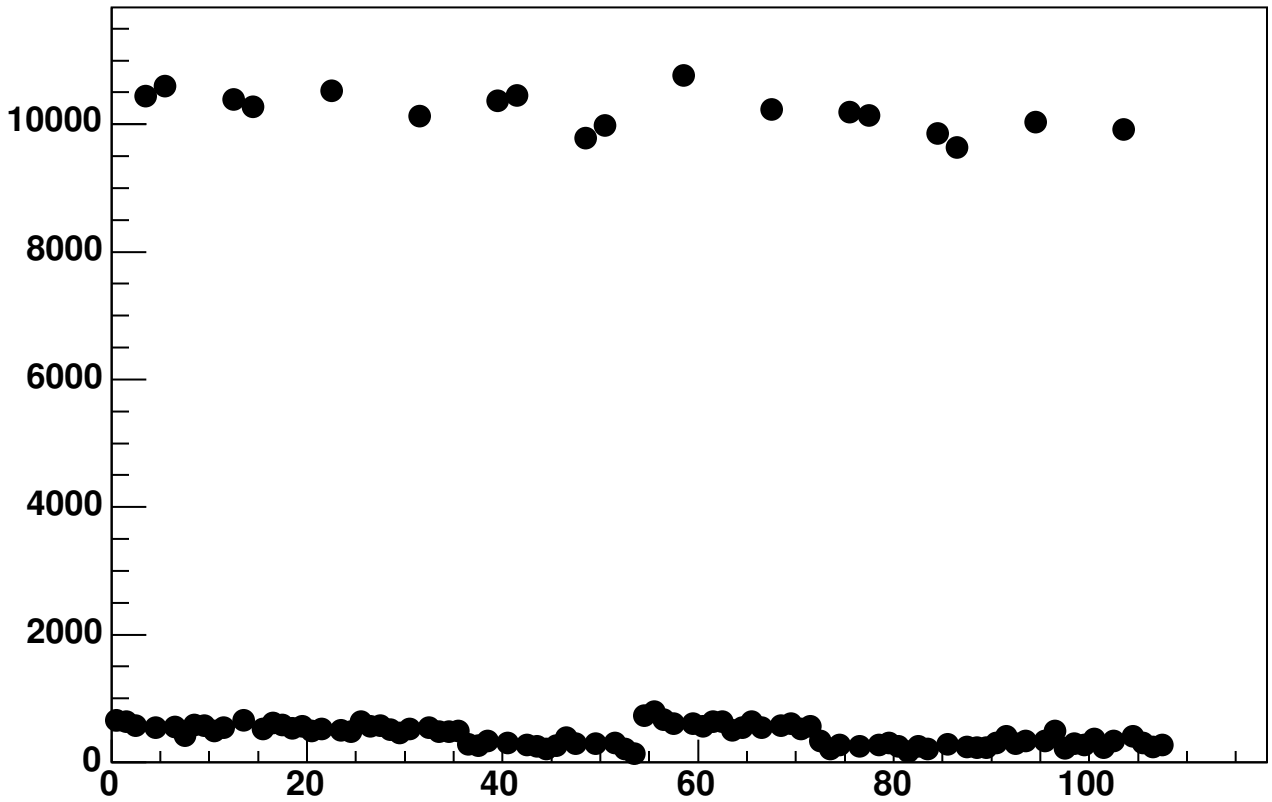
Enable 1, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



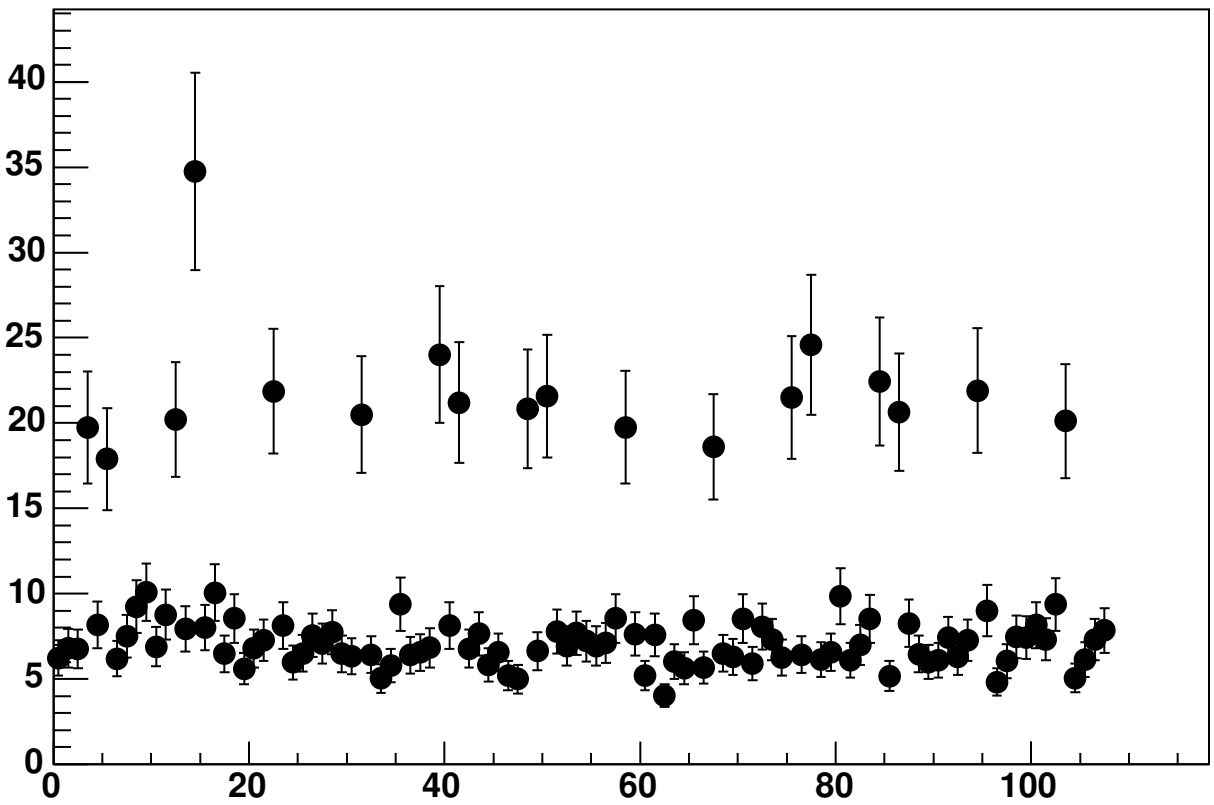
Enable 1, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



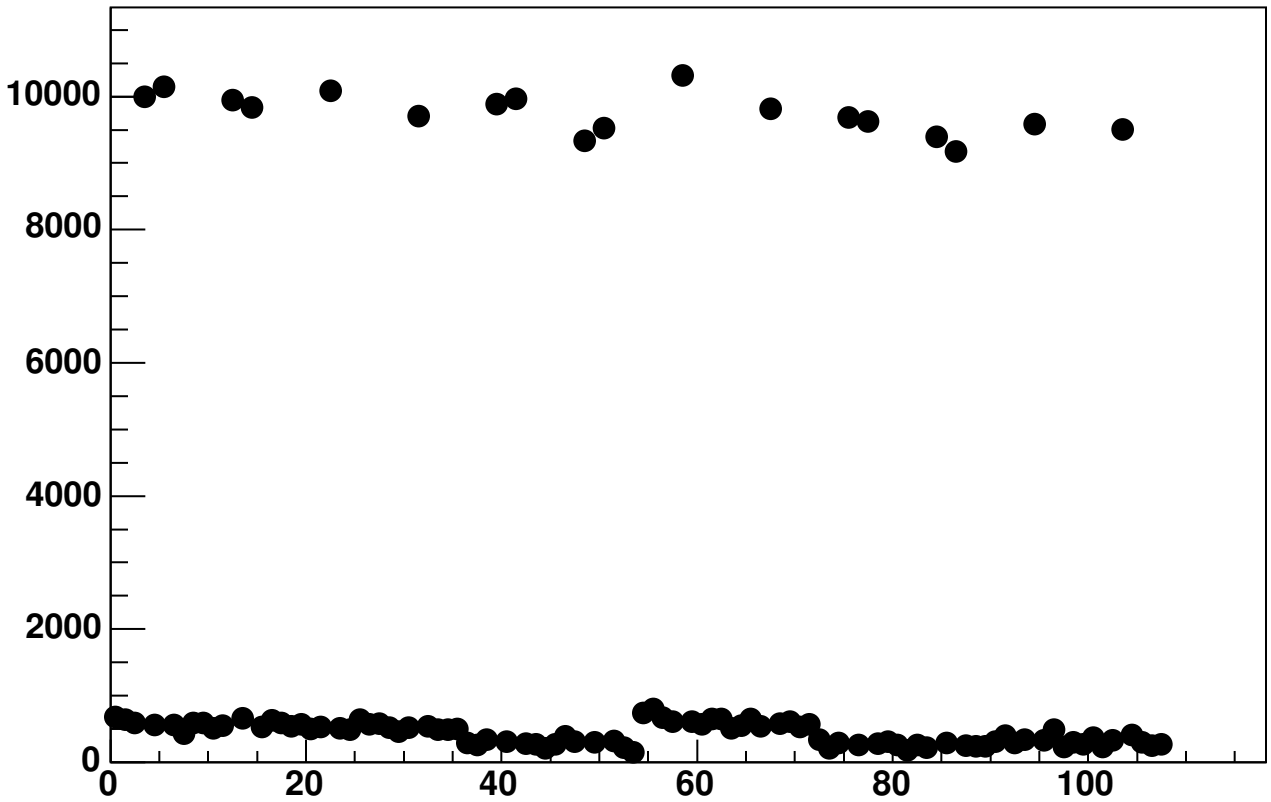
Enable 1, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



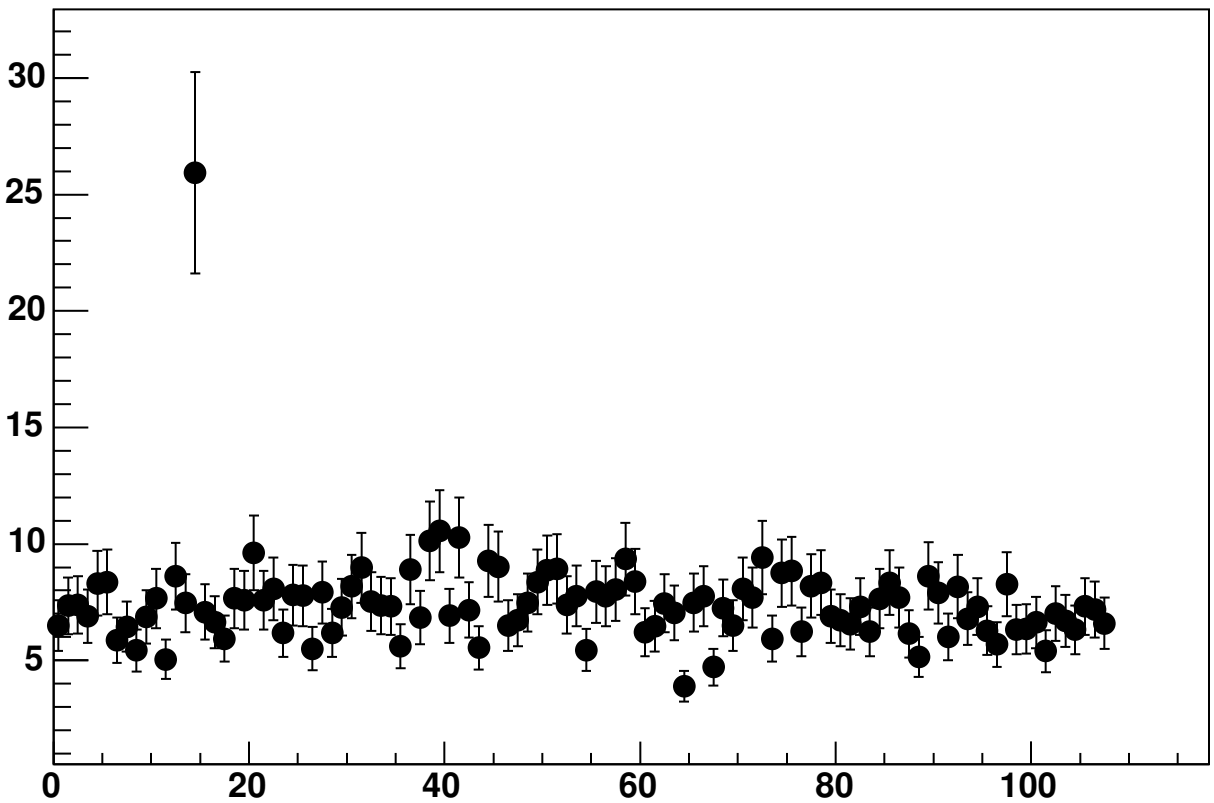
Enable 1, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



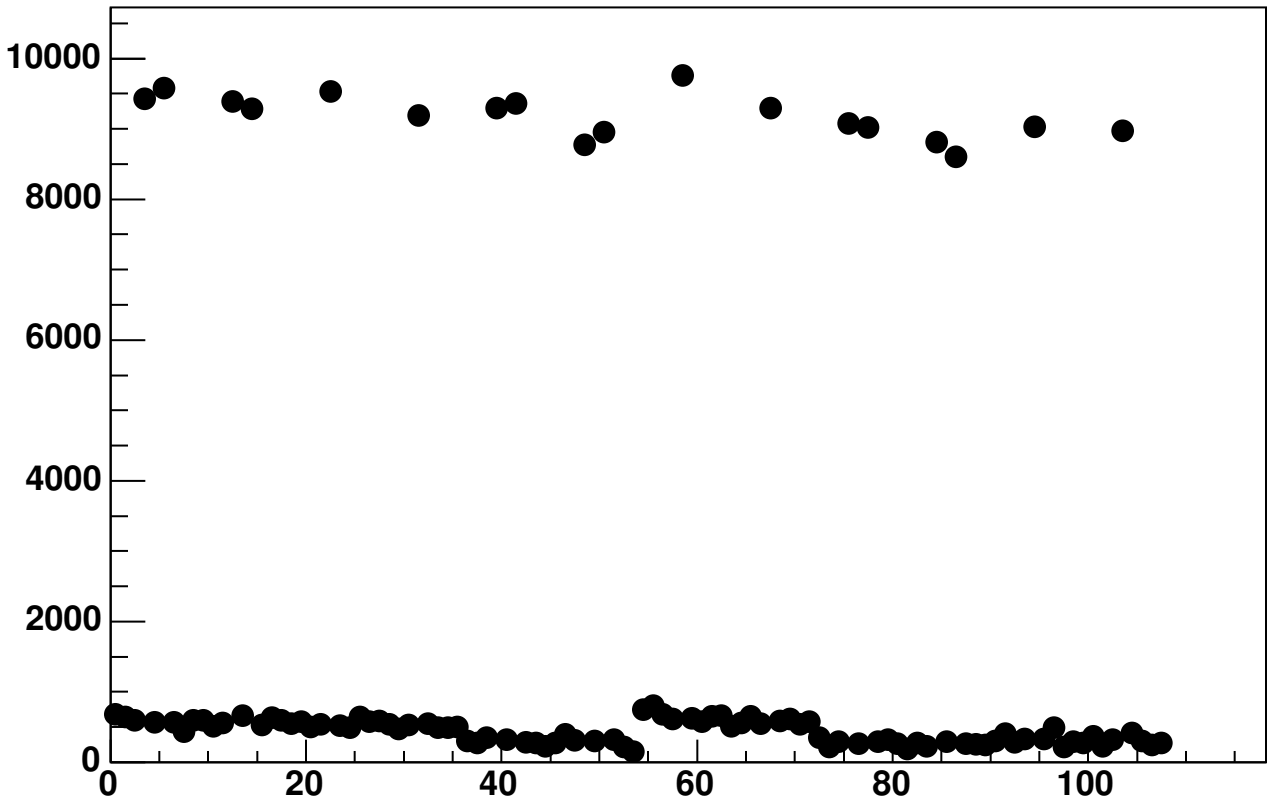
Enable 1, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



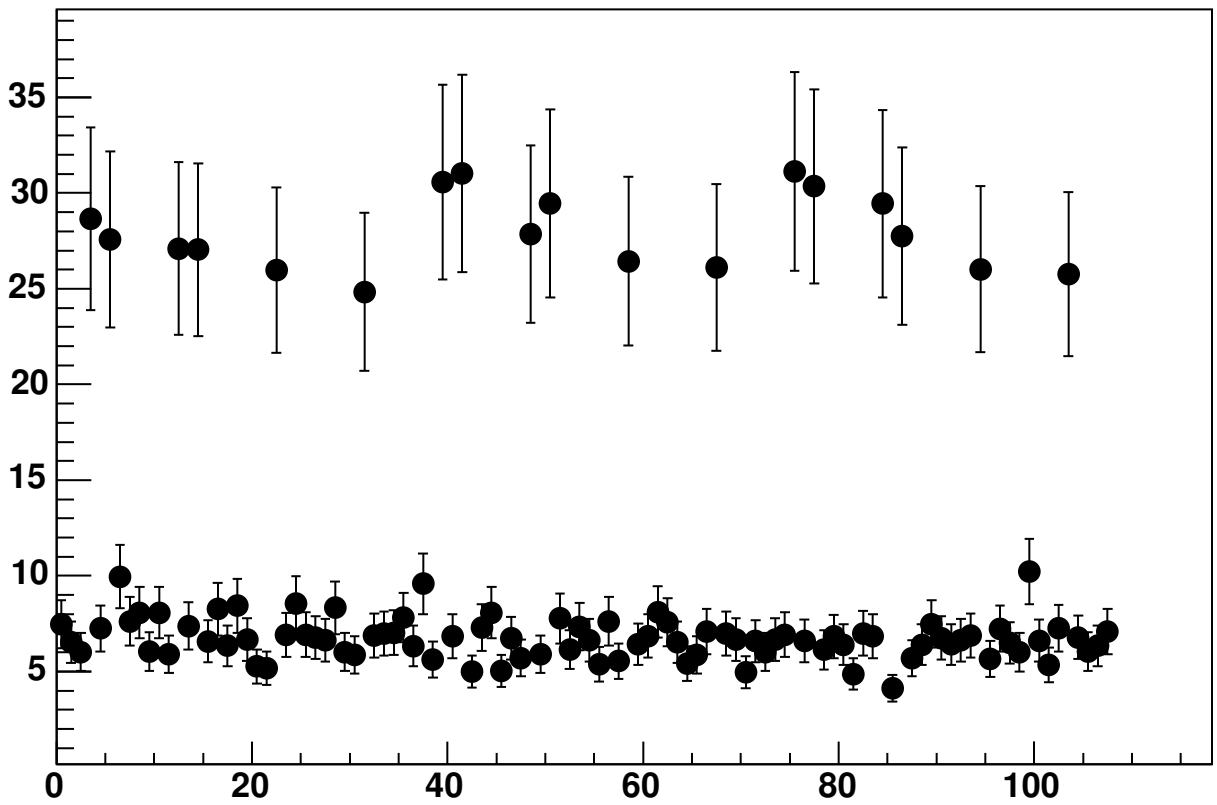
Enable 1, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



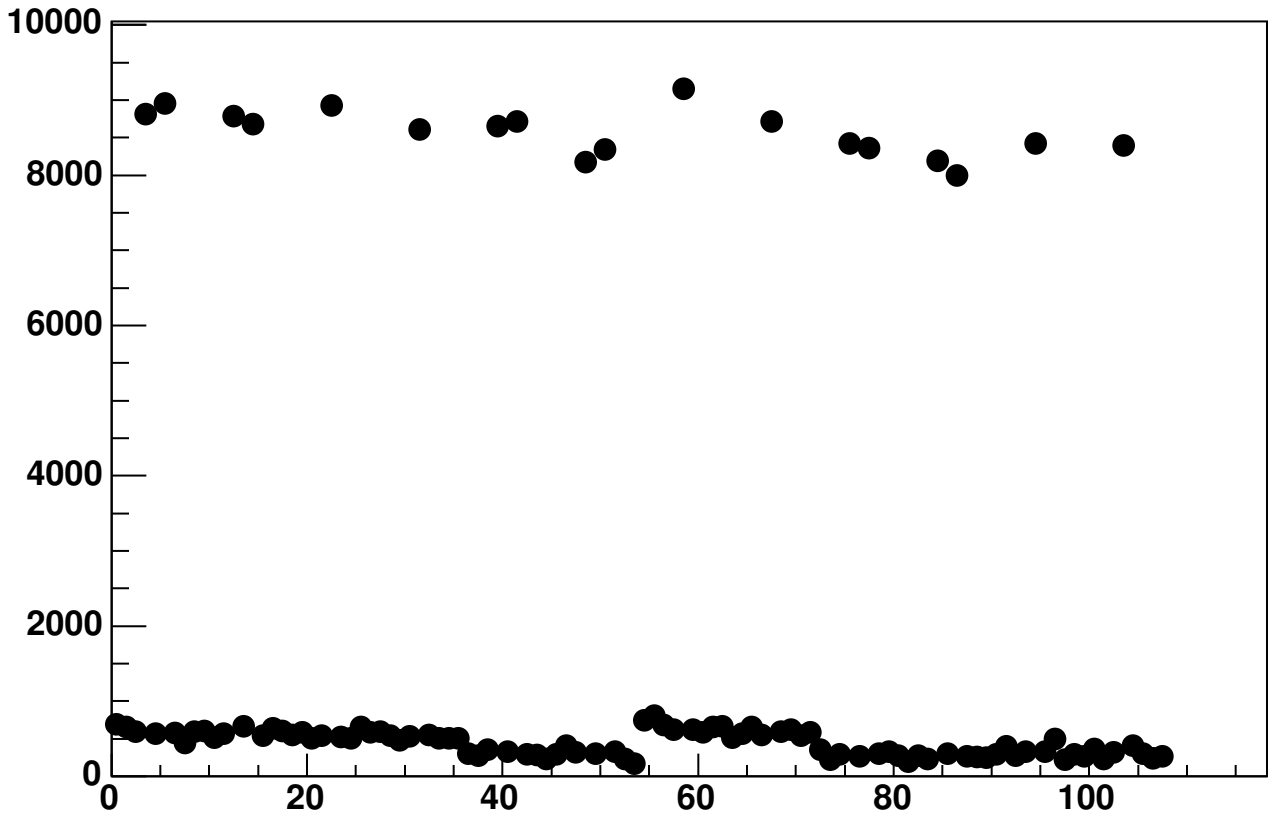
Enable 1, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



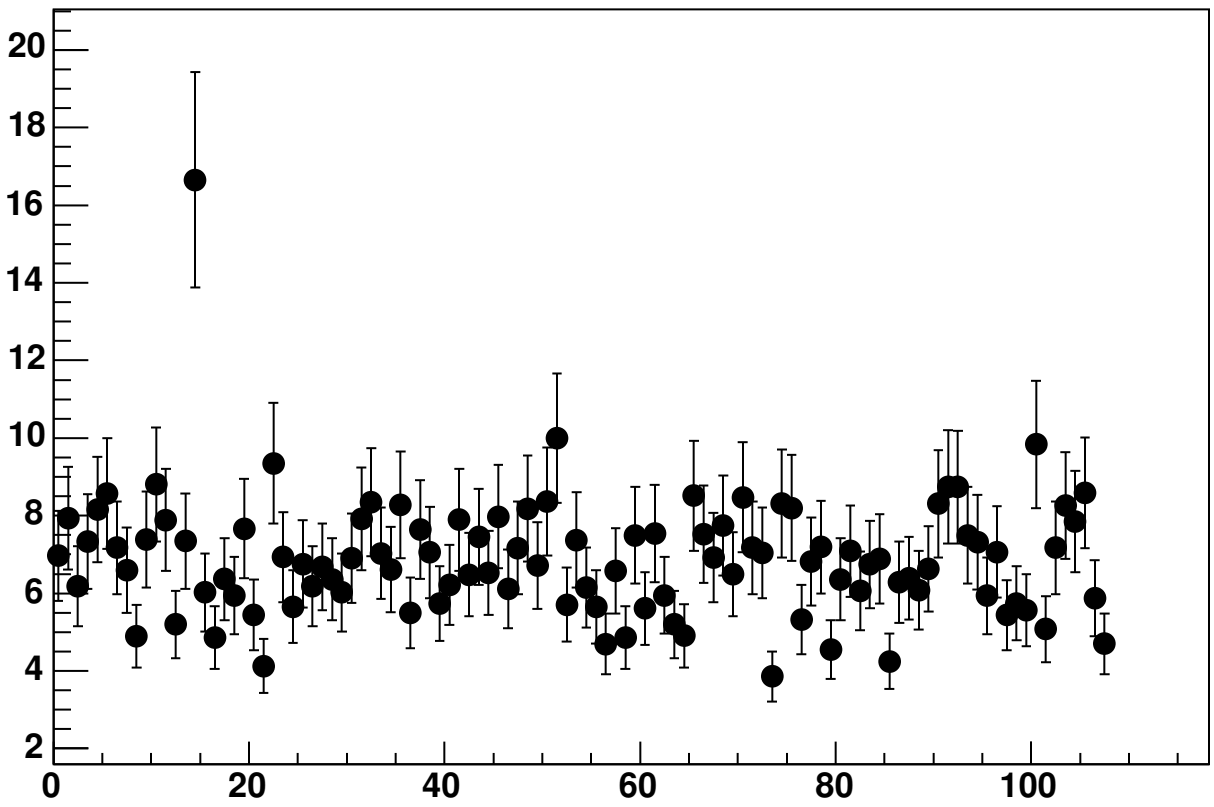
Enable 1, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



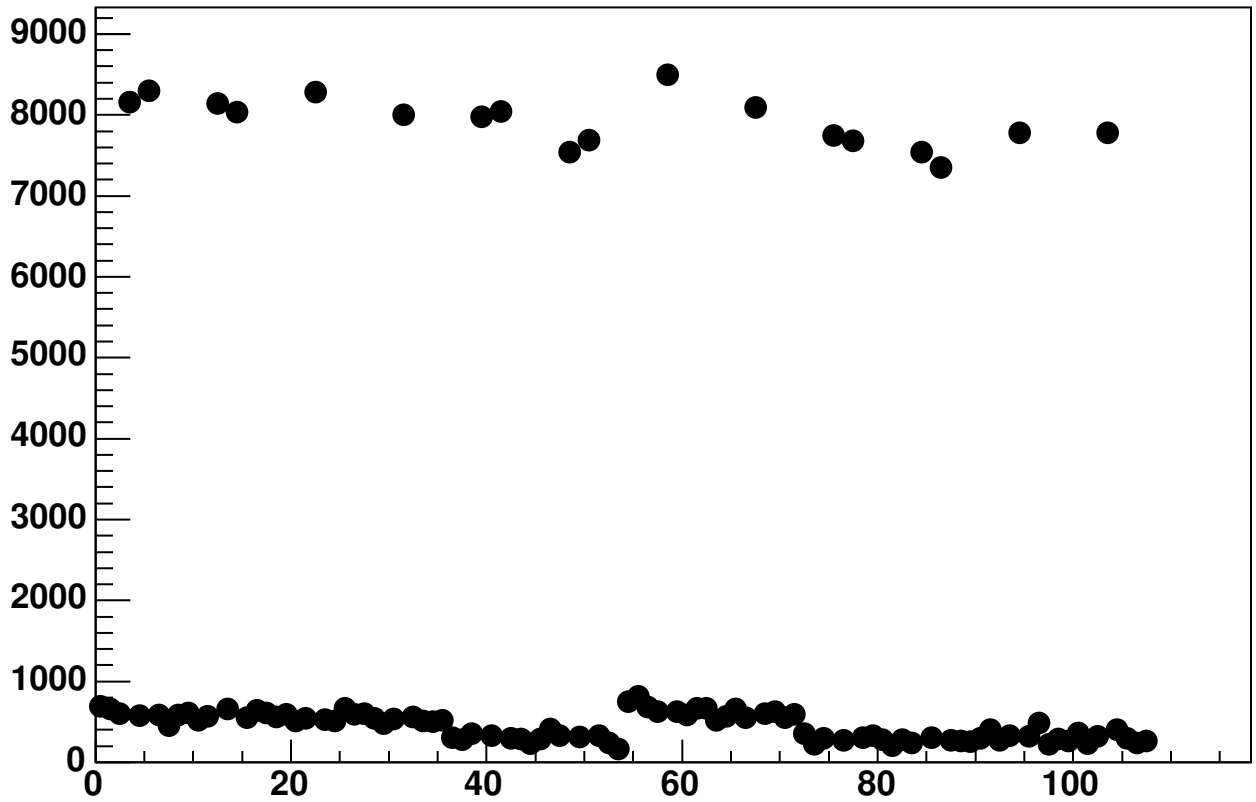
Enable 1, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



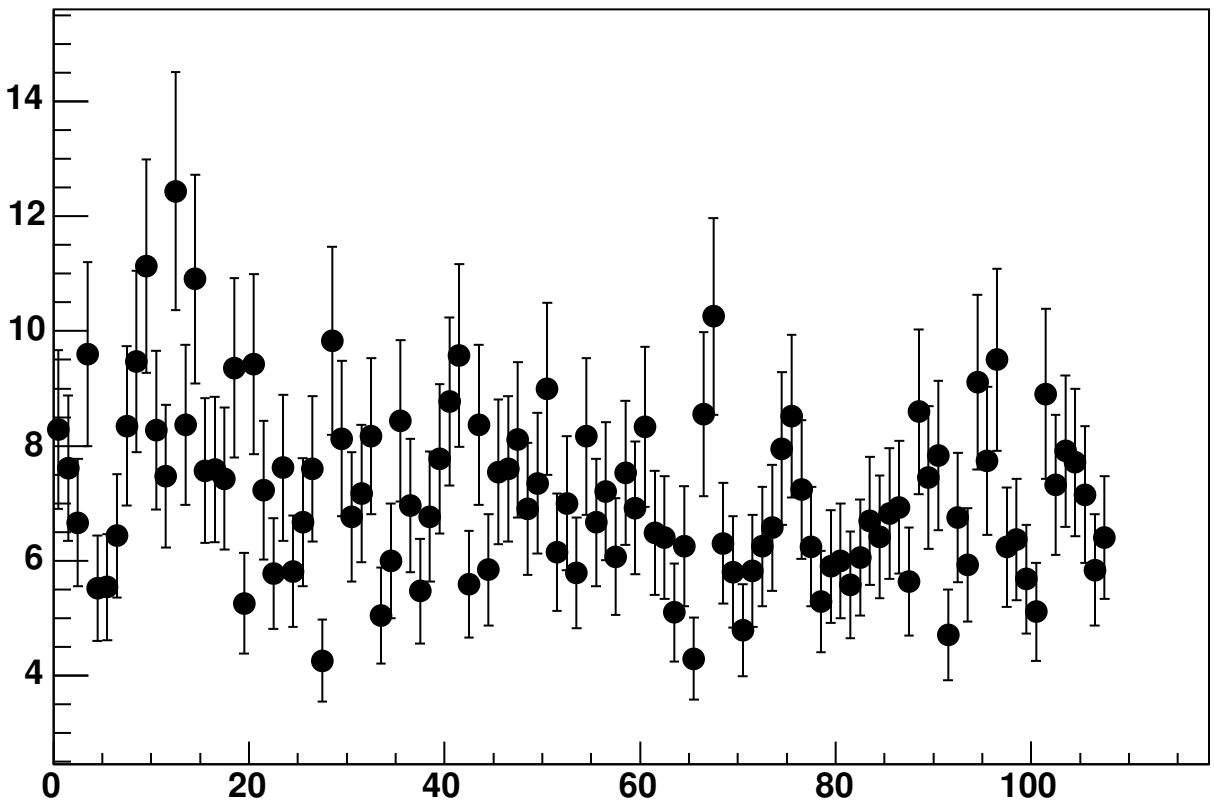
Enable 1, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



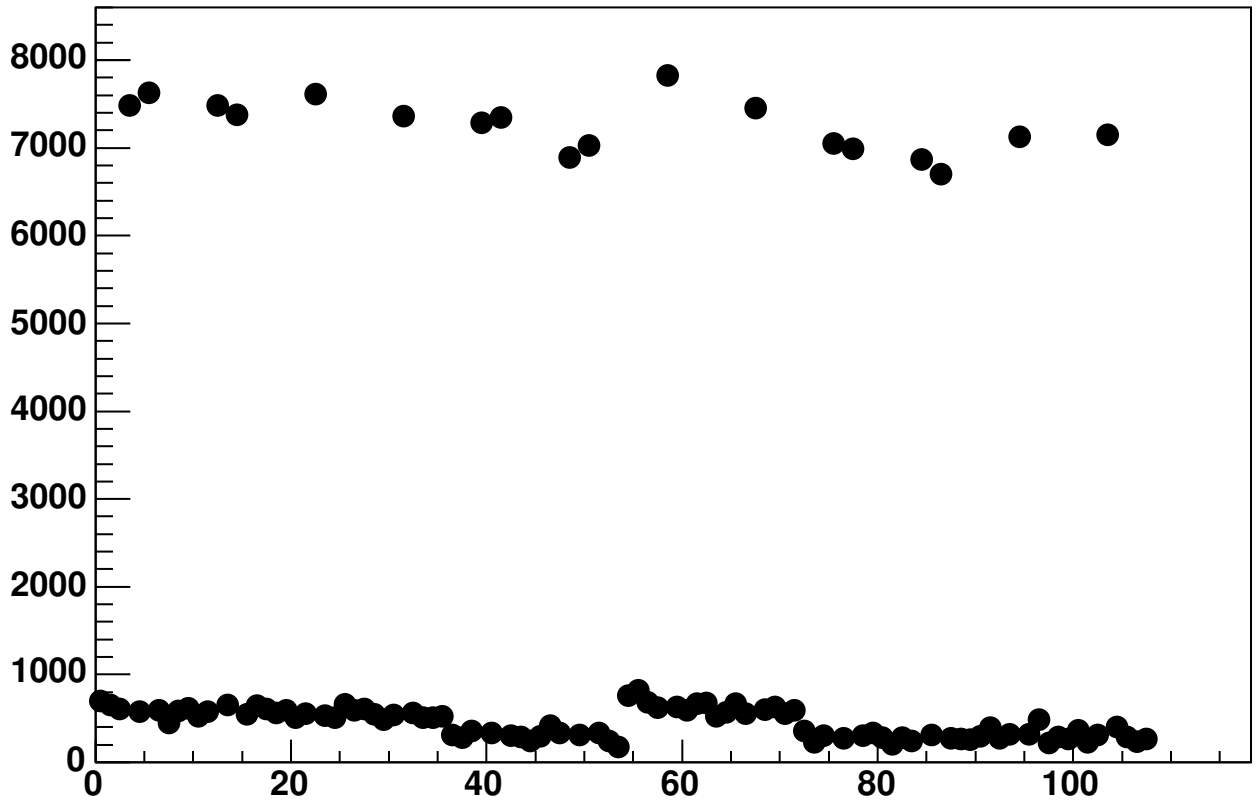
Enable 1, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



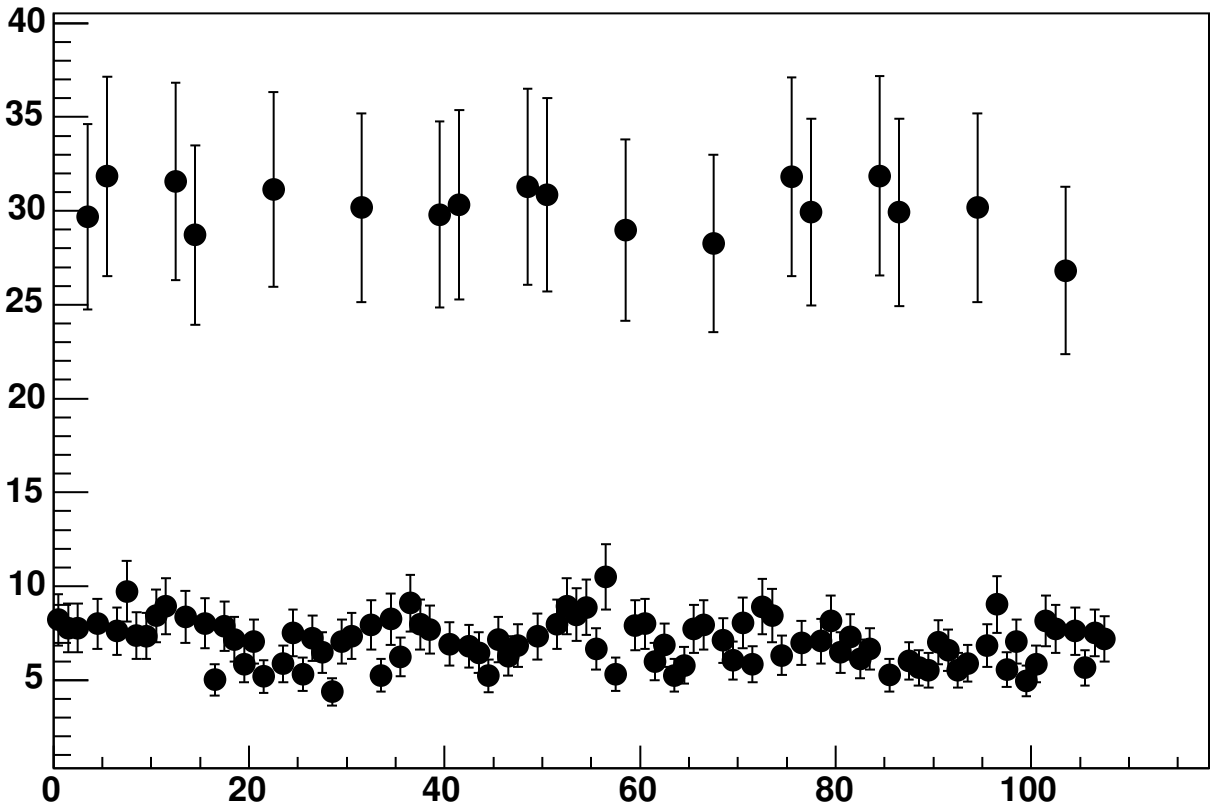
Enable 1, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



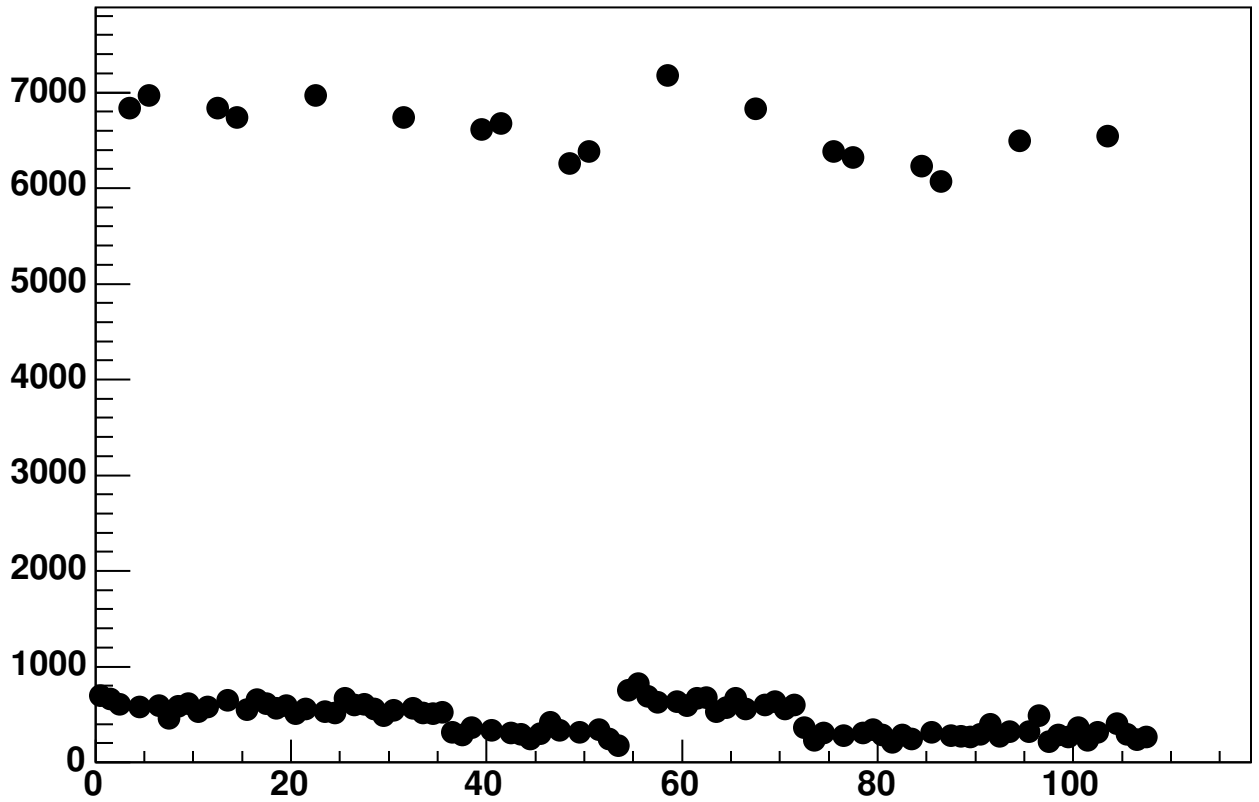
Enable 1, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



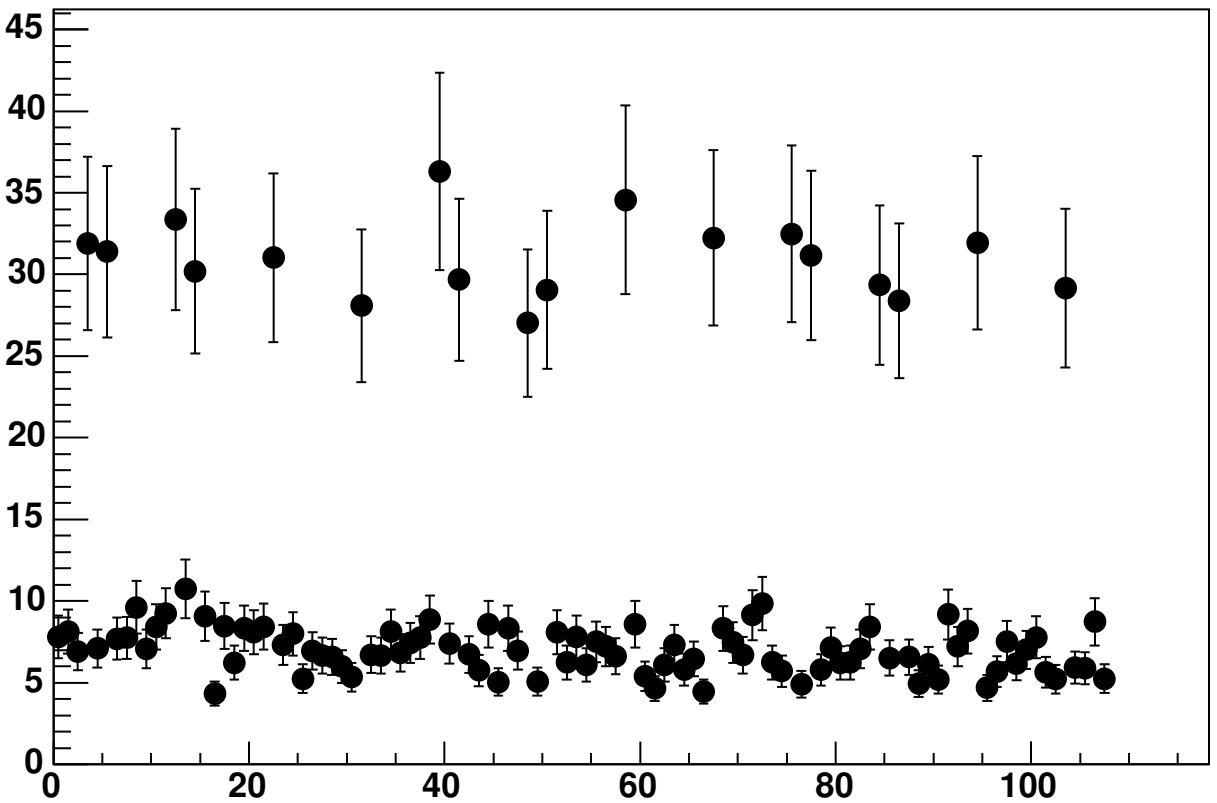
Enable 1, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



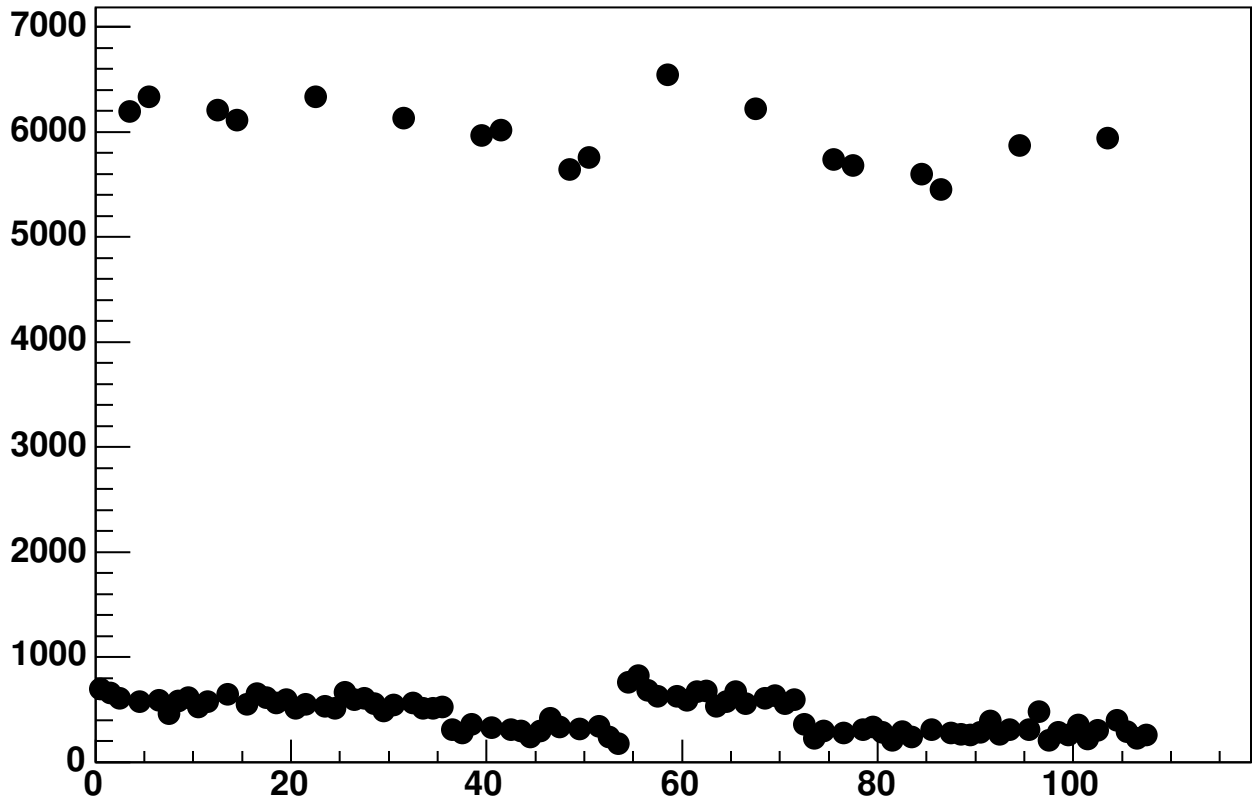
Enable 1, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



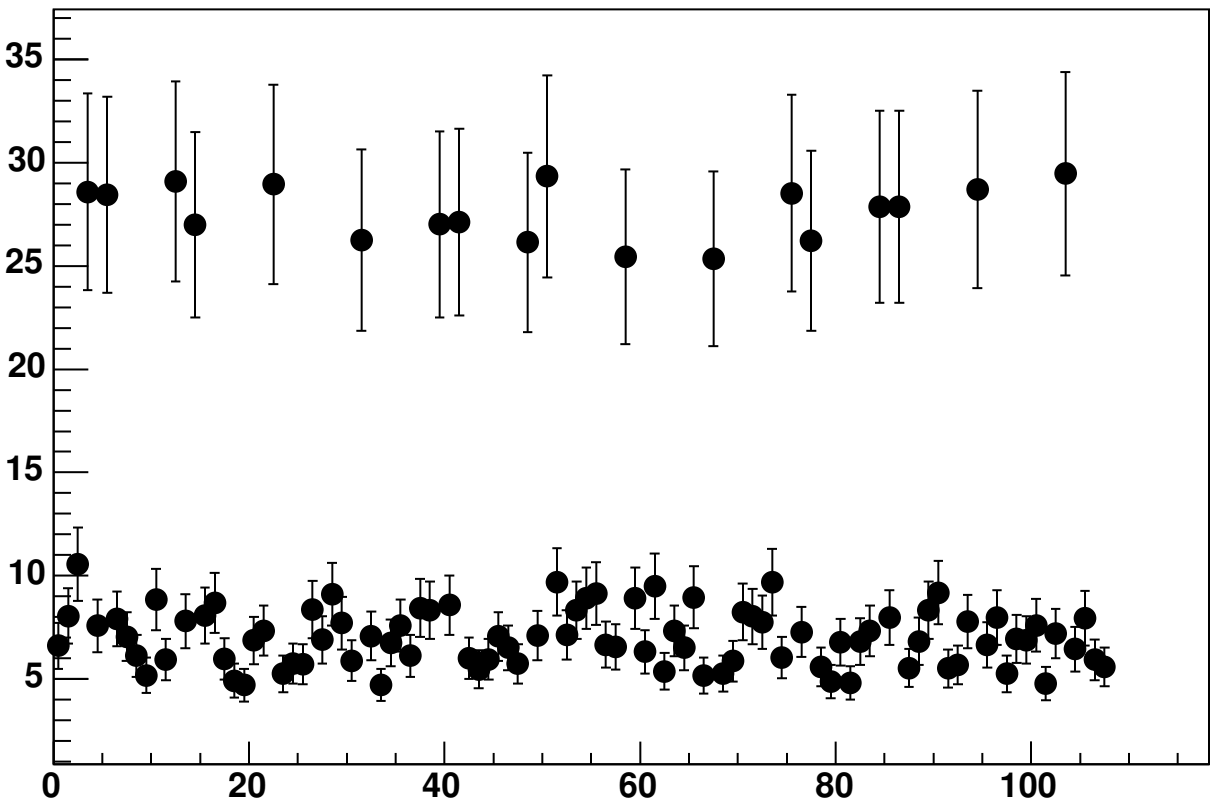
Enable 1, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



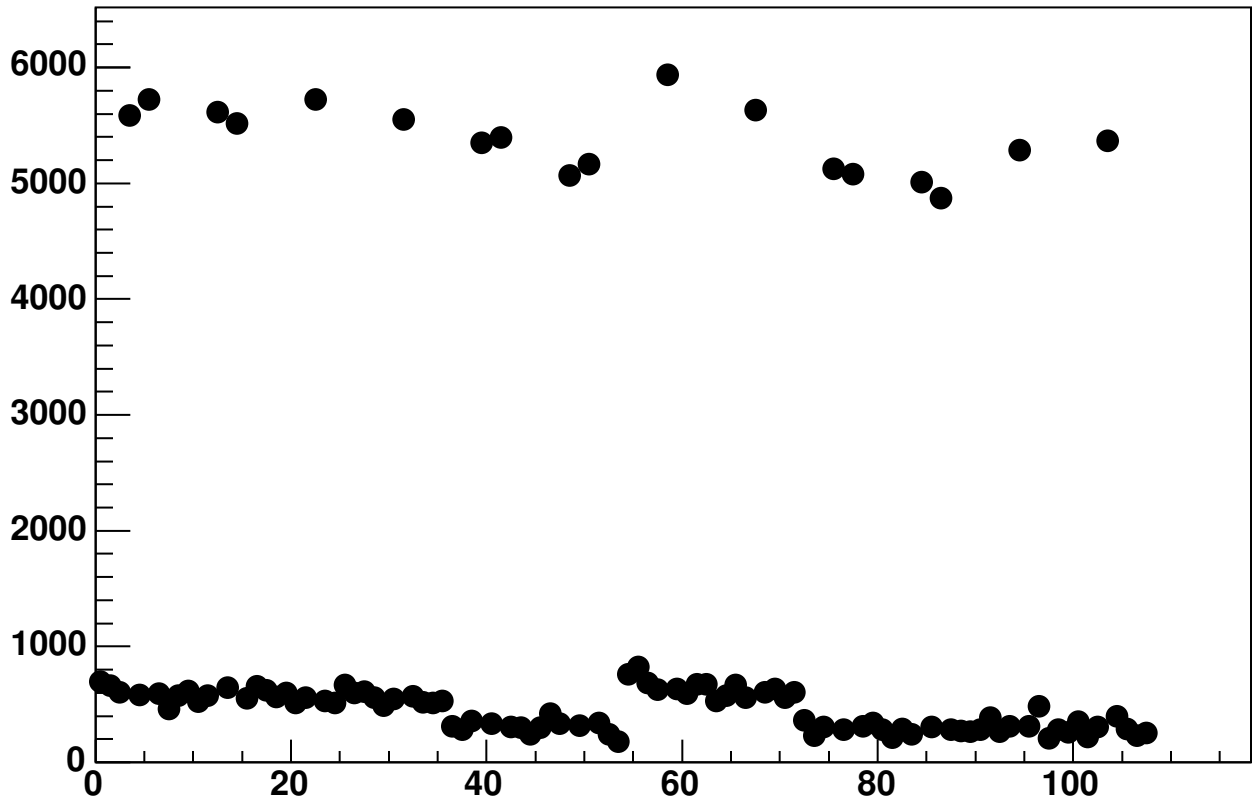
Enable 1, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



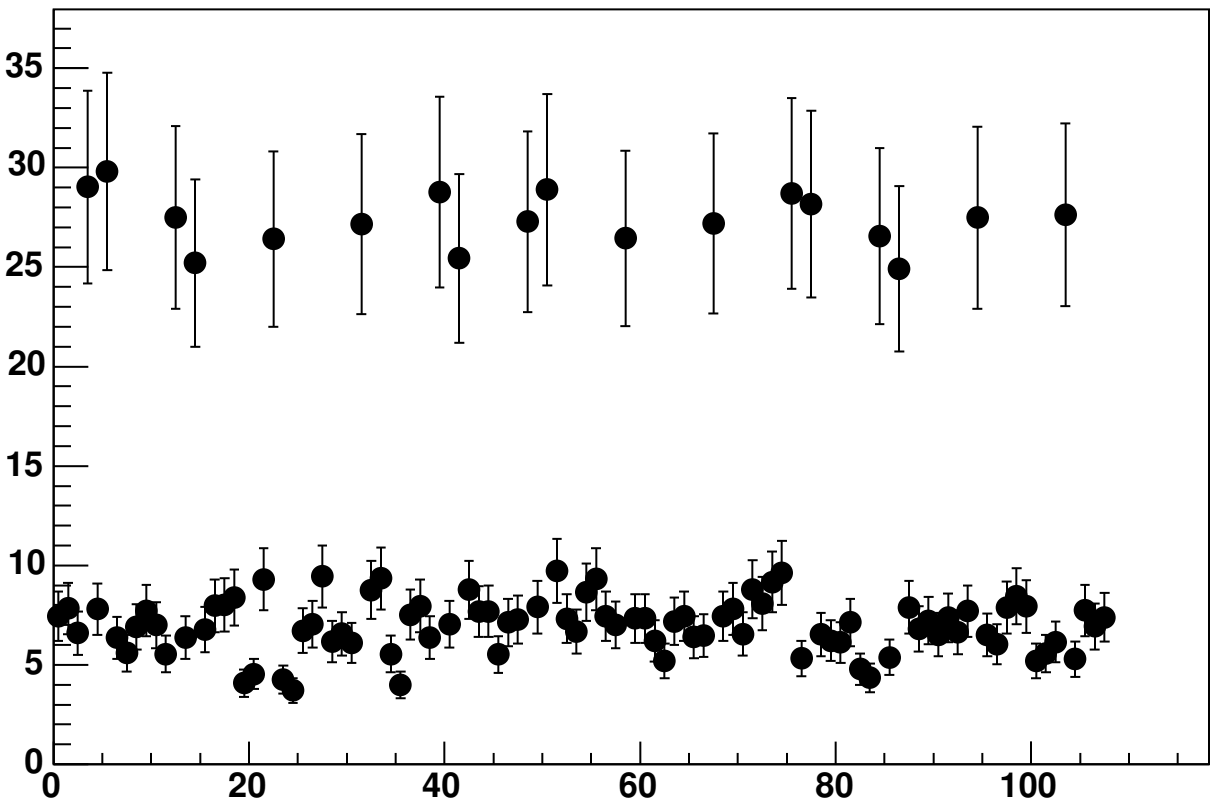
Enable 1, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



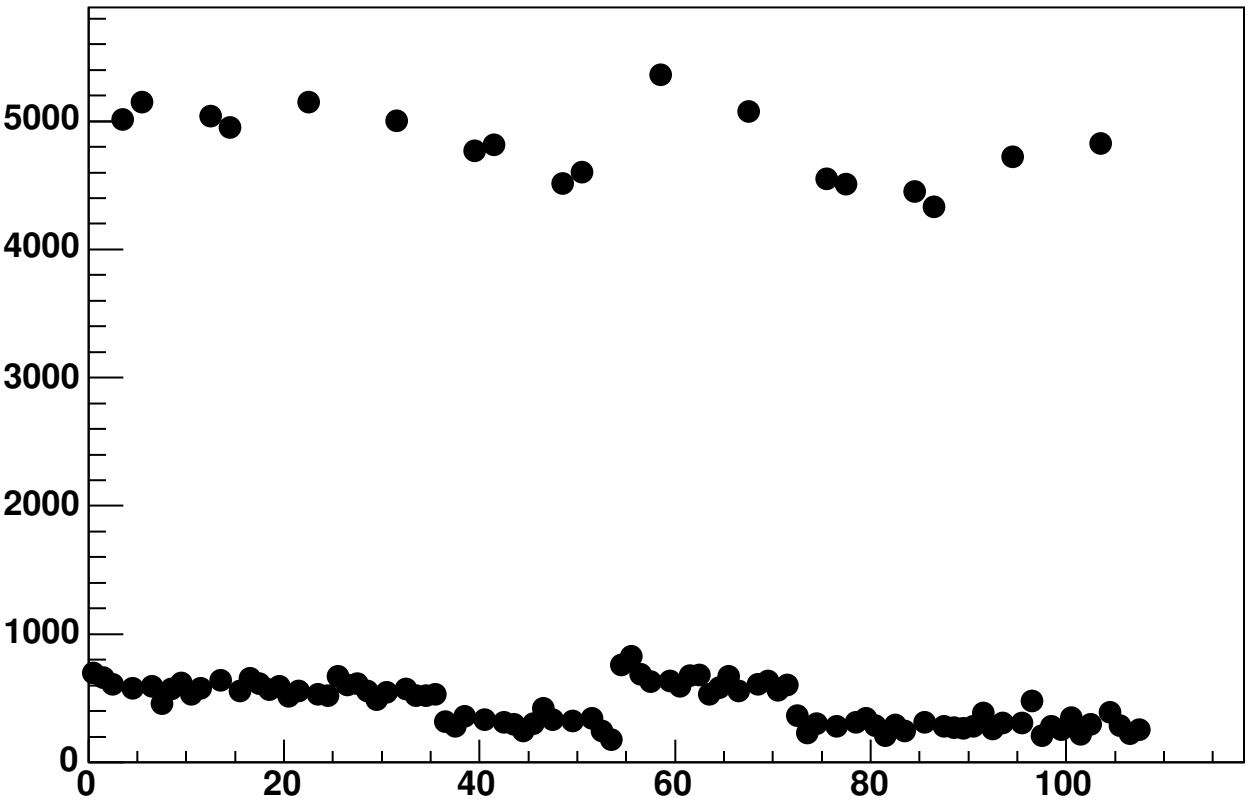
Enable 1, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



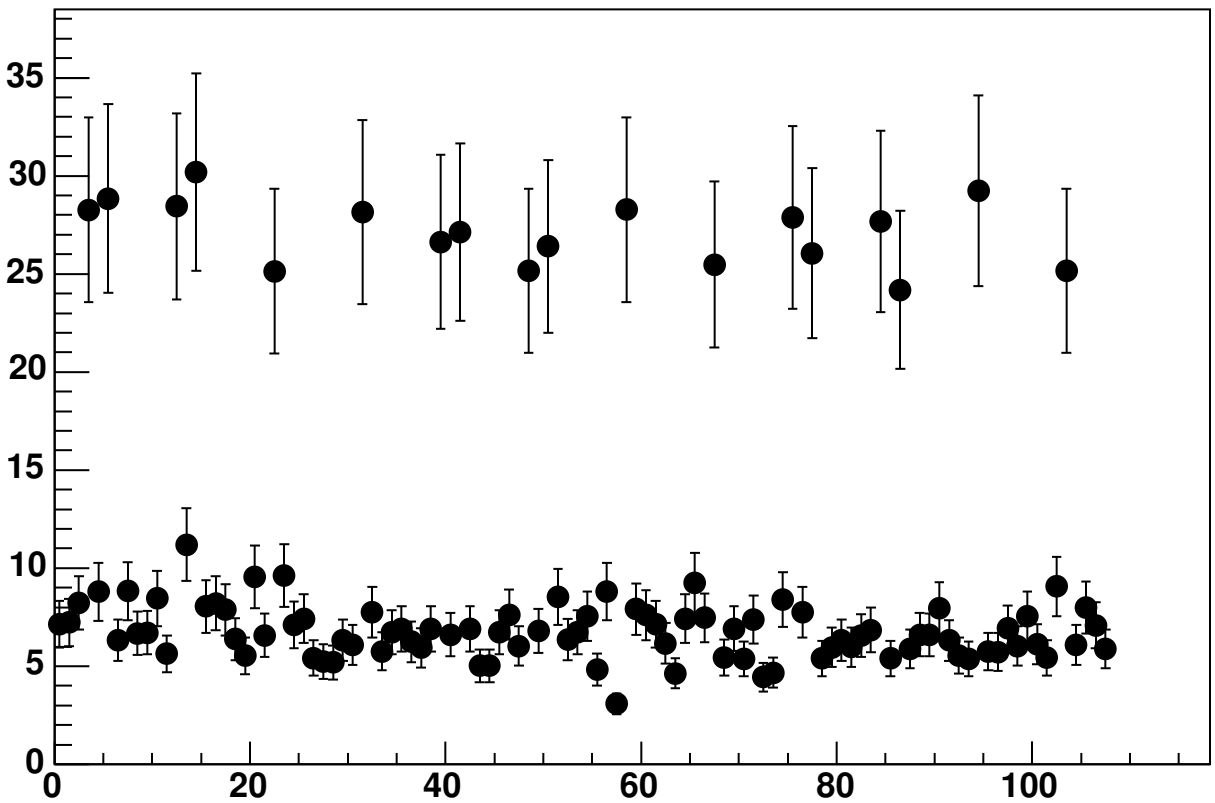
Enable 1, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



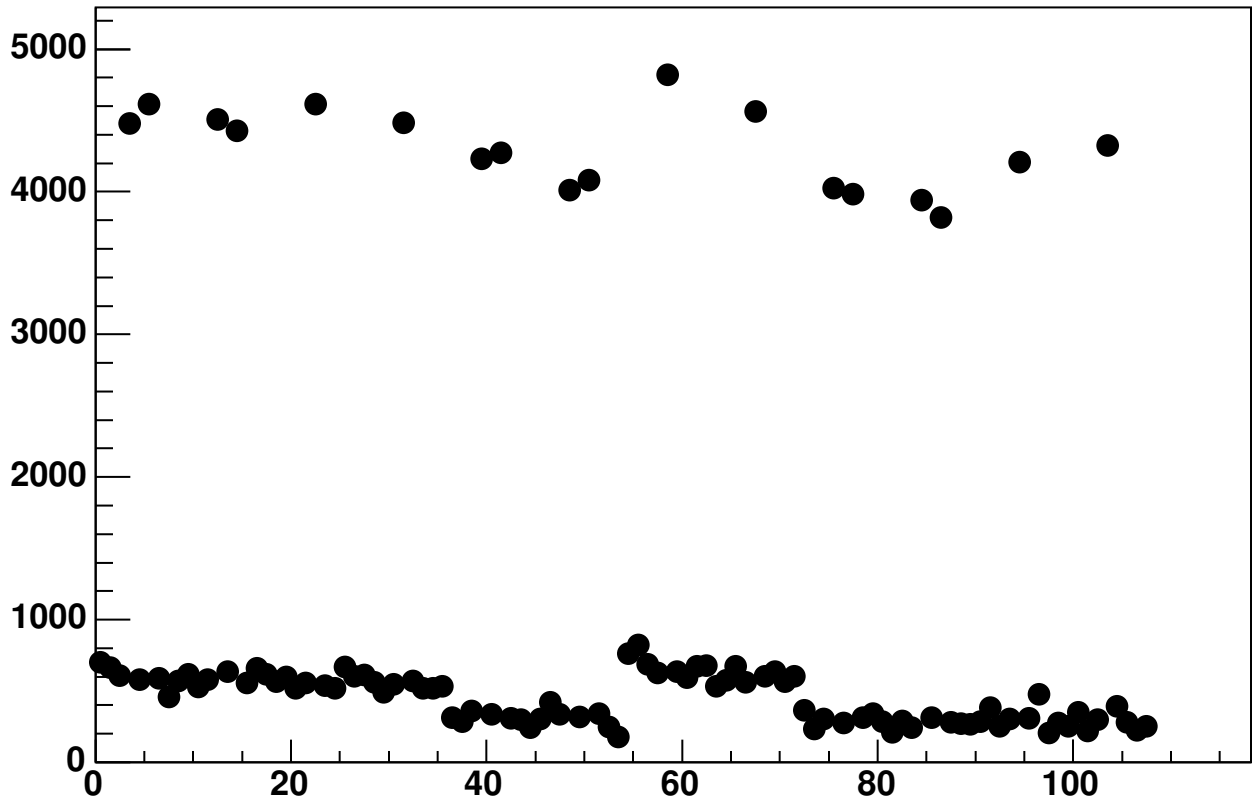
Enable 1, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



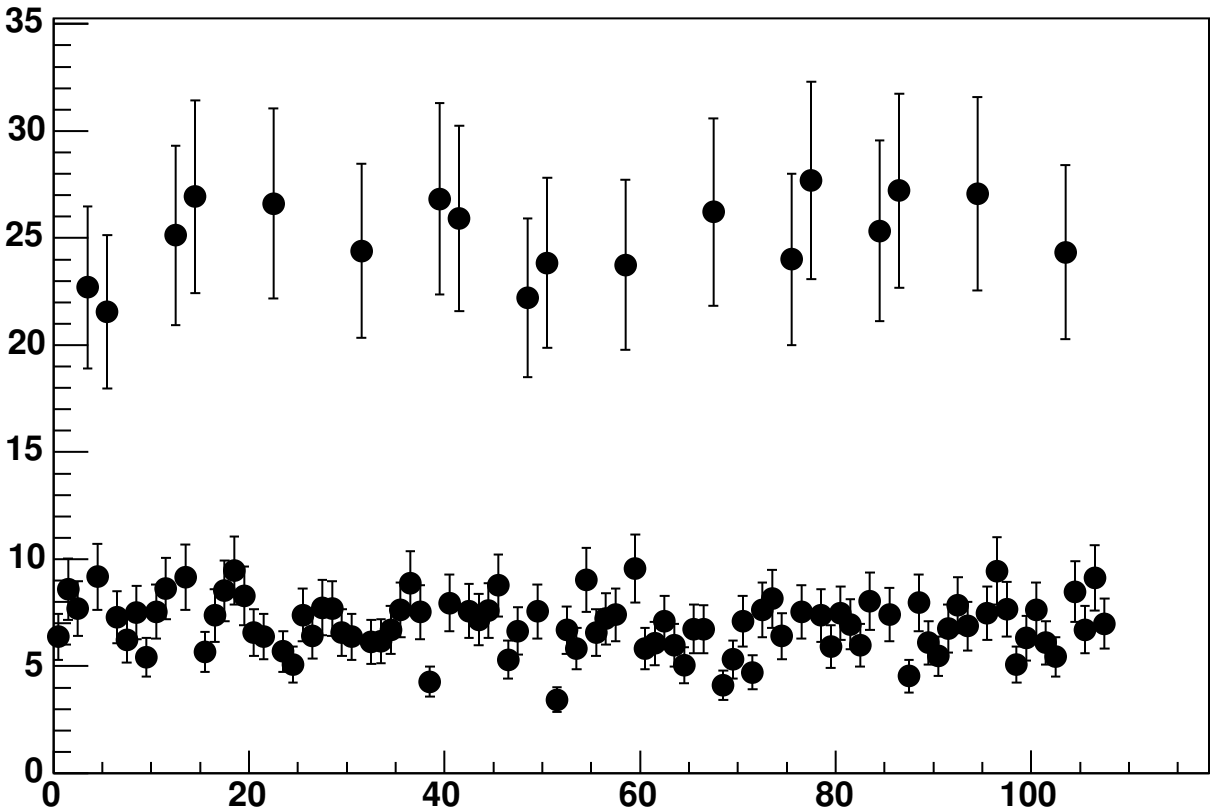
Enable 1, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



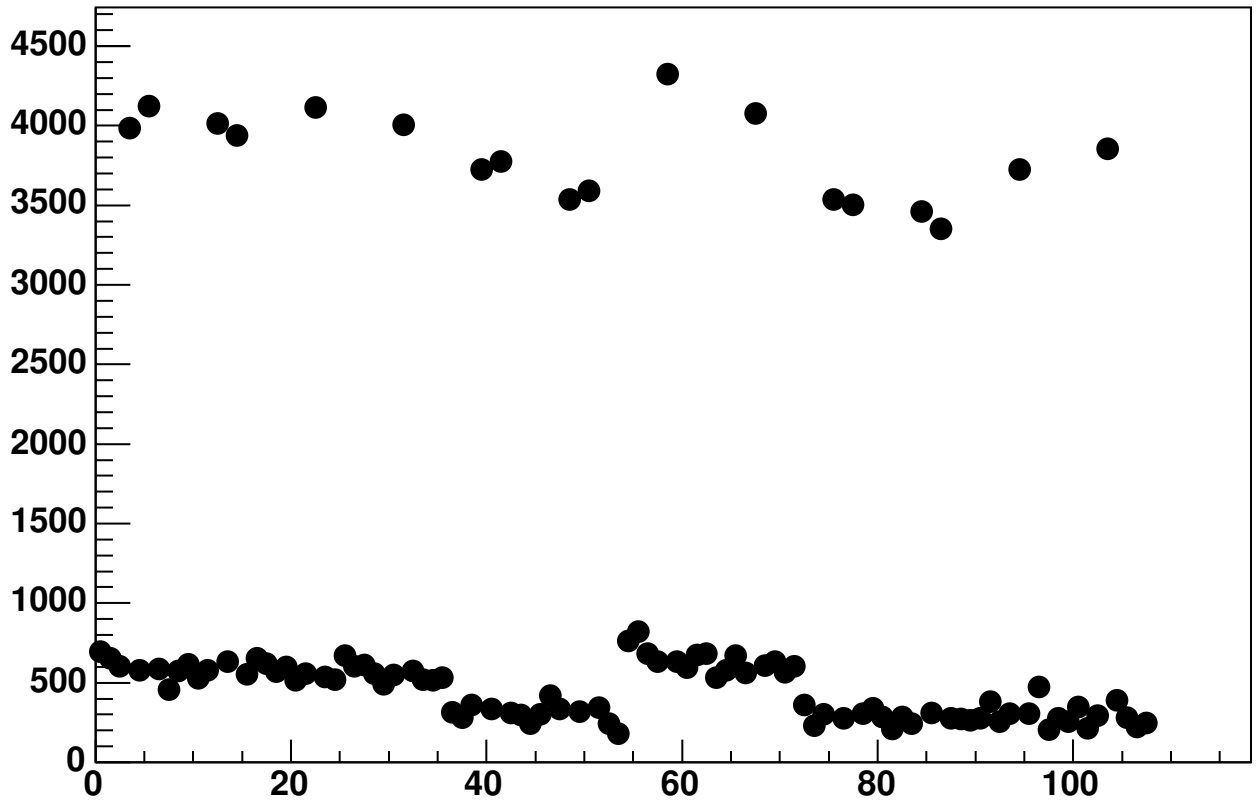
Enable 1, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



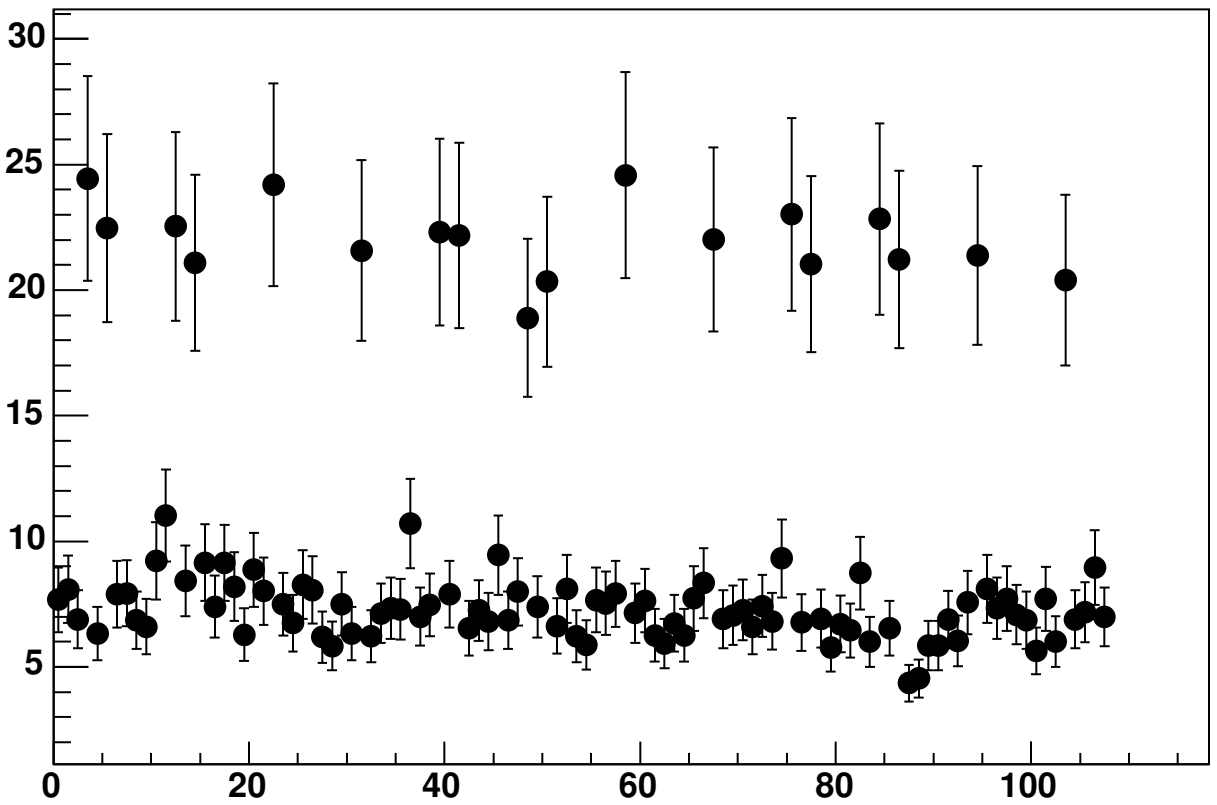
Enable 1, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



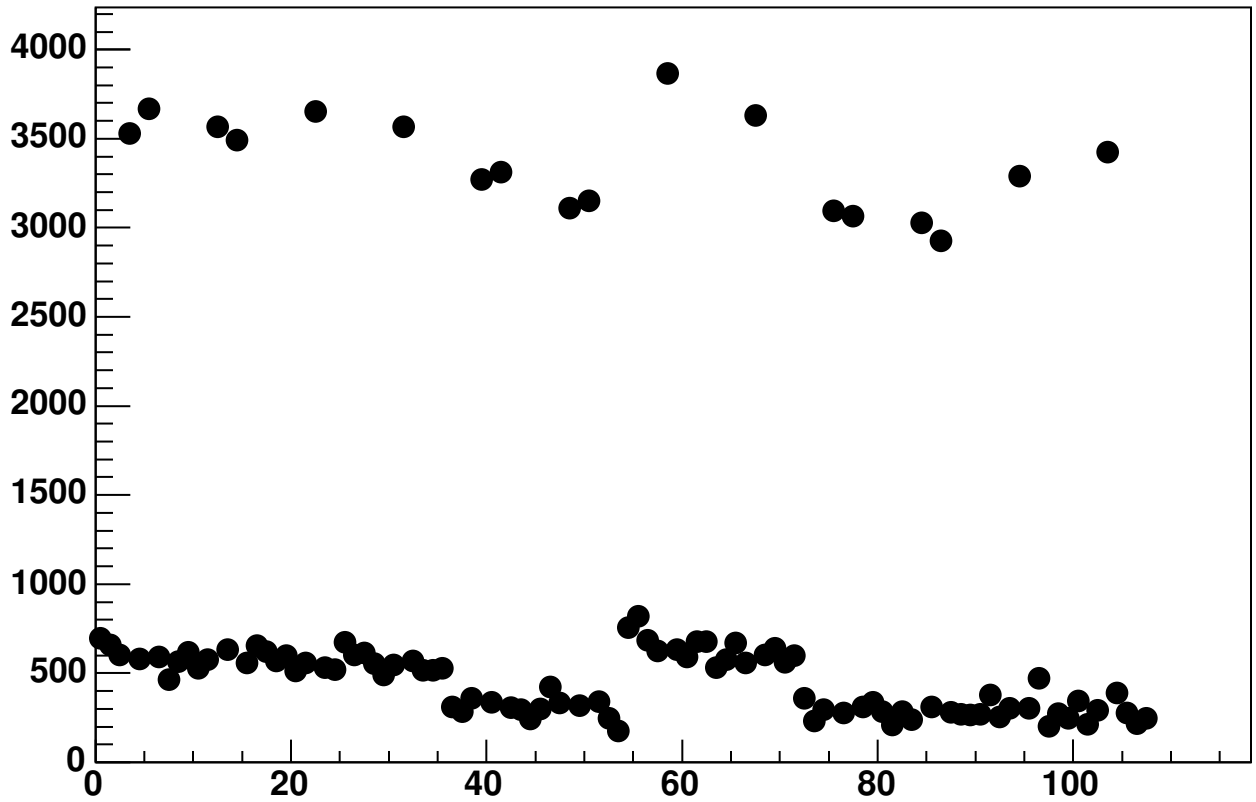
Enable 1, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



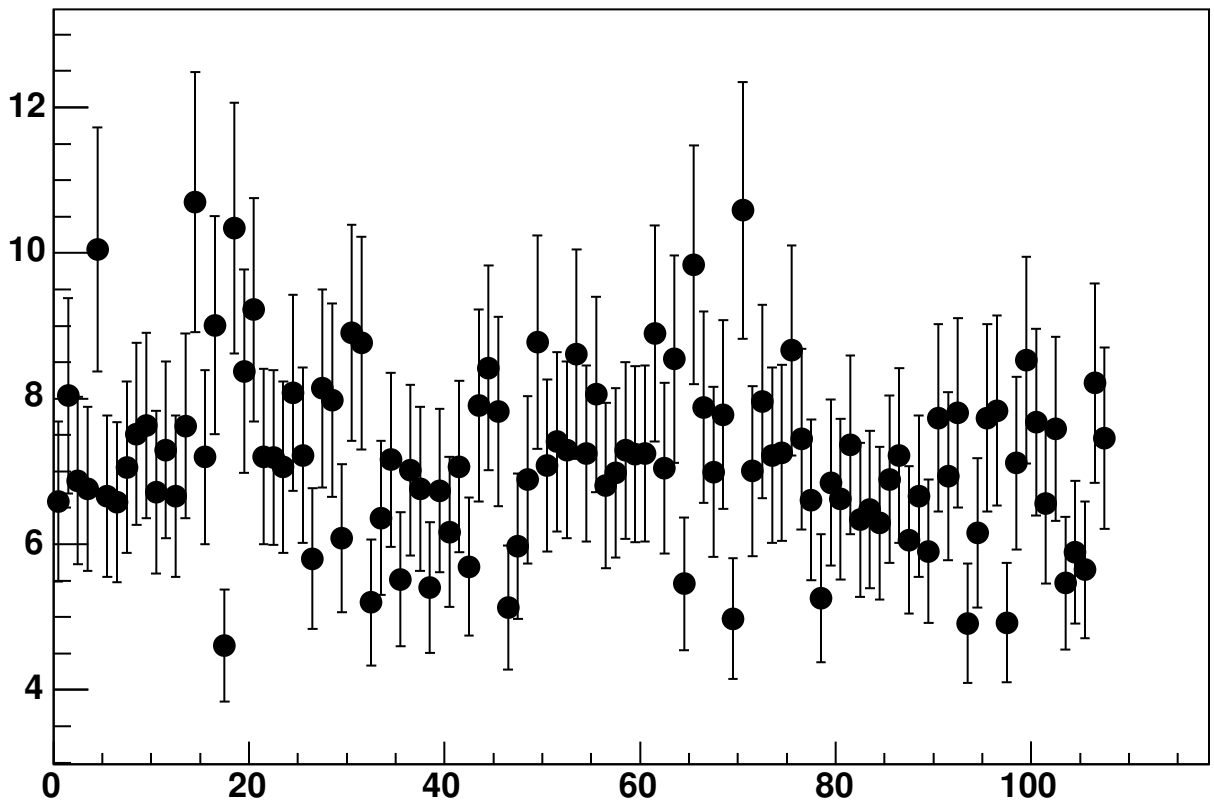
Enable 1, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



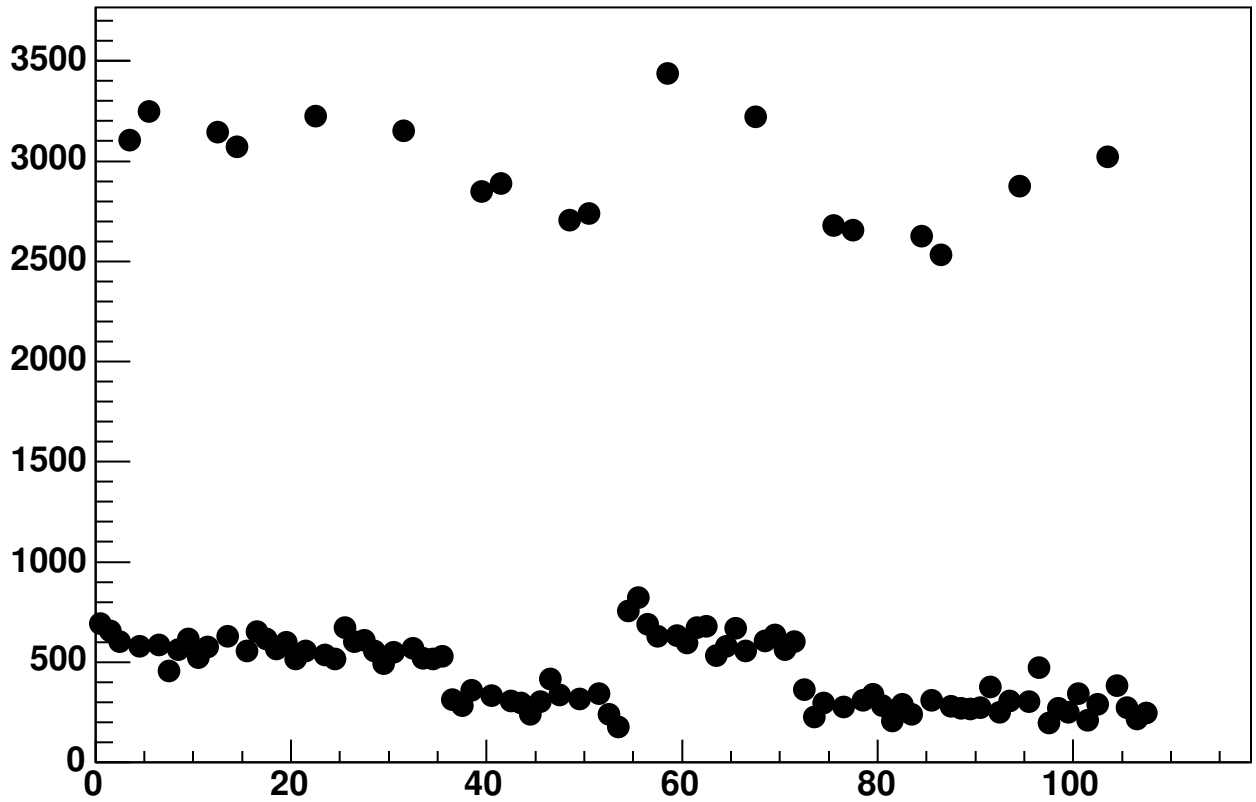
Enable 1, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



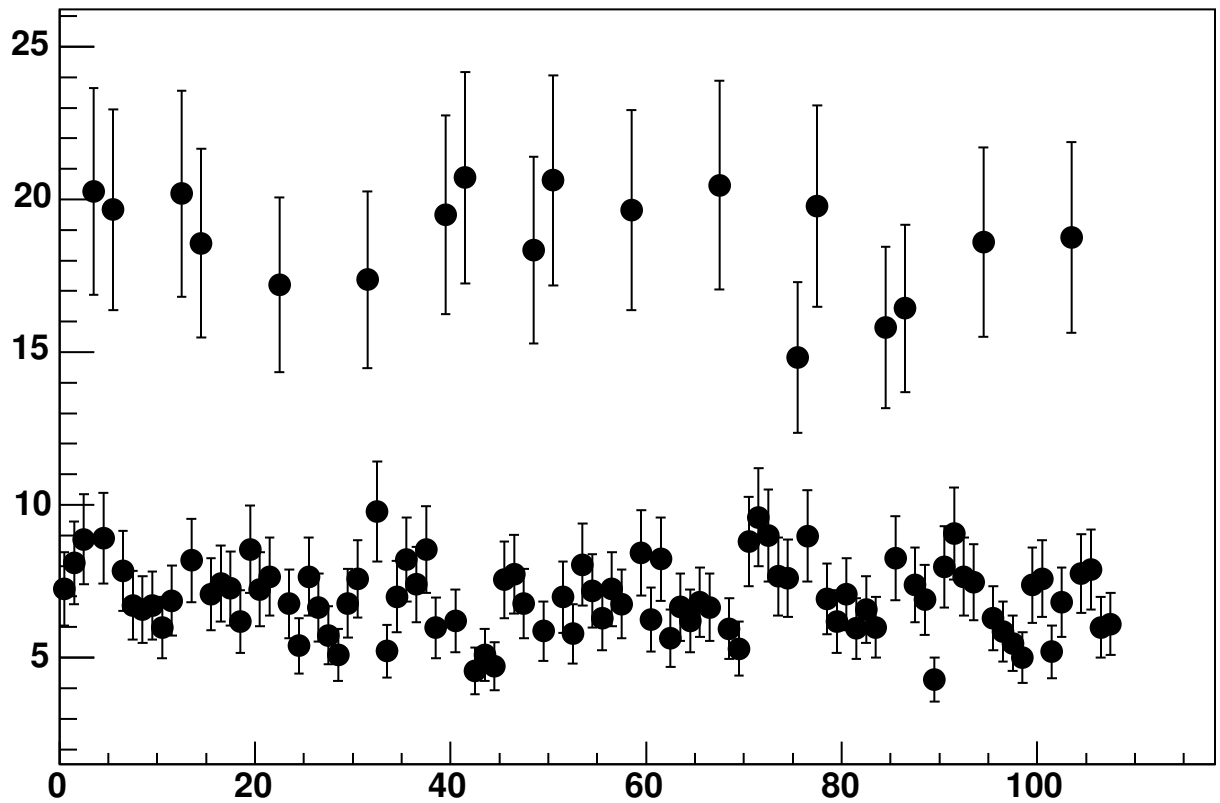
Enable 1, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



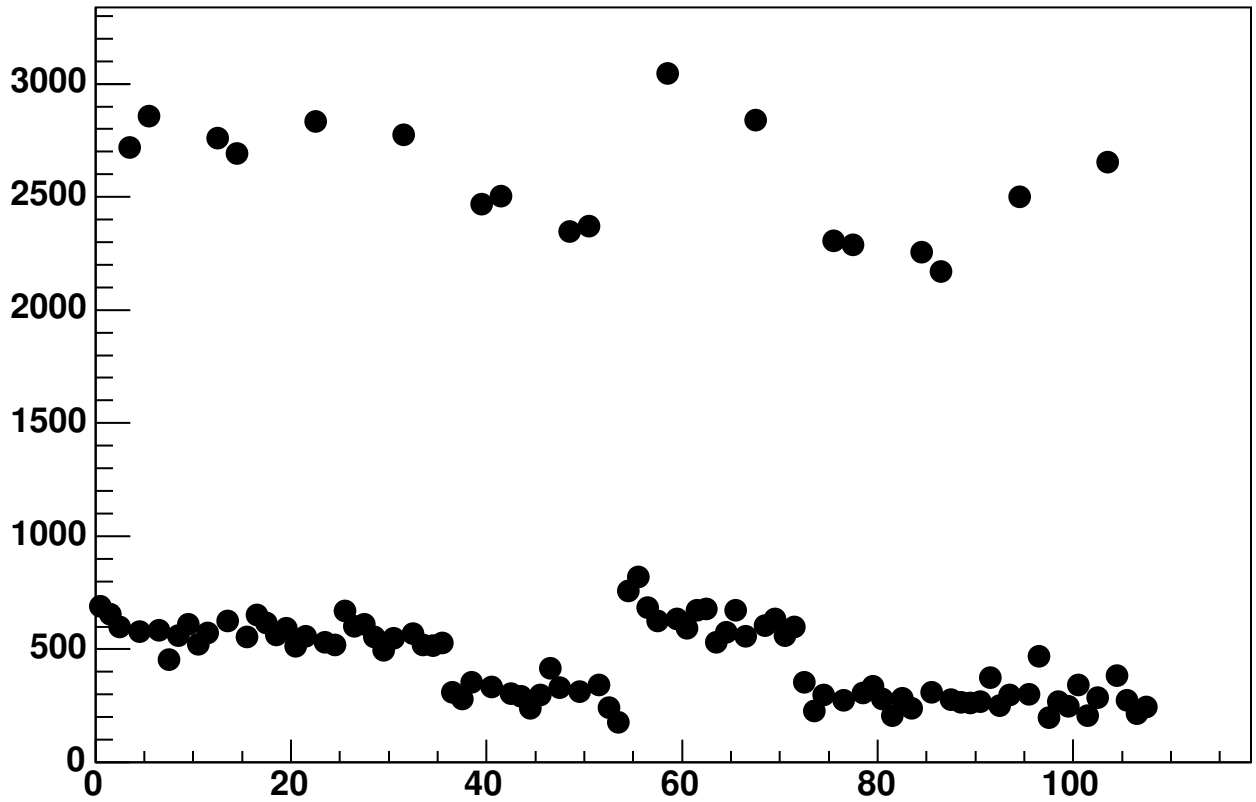
Enable 1, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



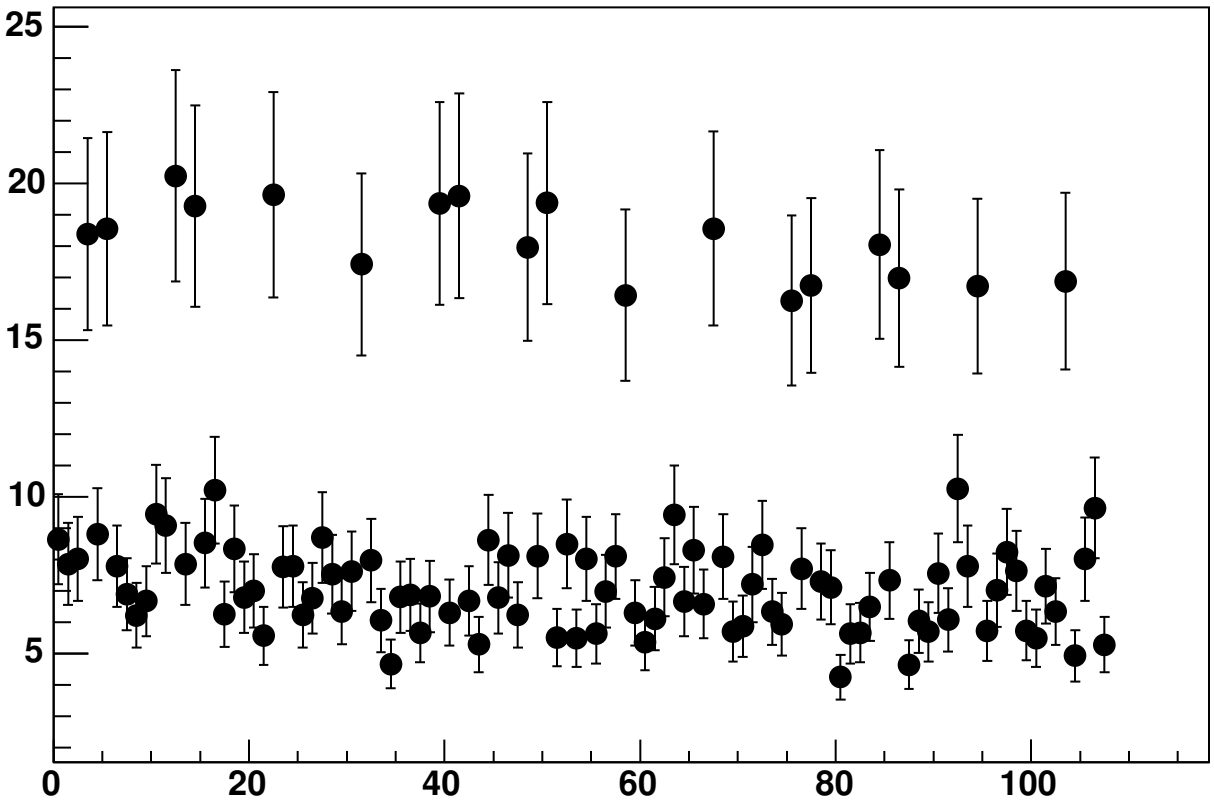
Enable 1, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



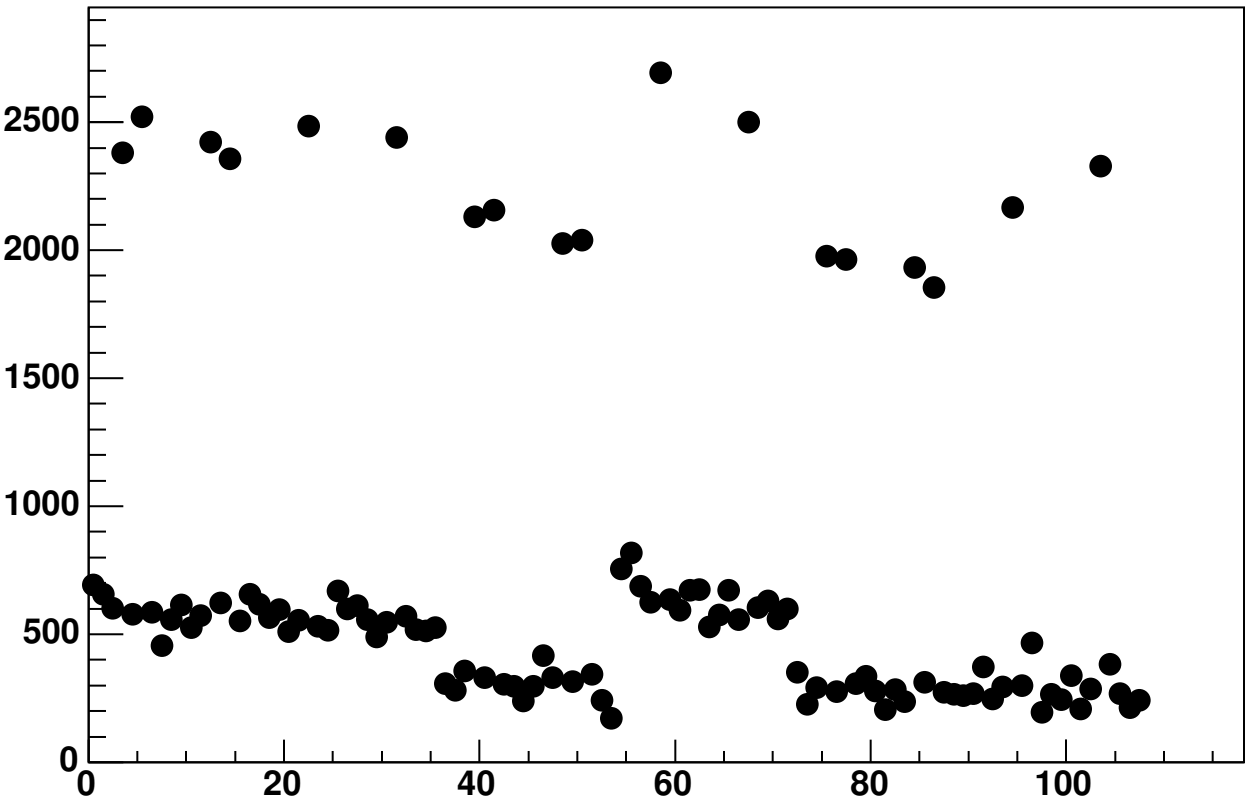
Enable 1, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



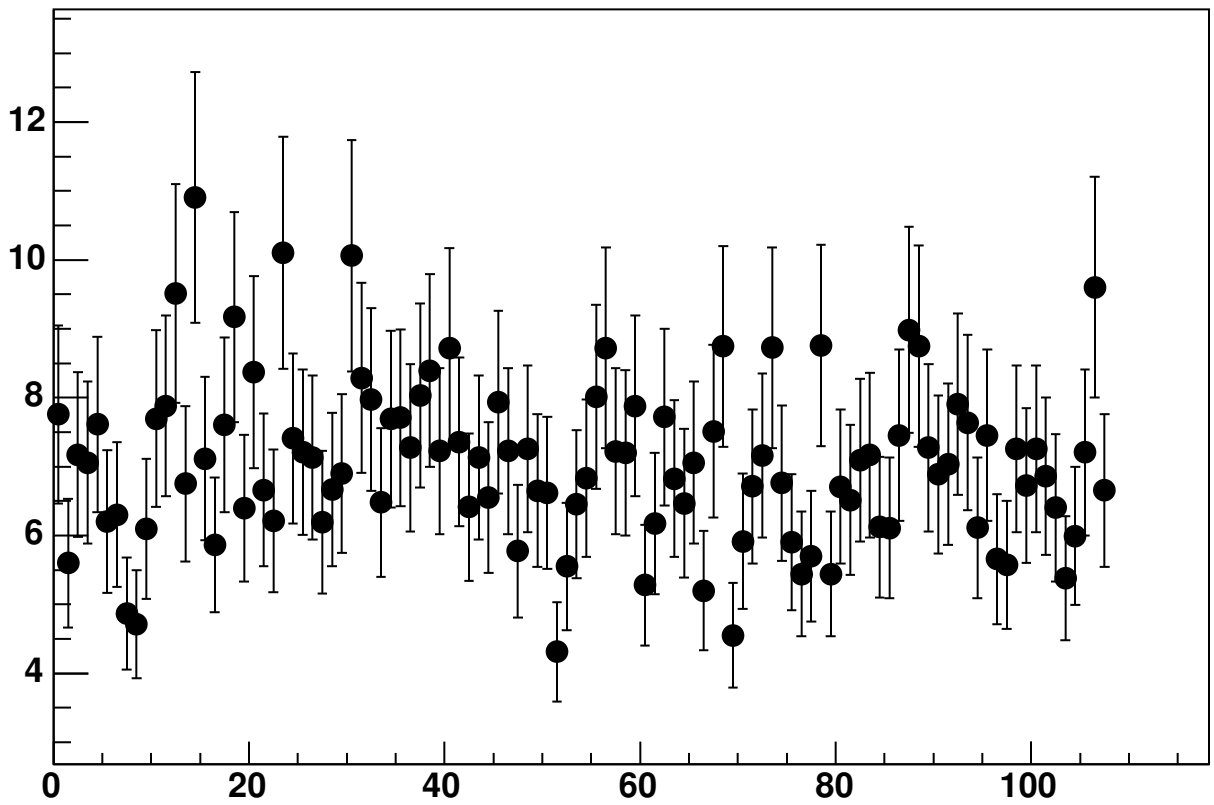
Enable 1, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



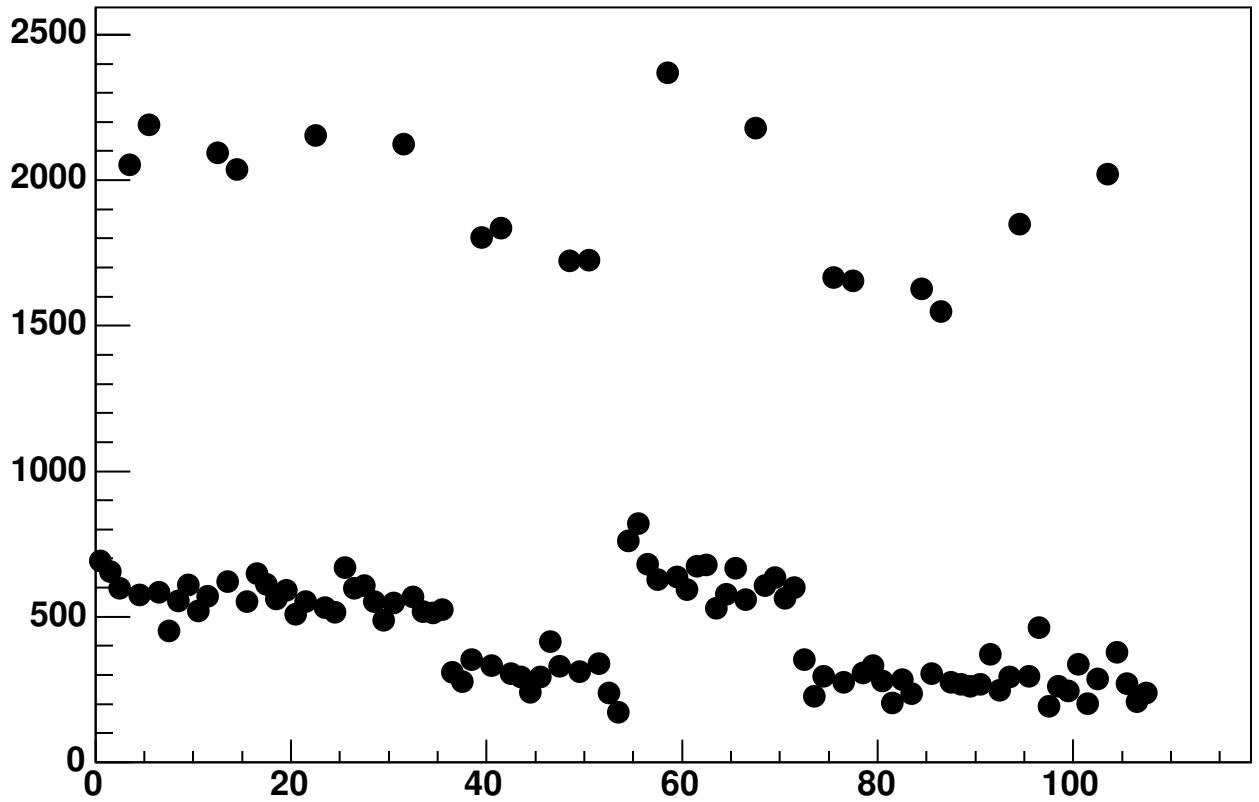
Enable 1, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



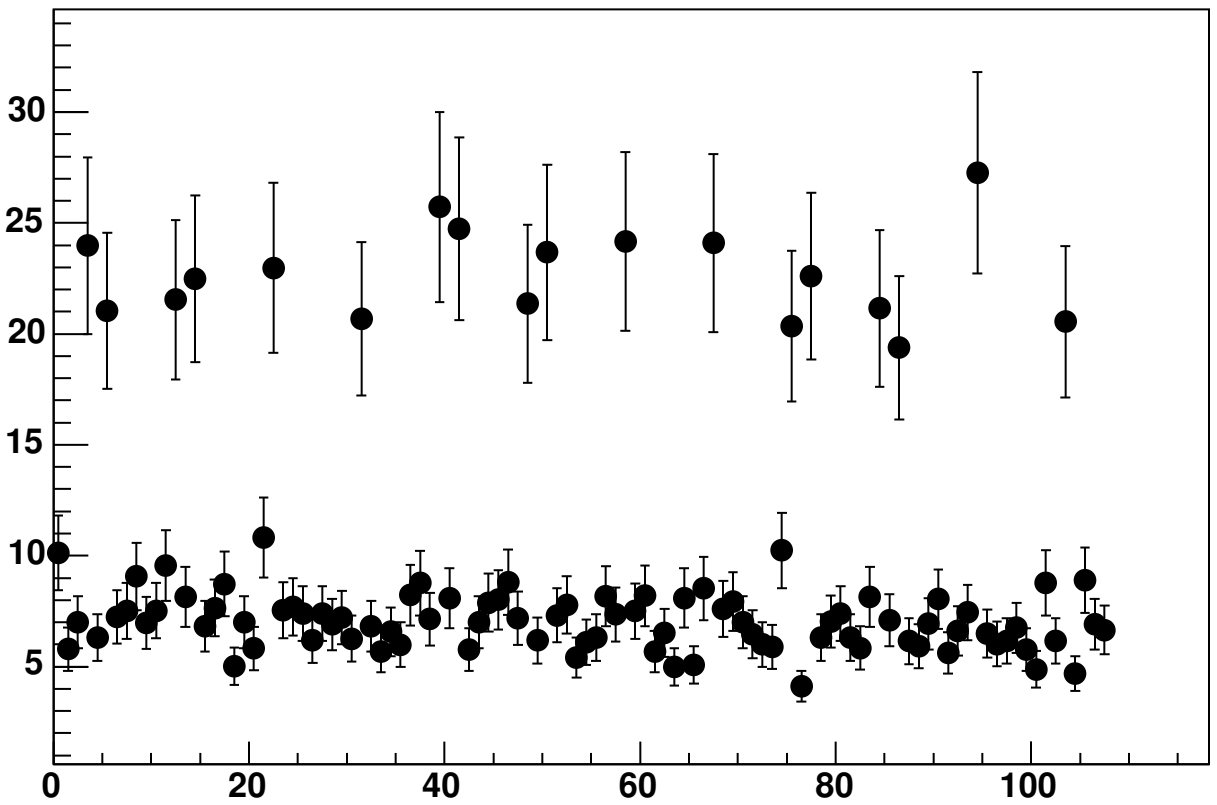
Enable 1, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



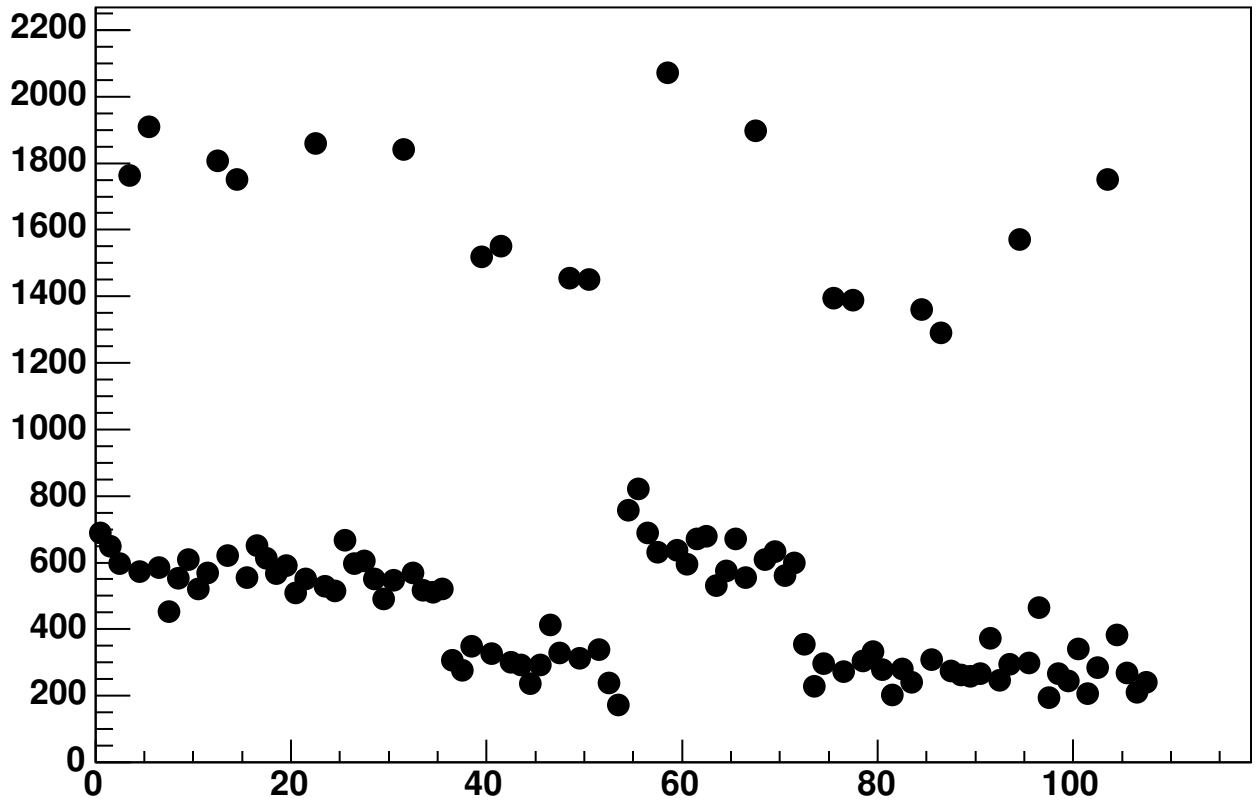
Enable 1, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



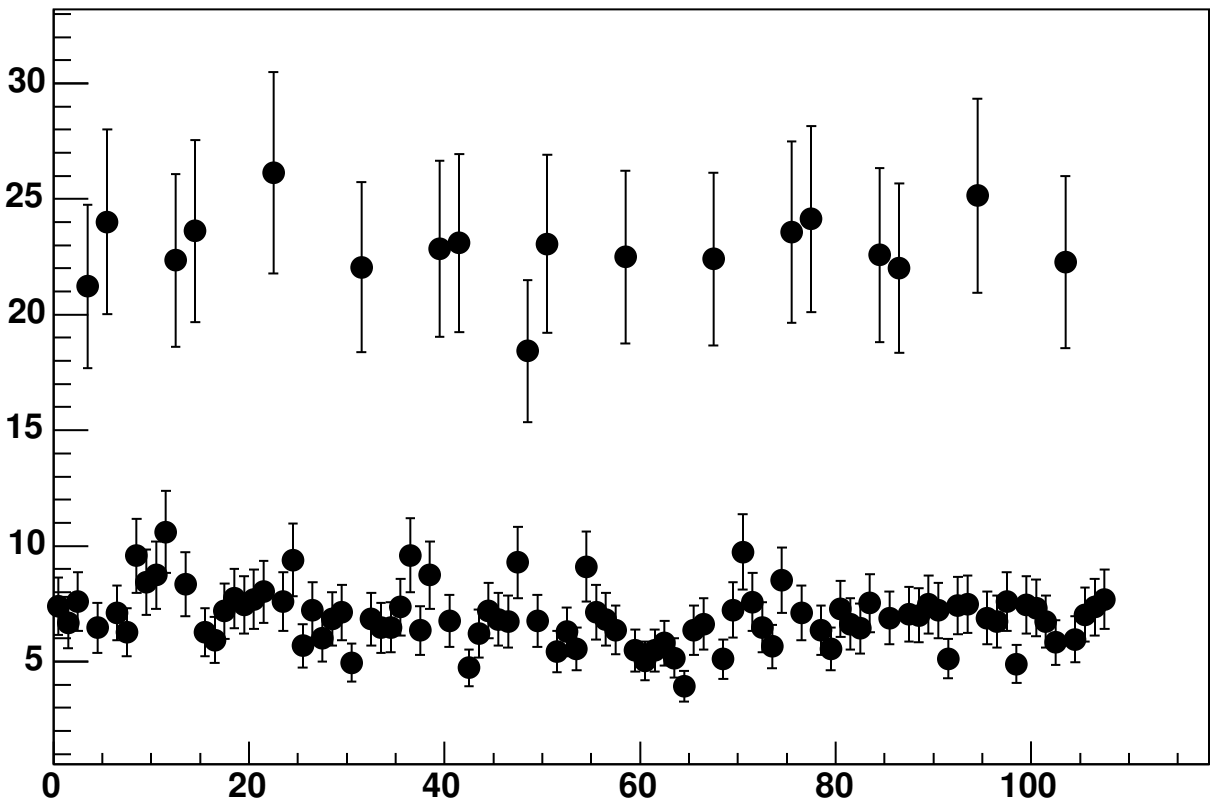
Enable 1, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



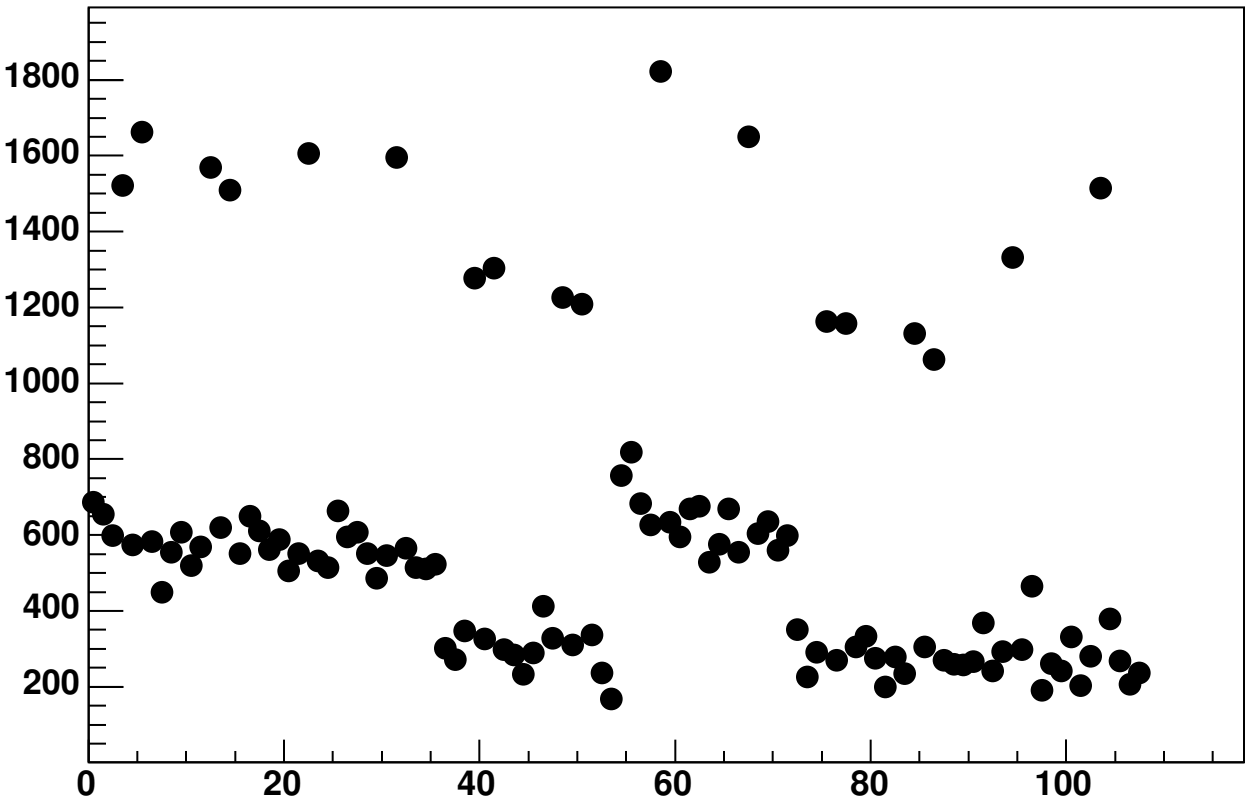
Enable 1, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



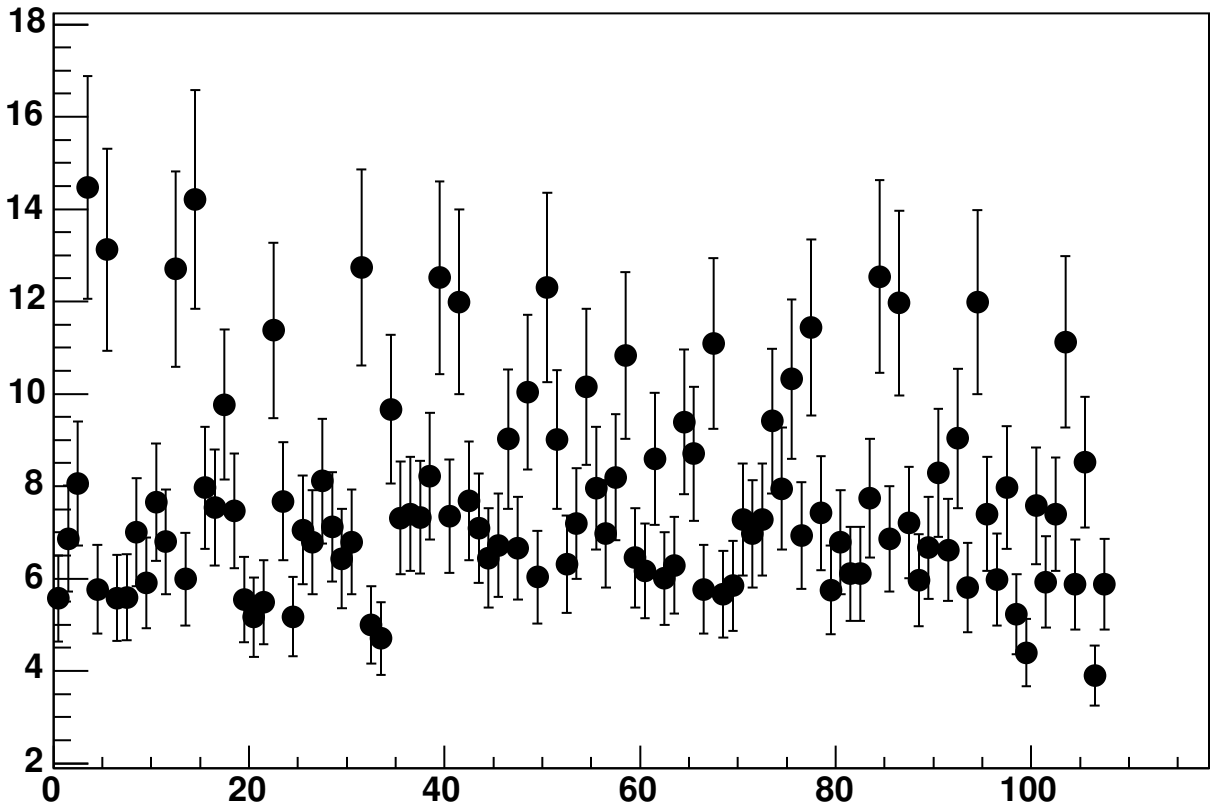
Enable 1, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



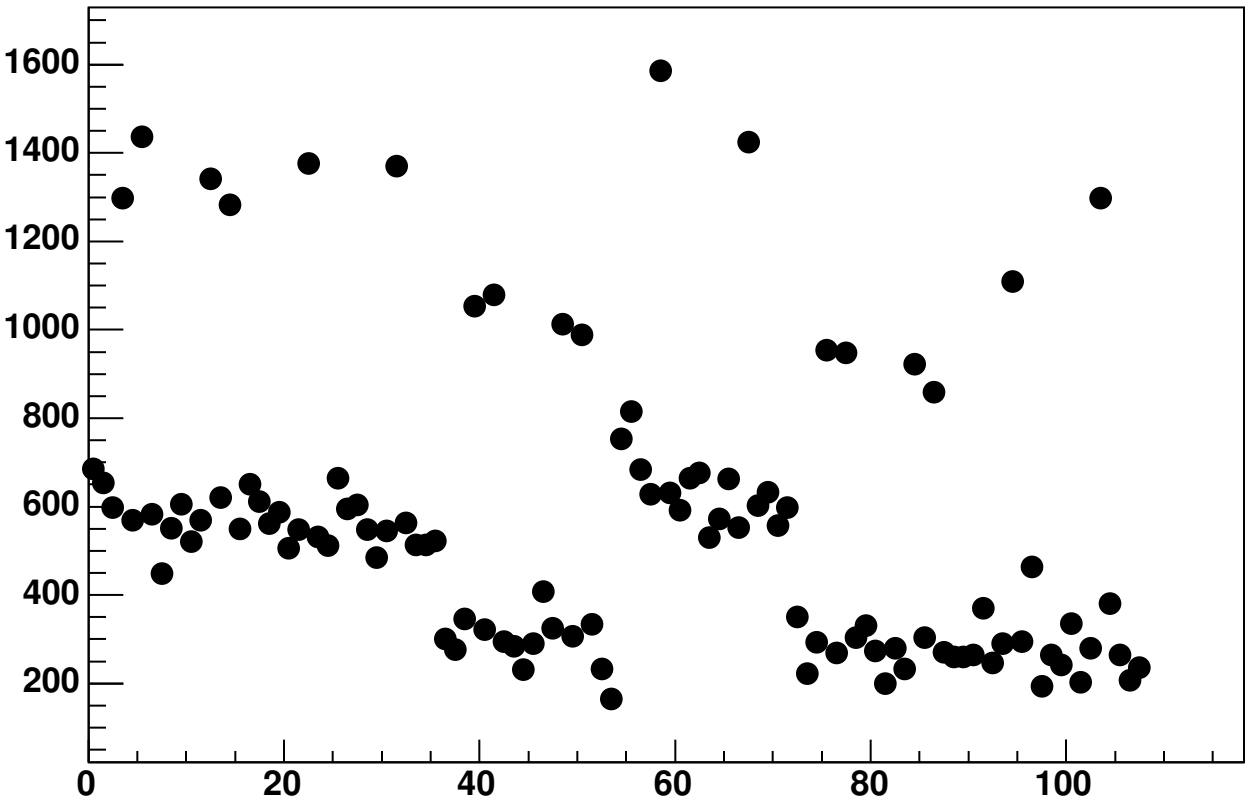
Enable 1, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



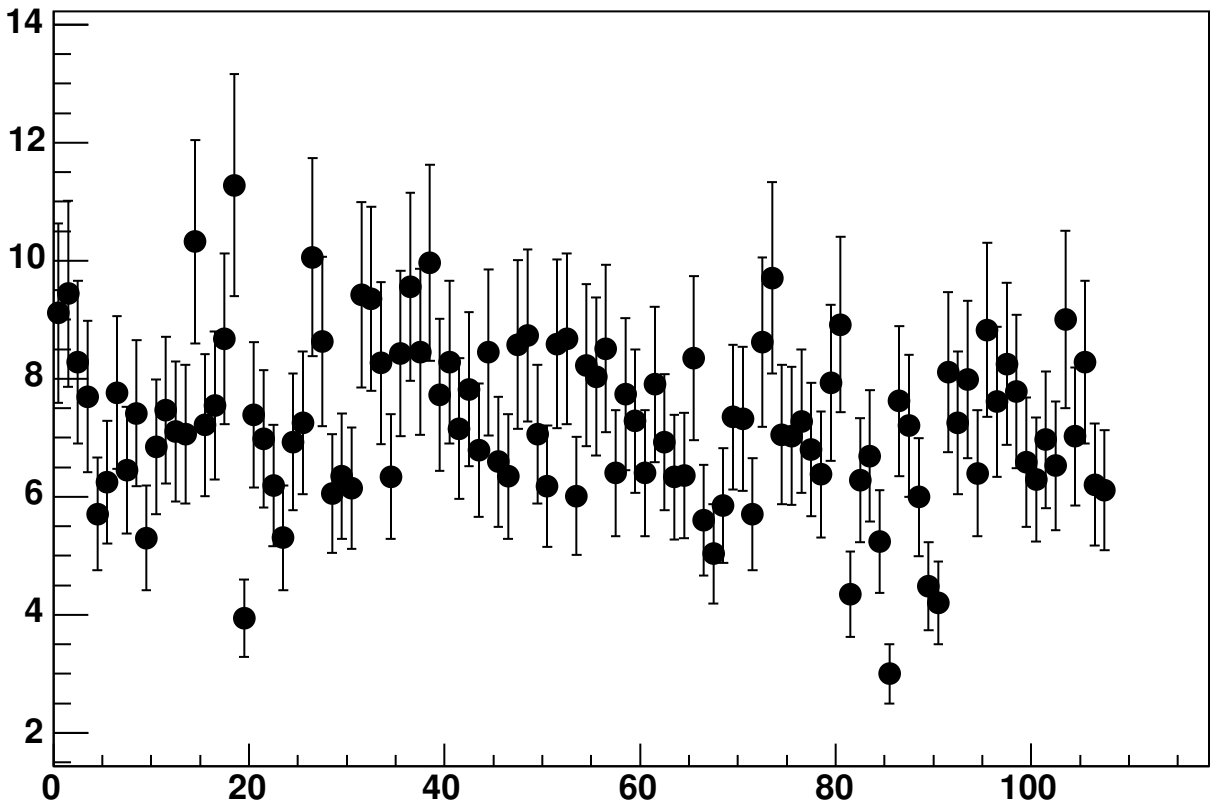
Enable 1, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



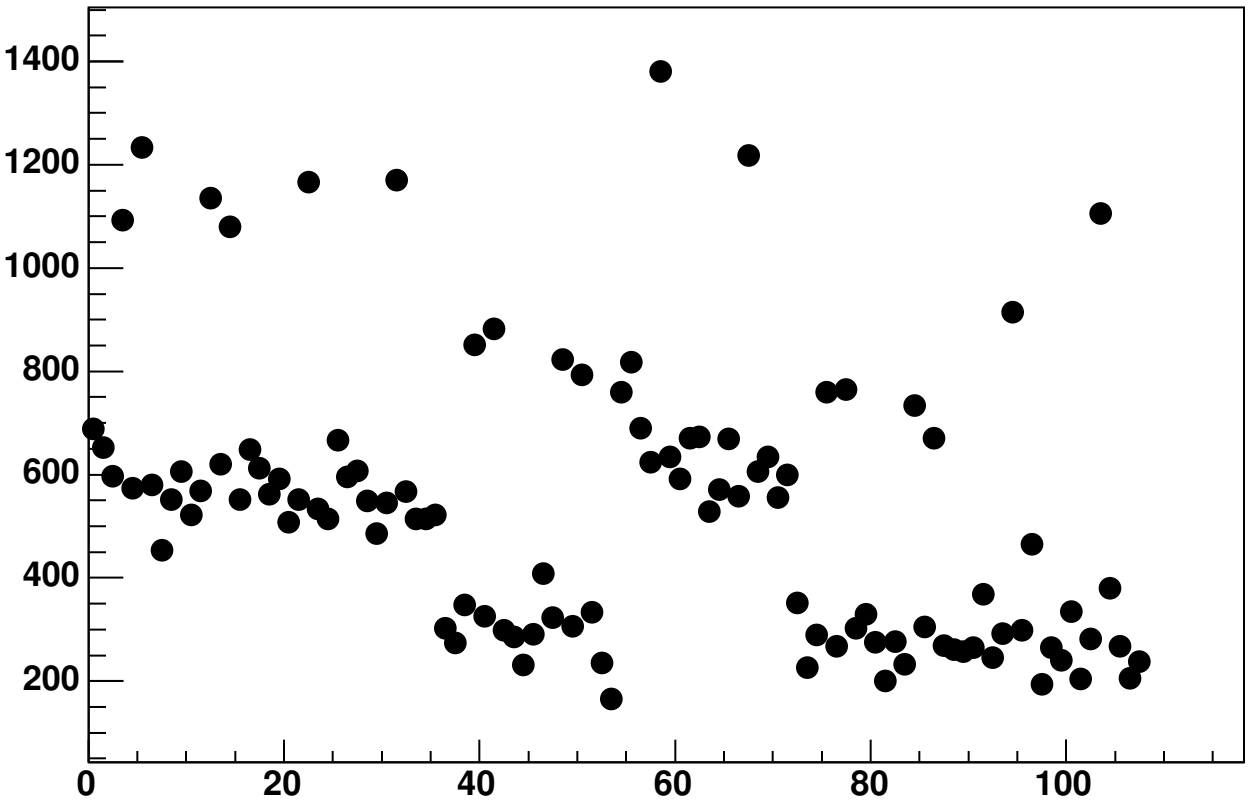
Enable 1, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



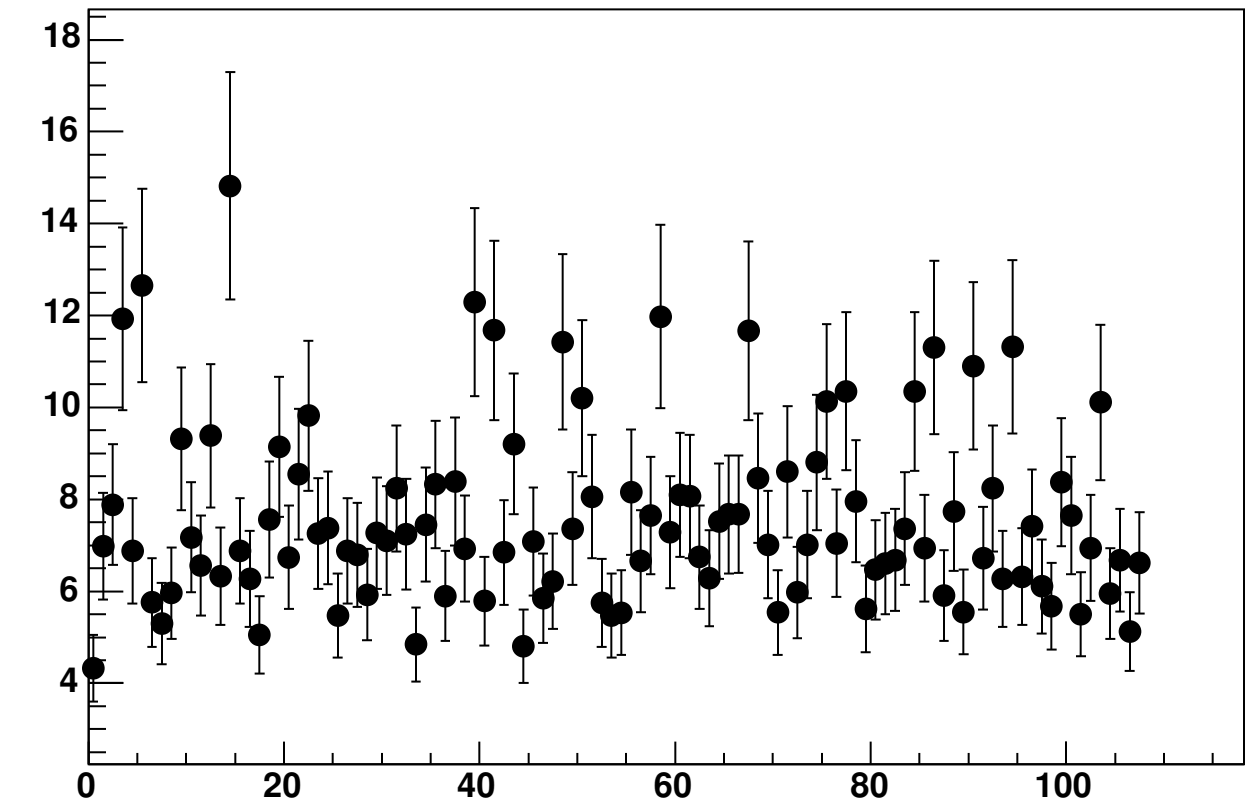
Enable 1, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



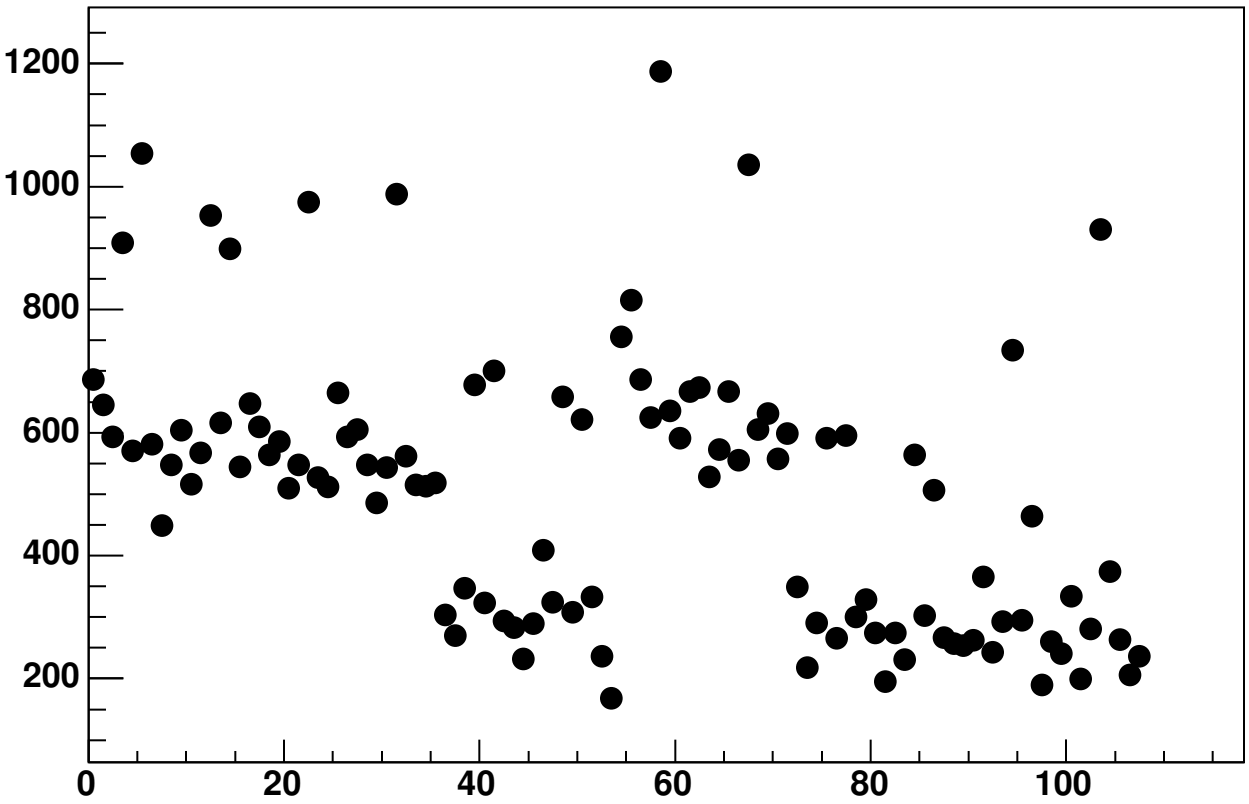
Enable 1, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



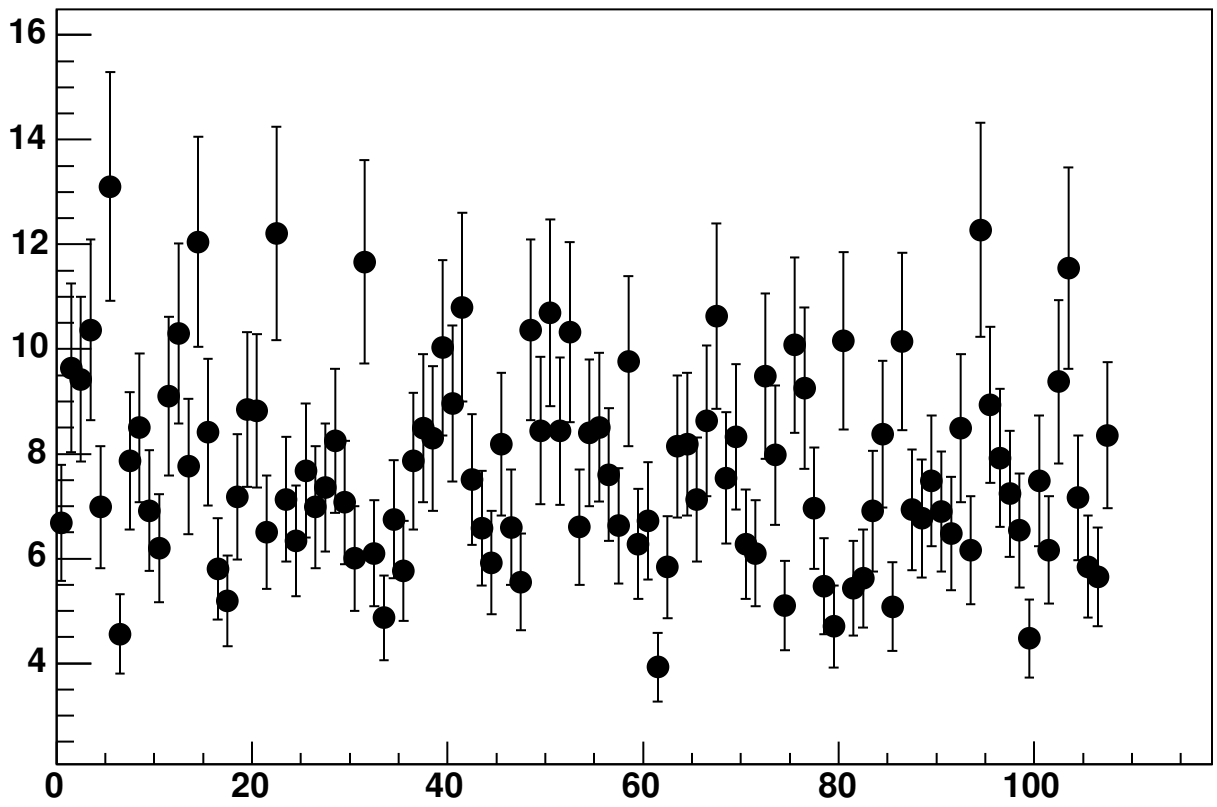
Enable 1, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



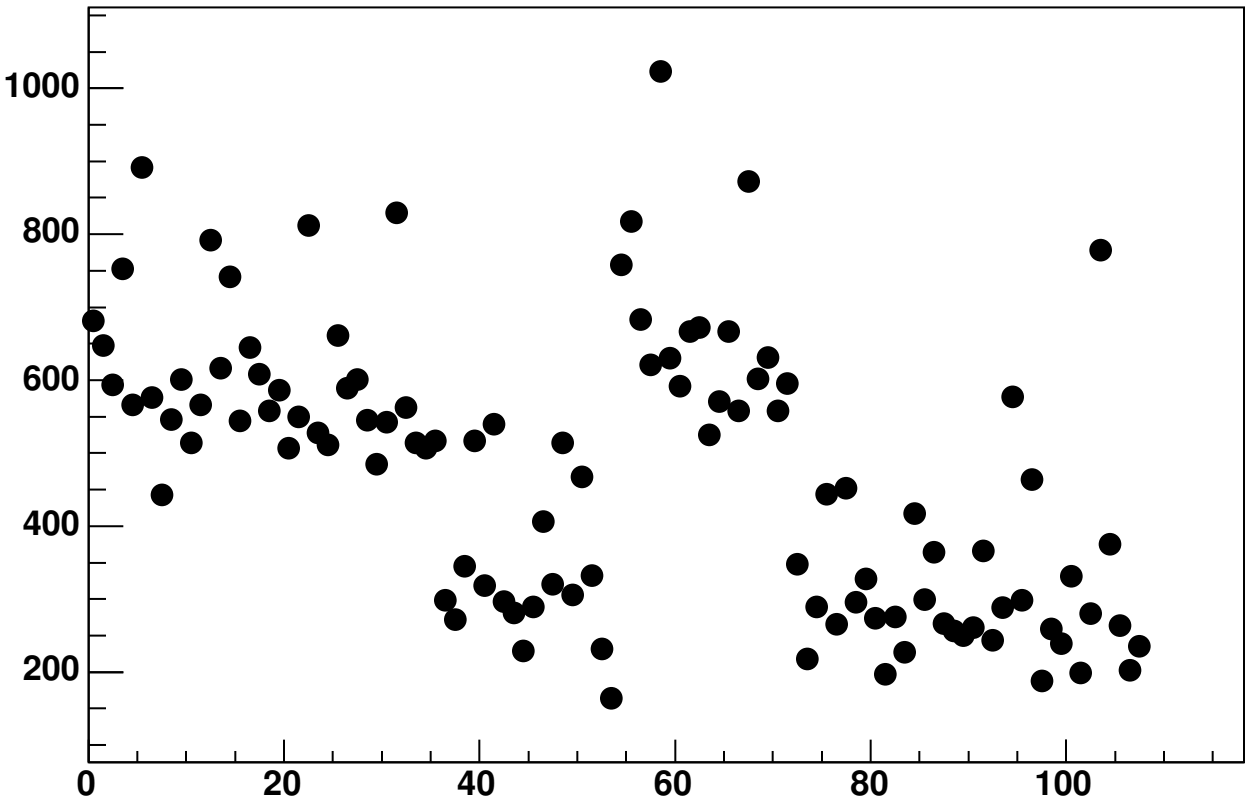
Enable 1, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



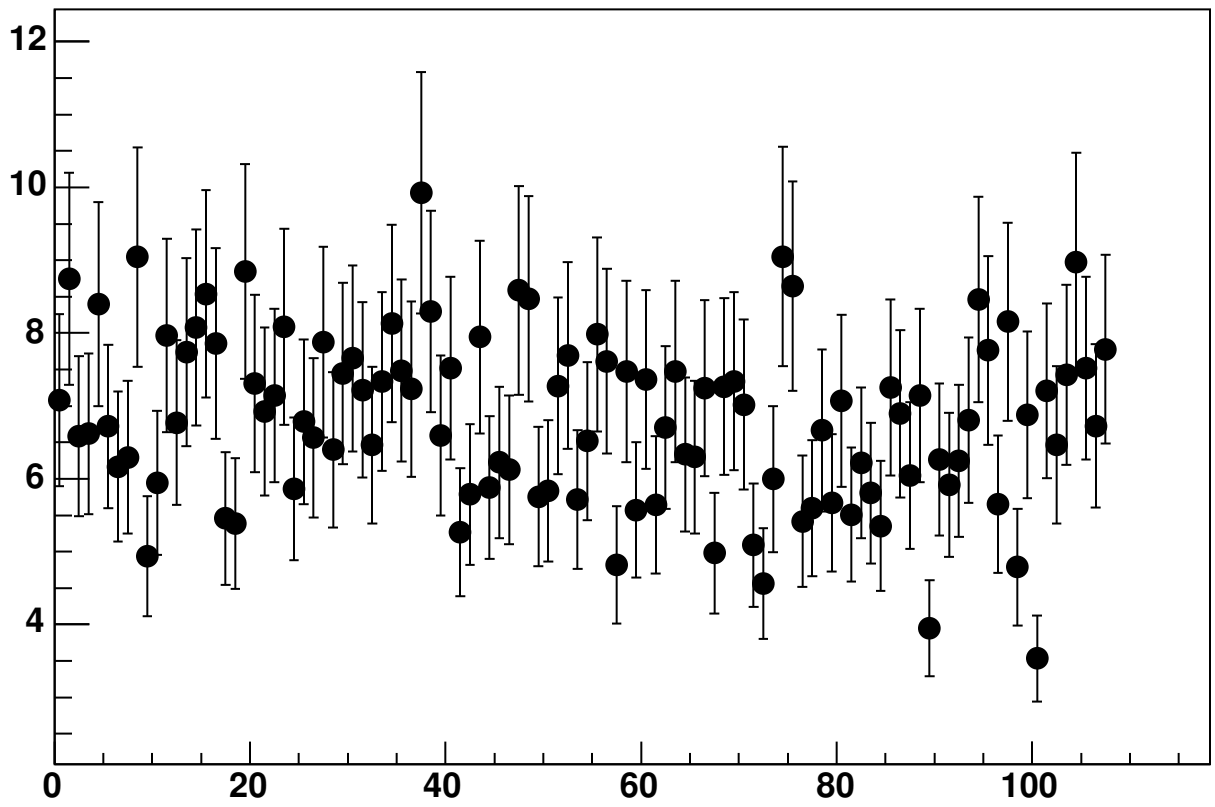
Enable 1, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



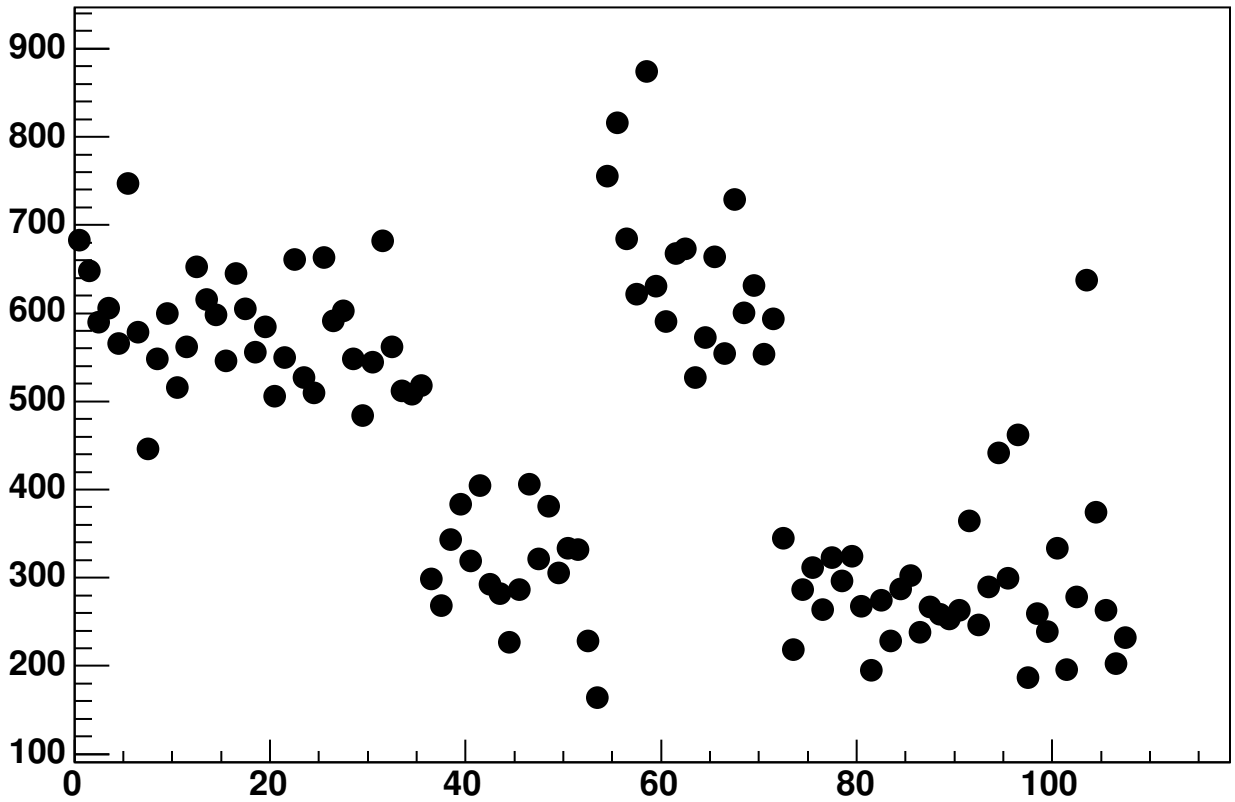
Enable 1, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



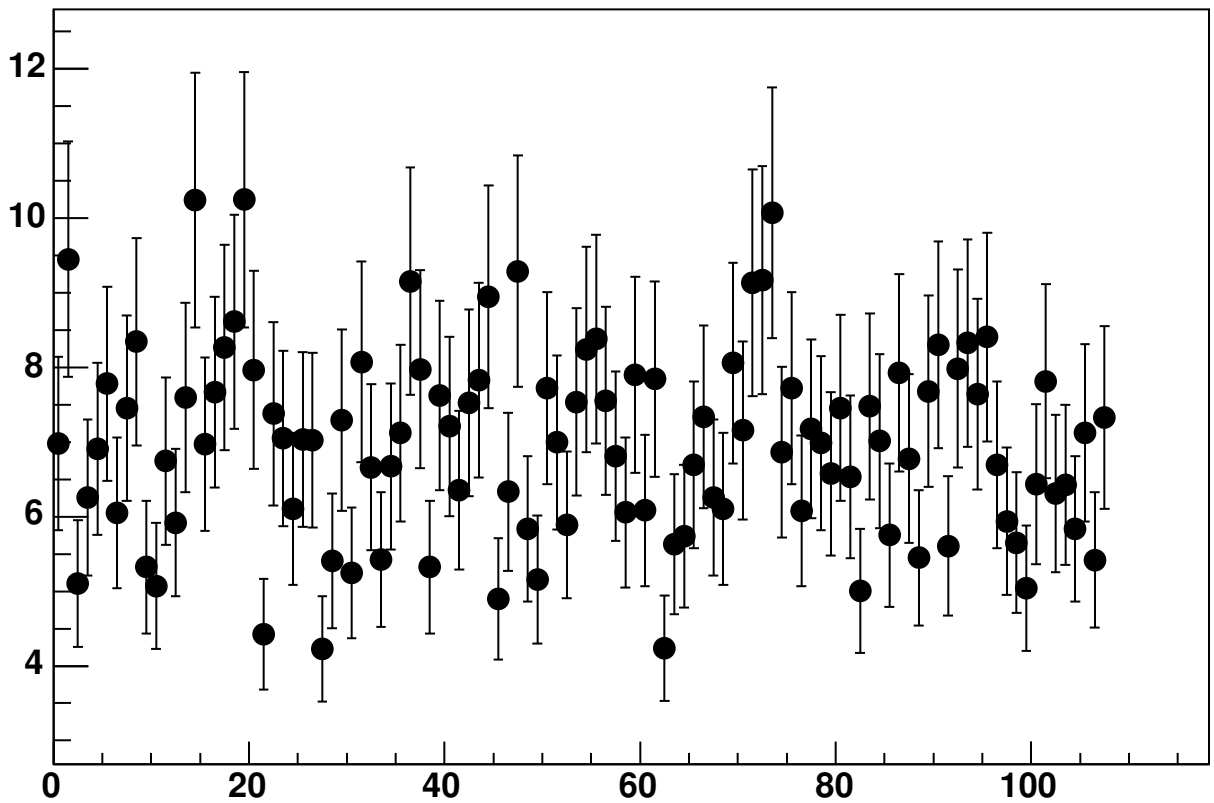
Enable 1, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



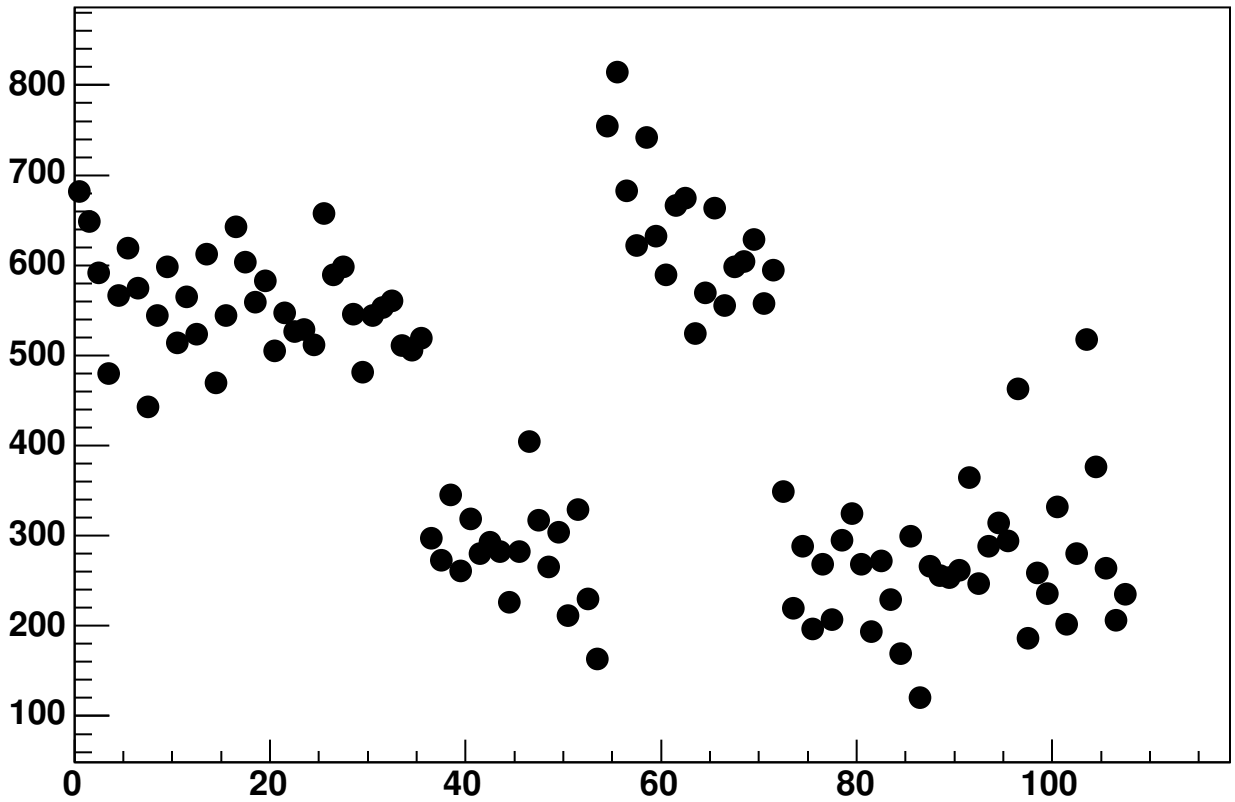
Enable 1, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



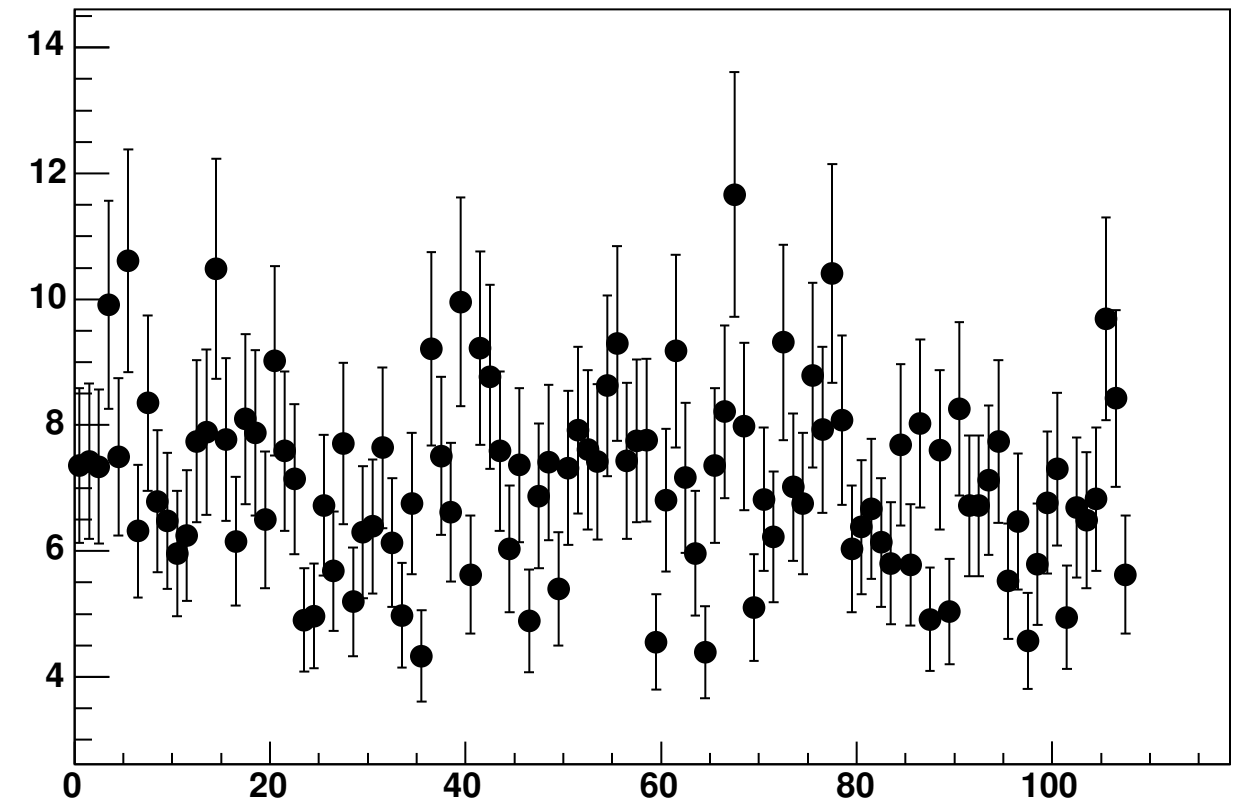
Enable 1, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



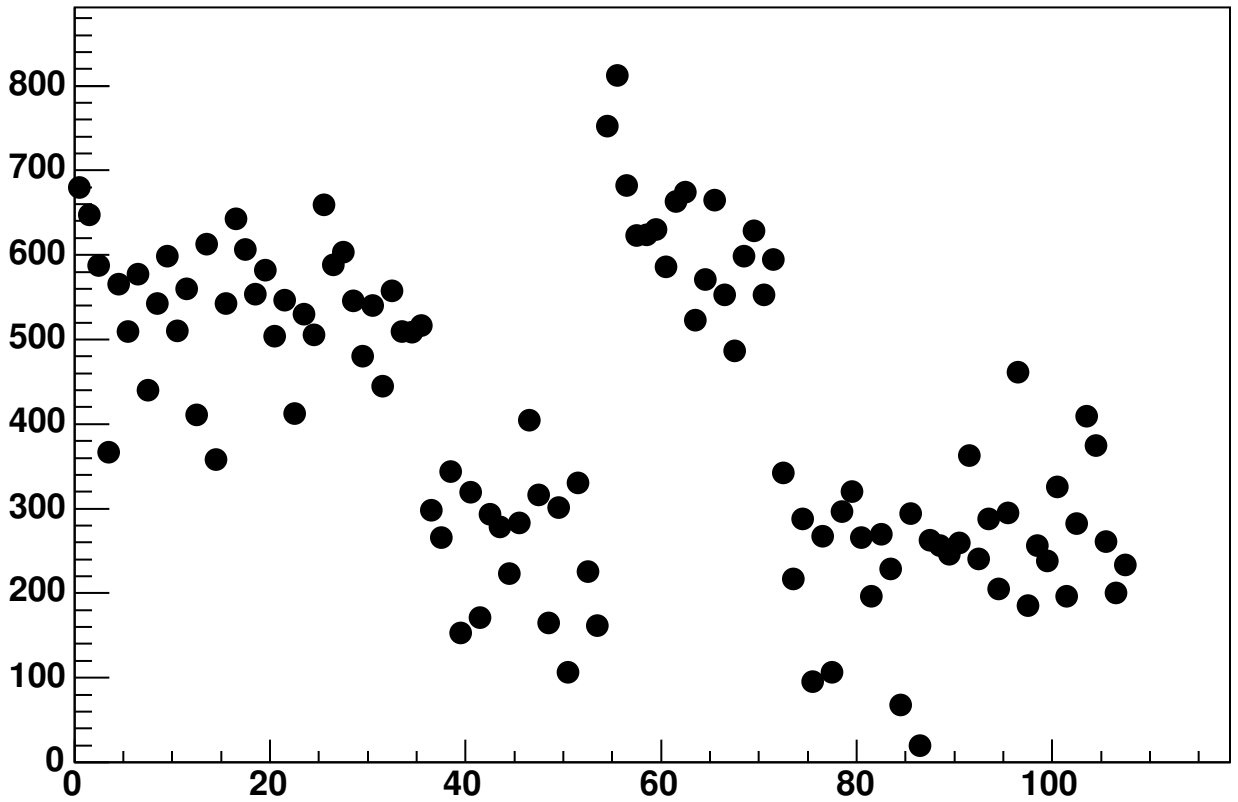
Enable 1, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



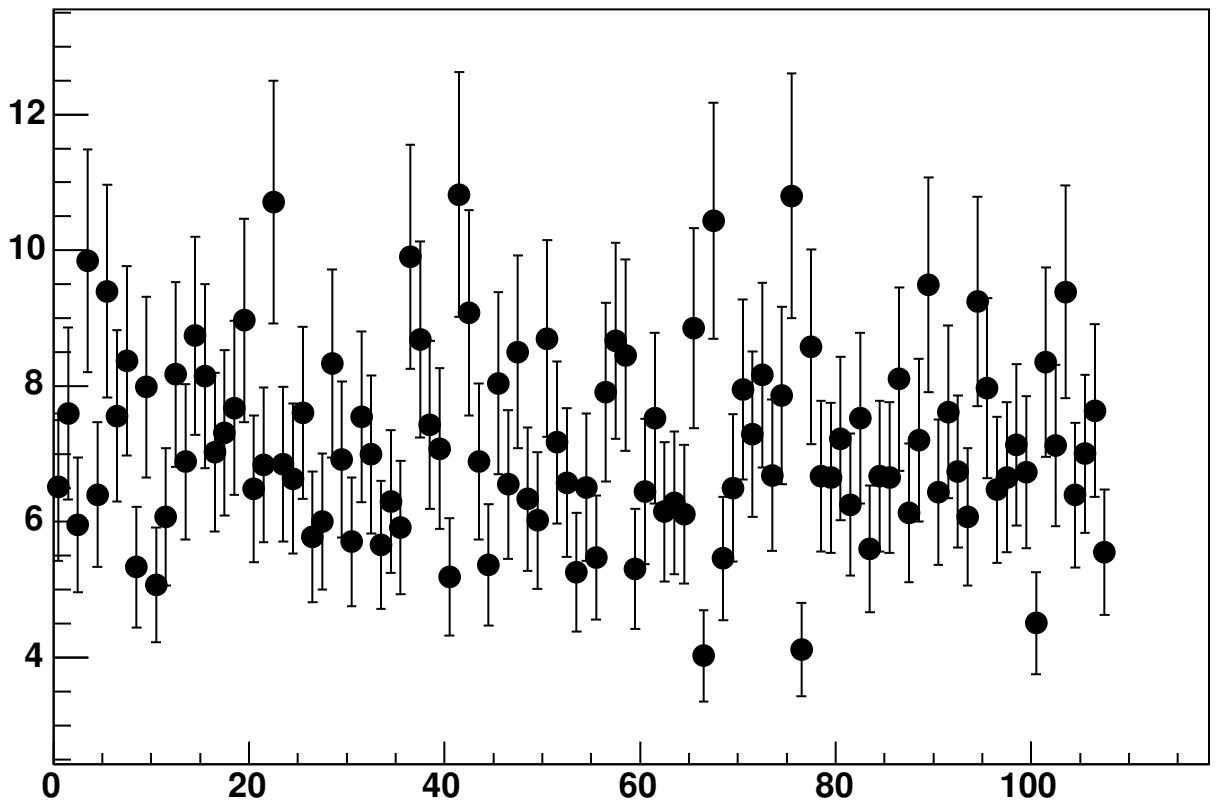
Enable 1, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



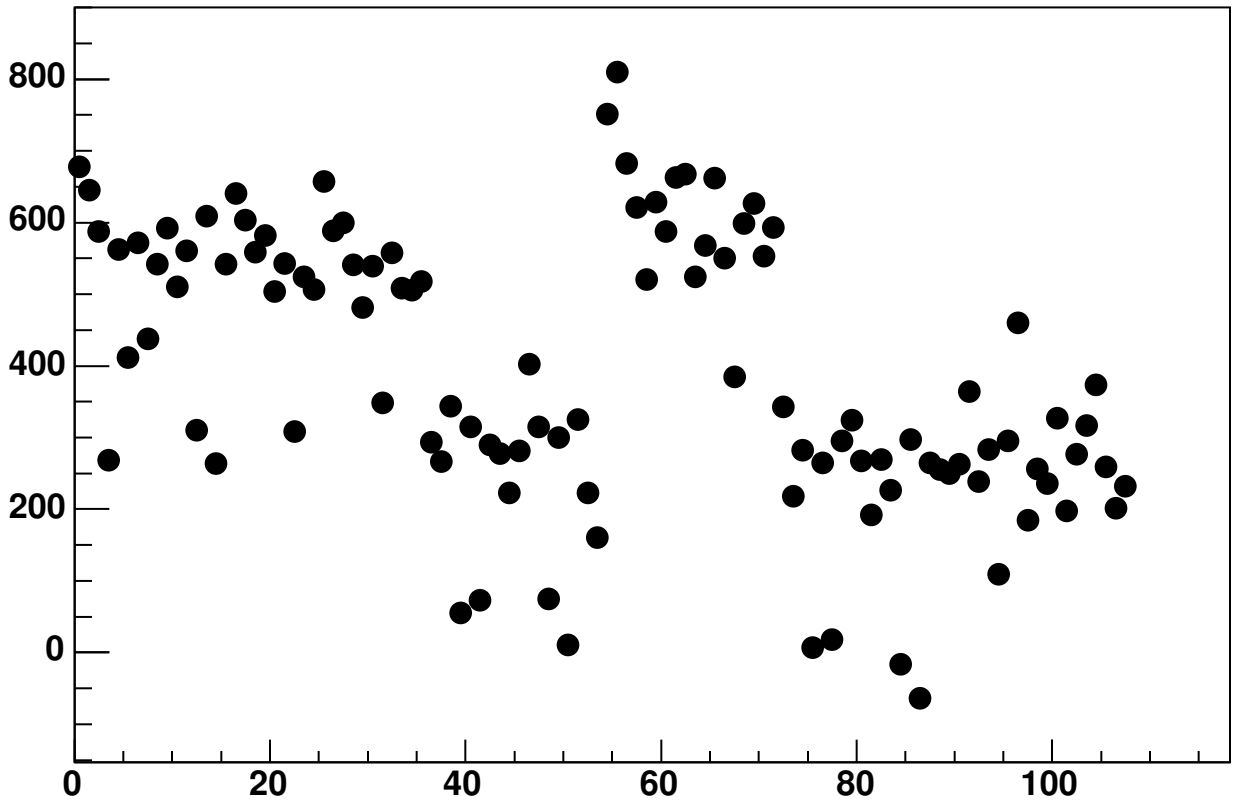
Enable 1, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



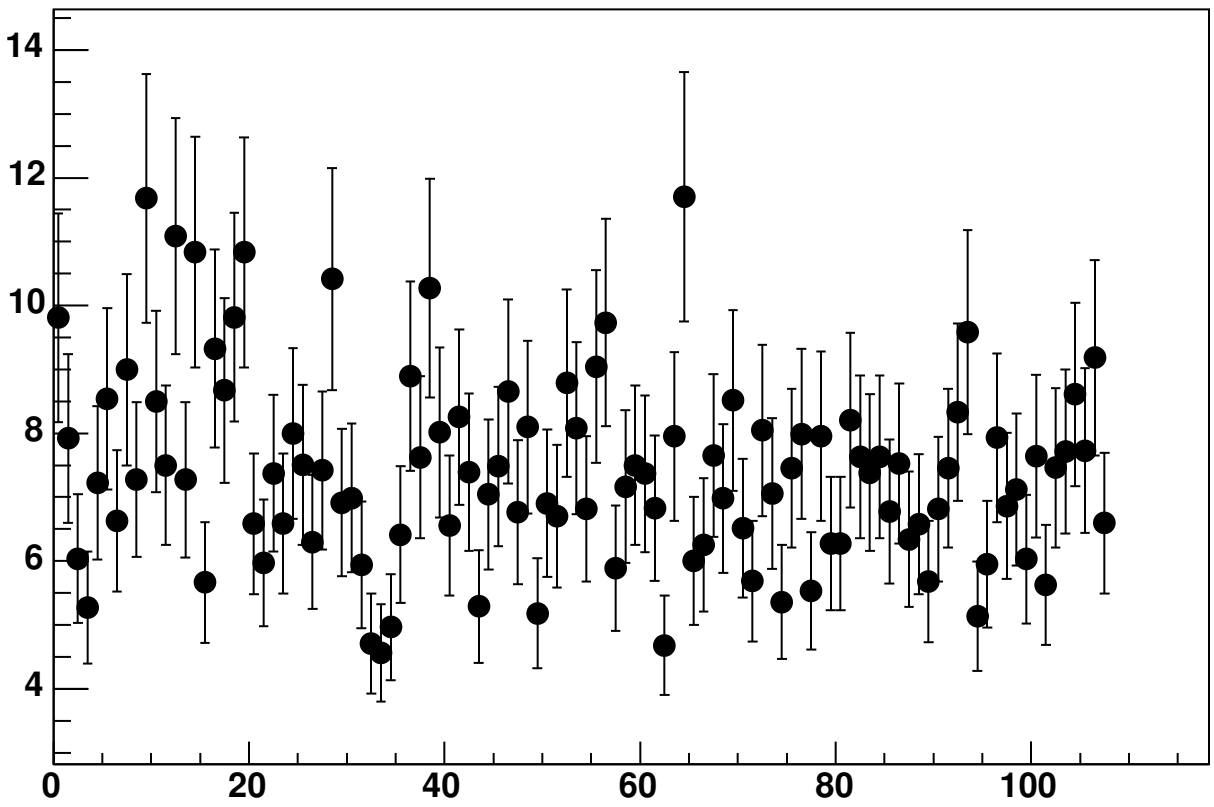
Enable 1, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



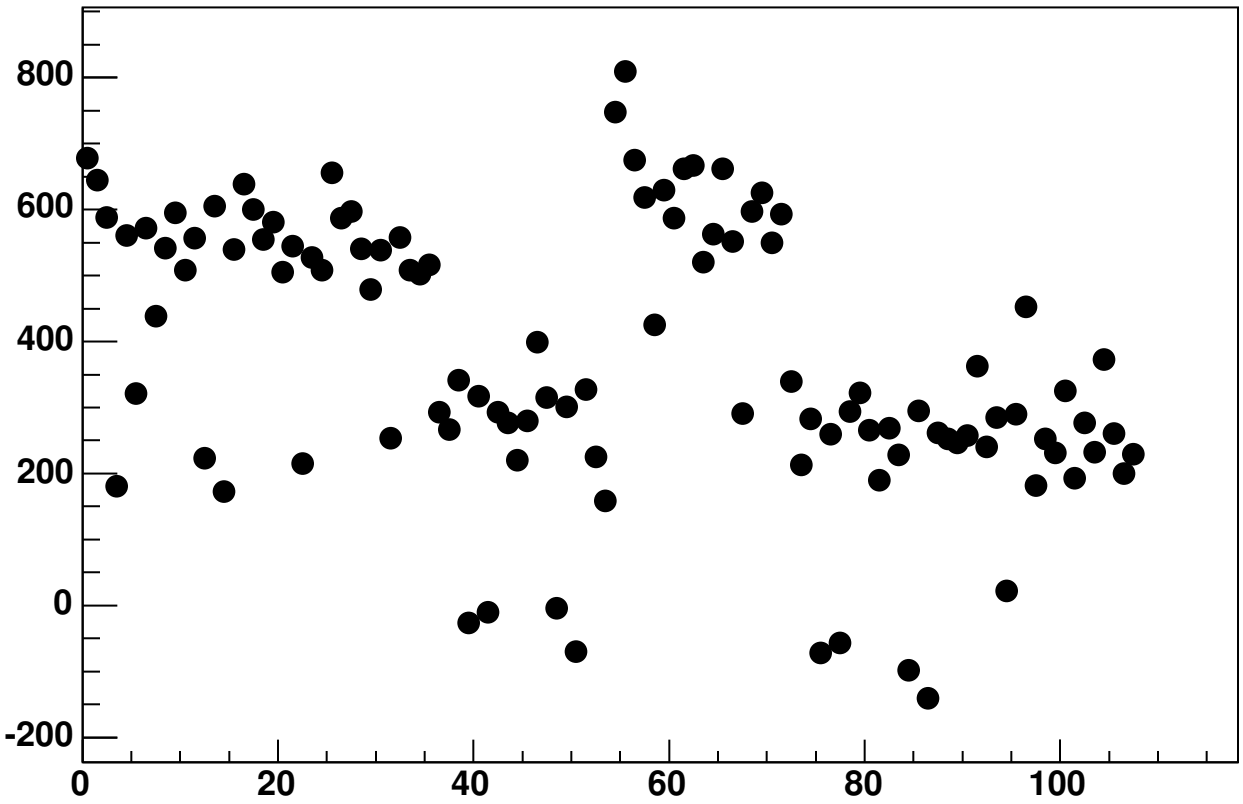
Enable 1, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



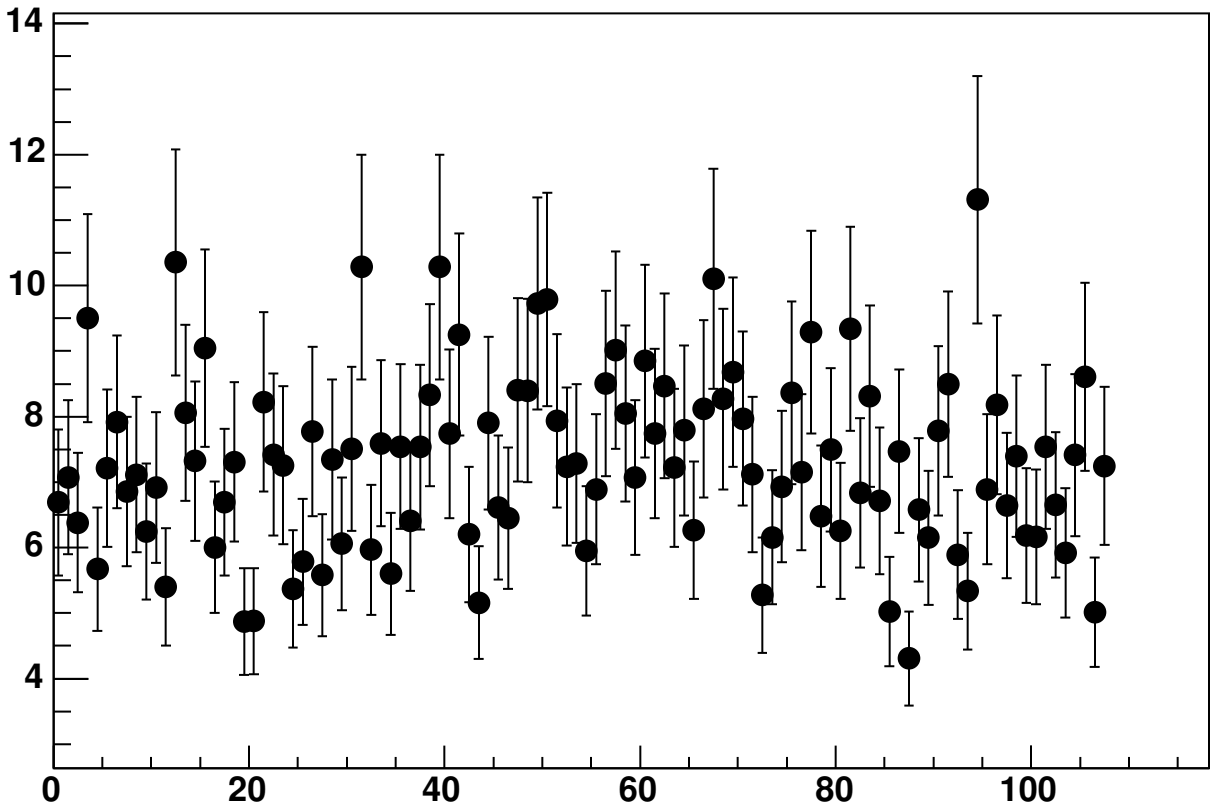
Enable 1, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



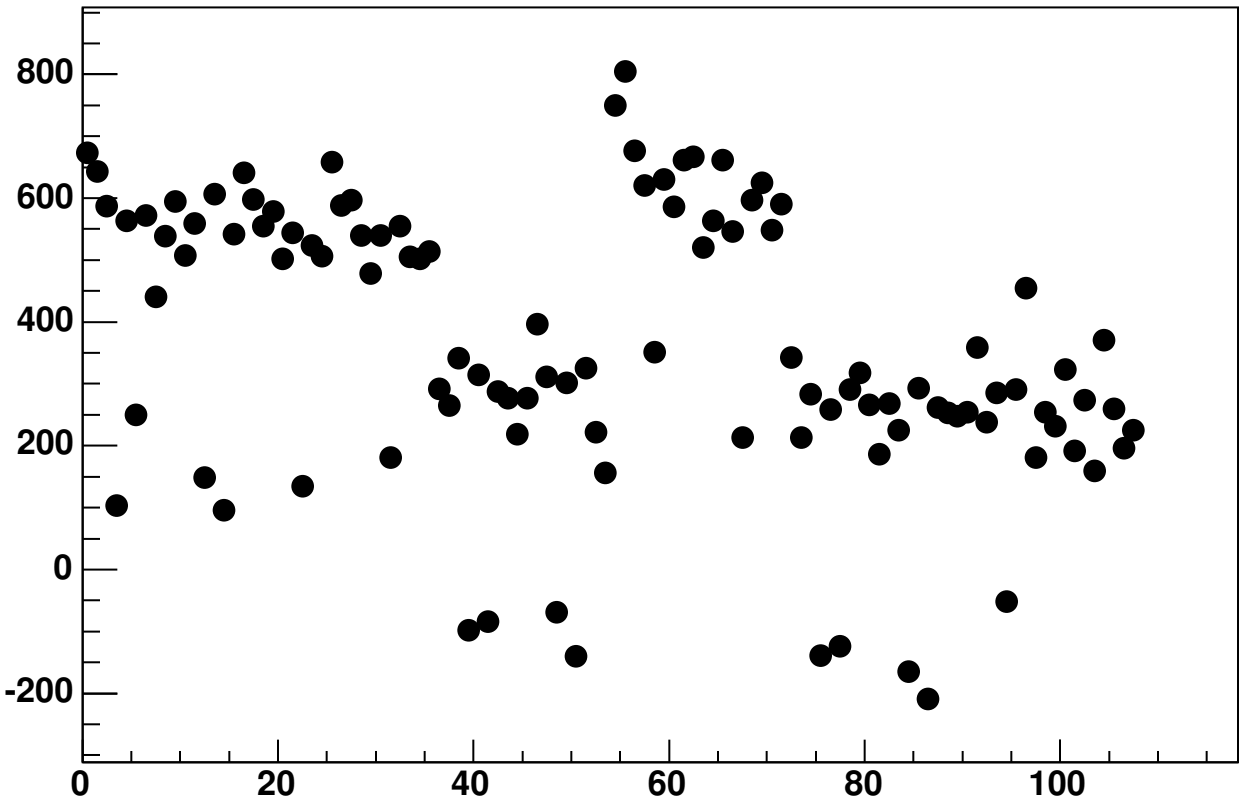
Enable 1, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



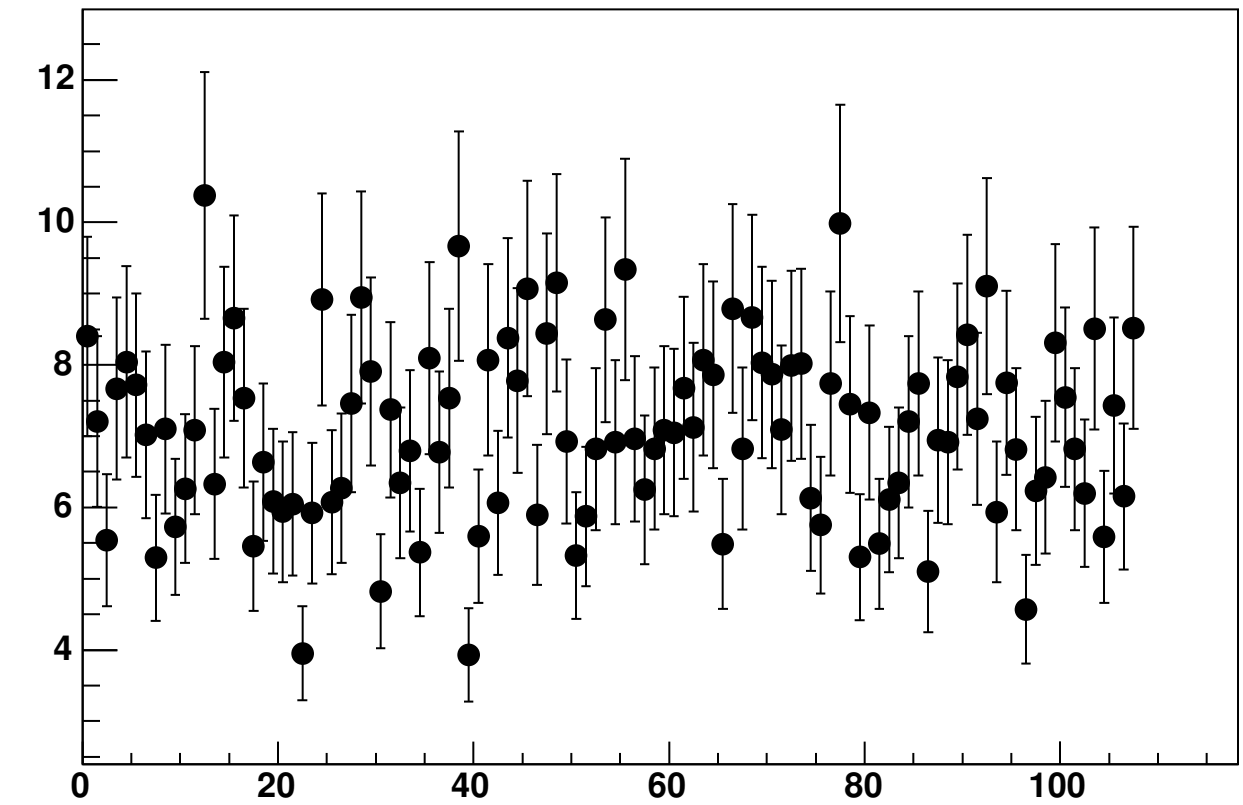
Enable 1, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



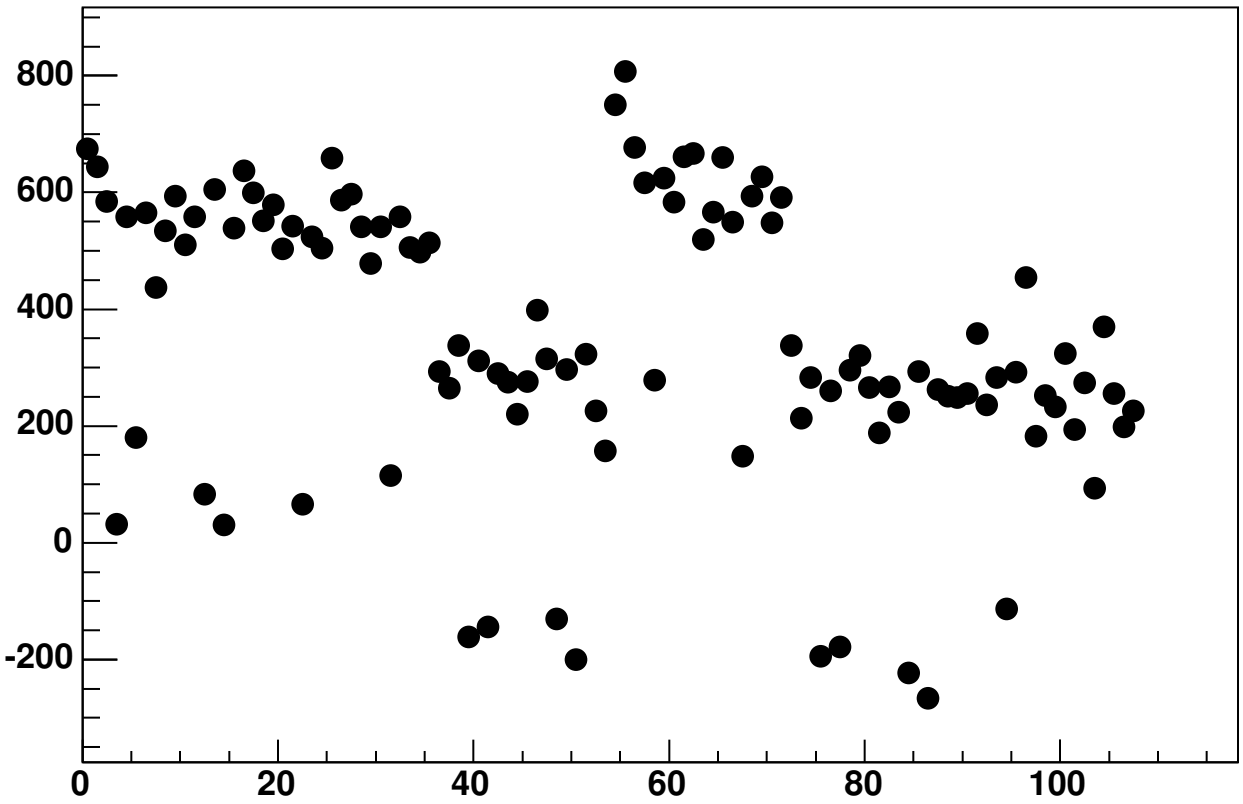
Enable 1, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



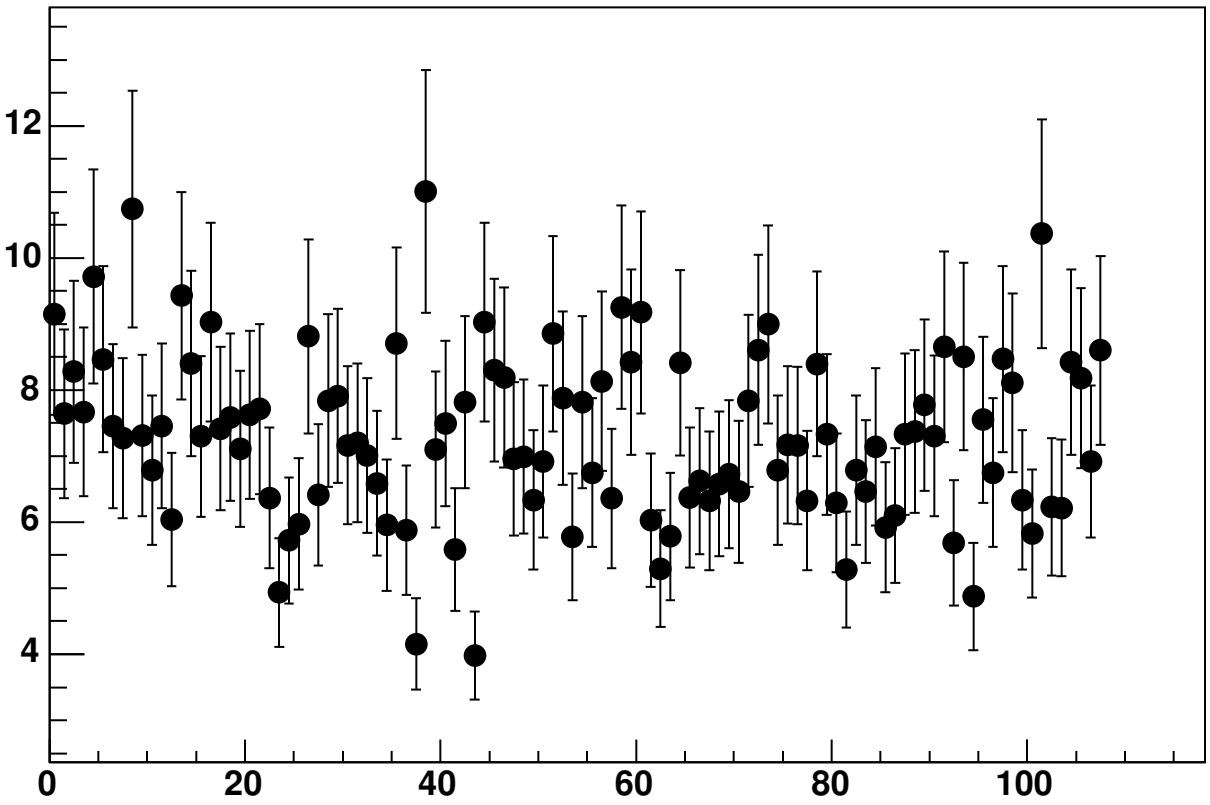
Enable 1, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



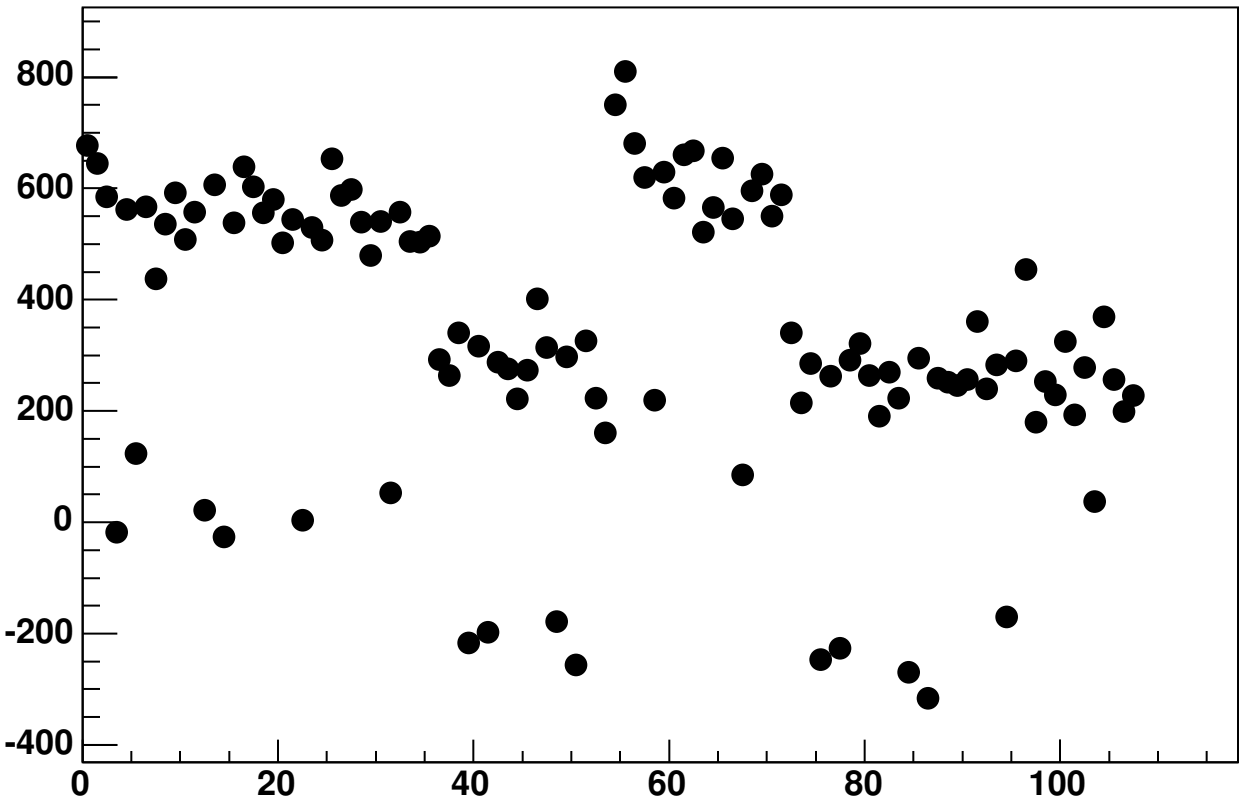
Enable 1, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



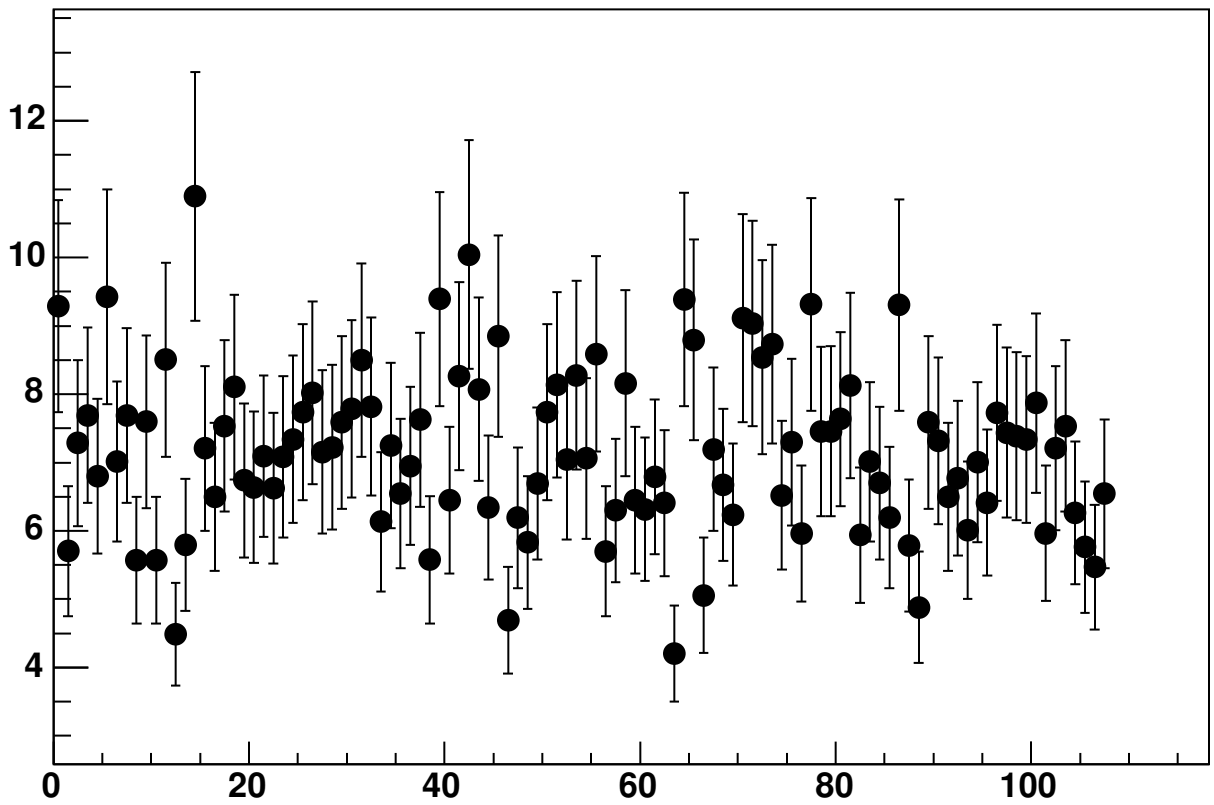
Enable 1, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



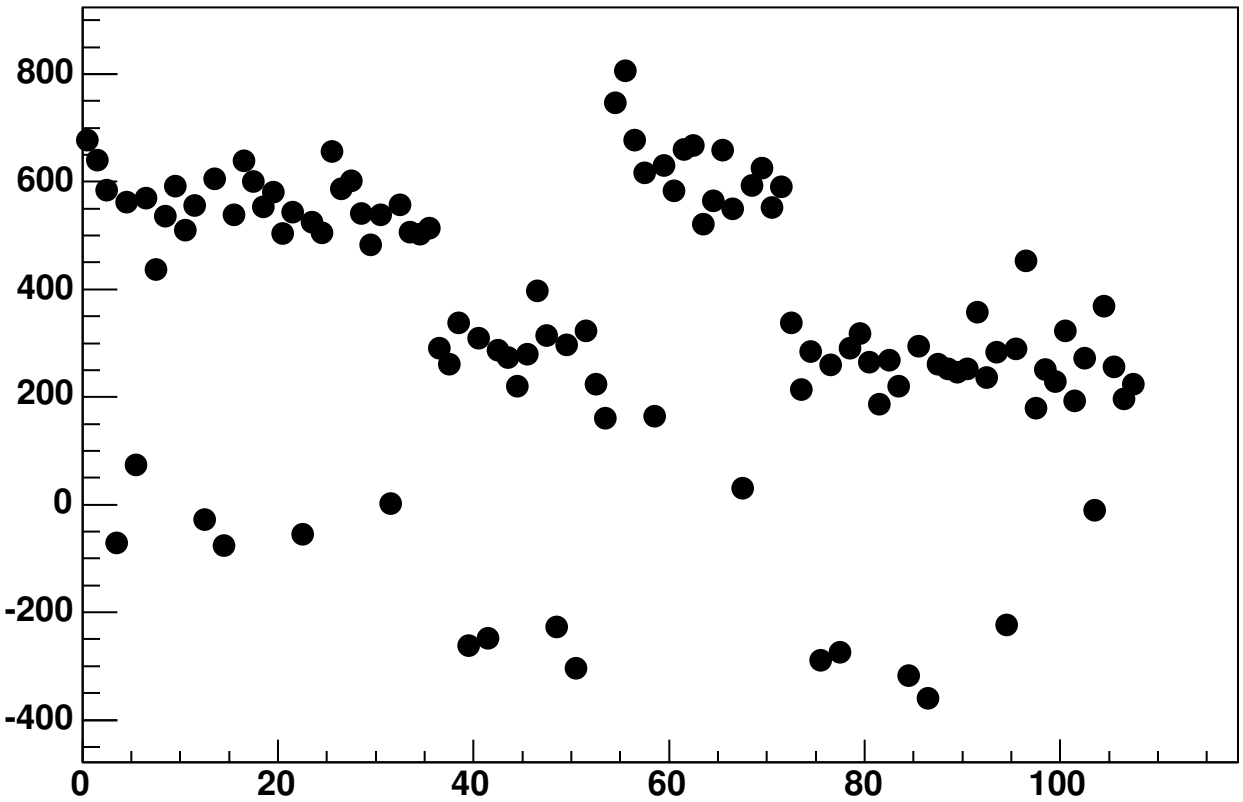
Enable 1, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



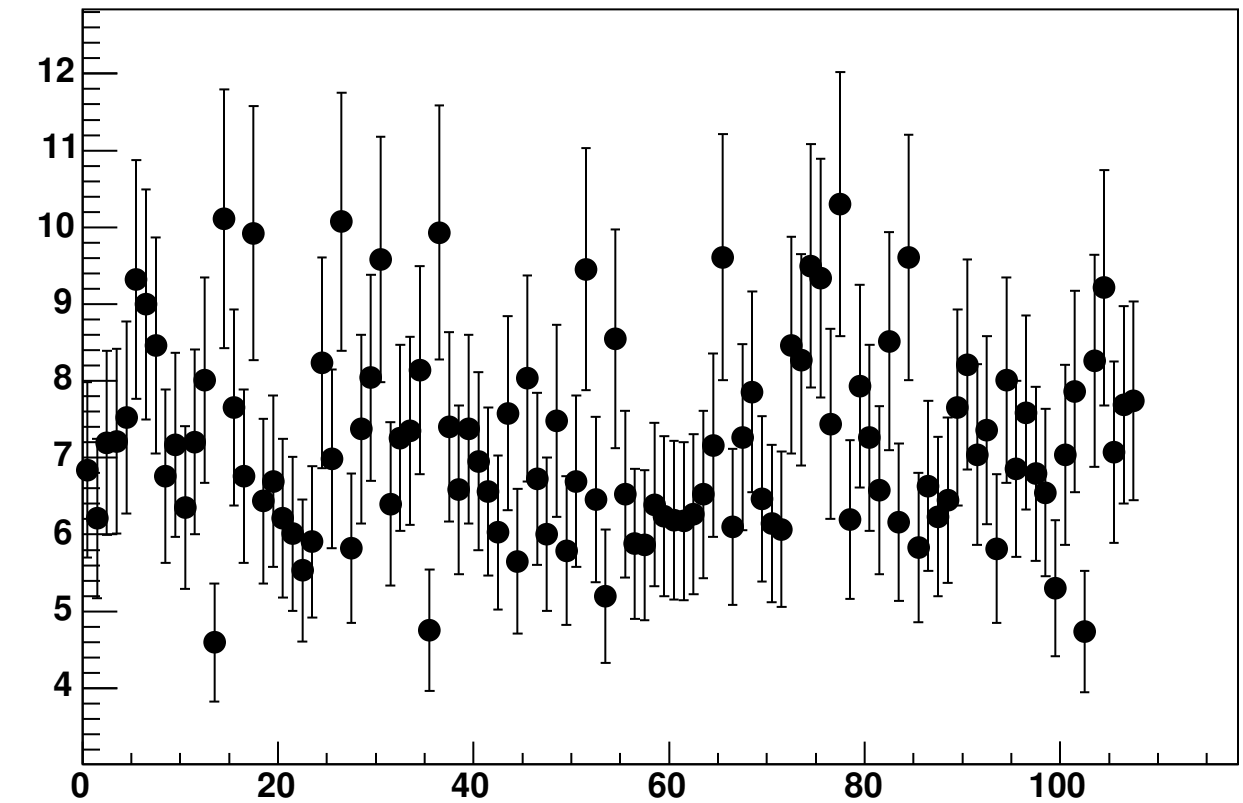
Enable 1, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



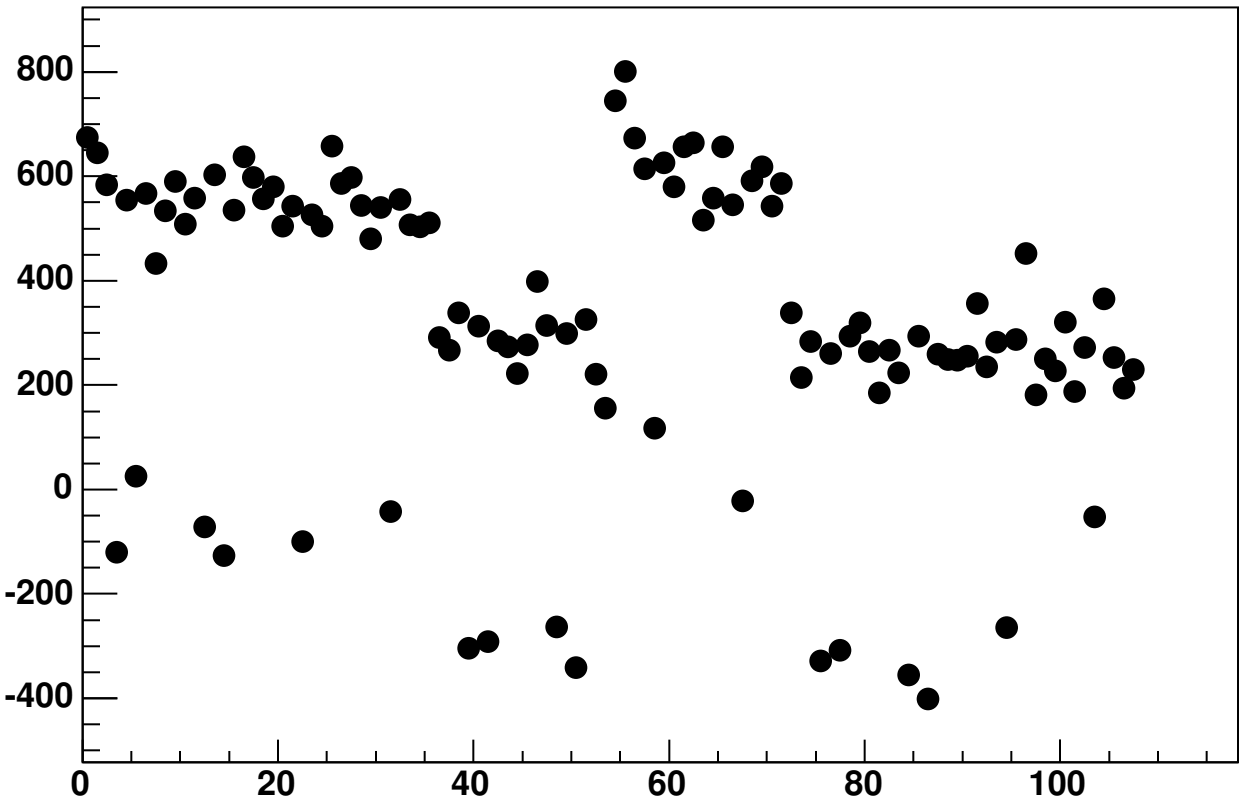
Enable 1, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



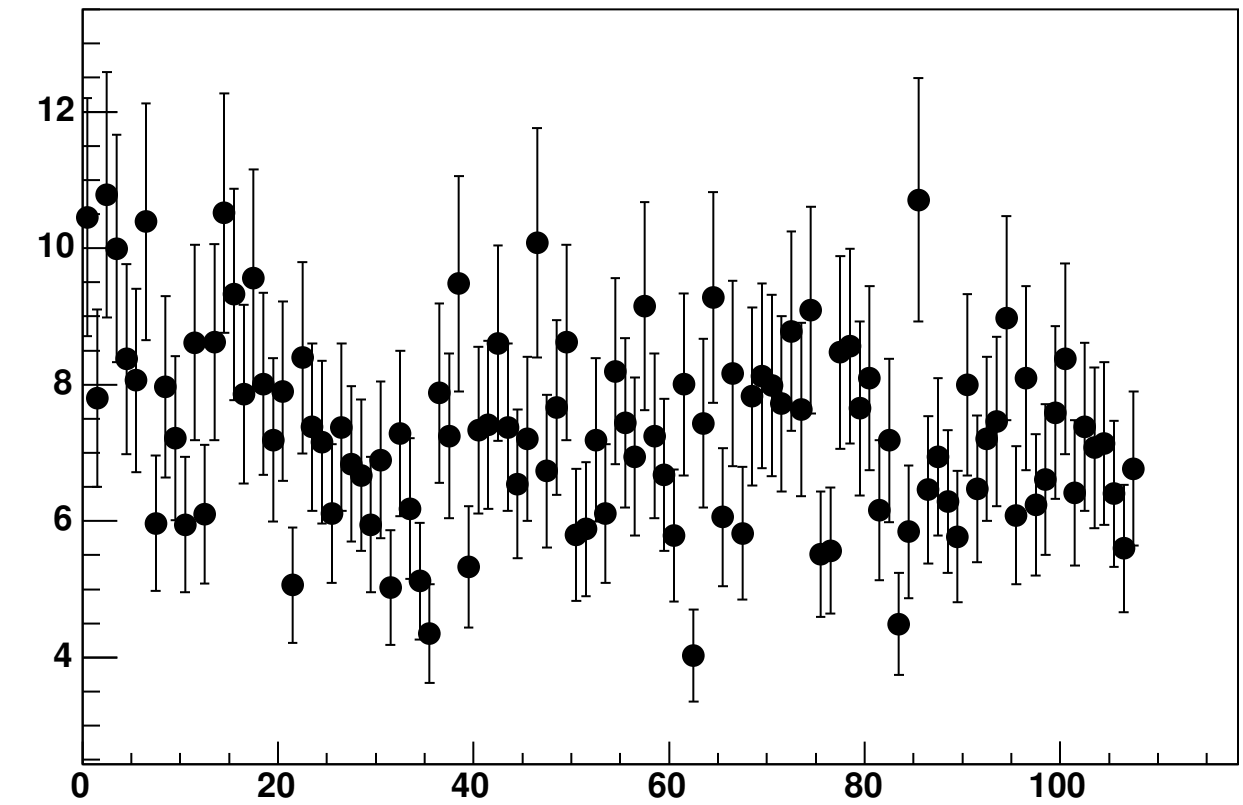
Enable 1, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



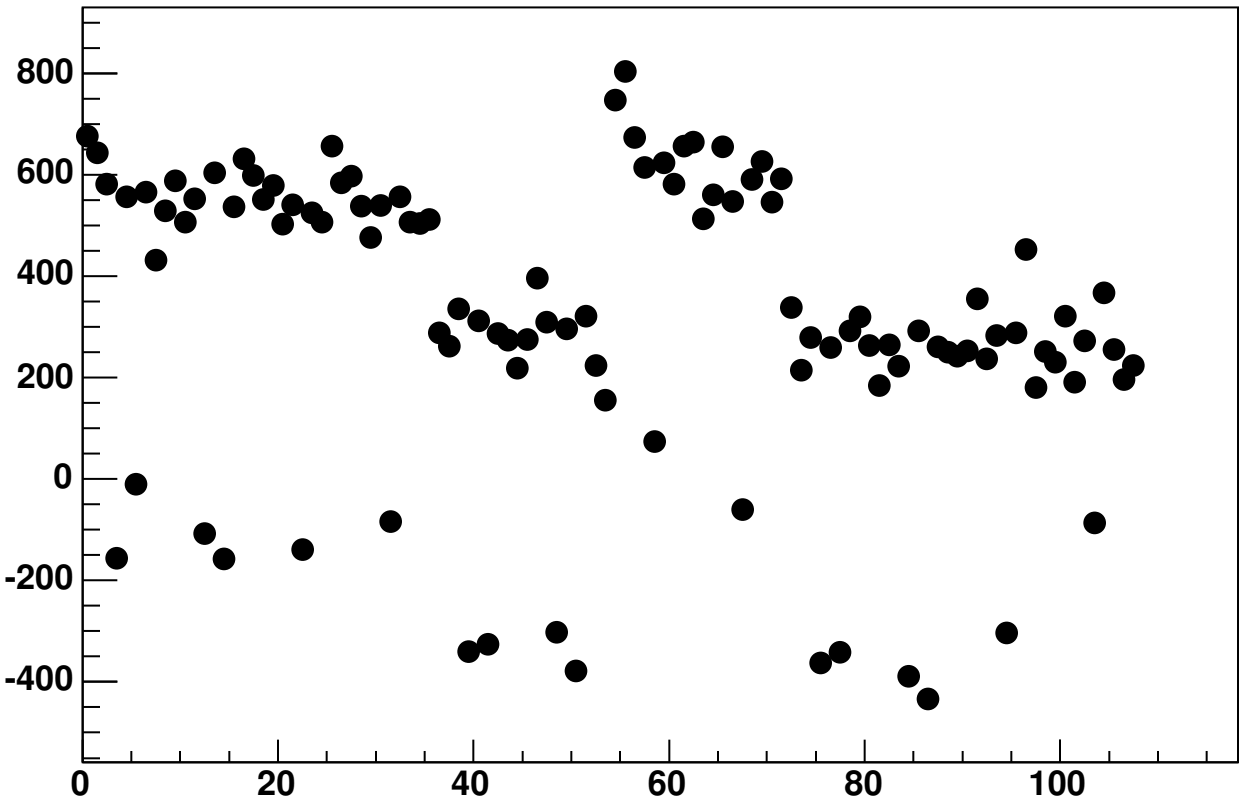
Enable 1, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



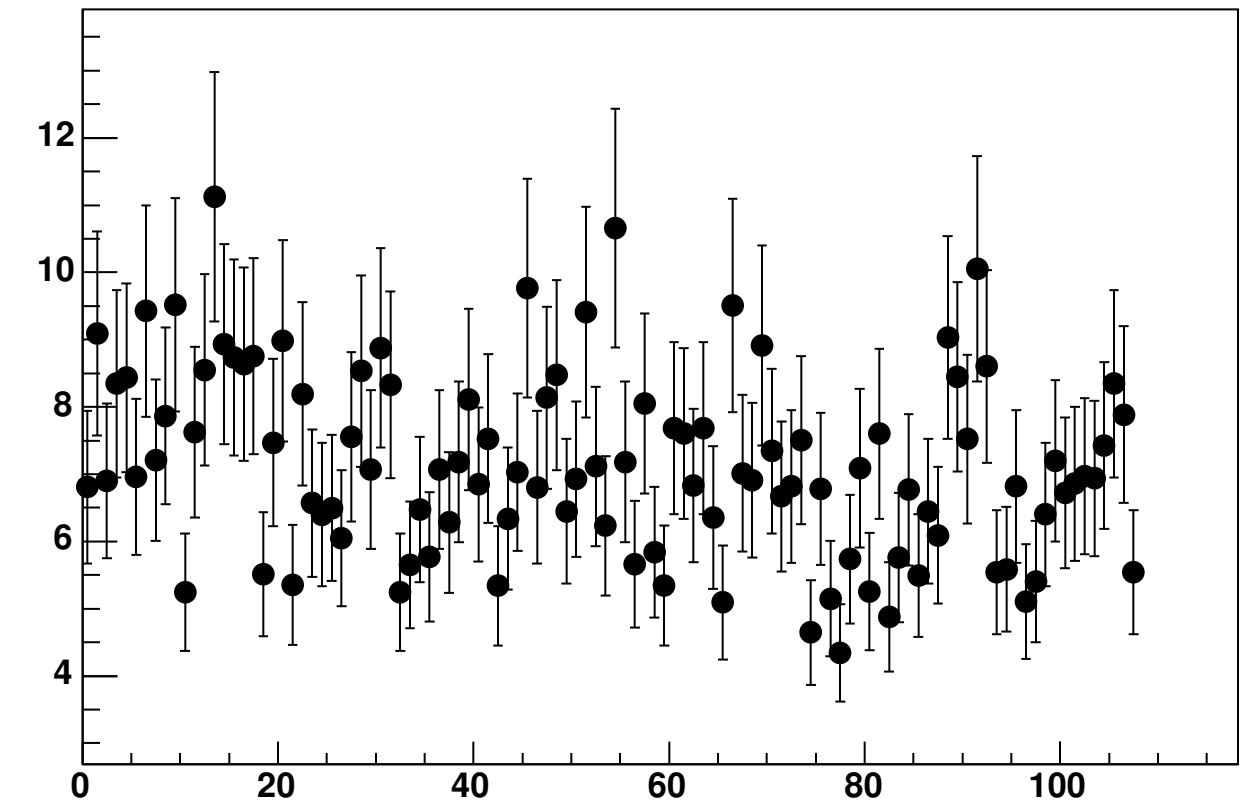
Enable 1, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



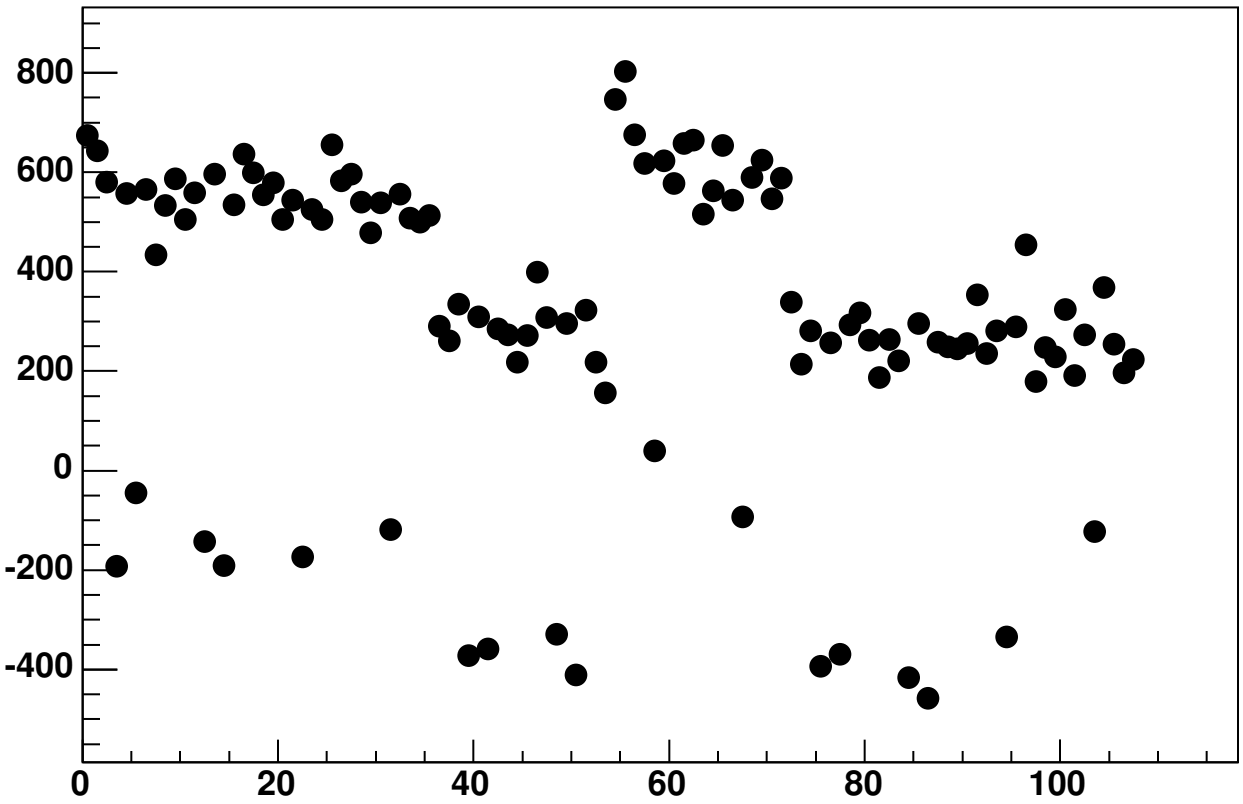
Enable 1, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



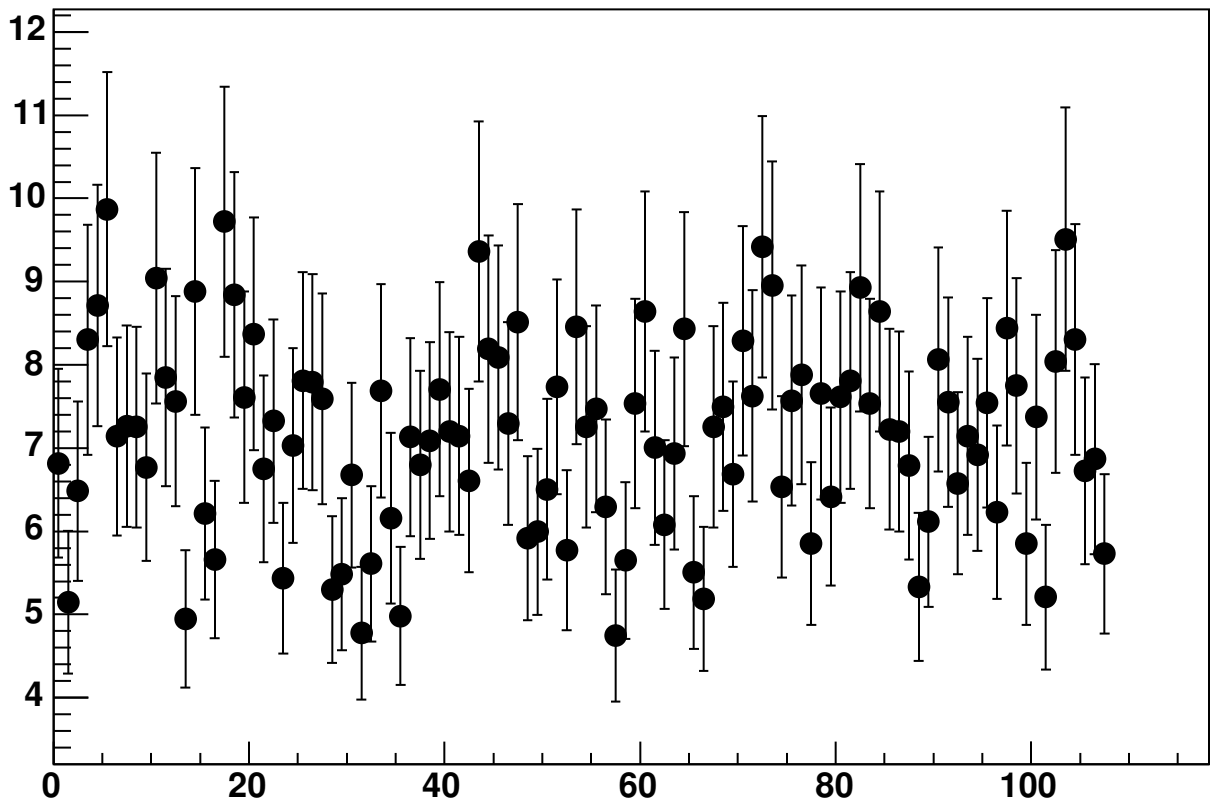
Enable 1, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



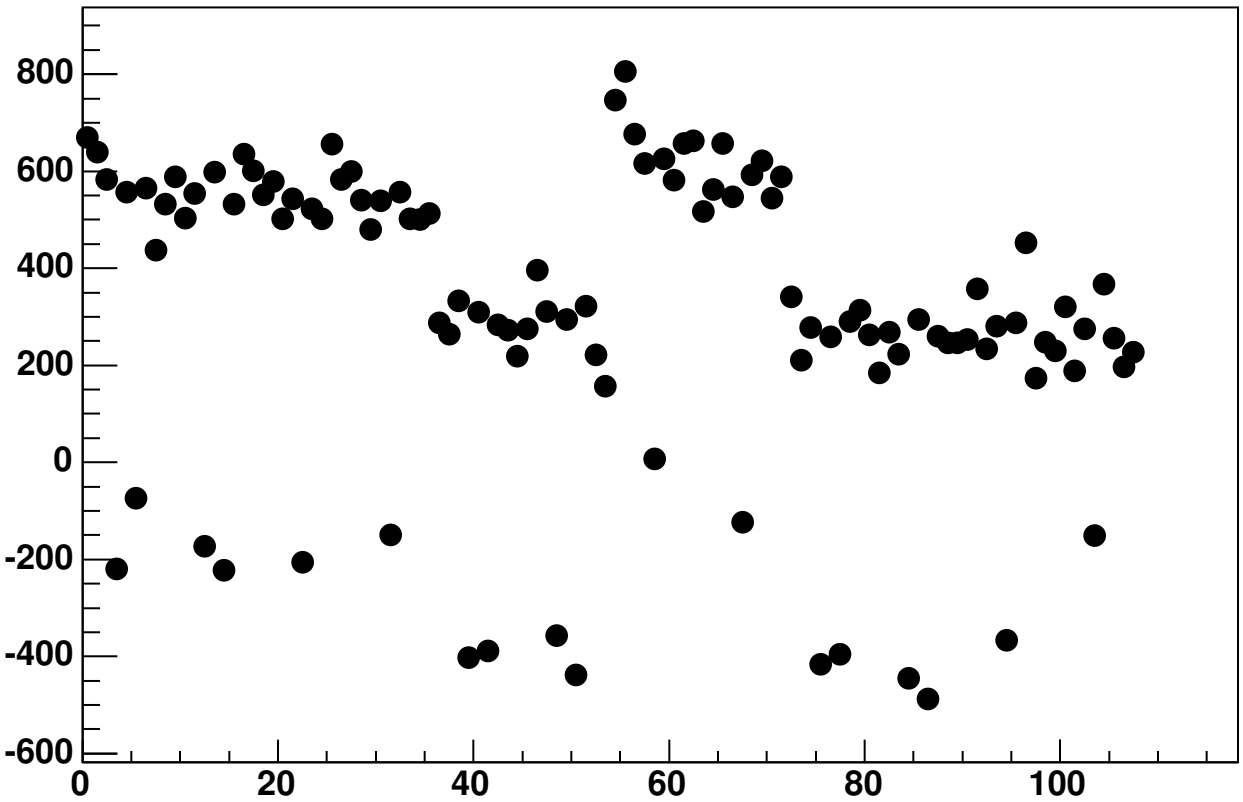
Enable 1, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



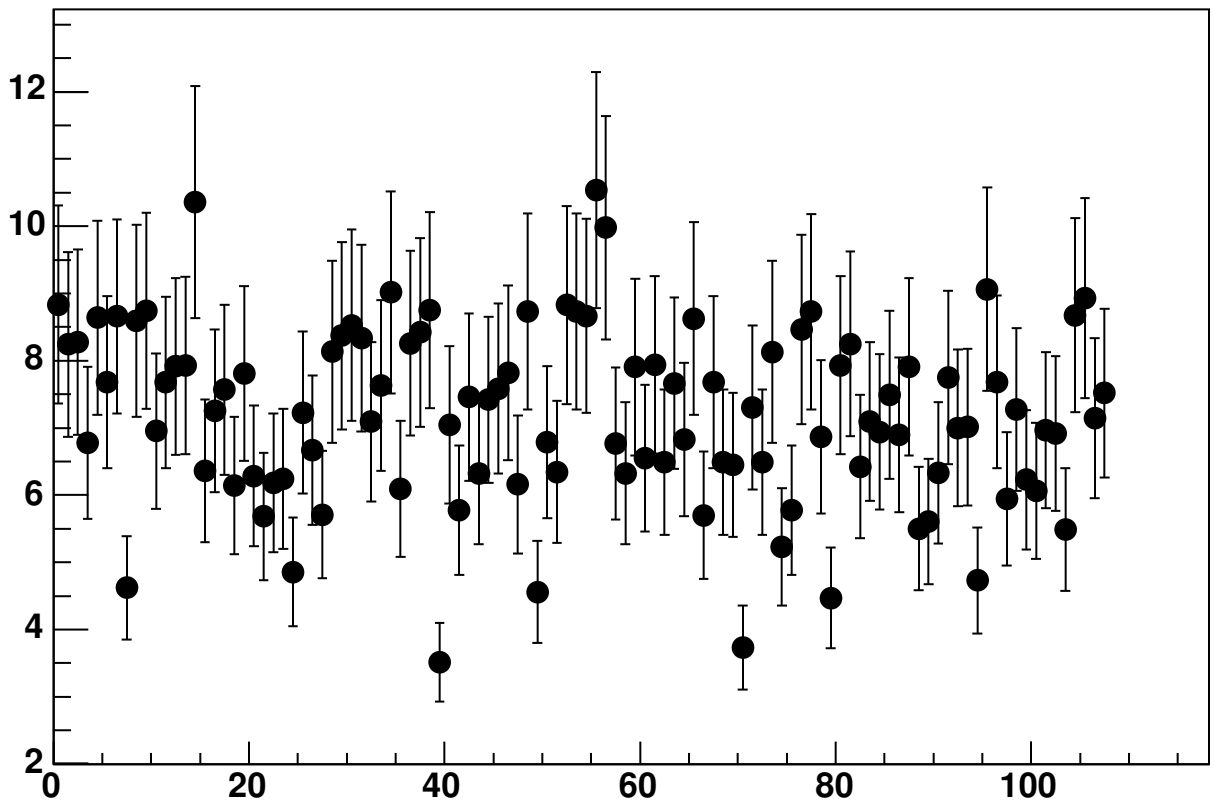
Enable 1, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



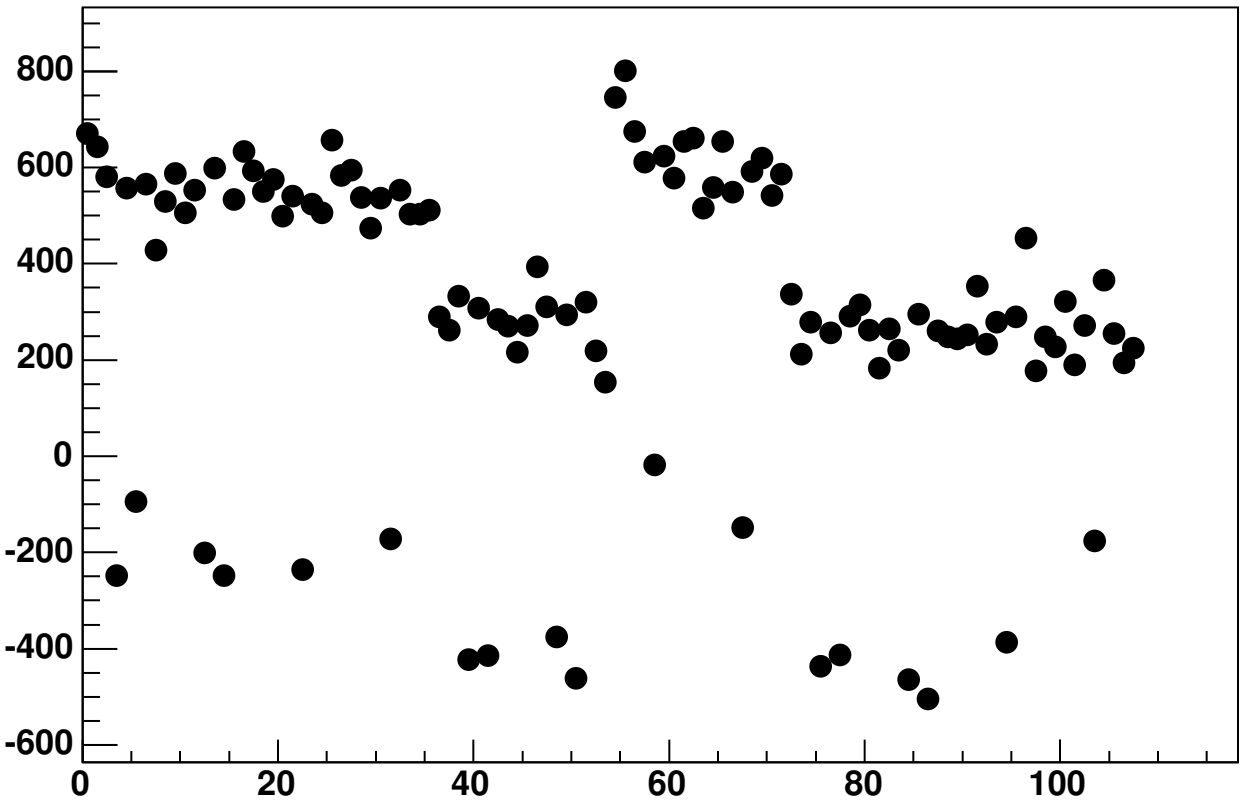
Enable 1, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



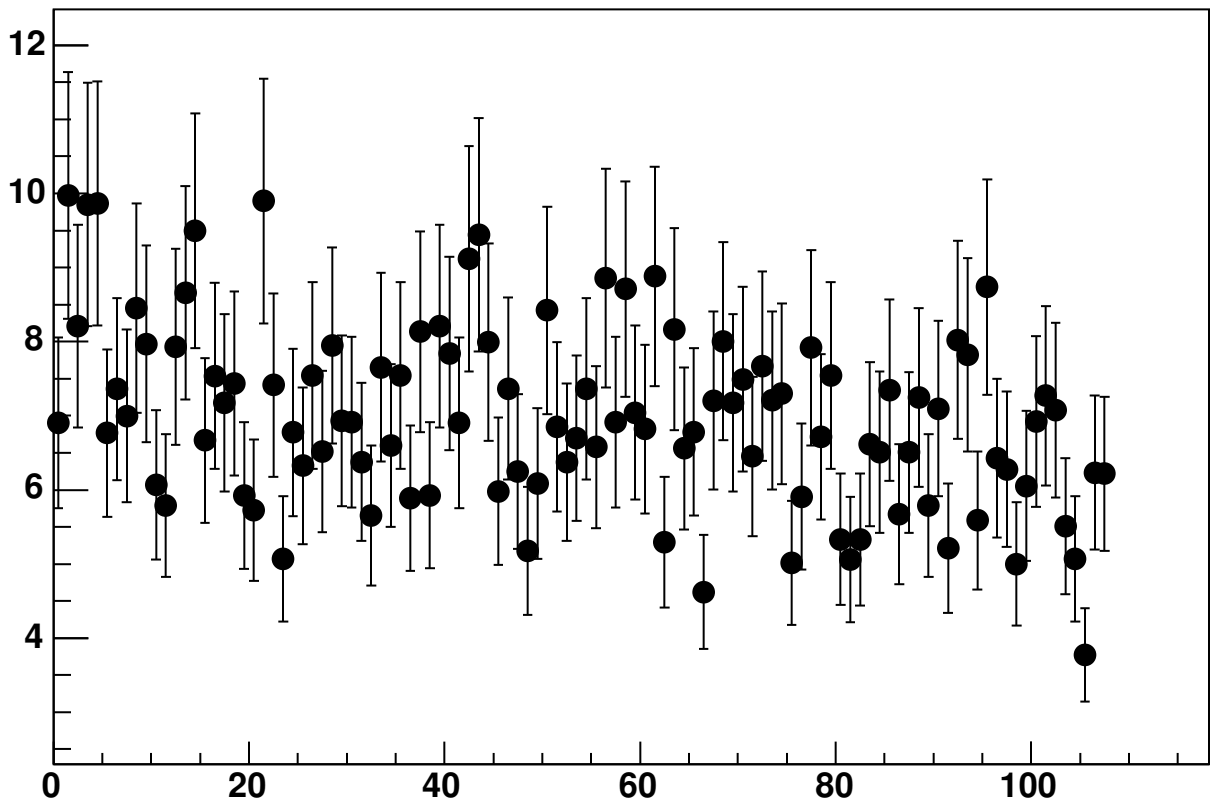
Enable 1, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



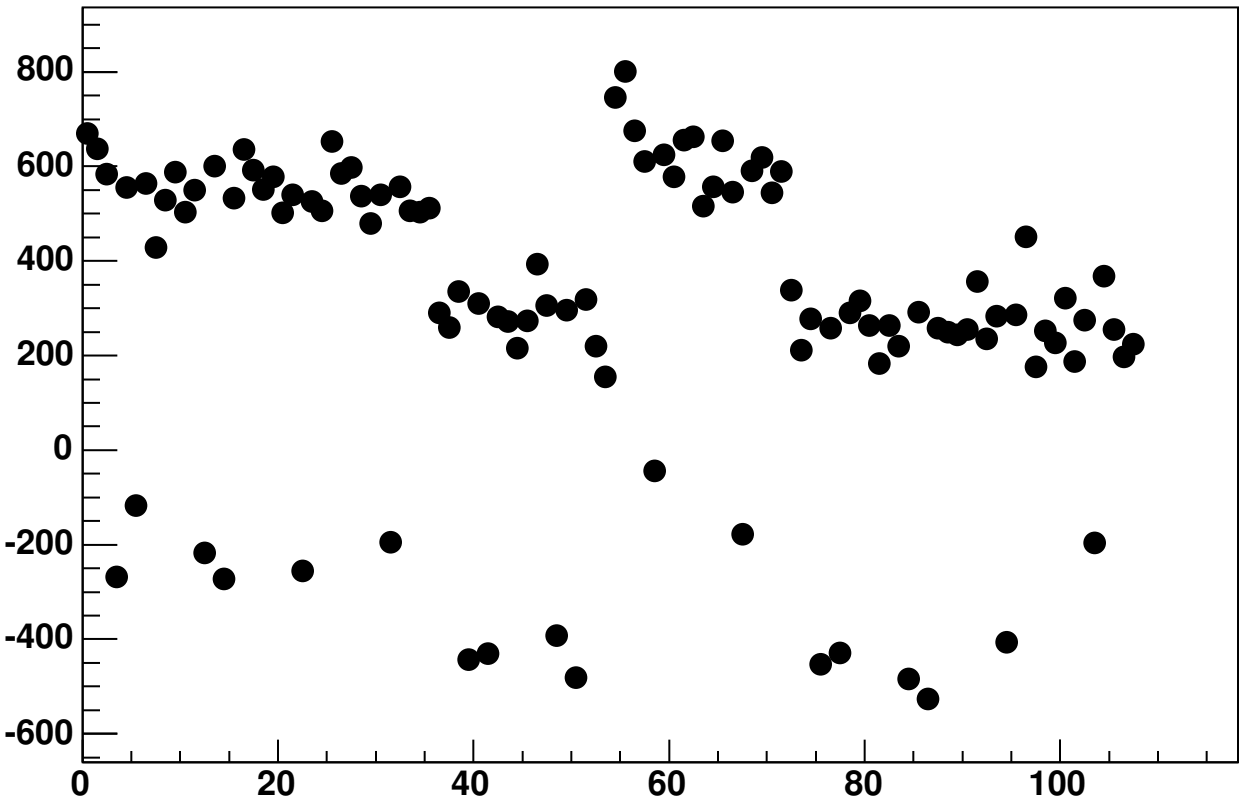
Enable 1, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



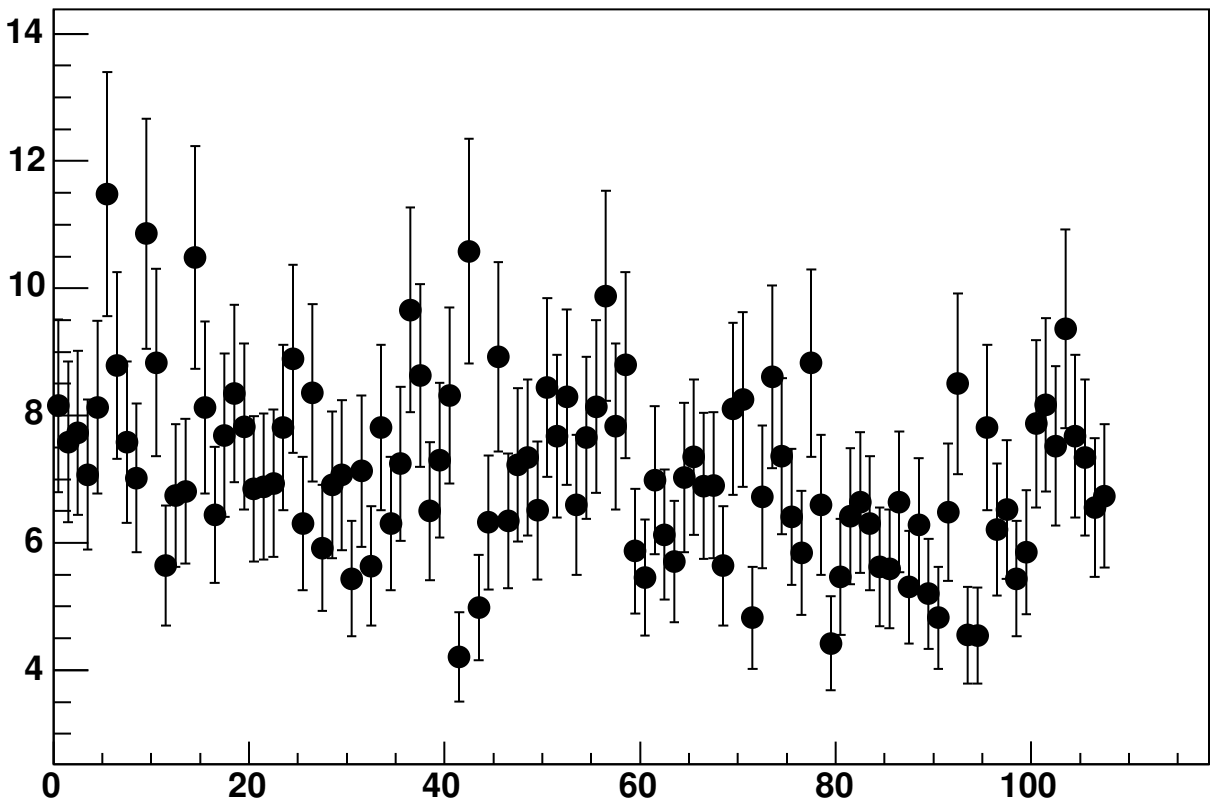
Enable 1, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



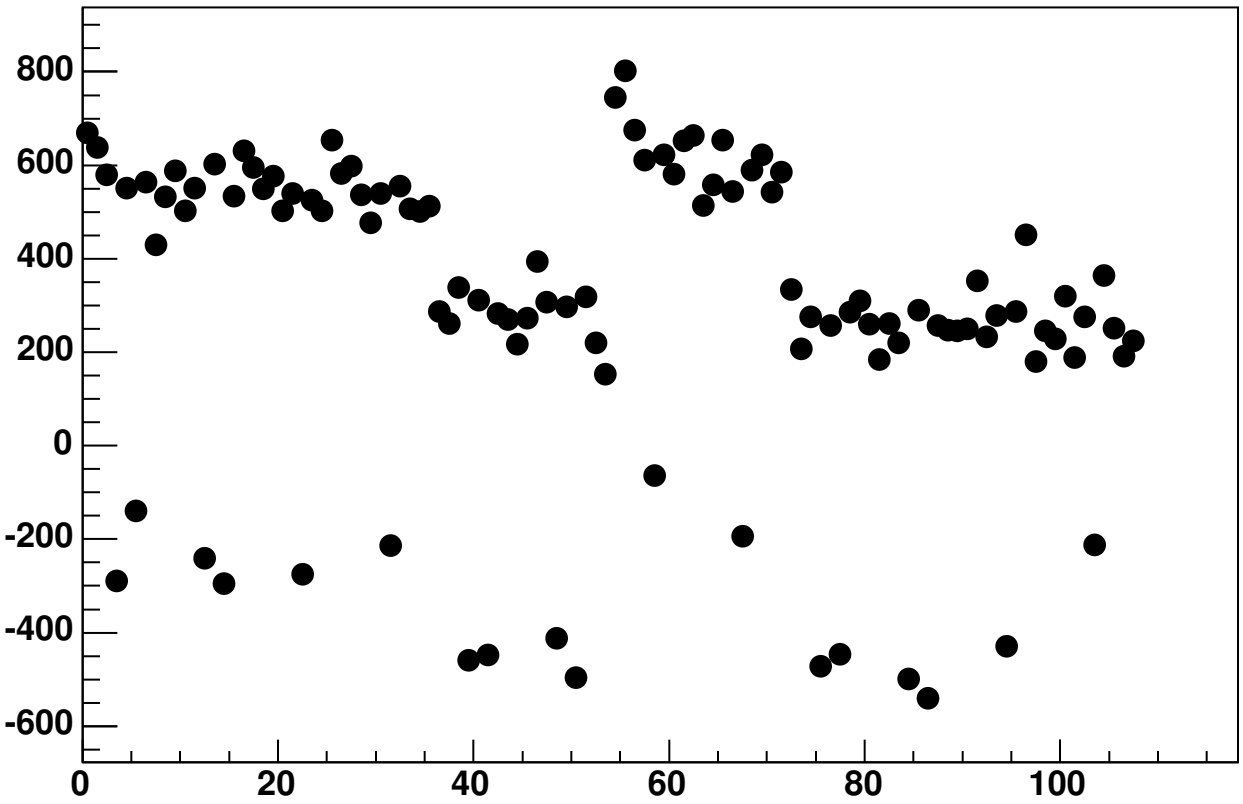
Enable 1, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



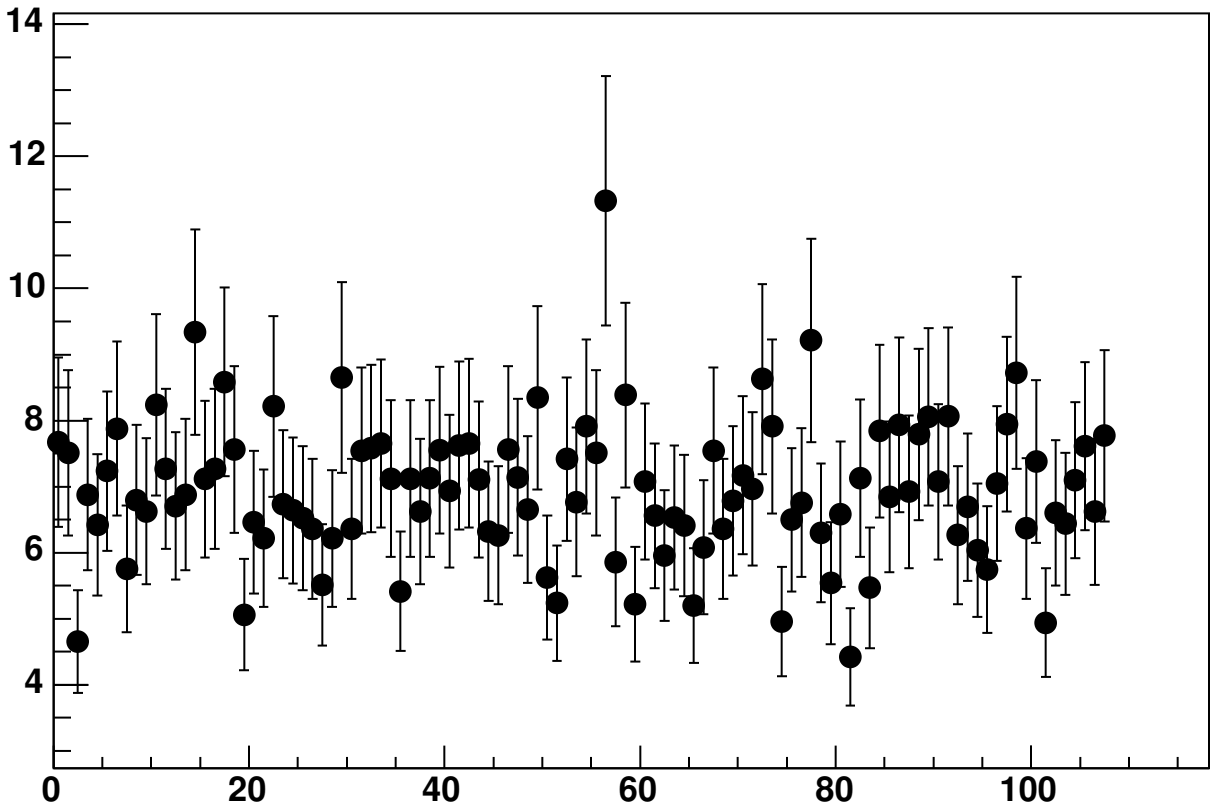
Enable 1, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



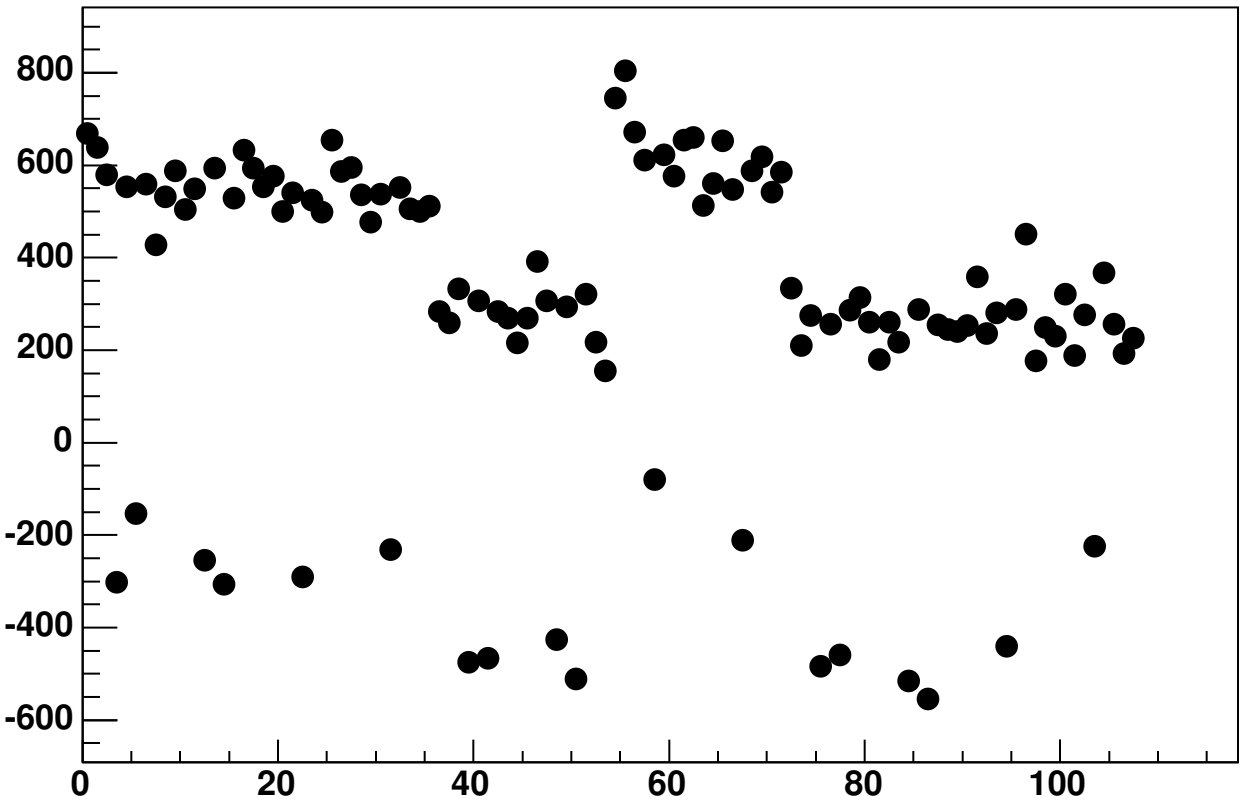
Enable 1, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



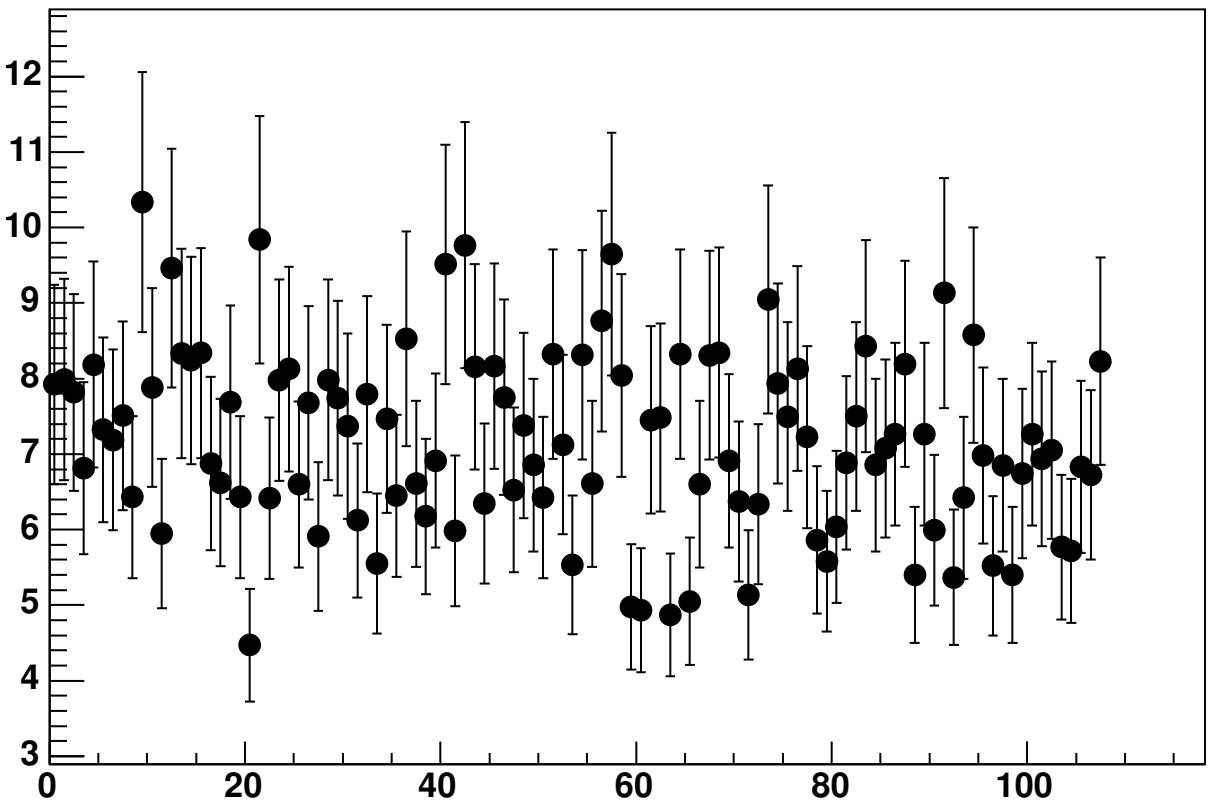
Enable 1, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



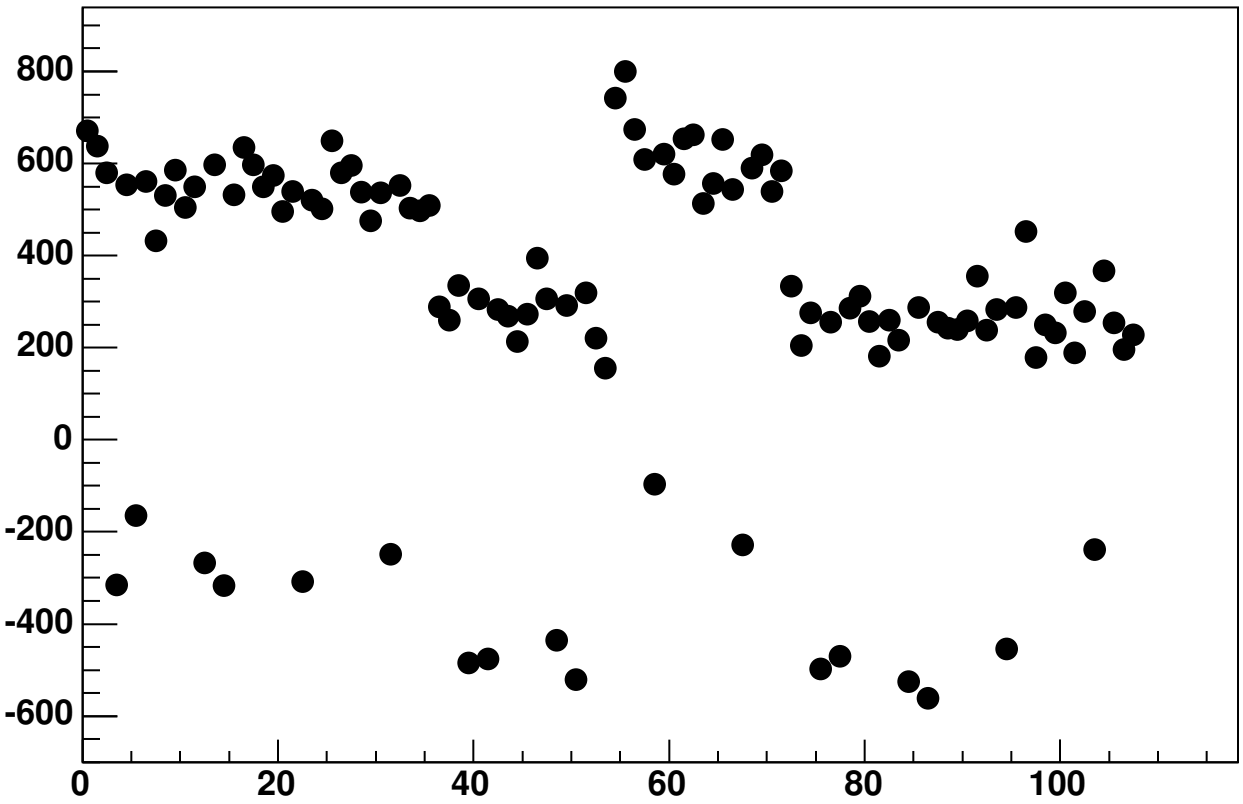
Enable 1, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



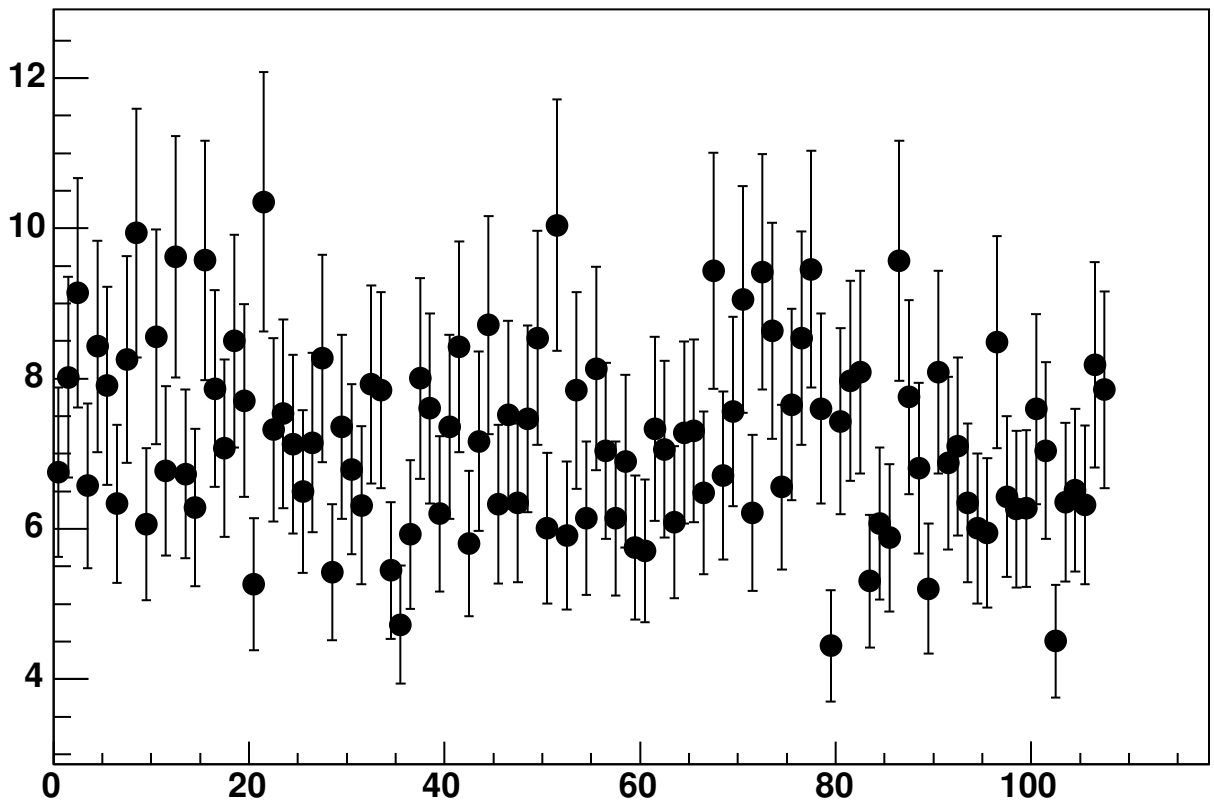
Enable 1, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan



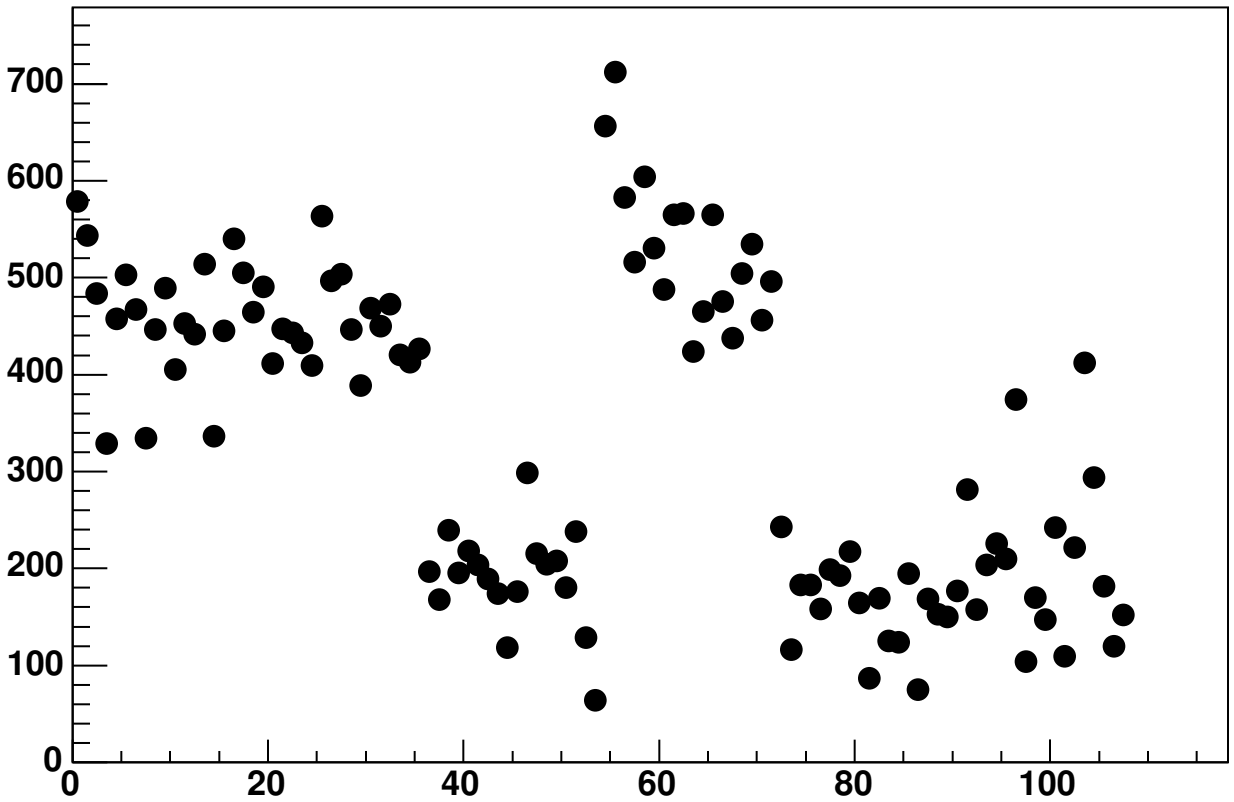
Enable 1, DAC=4000, Hold=245, ADC Mean vs 18*Chip+Chan



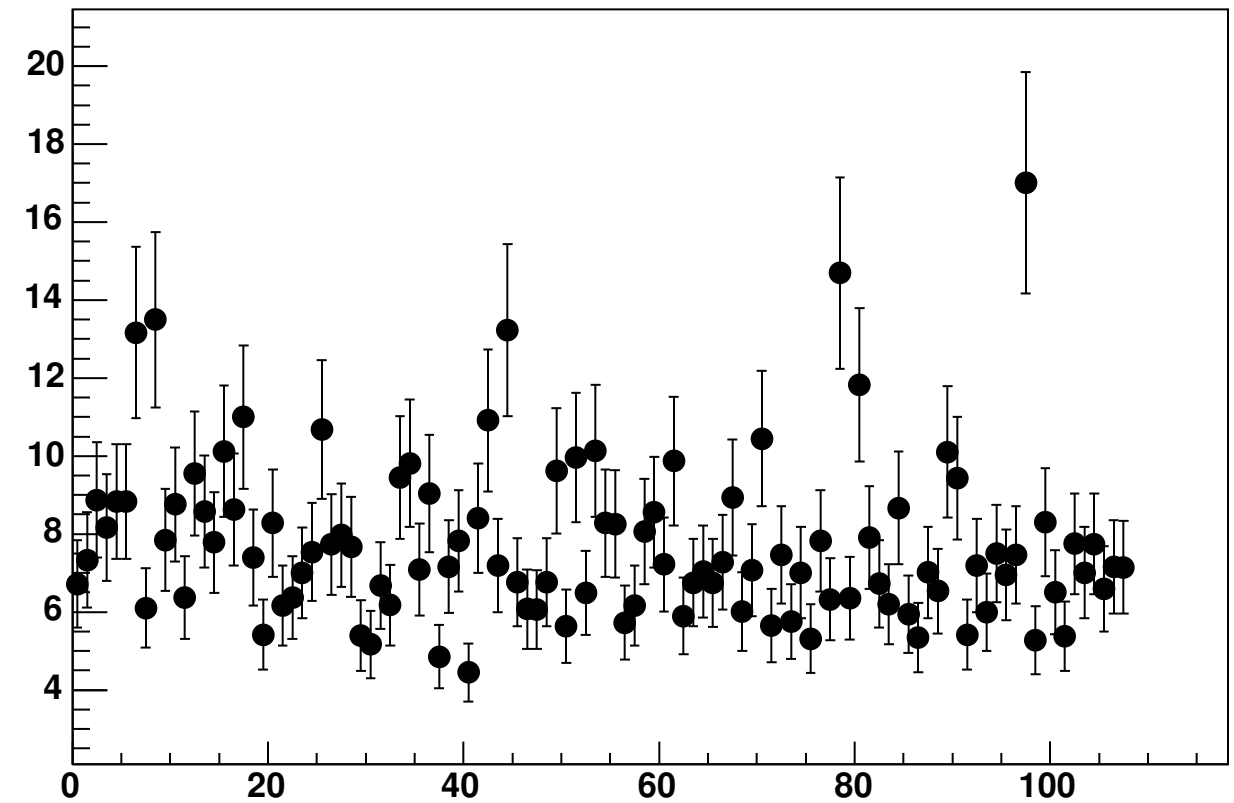
Enable 1, DAC=4000, Hold=245, ADC Noise vs 18*Chip+Chan



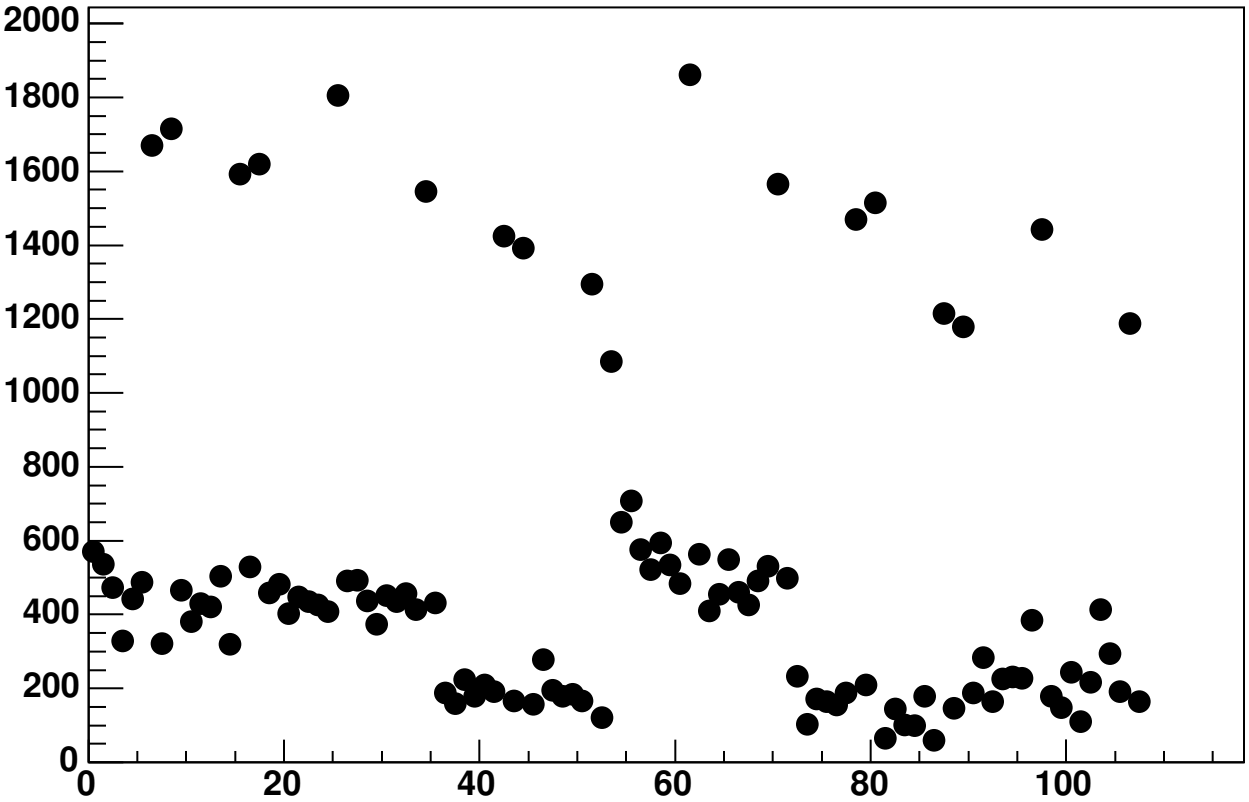
Enable 2, DAC=4000, Hold=0, ADC Mean vs 18*Chip+Chan



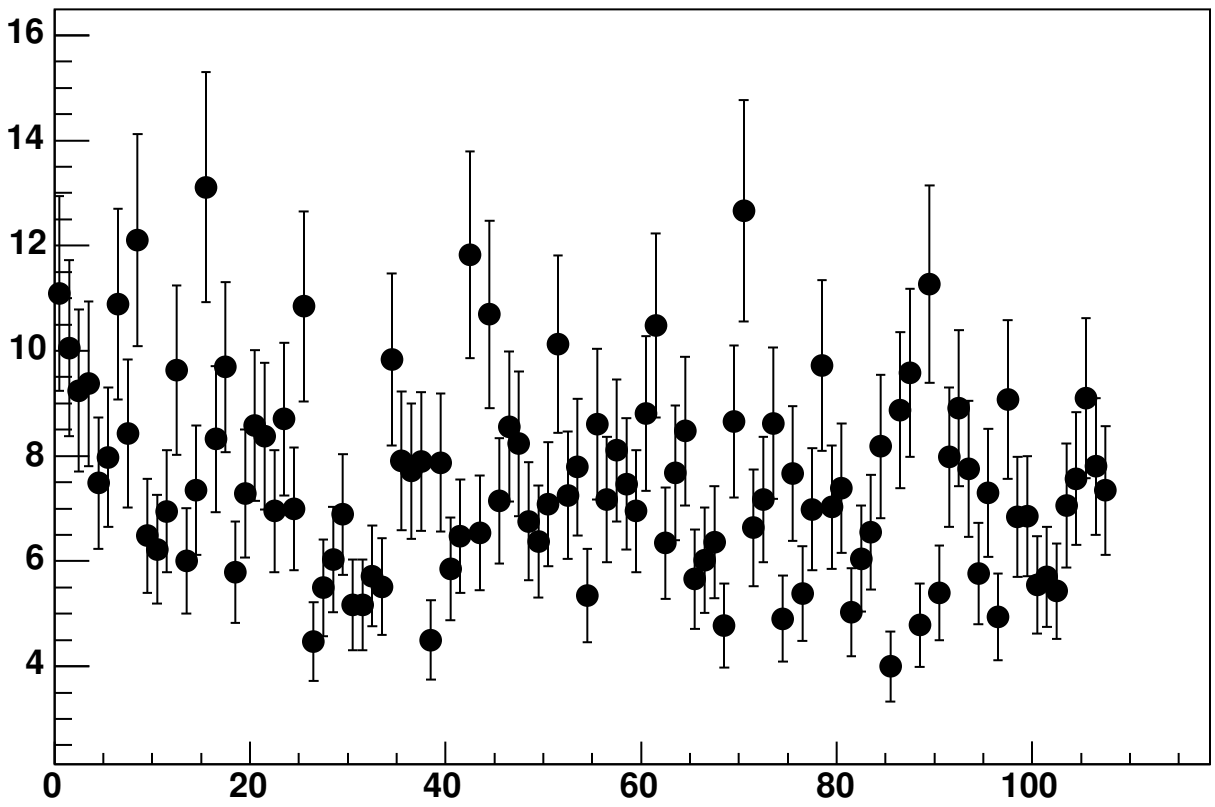
Enable 2, DAC=4000, Hold=0, ADC Noise vs 18*Chip+Chan



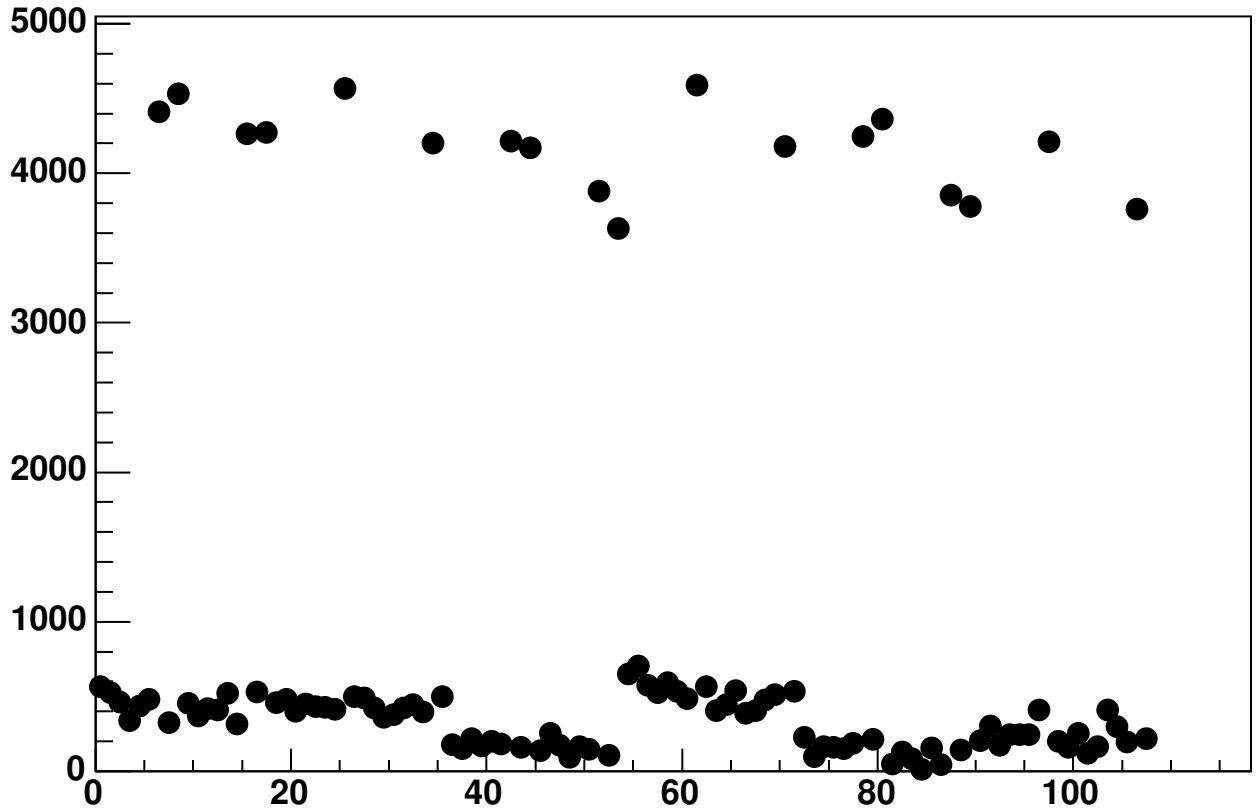
Enable 2, DAC=4000, Hold=5, ADC Mean vs 18*Chip+Chan



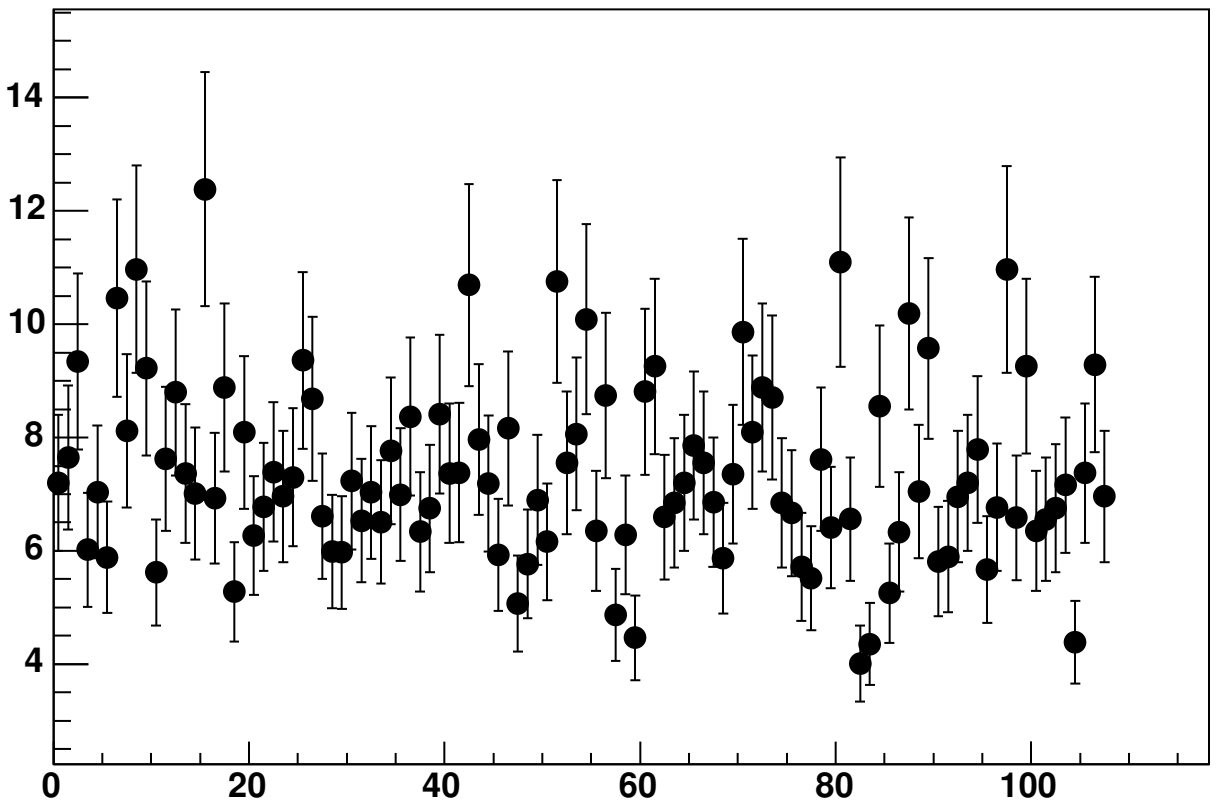
Enable 2, DAC=4000, Hold=5, ADC Noise vs 18*Chip+Chan



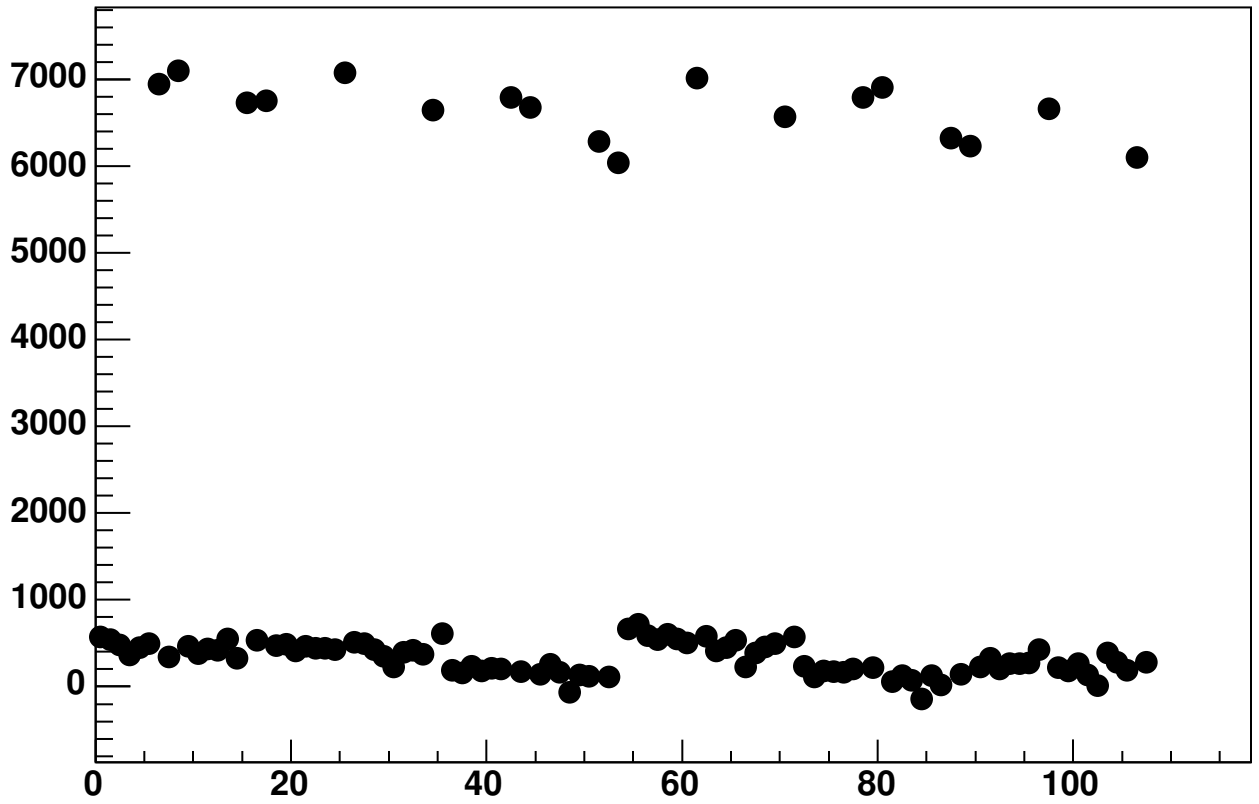
Enable 2, DAC=4000, Hold=10, ADC Mean vs 18*Chip+Chan



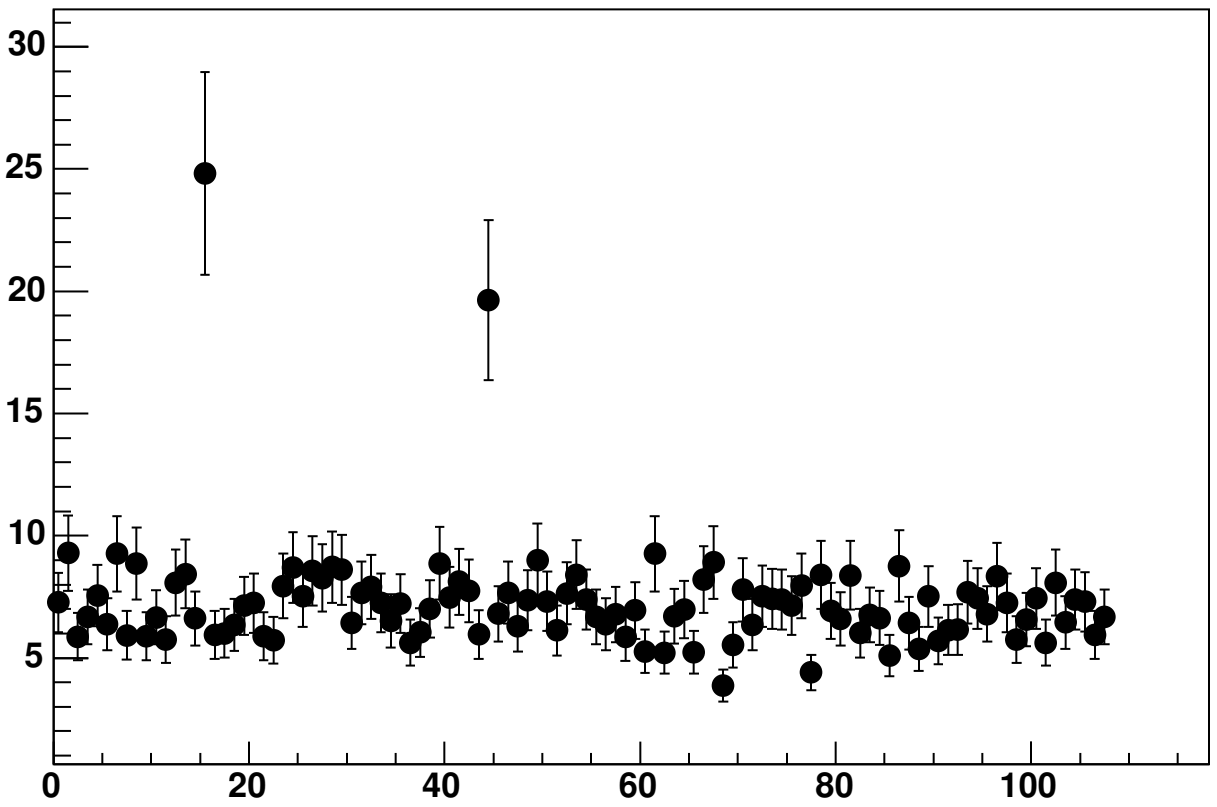
Enable 2, DAC=4000, Hold=10, ADC Noise vs 18*Chip+Chan



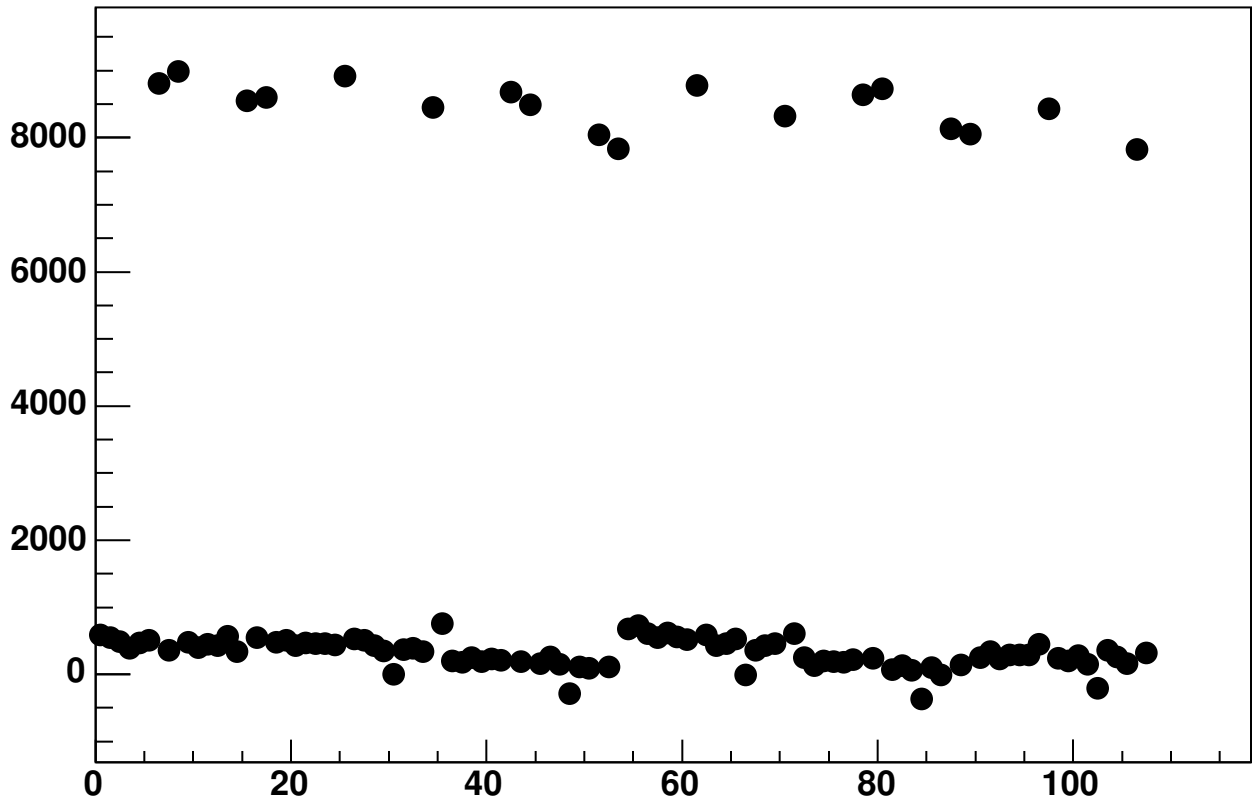
Enable 2, DAC=4000, Hold=15, ADC Mean vs 18*Chip+Chan



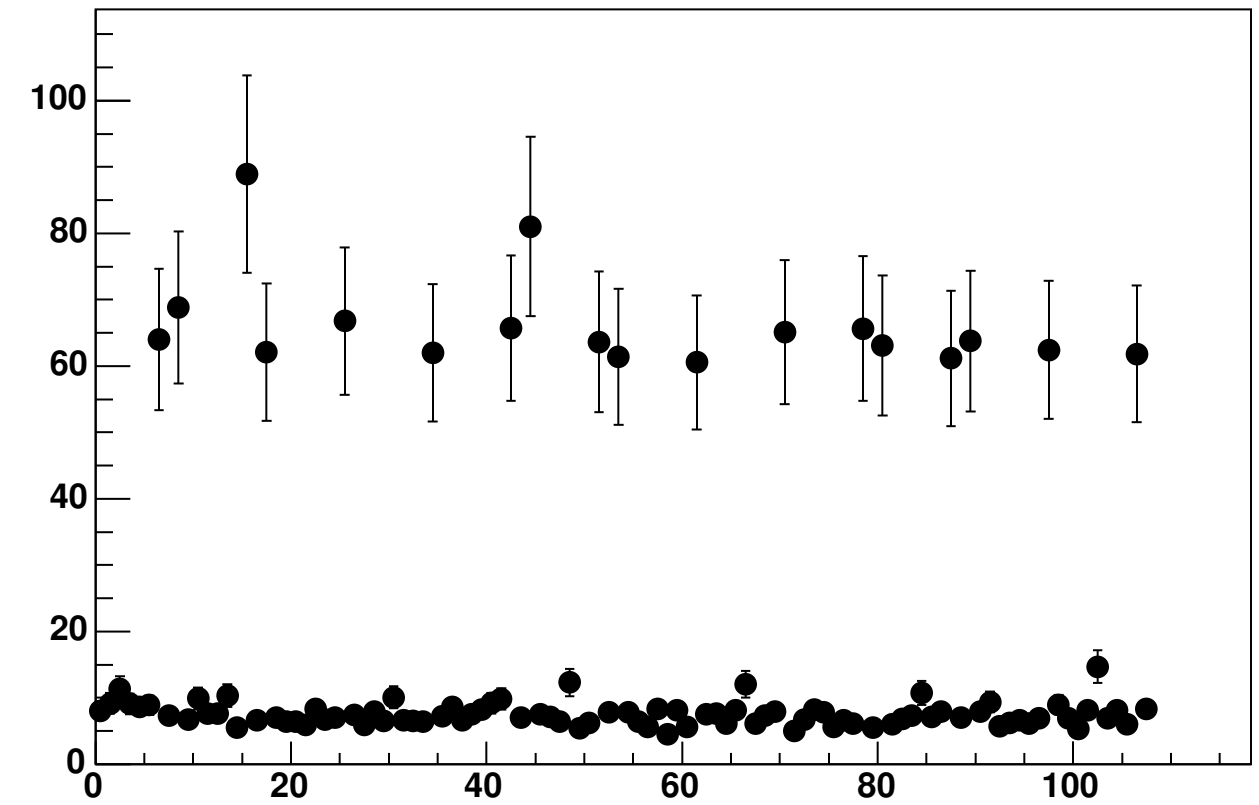
Enable 2, DAC=4000, Hold=15, ADC Noise vs 18*Chip+Chan



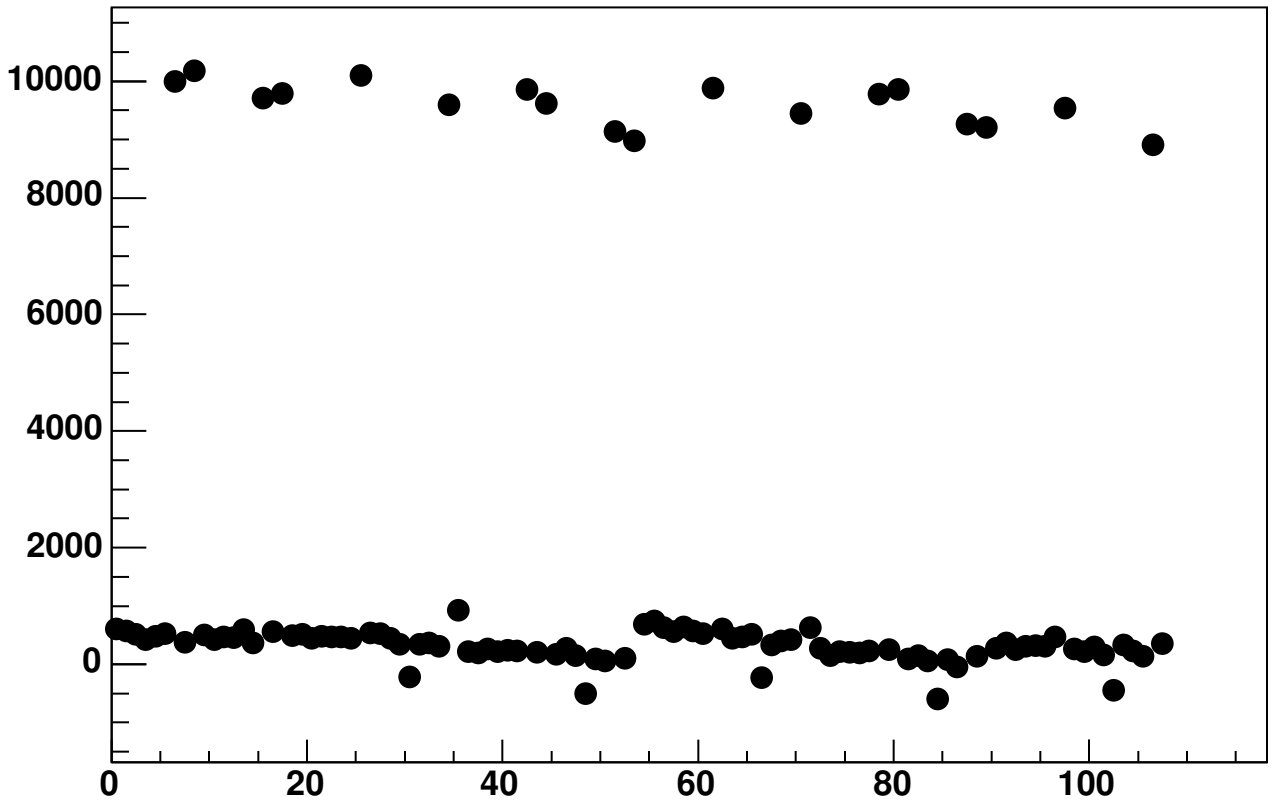
Enable 2, DAC=4000, Hold=20, ADC Mean vs 18*Chip+Chan



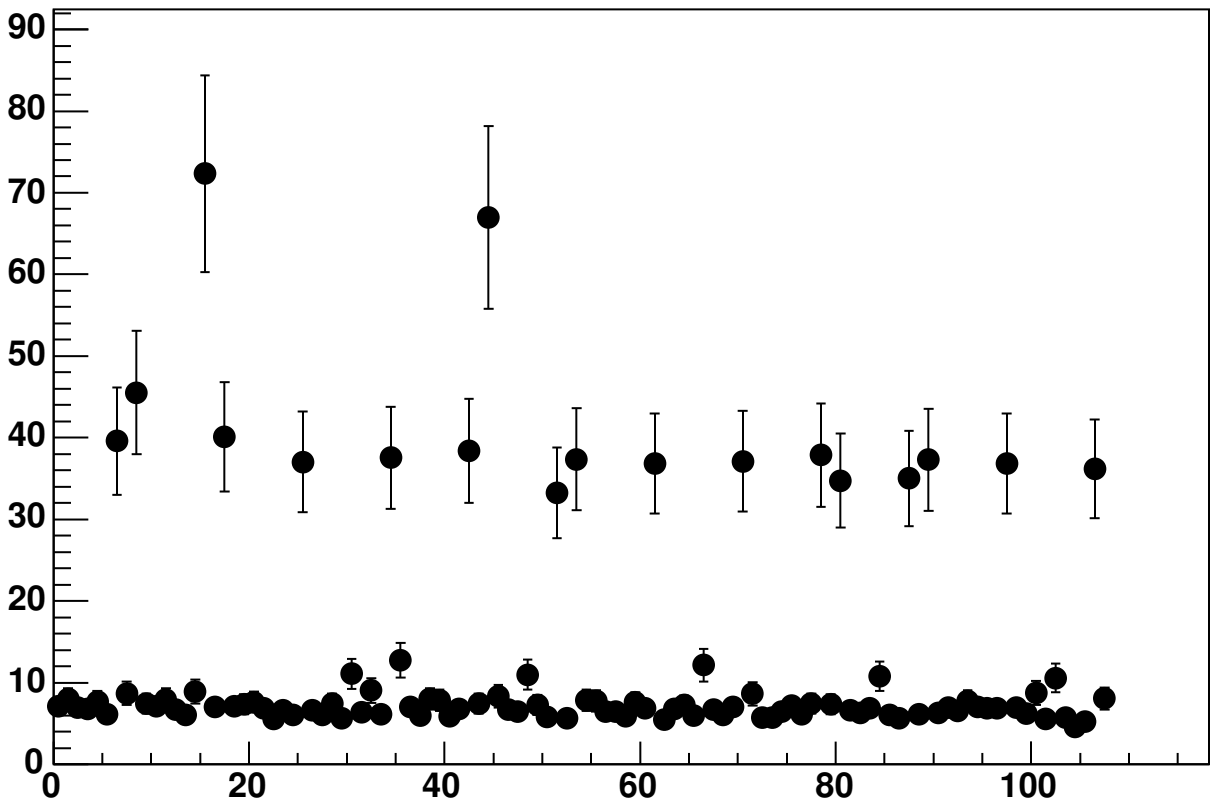
Enable 2, DAC=4000, Hold=20, ADC Noise vs 18*Chip+Chan



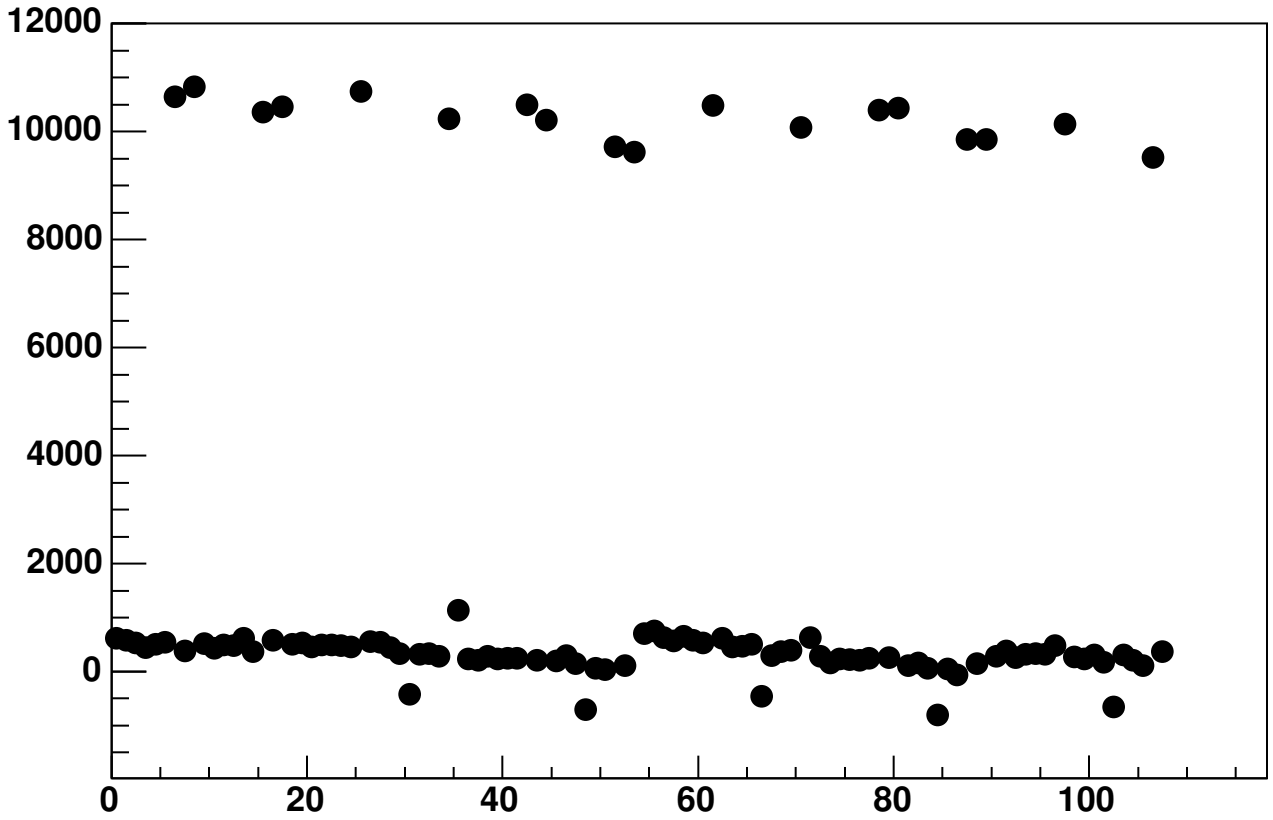
Enable 2, DAC=4000, Hold=25, ADC Mean vs 18*Chip+Chan



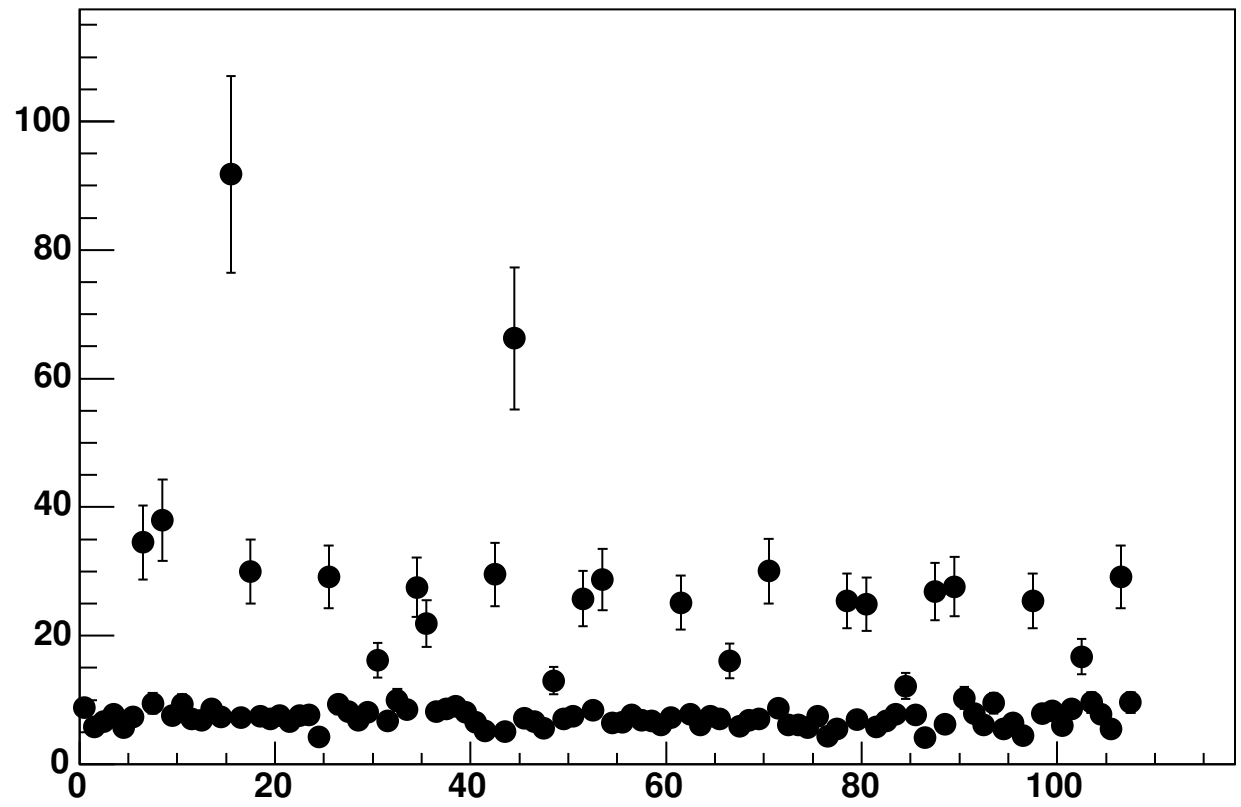
Enable 2, DAC=4000, Hold=25, ADC Noise vs 18*Chip+Chan



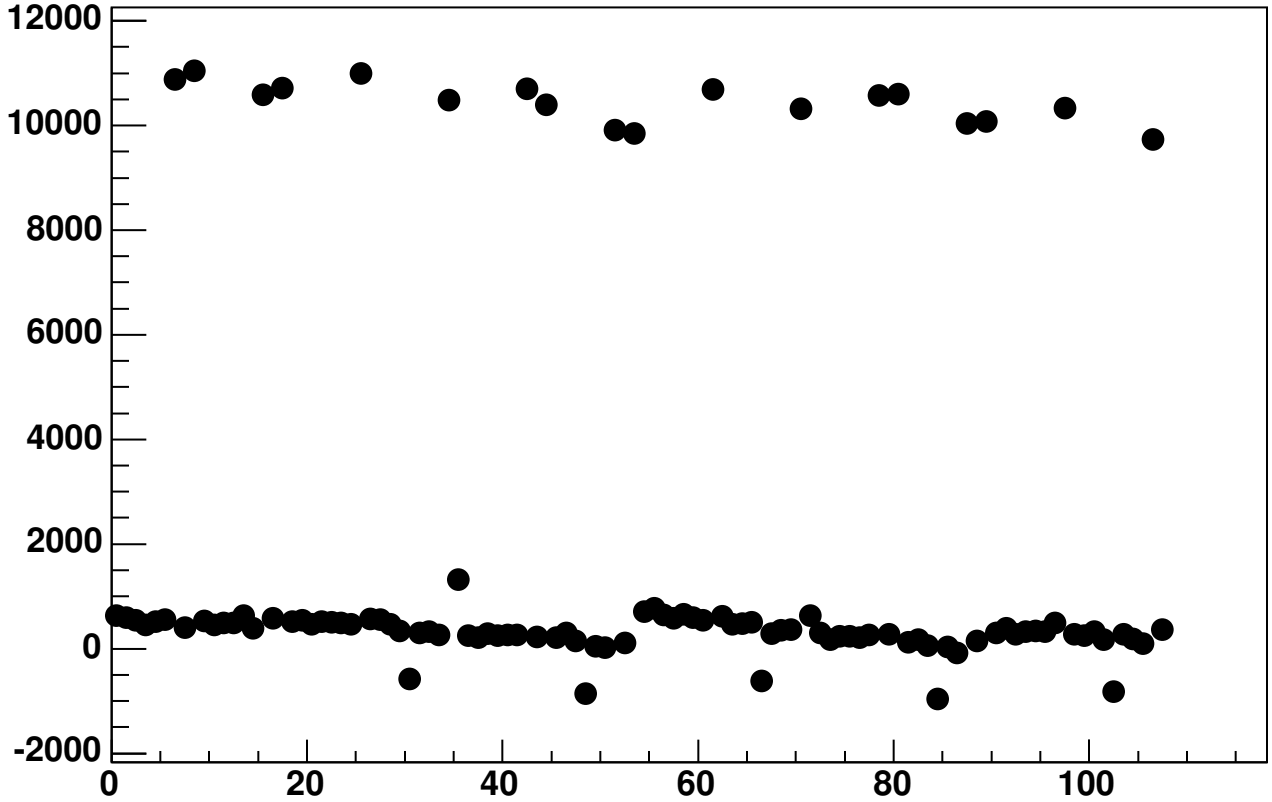
Enable 2, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



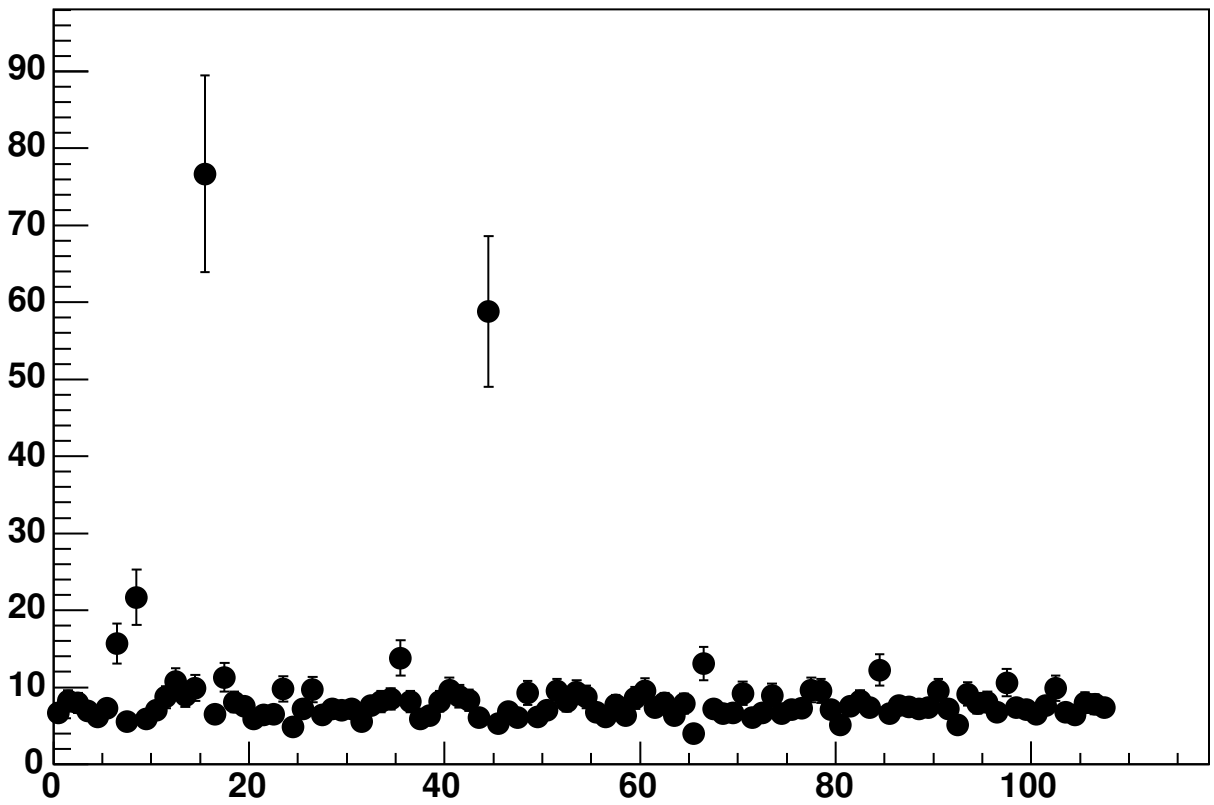
Enable 2, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



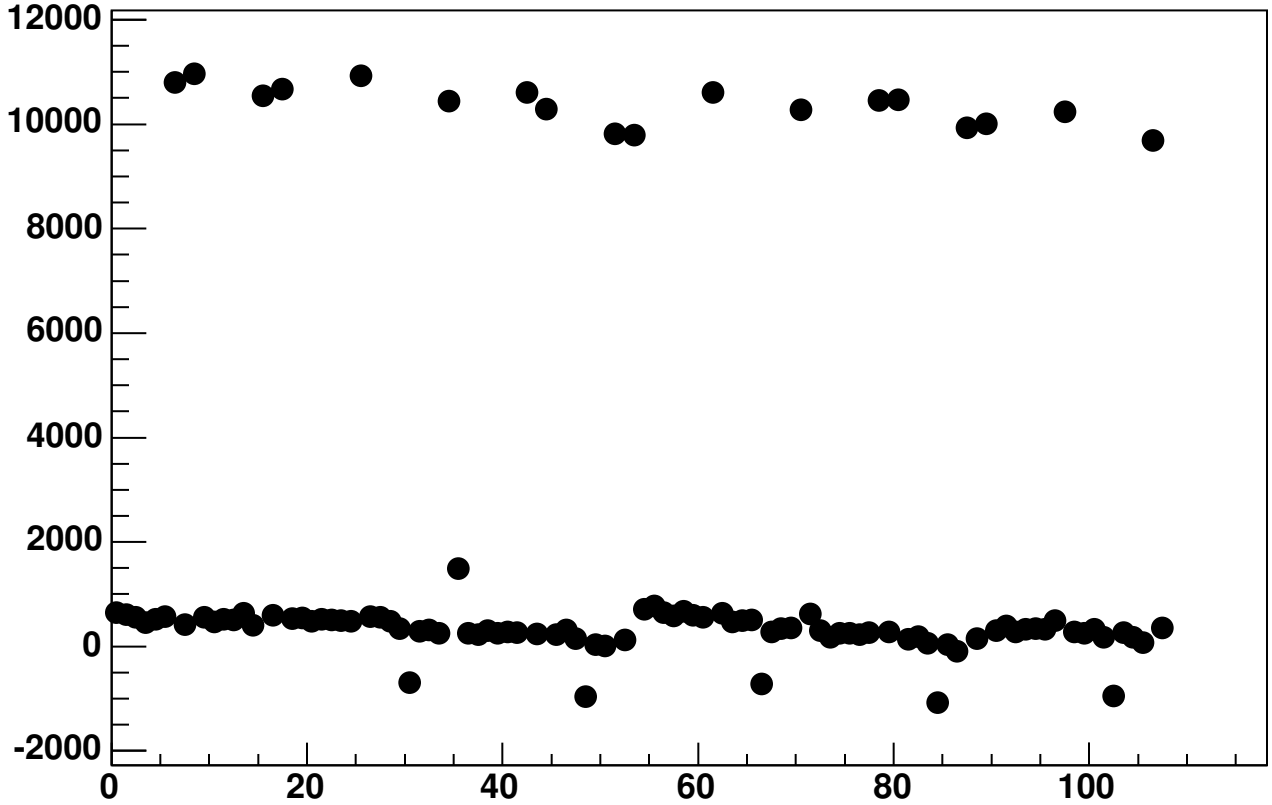
Enable 2, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



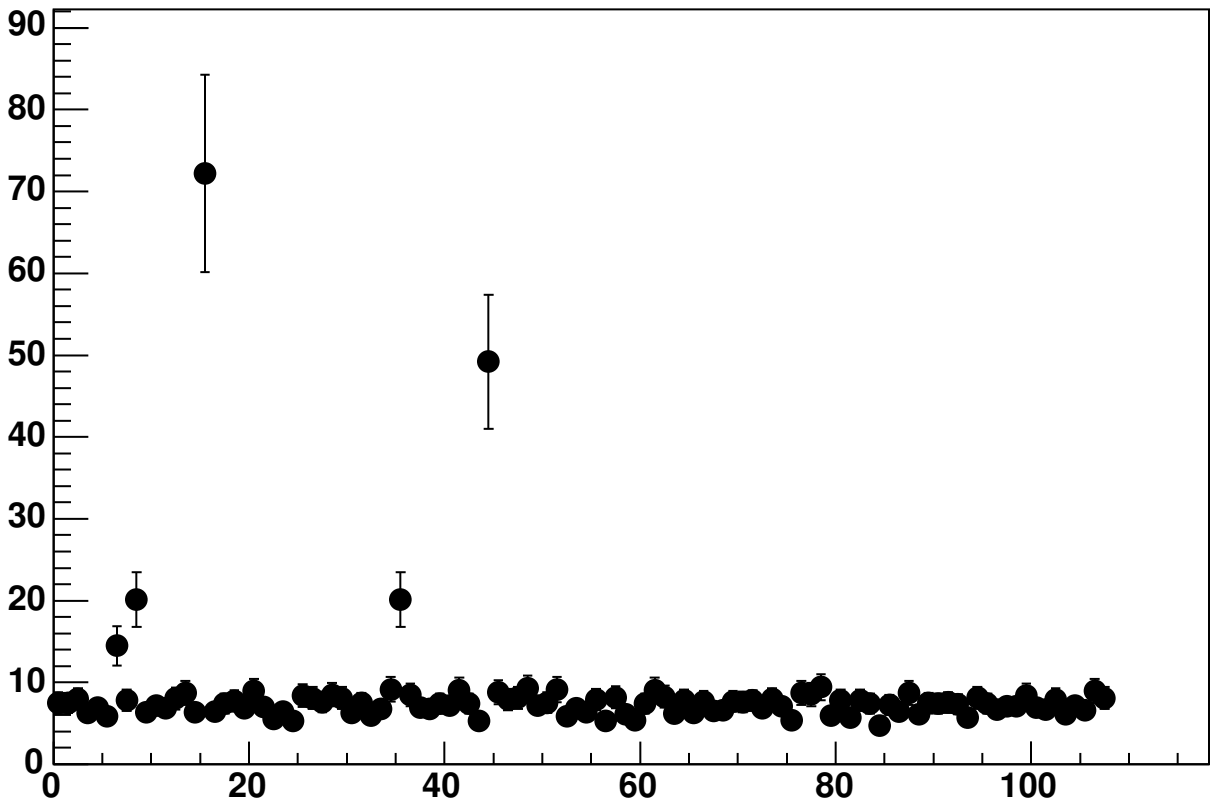
Enable 2, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



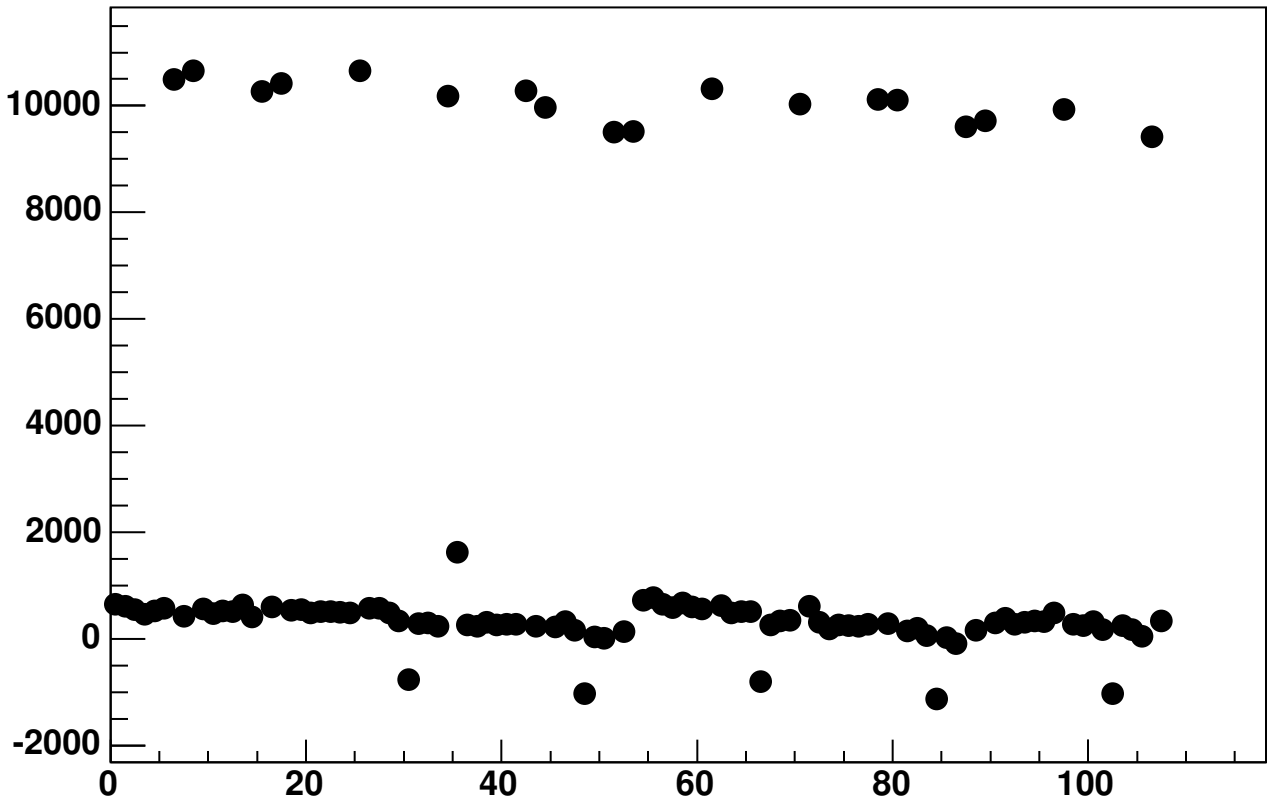
Enable 2, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



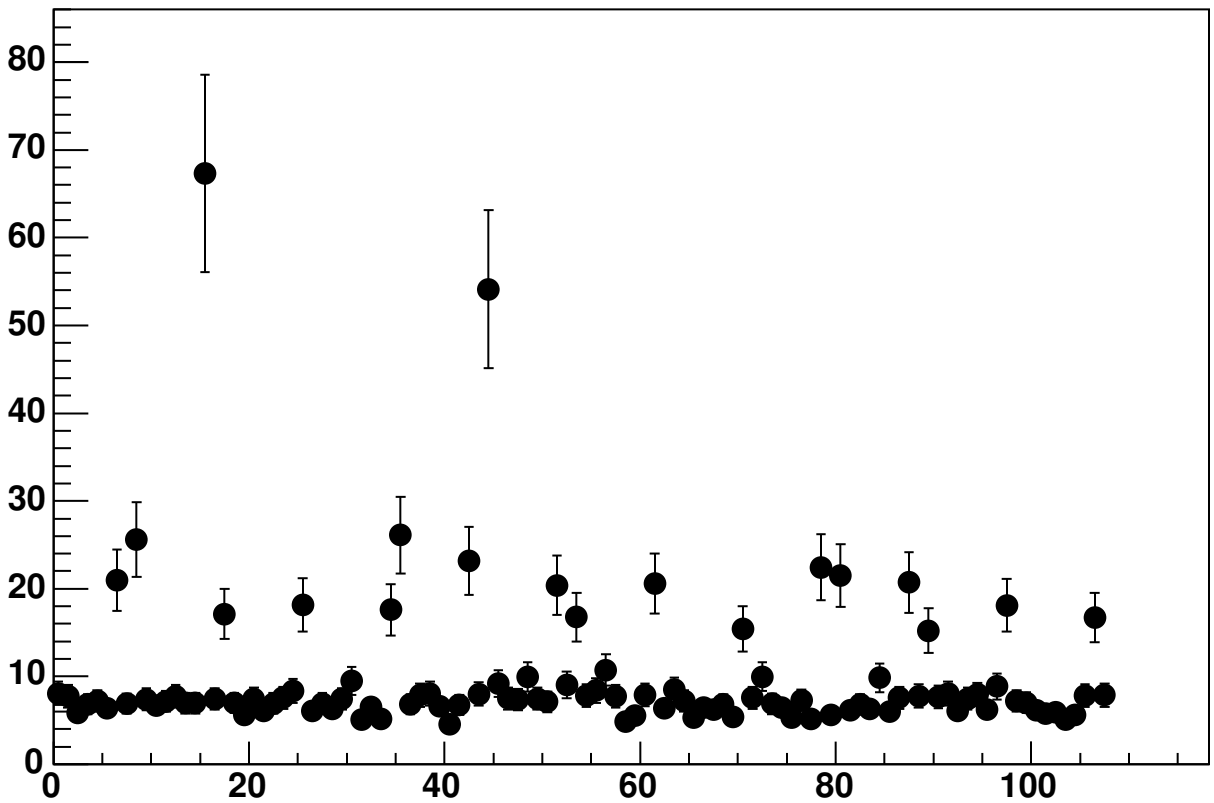
Enable 2, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



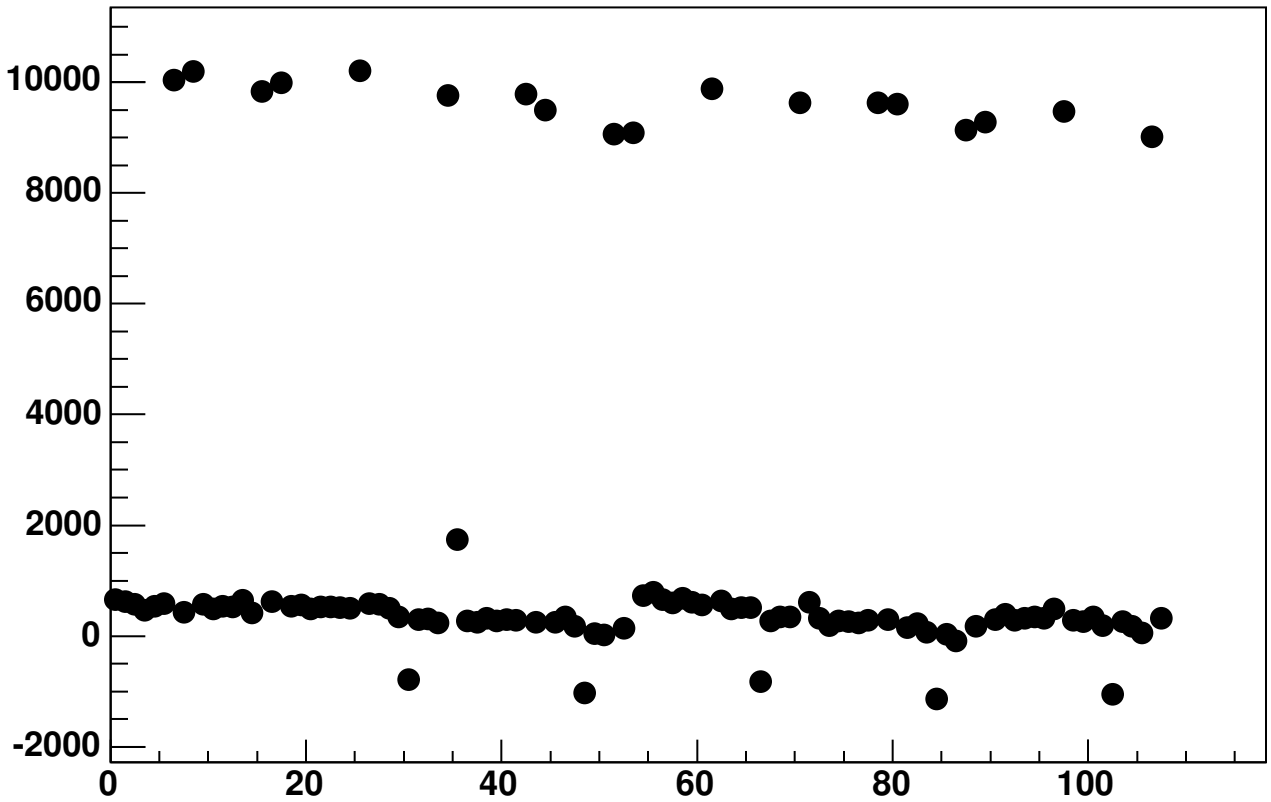
Enable 2, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



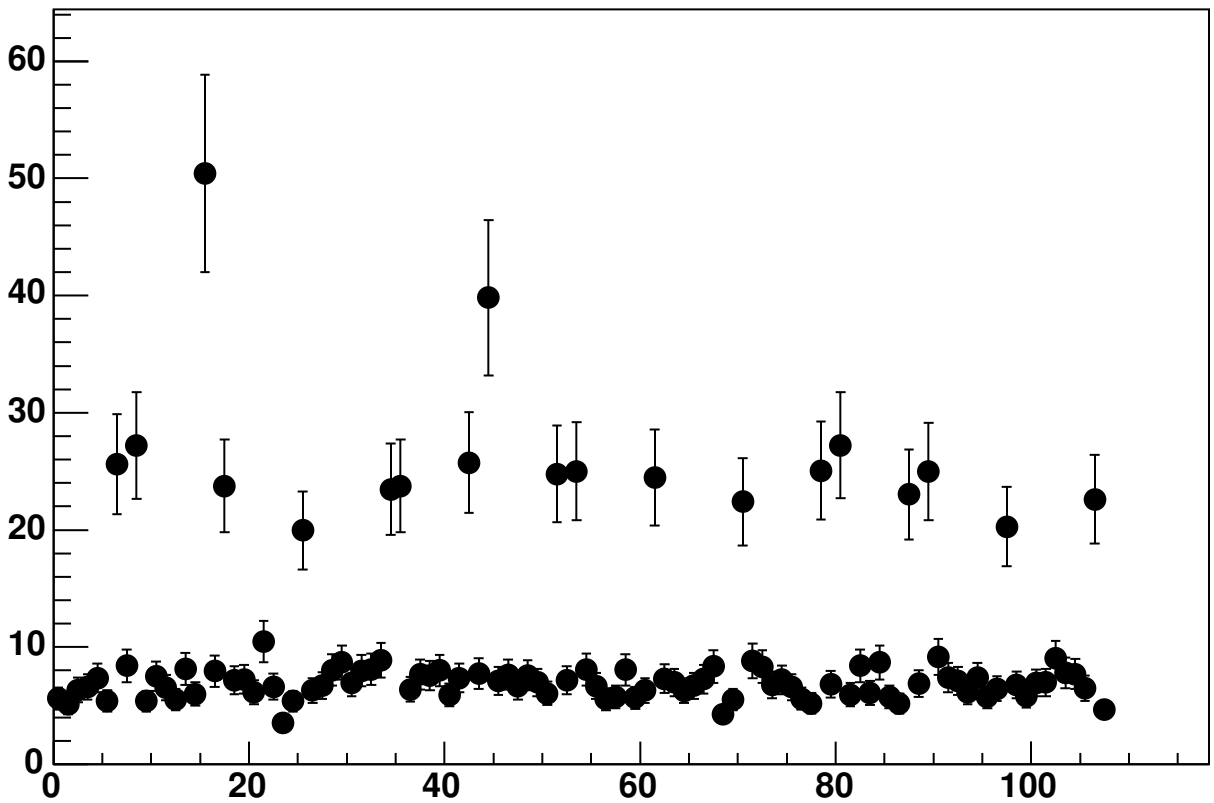
Enable 2, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



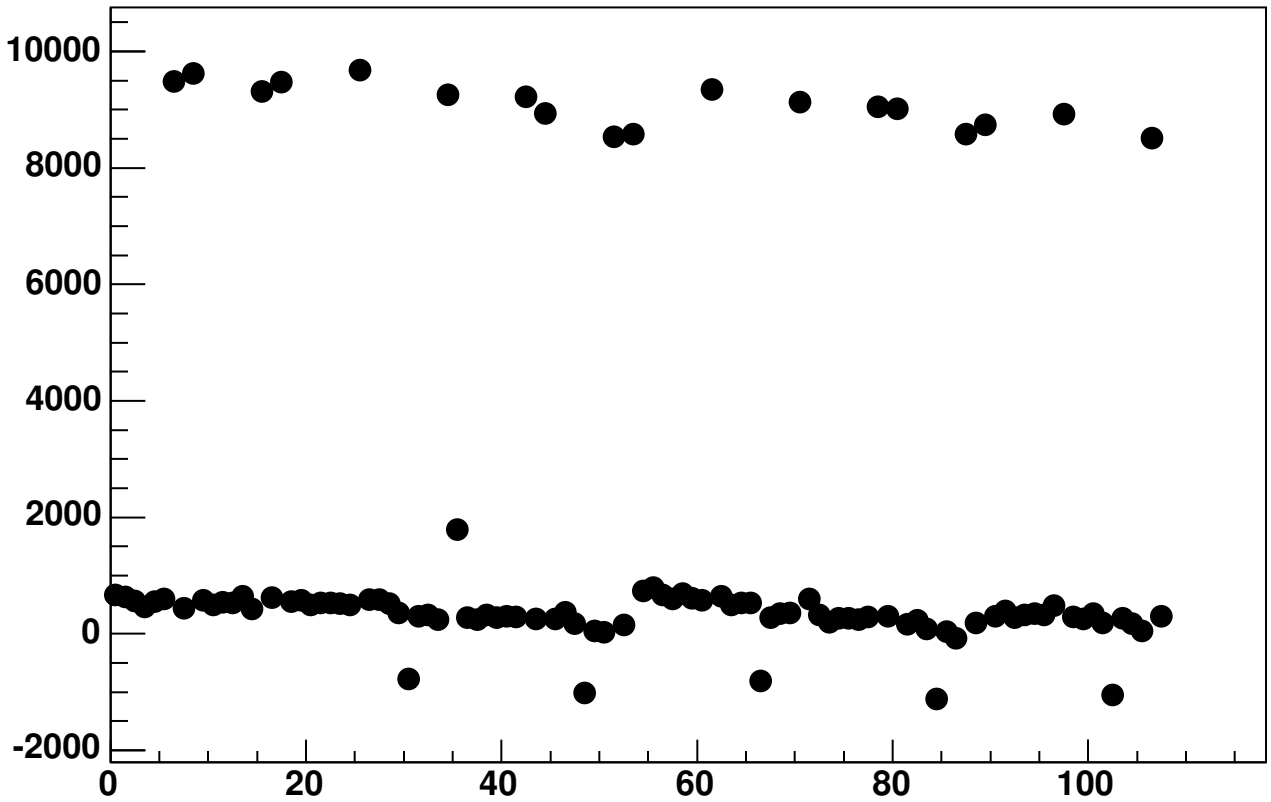
Enable 2, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



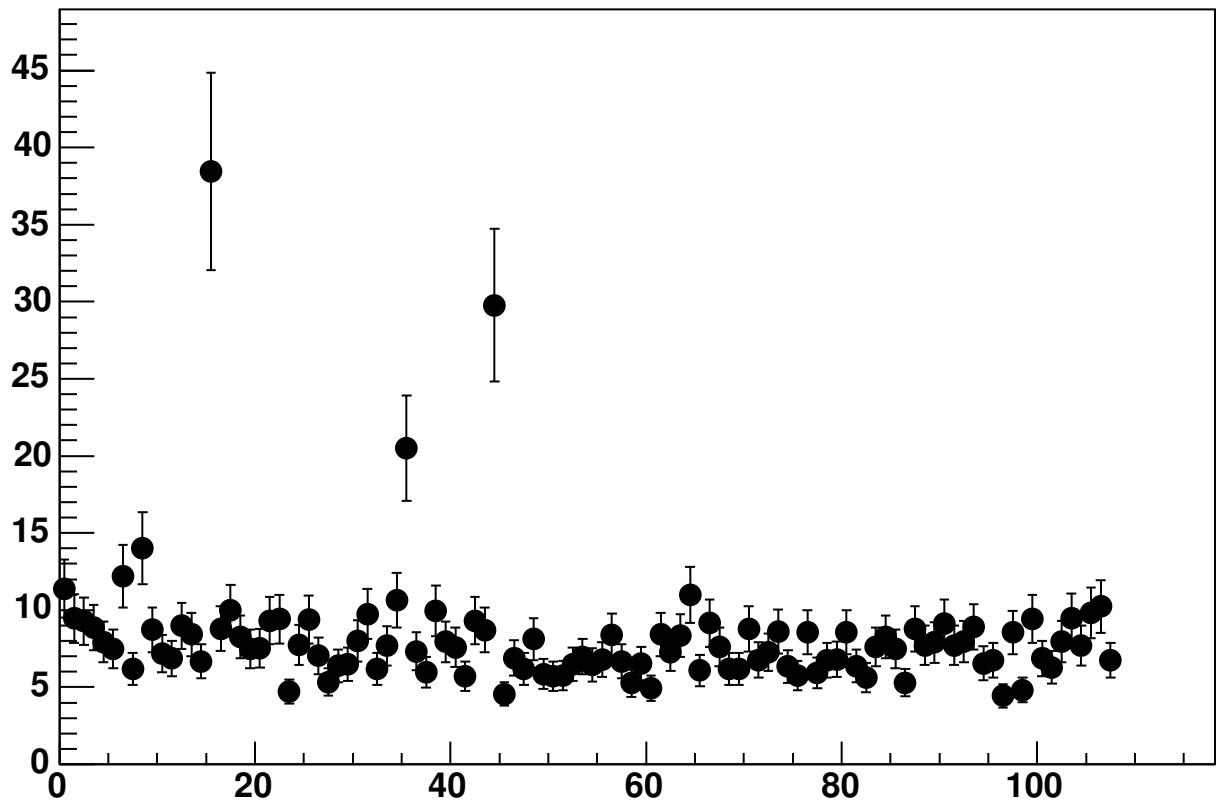
Enable 2, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



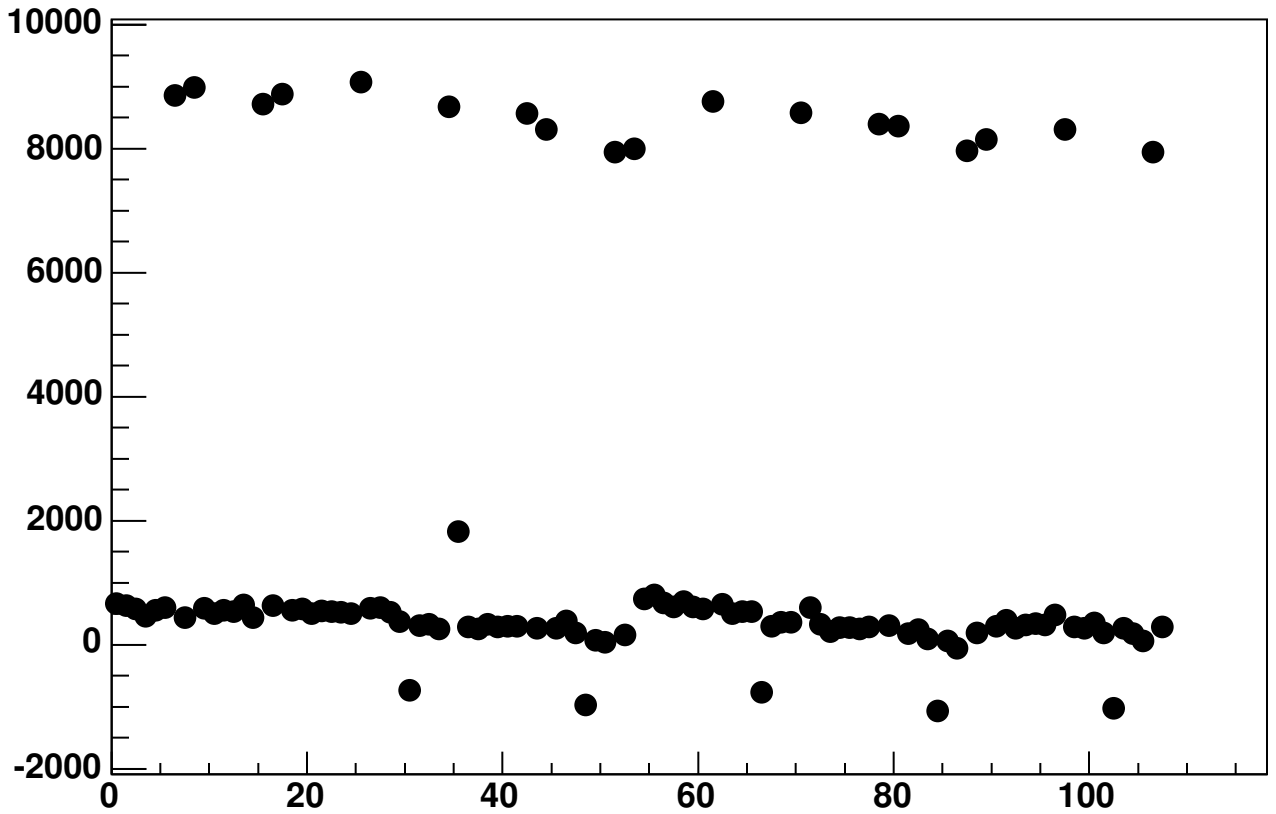
Enable 2, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



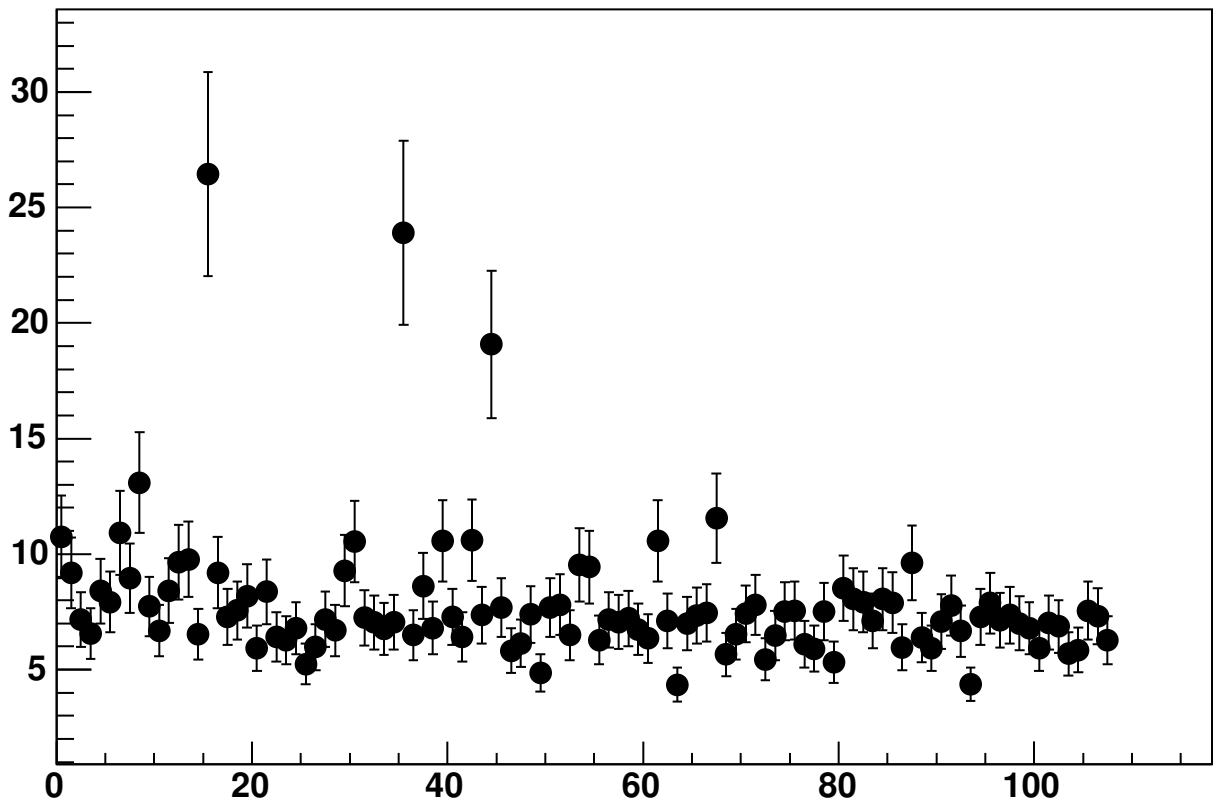
Enable 2, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



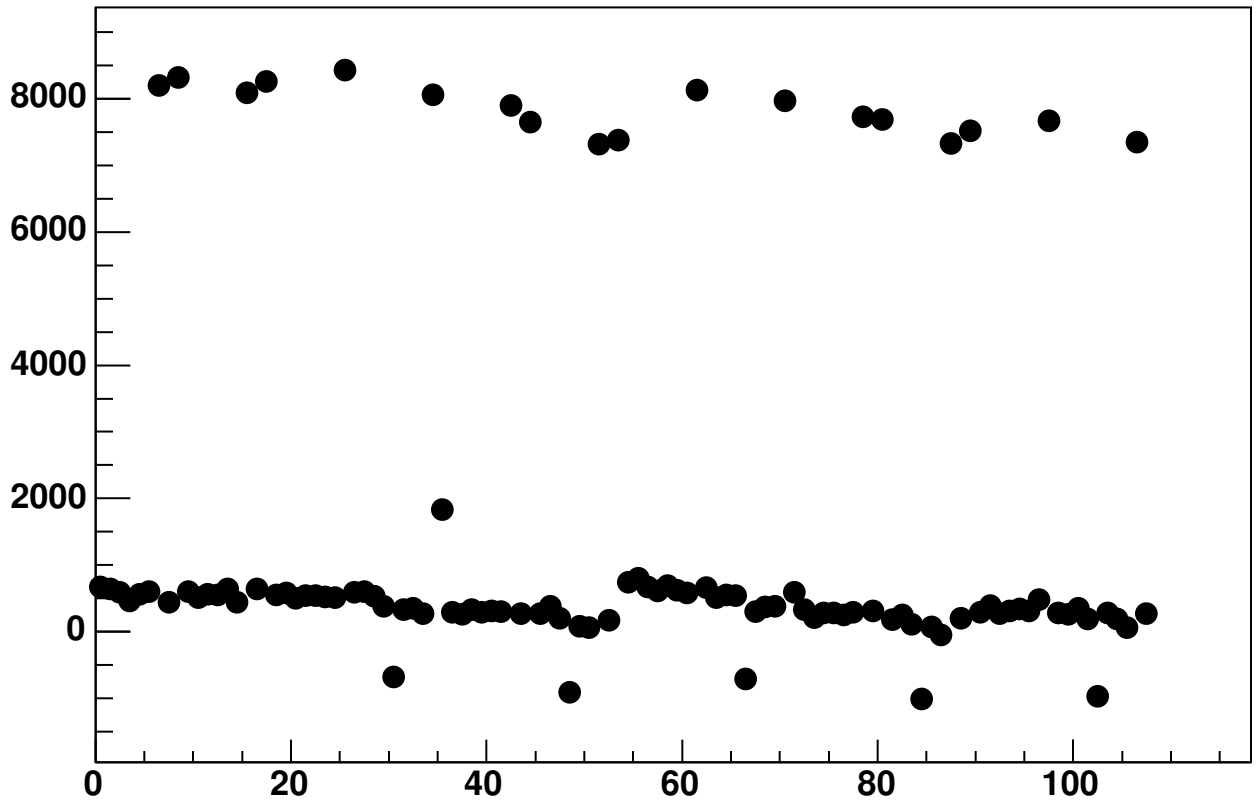
Enable 2, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



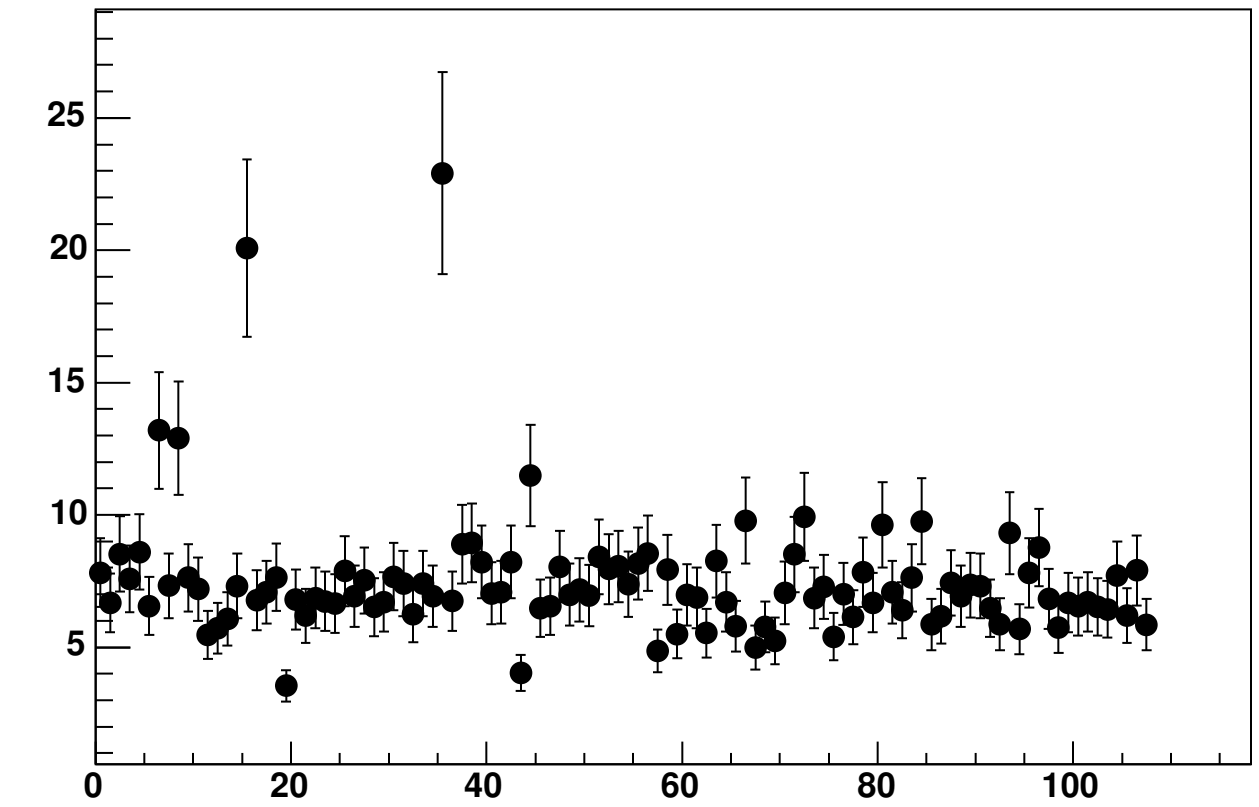
Enable 2, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



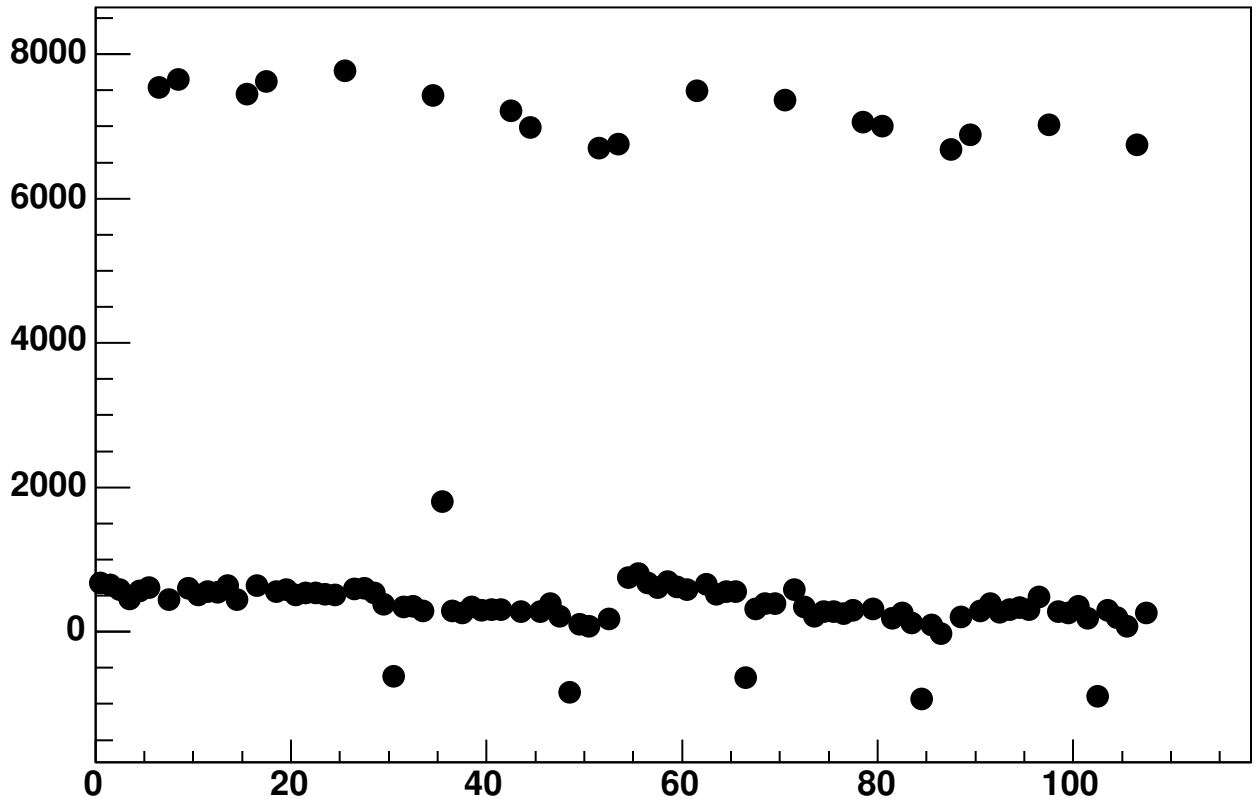
Enable 2, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



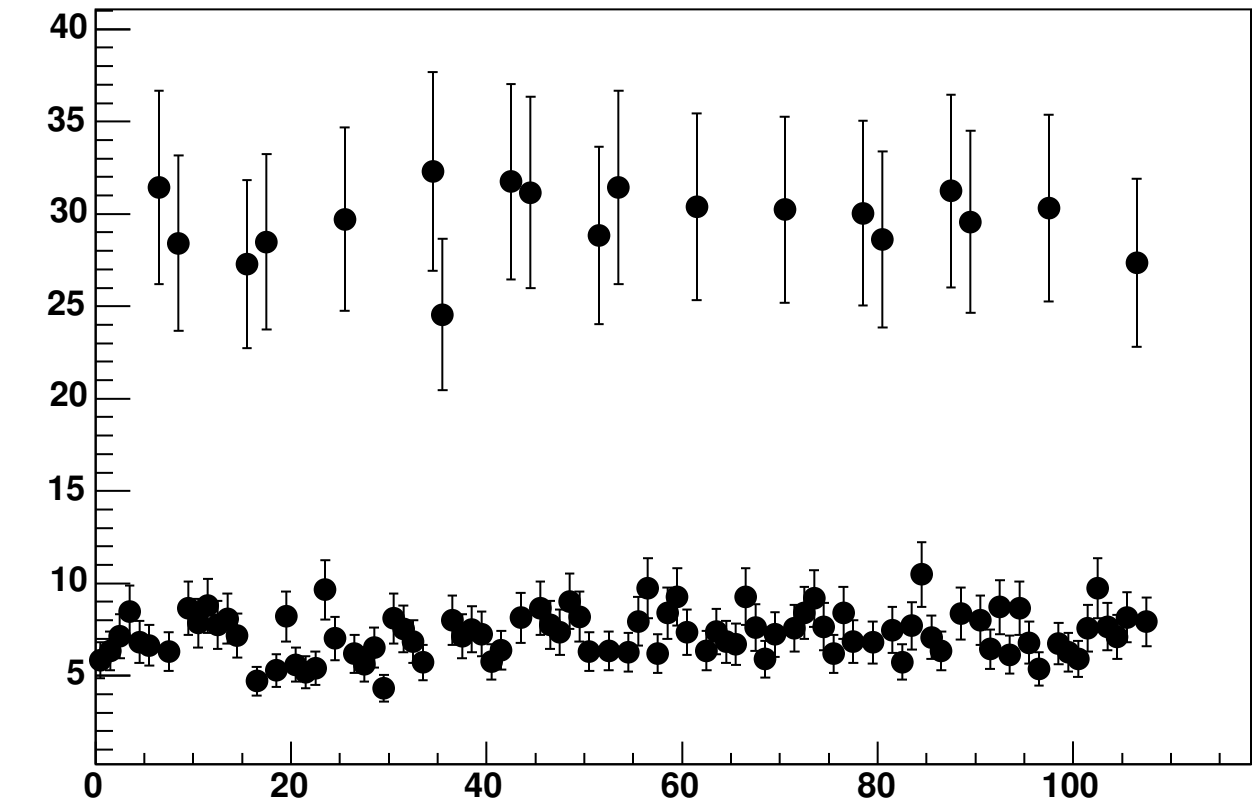
Enable 2, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



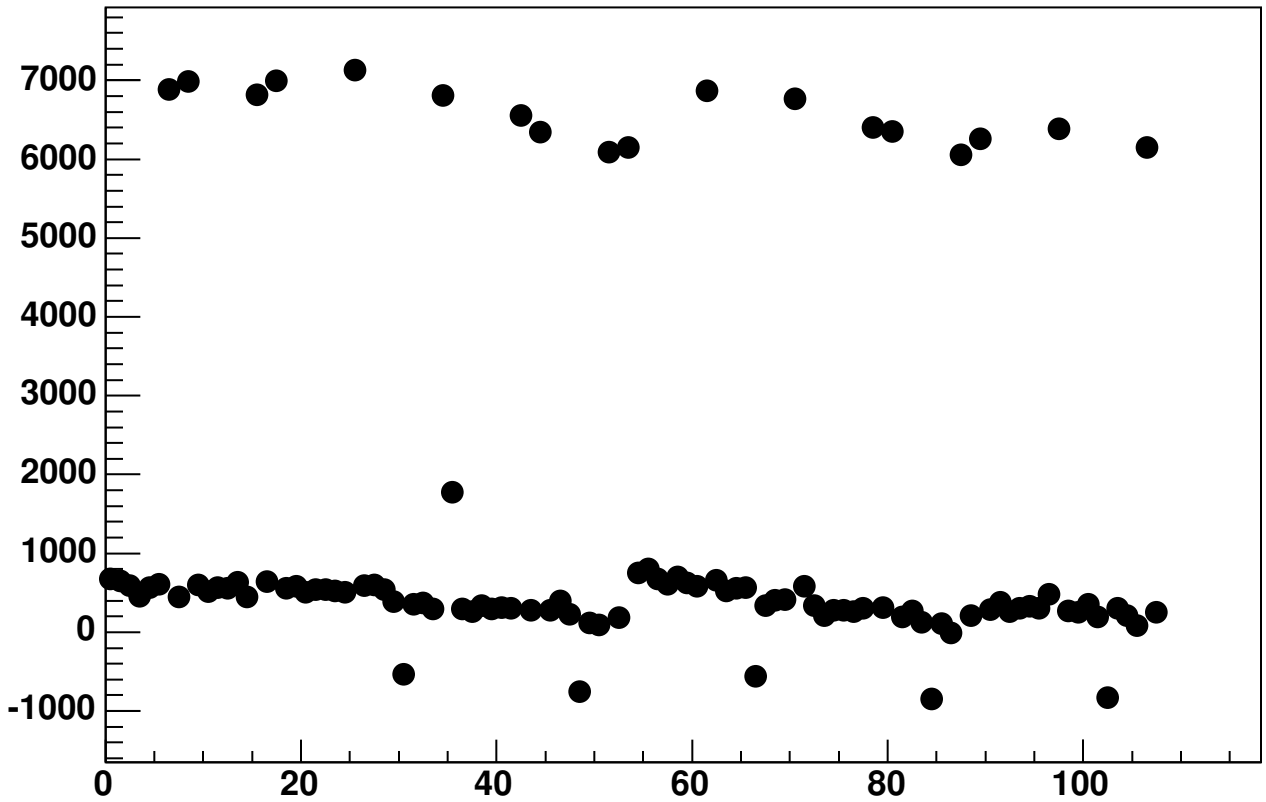
Enable 2, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



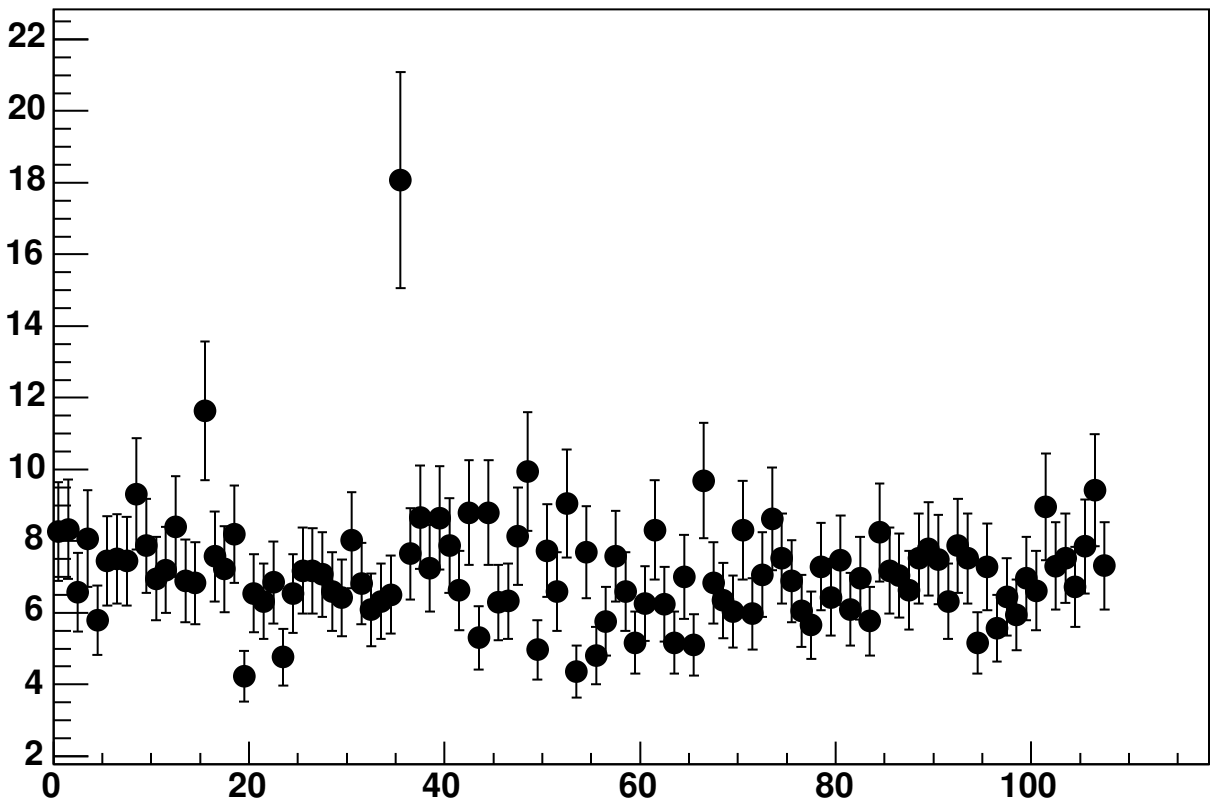
Enable 2, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



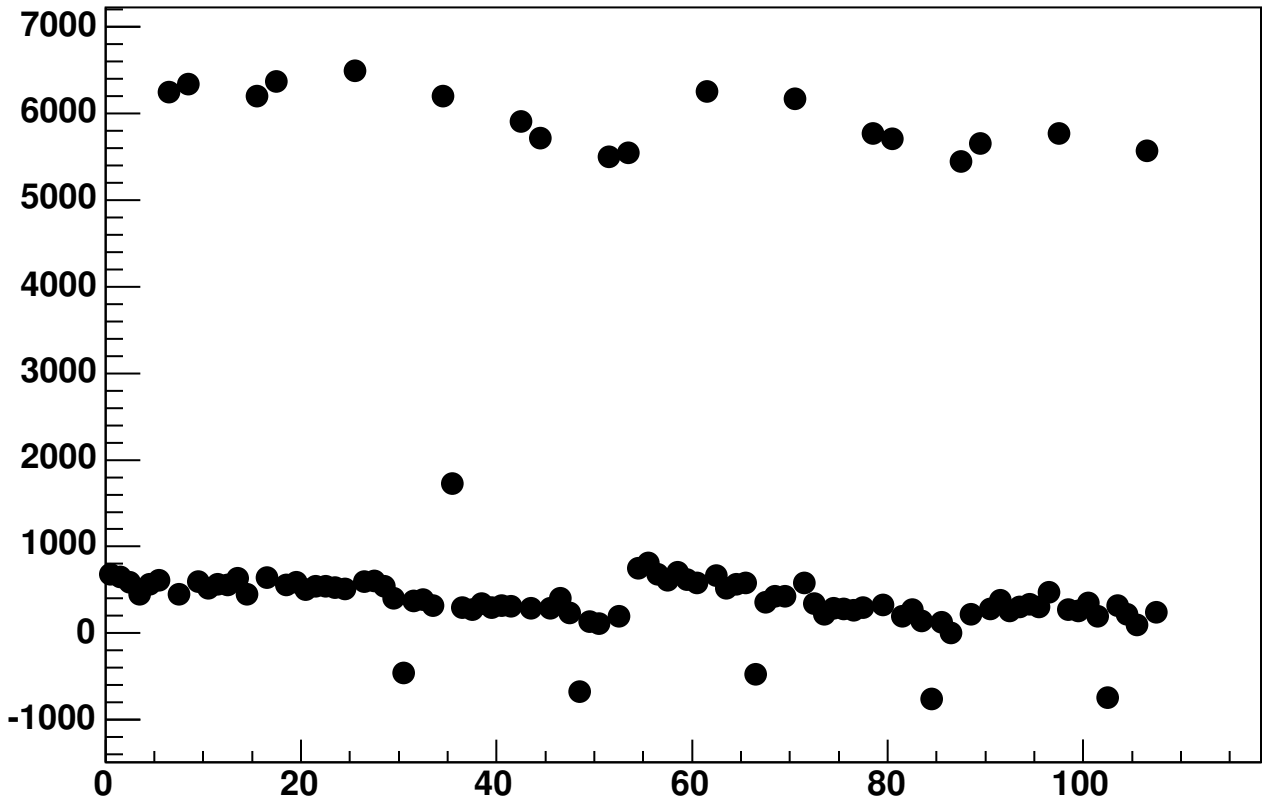
Enable 2, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



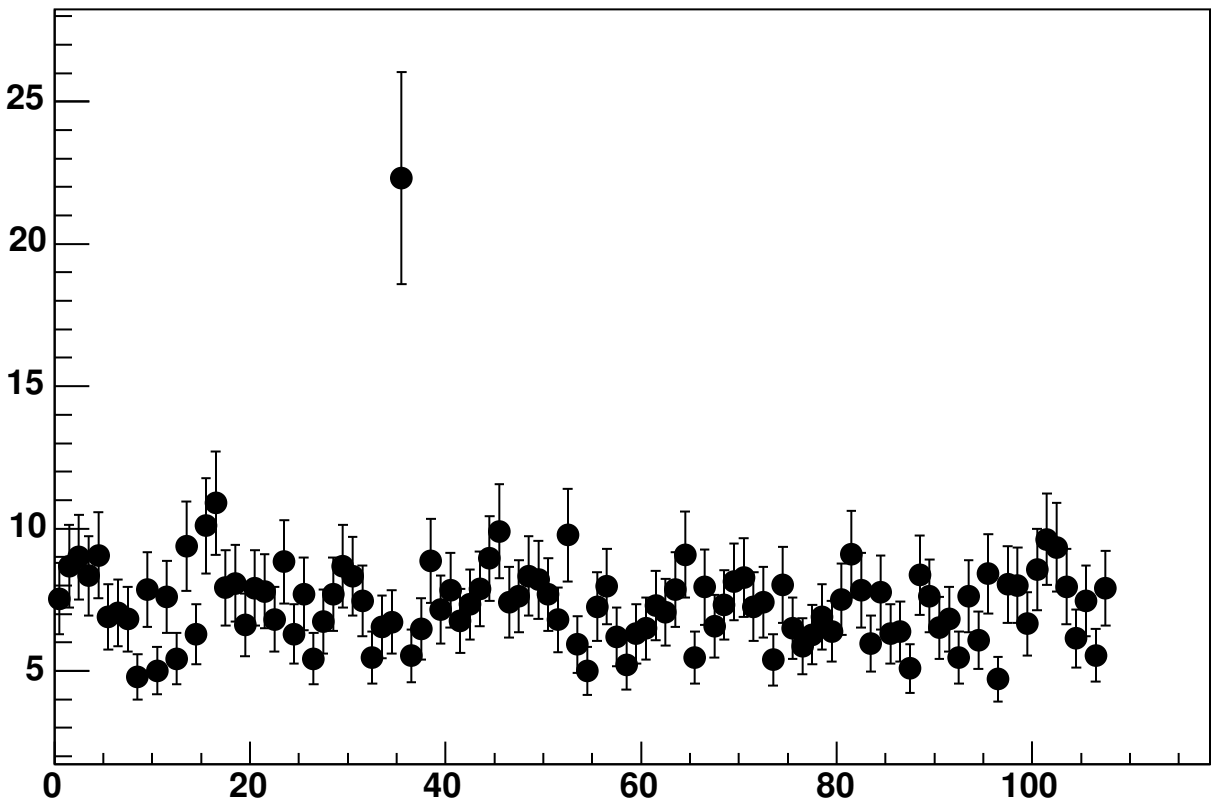
Enable 2, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



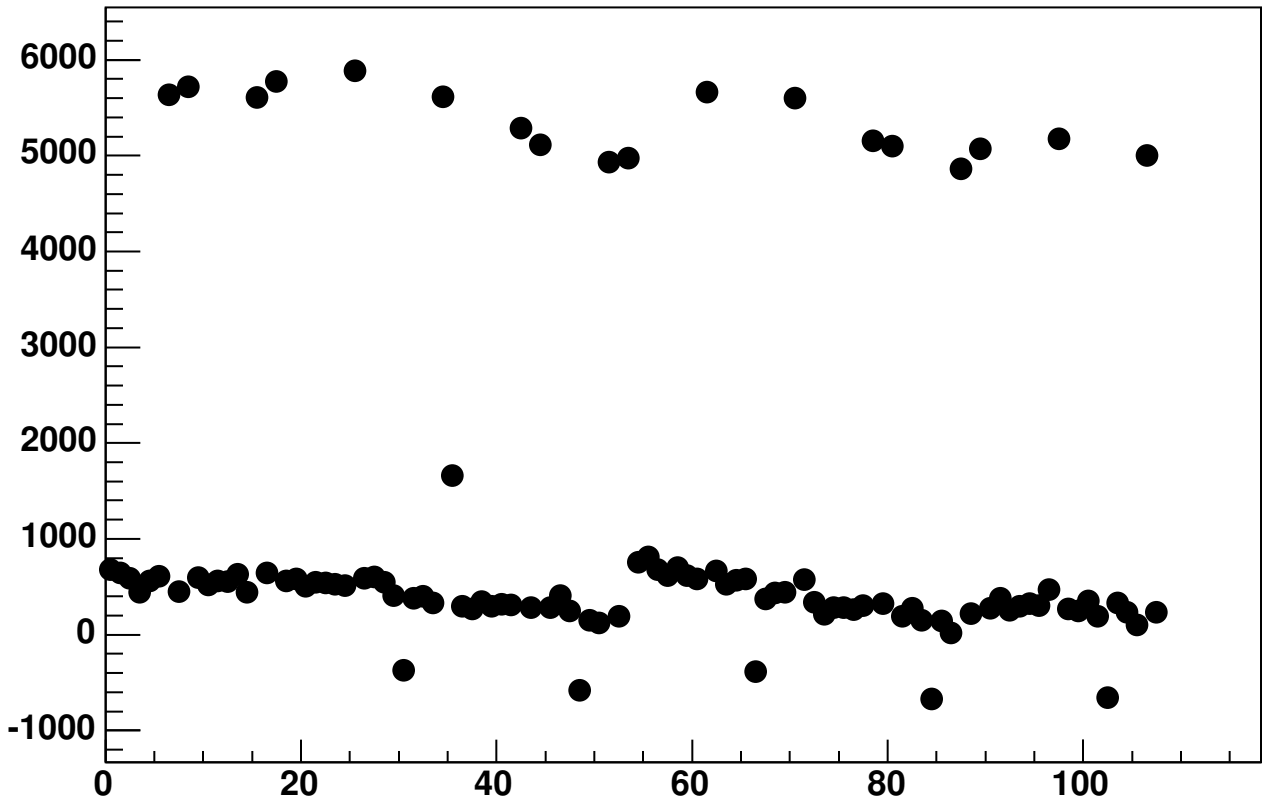
Enable 2, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



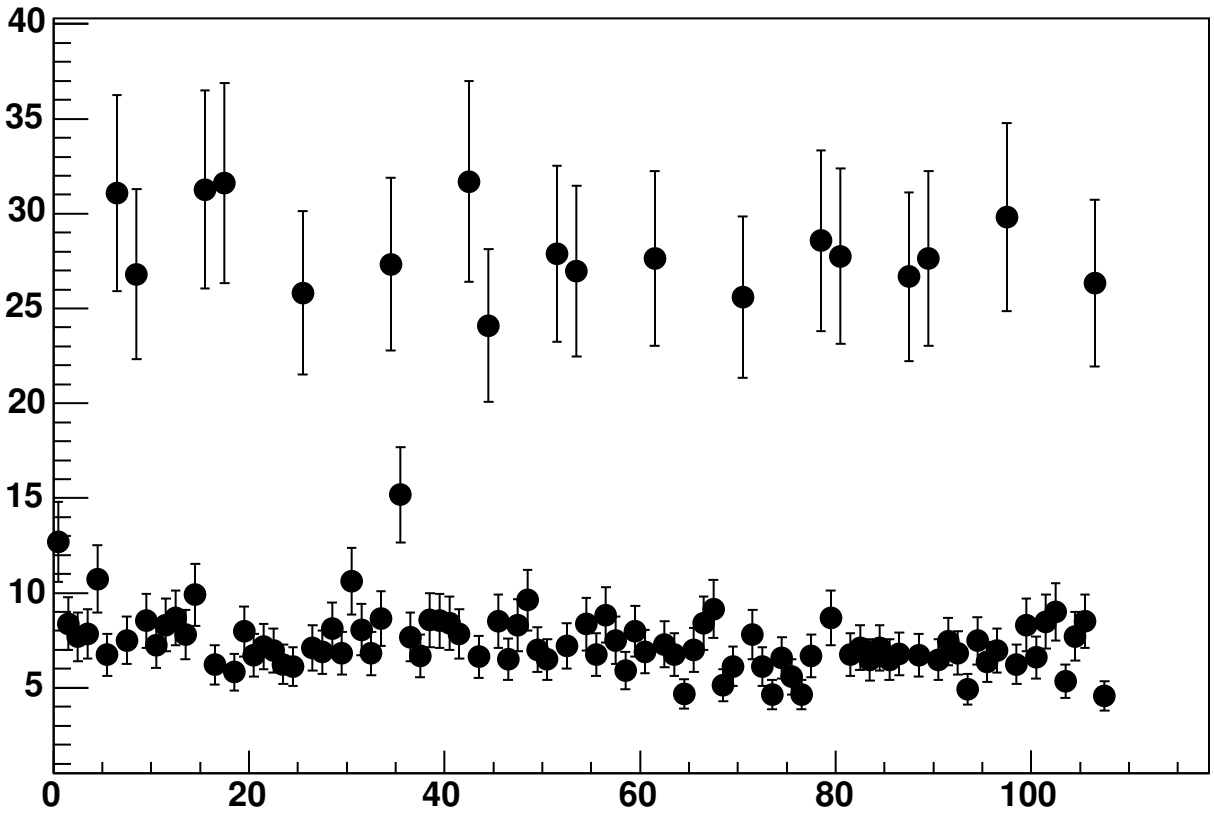
Enable 2, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



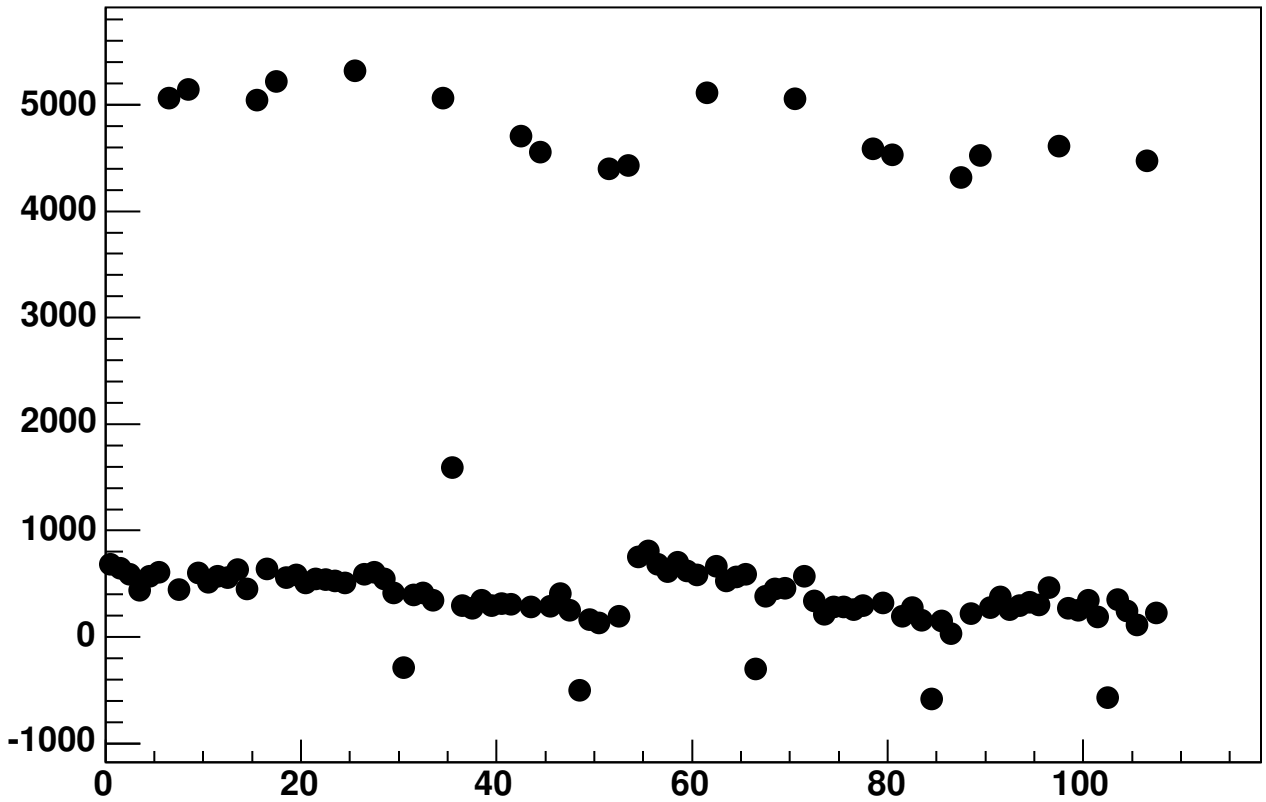
Enable 2, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



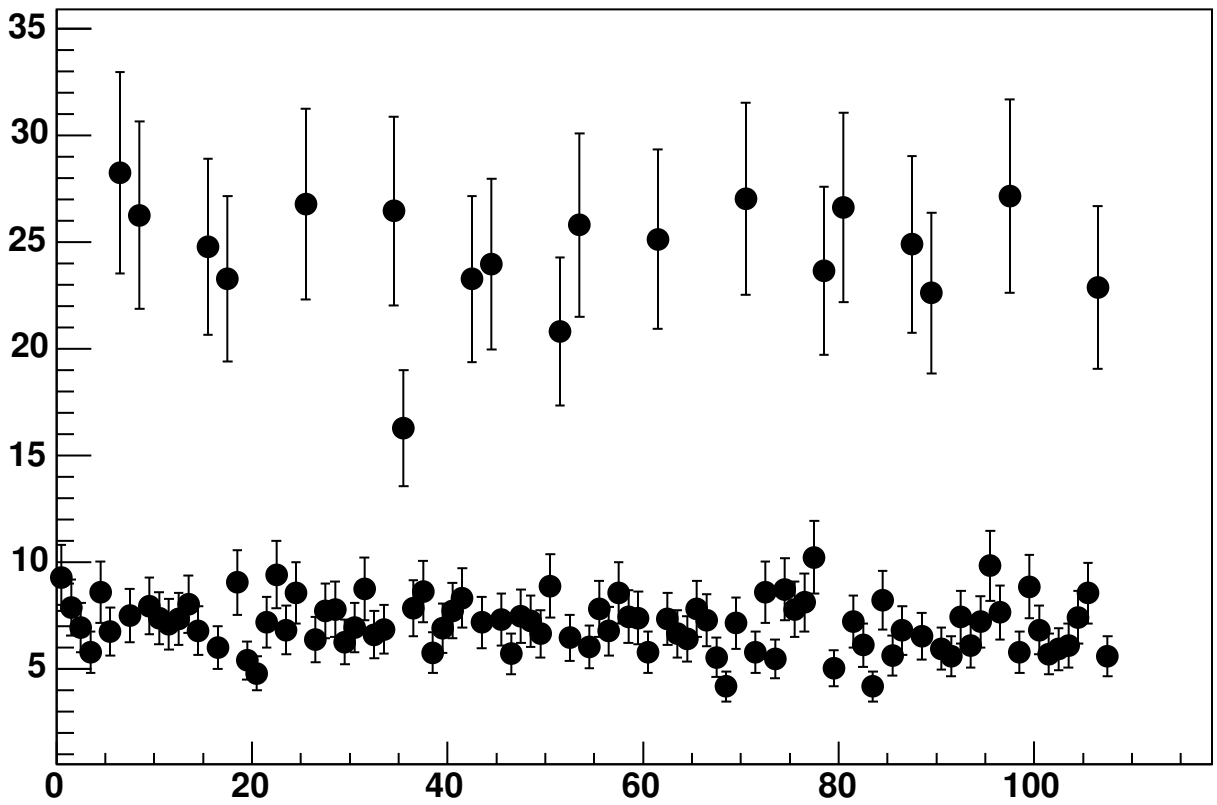
Enable 2, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



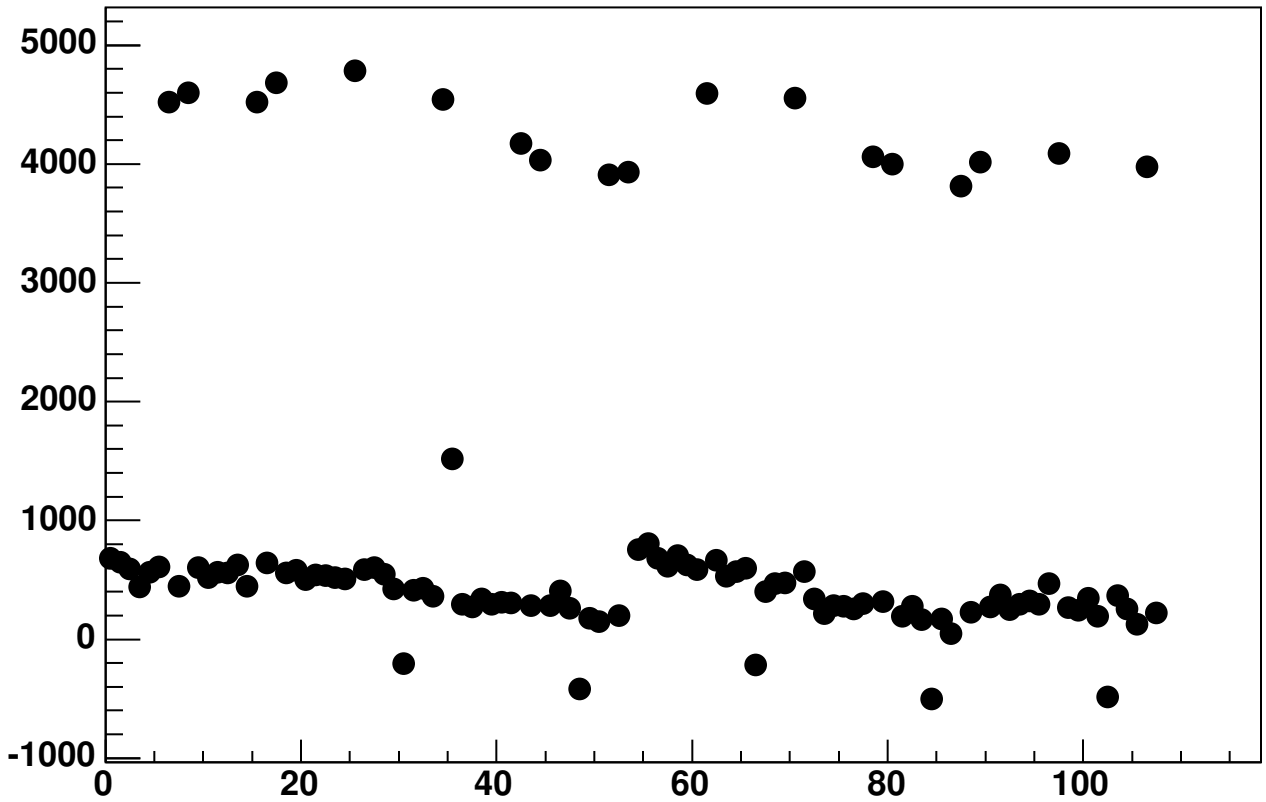
Enable 2, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



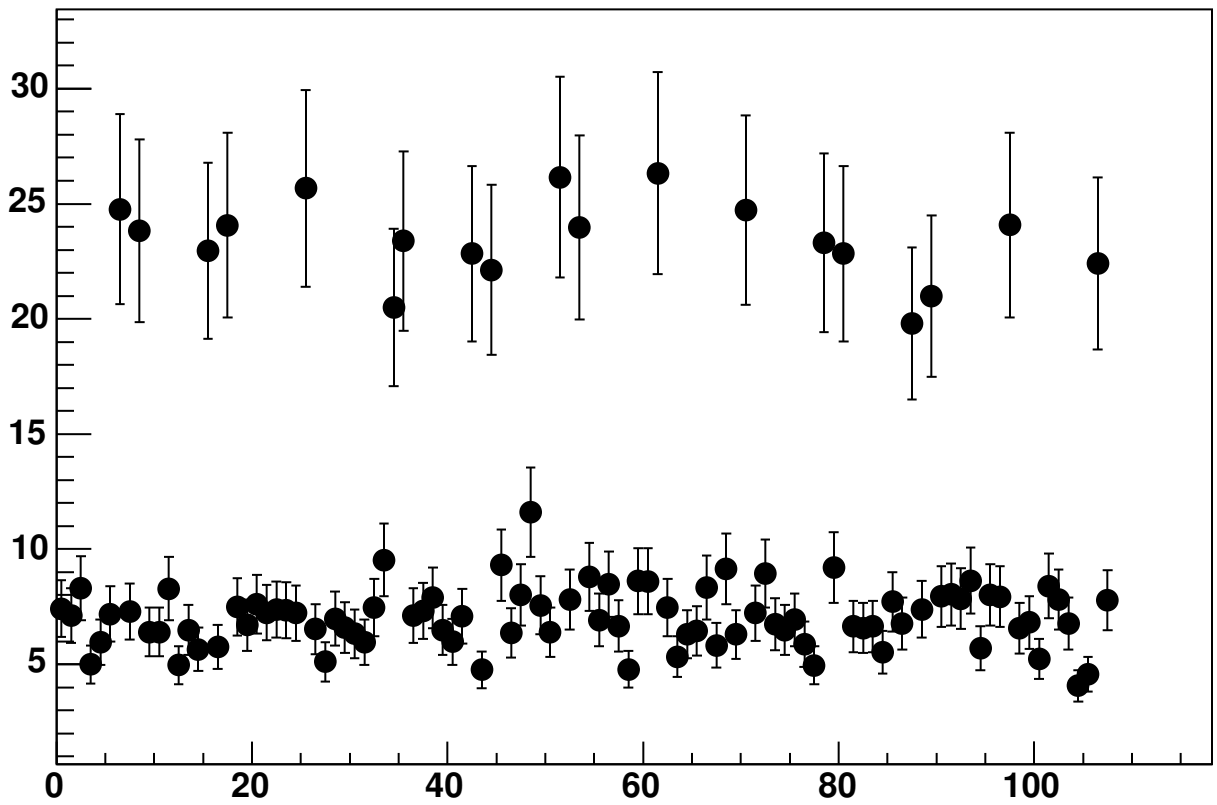
Enable 2, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



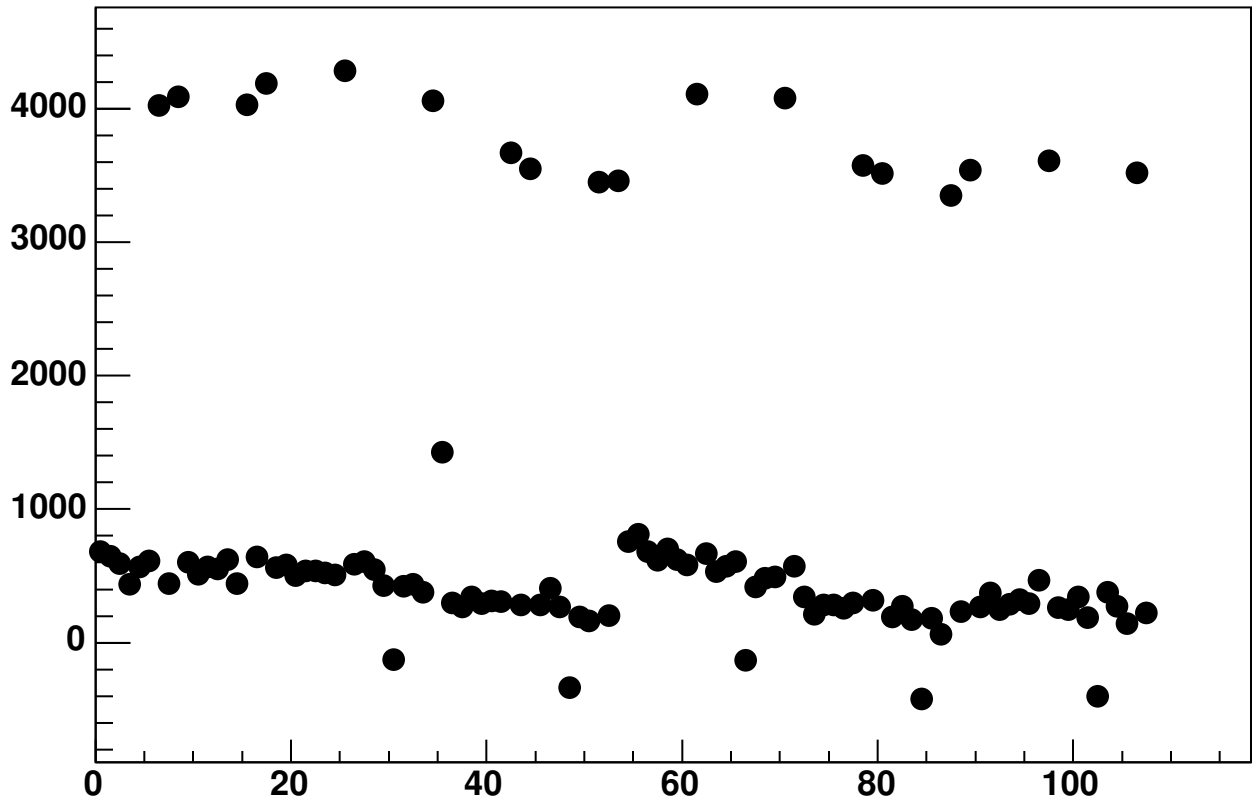
Enable 2, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



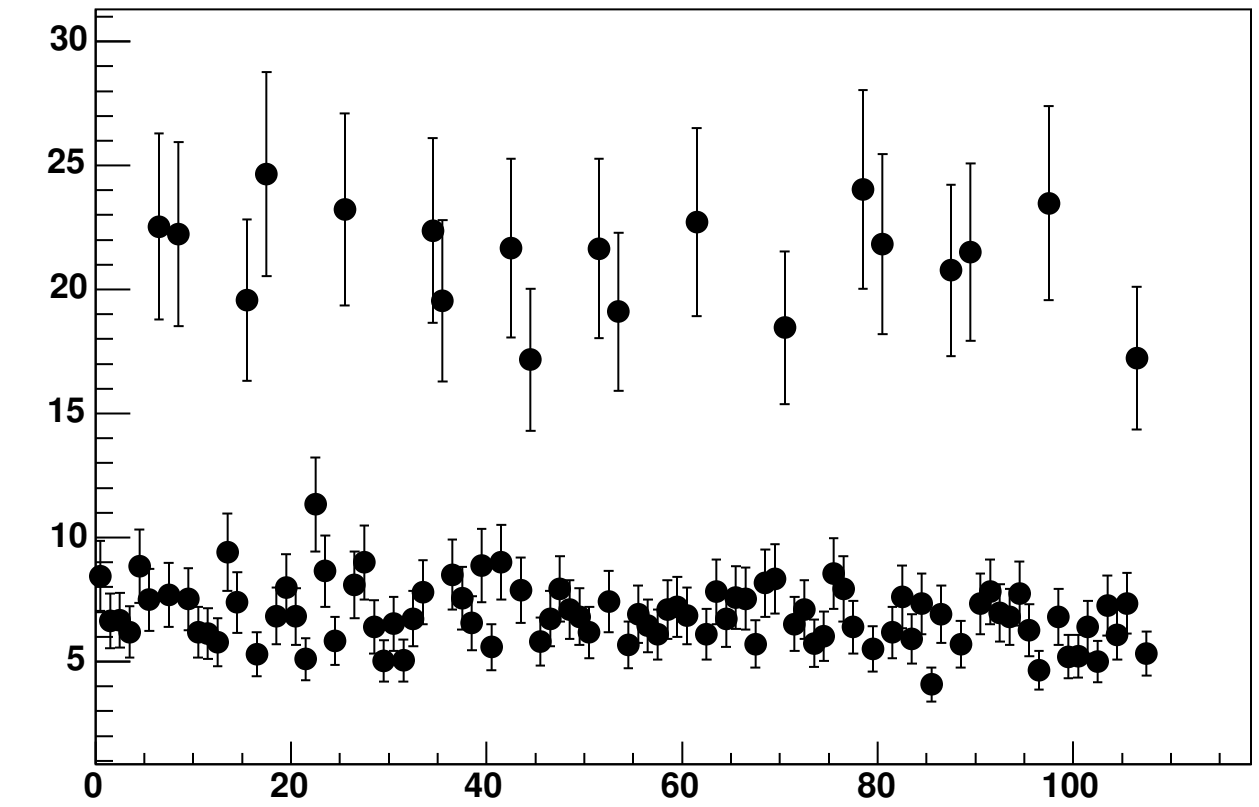
Enable 2, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



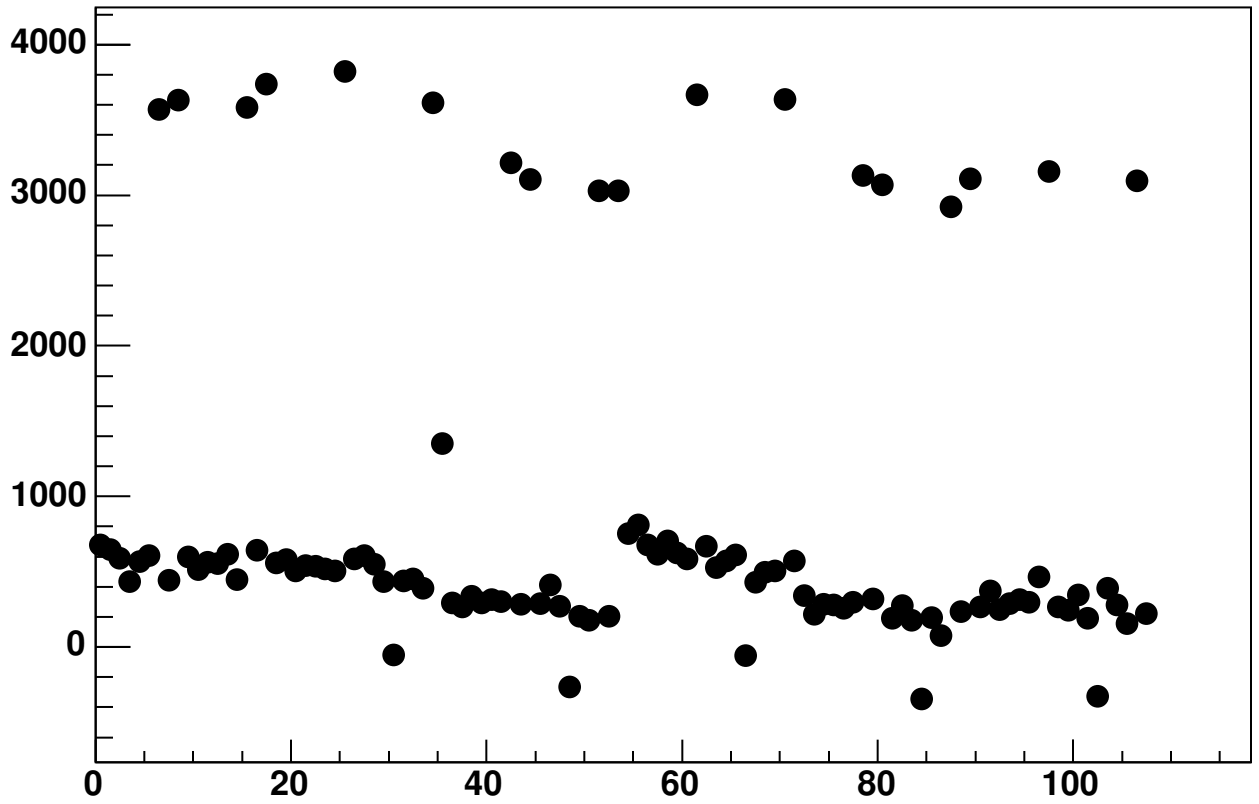
Enable 2, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



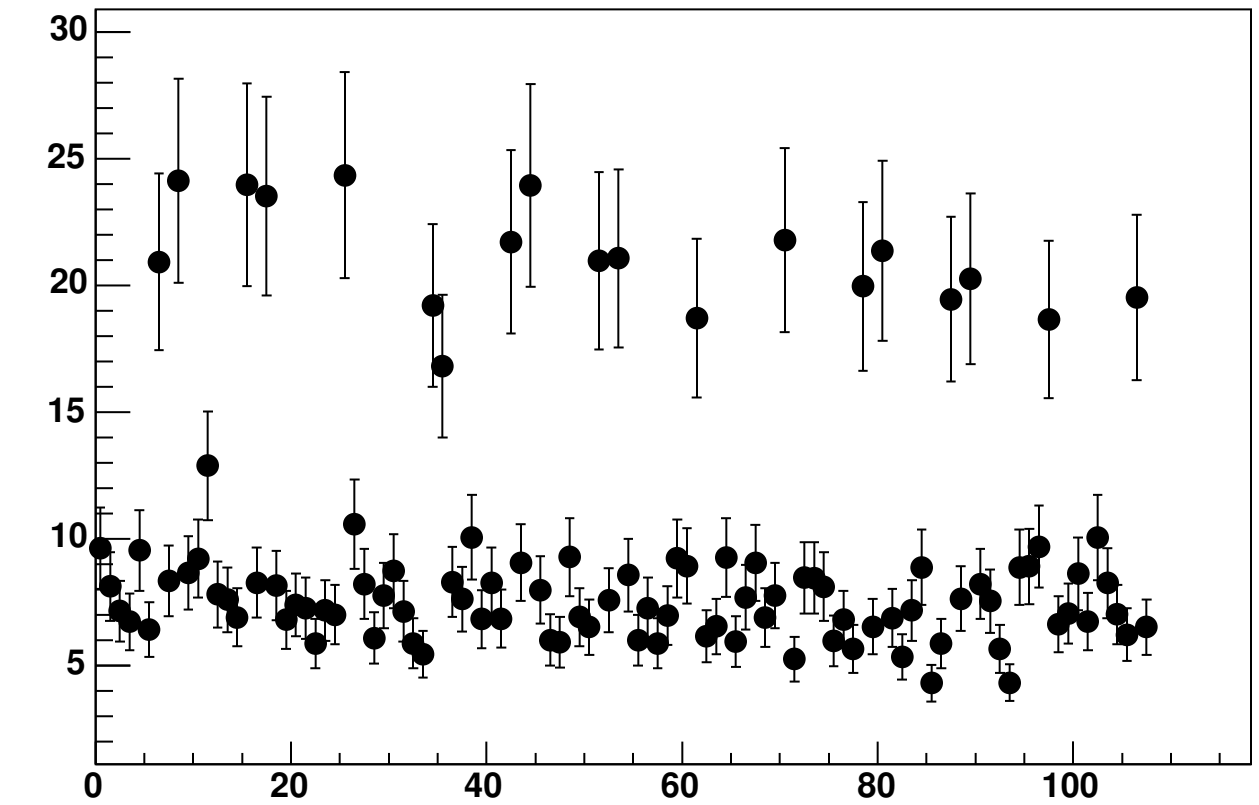
Enable 2, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



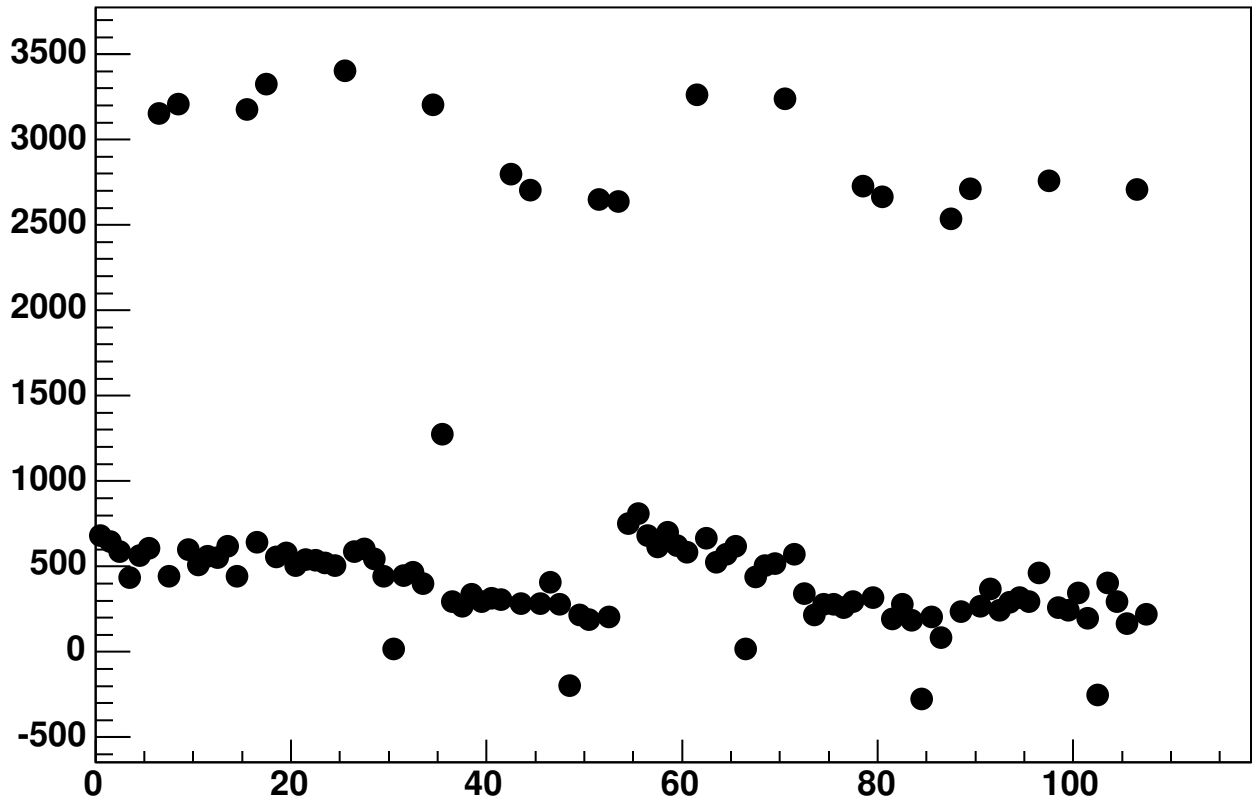
Enable 2, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



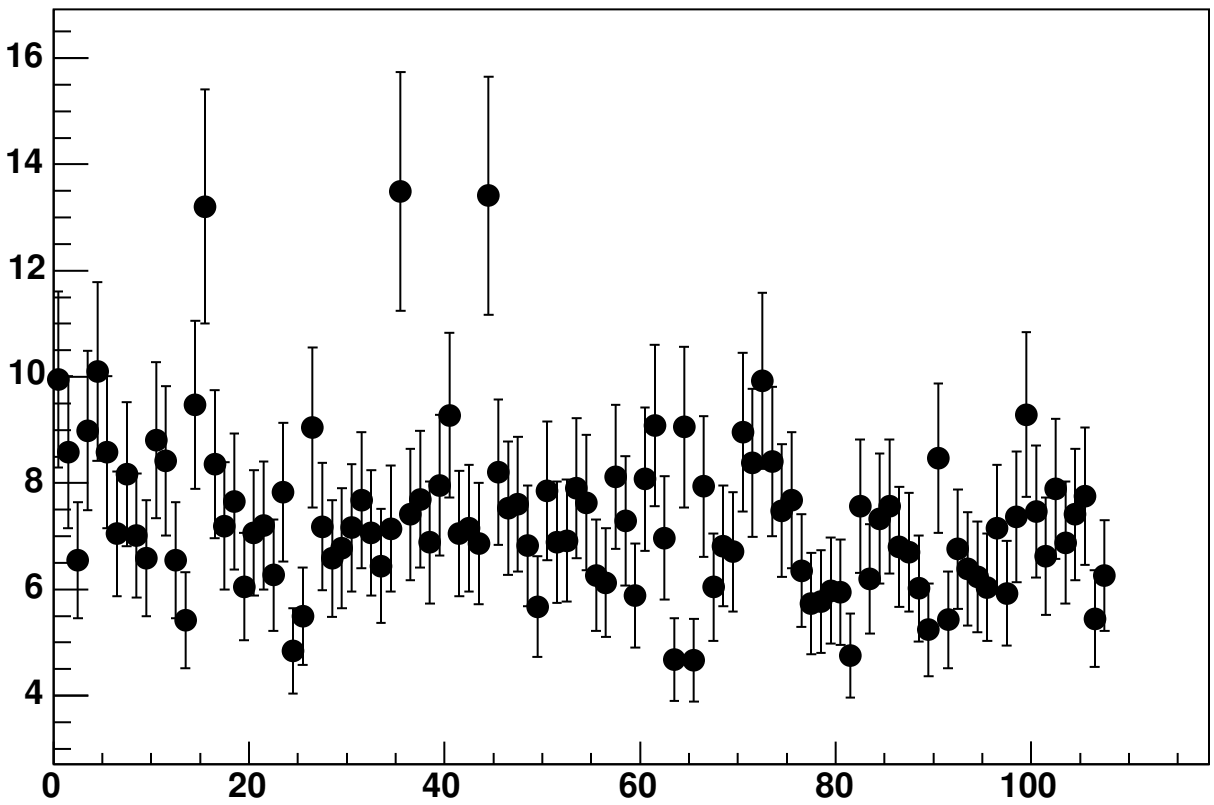
Enable 2, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



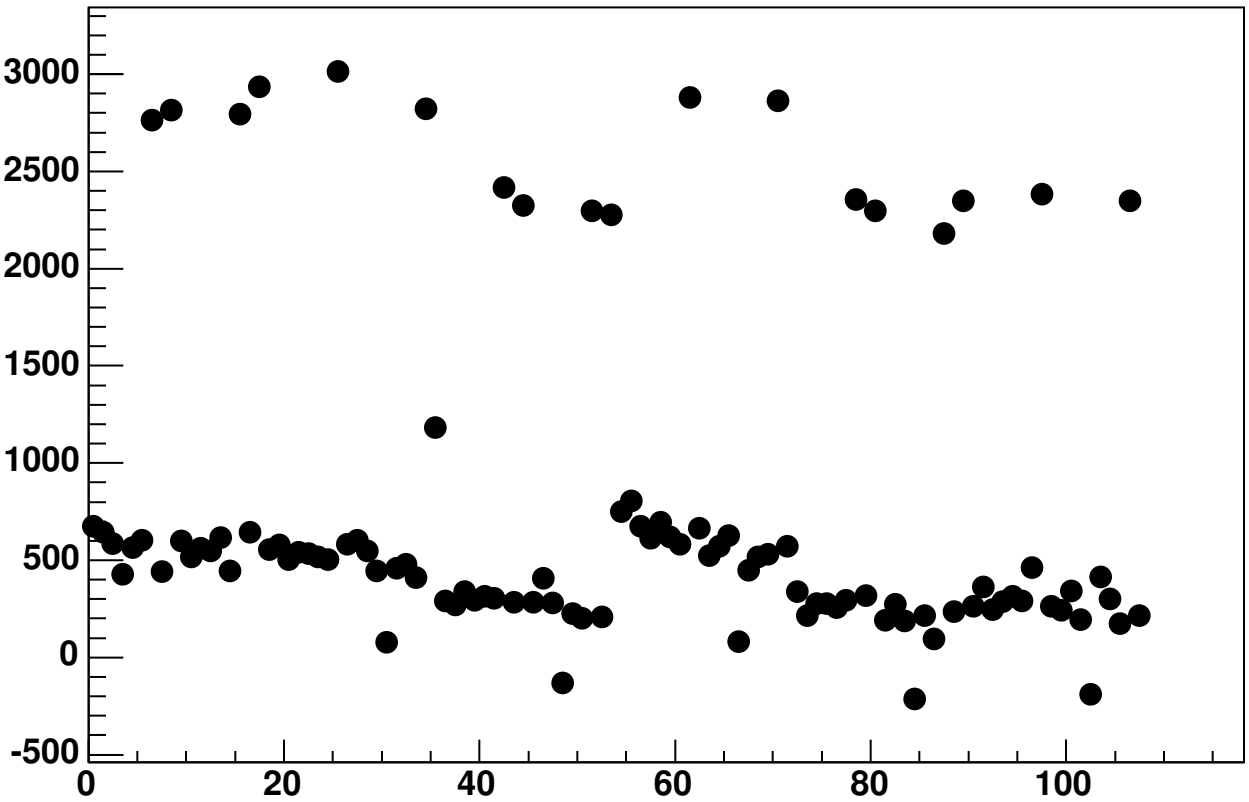
Enable 2, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



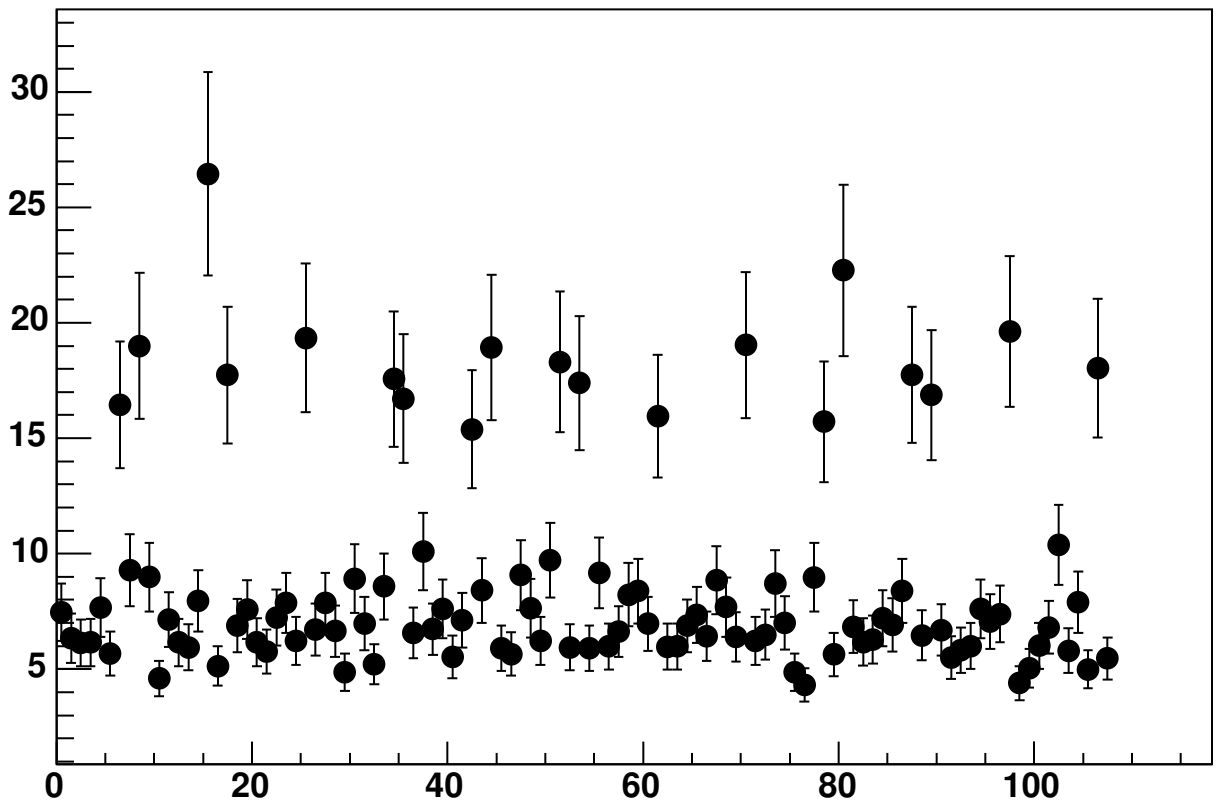
Enable 2, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



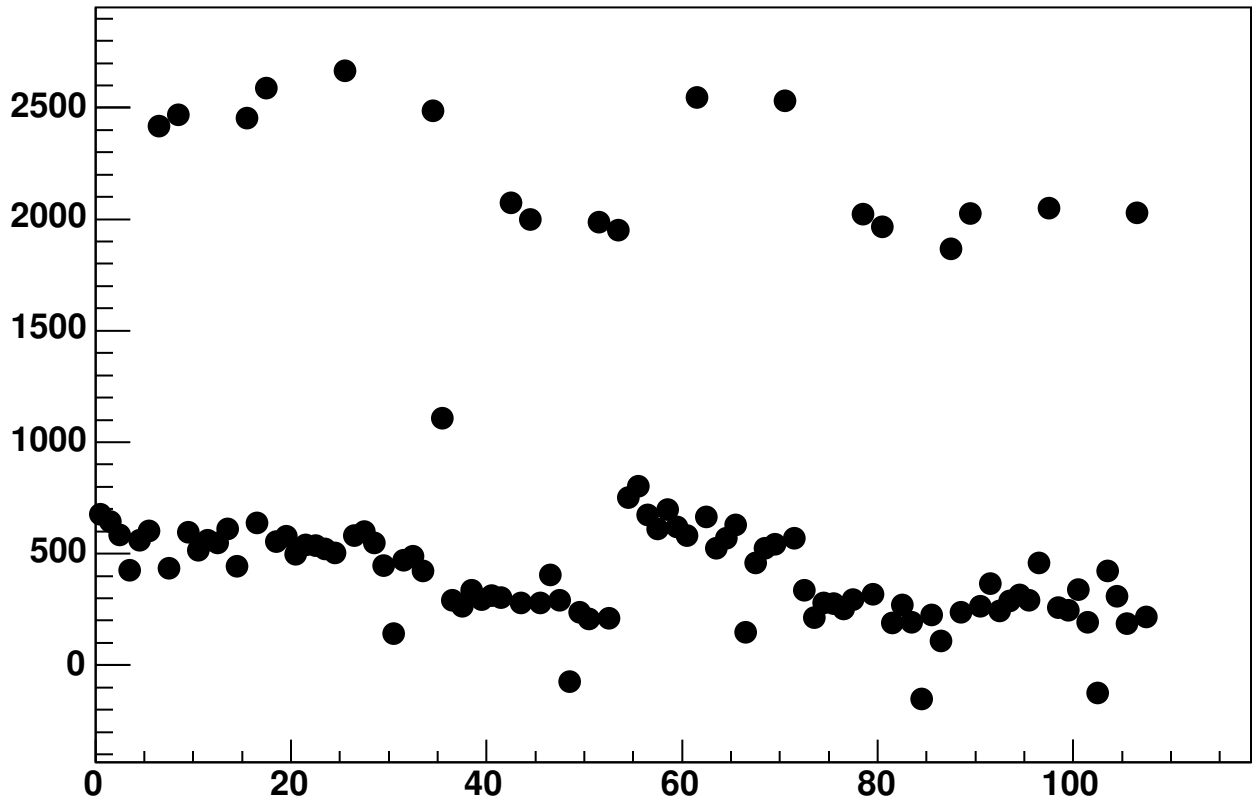
Enable 2, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



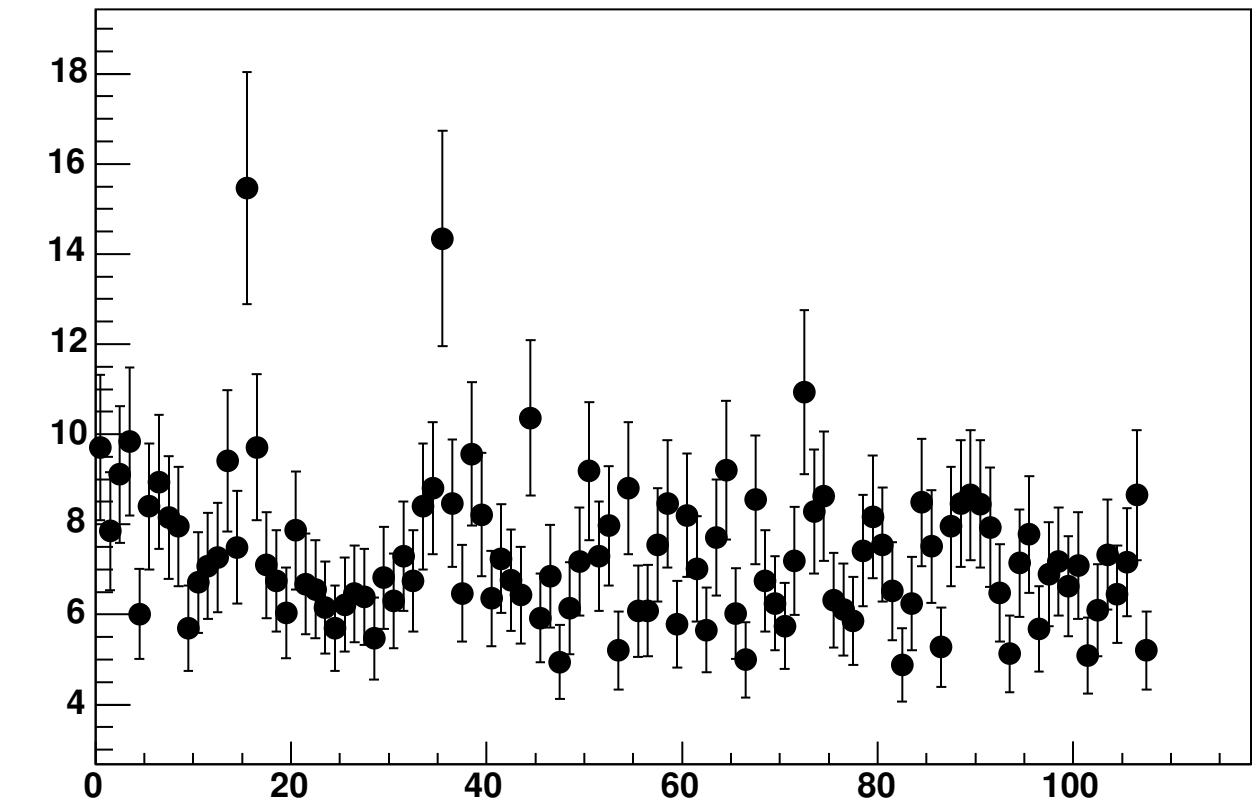
Enable 2, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



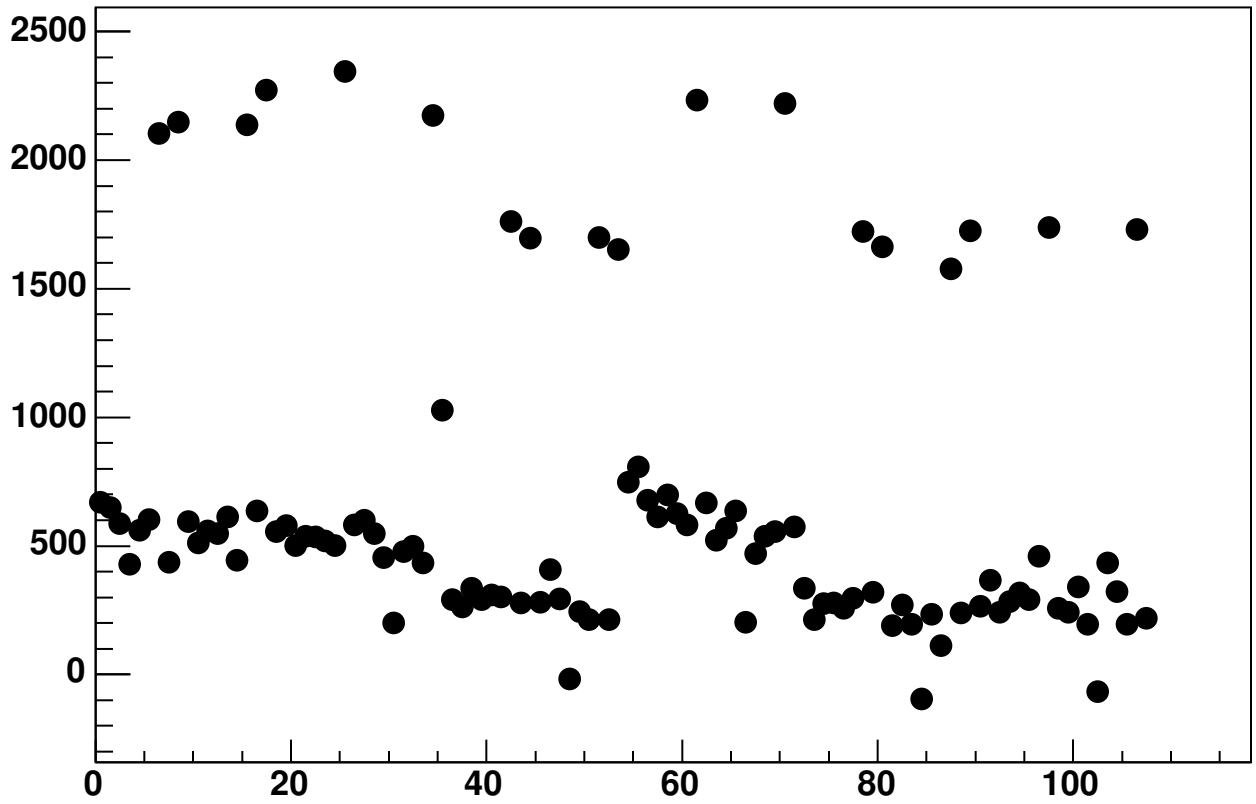
Enable 2, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



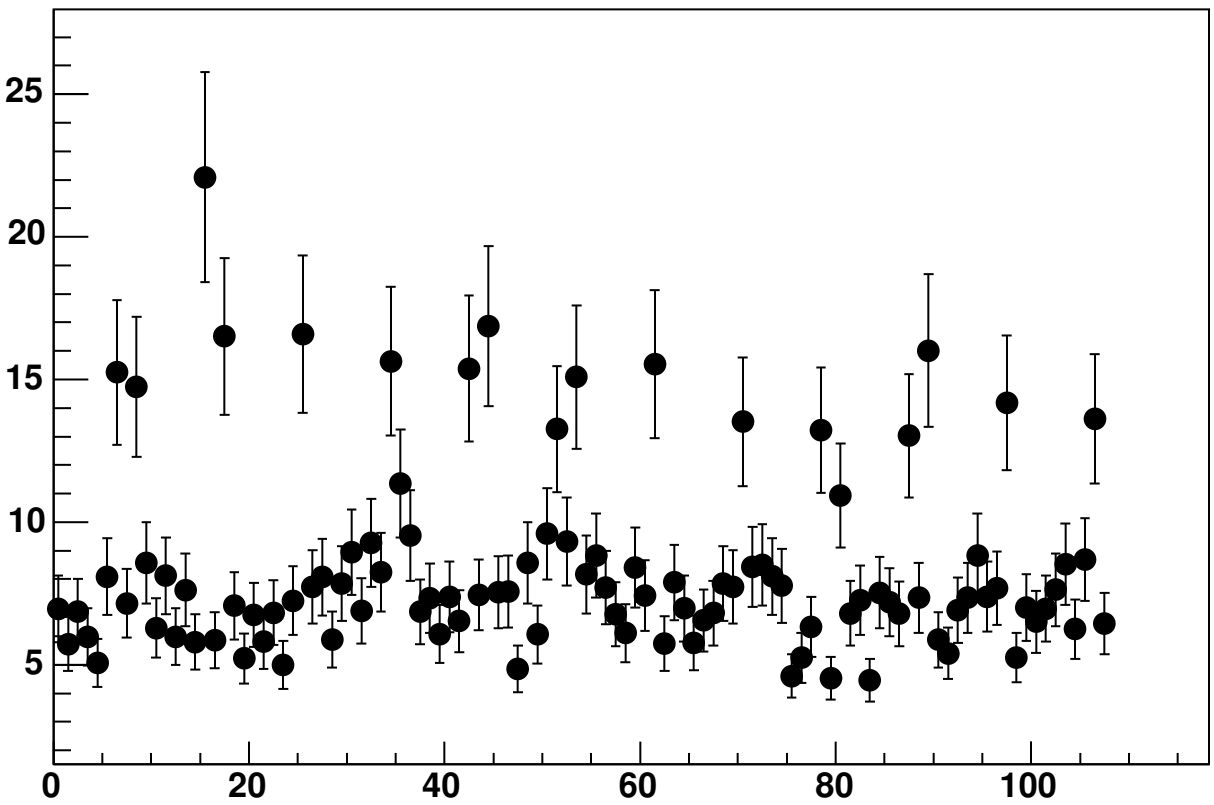
Enable 2, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



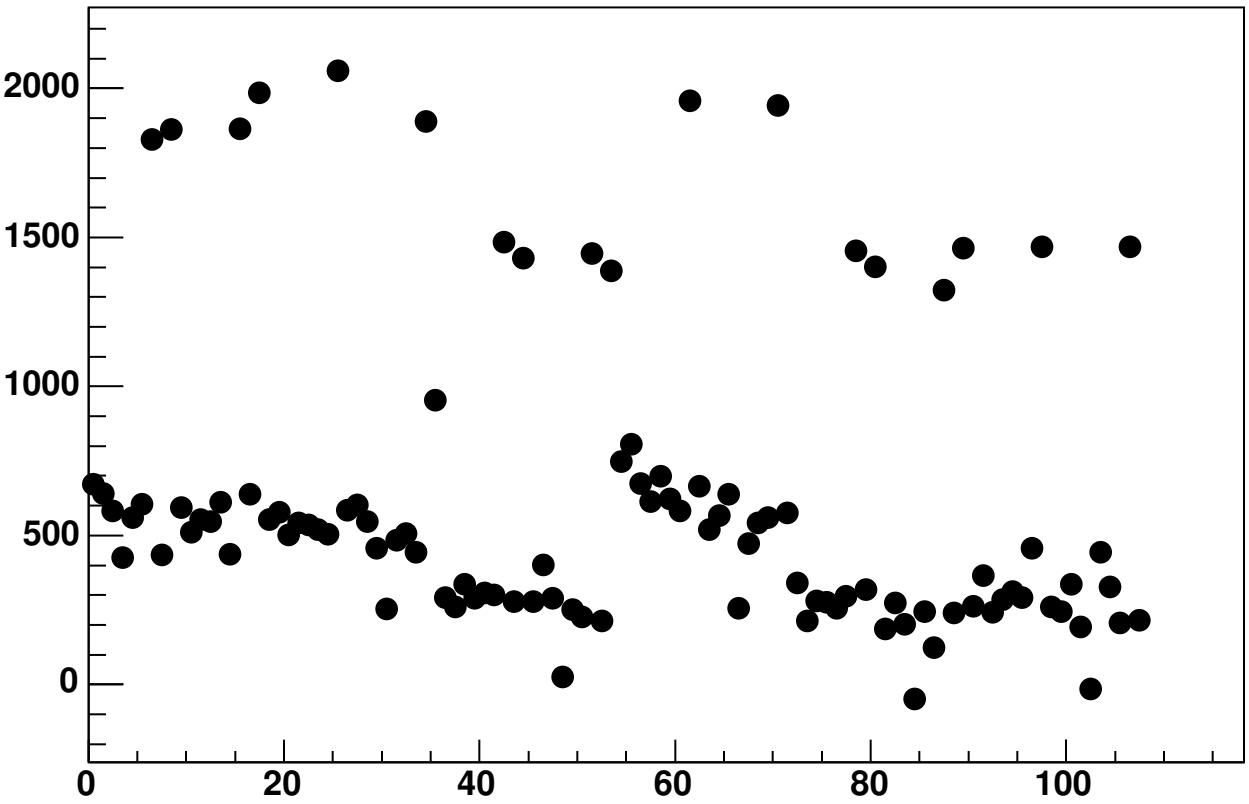
Enable 2, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



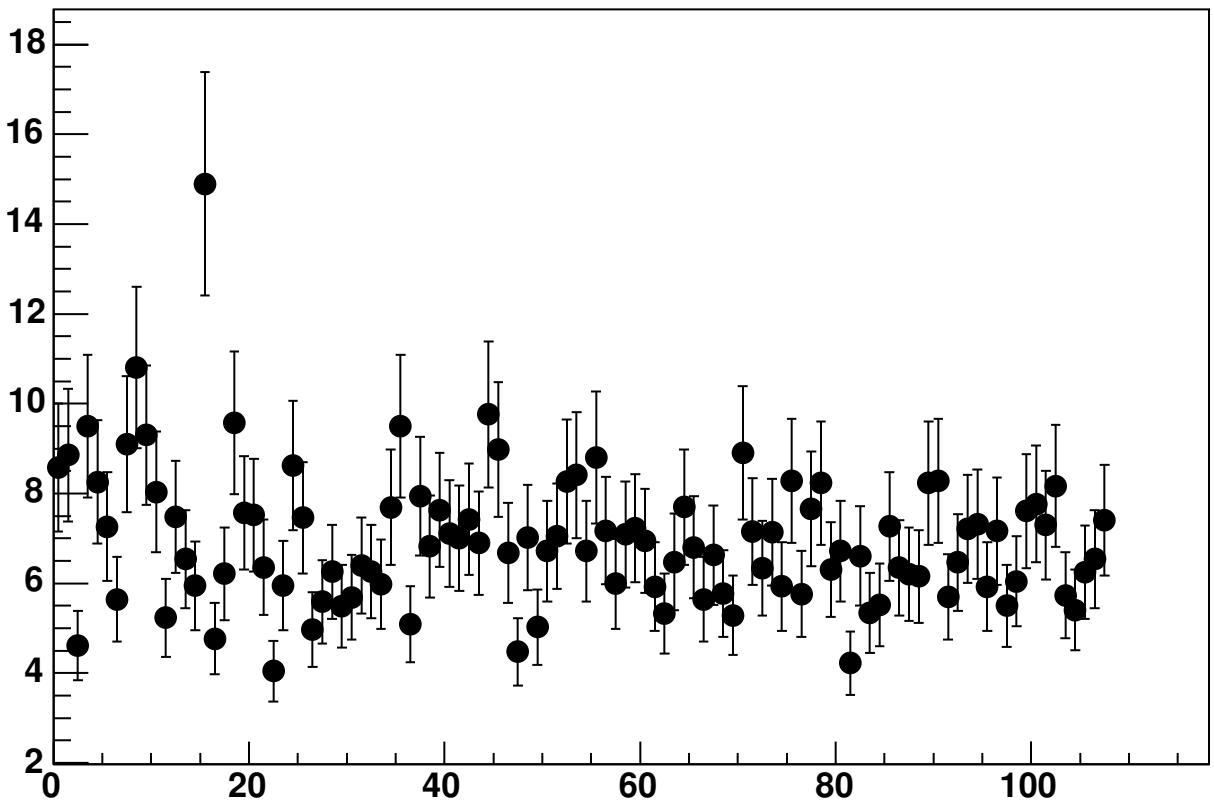
Enable 2, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



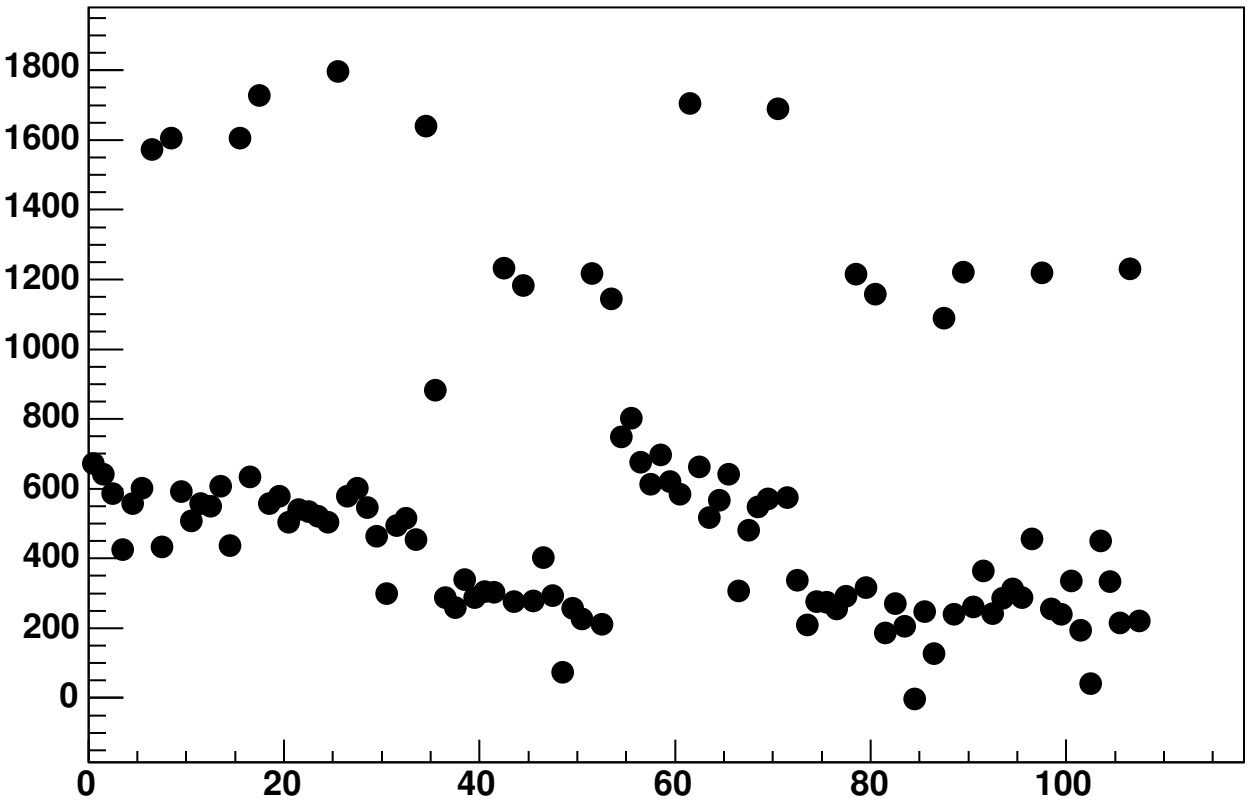
Enable 2, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



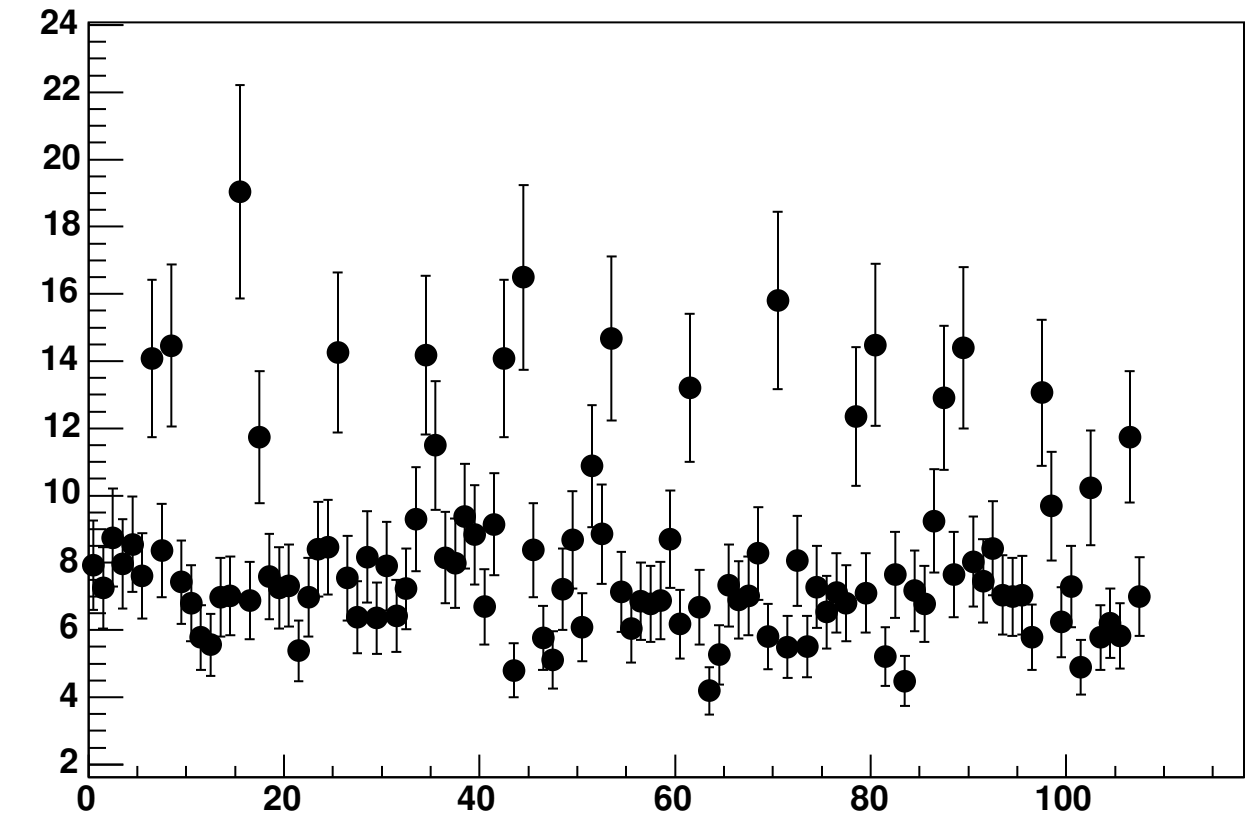
Enable 2, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



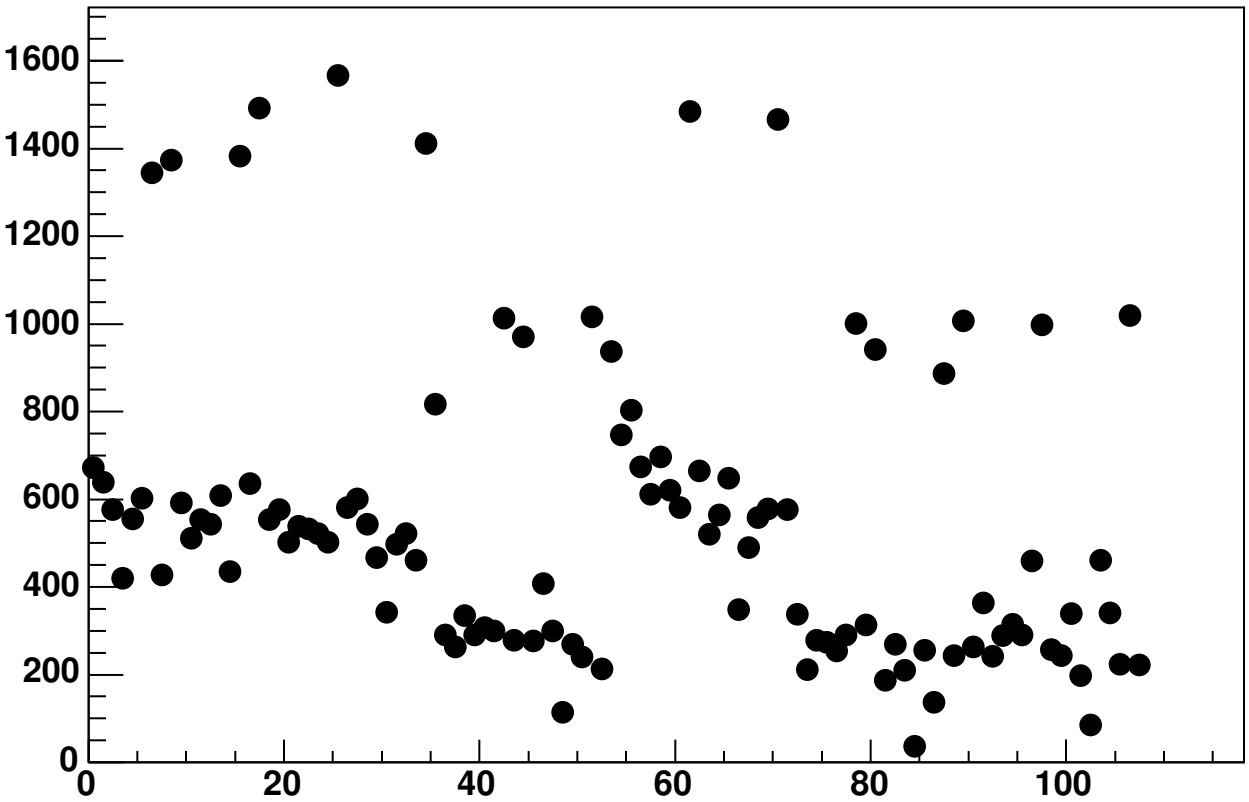
Enable 2, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



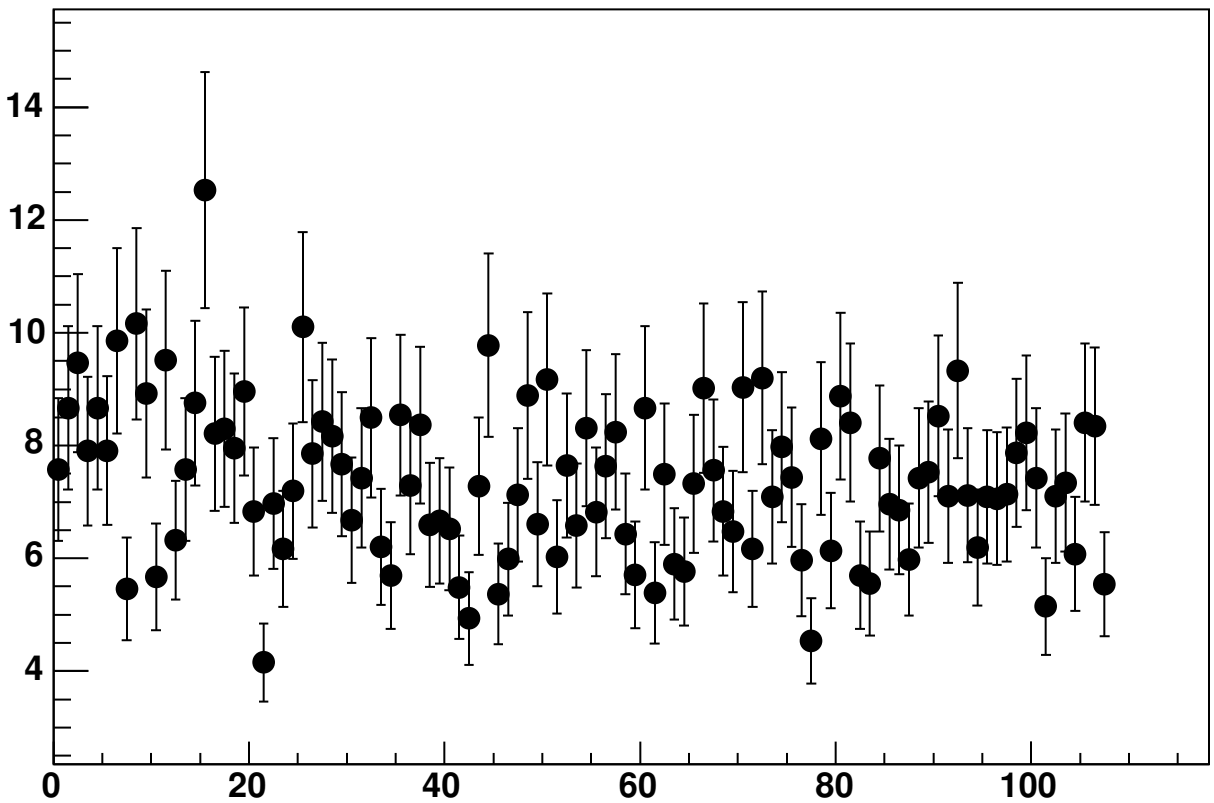
Enable 2, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



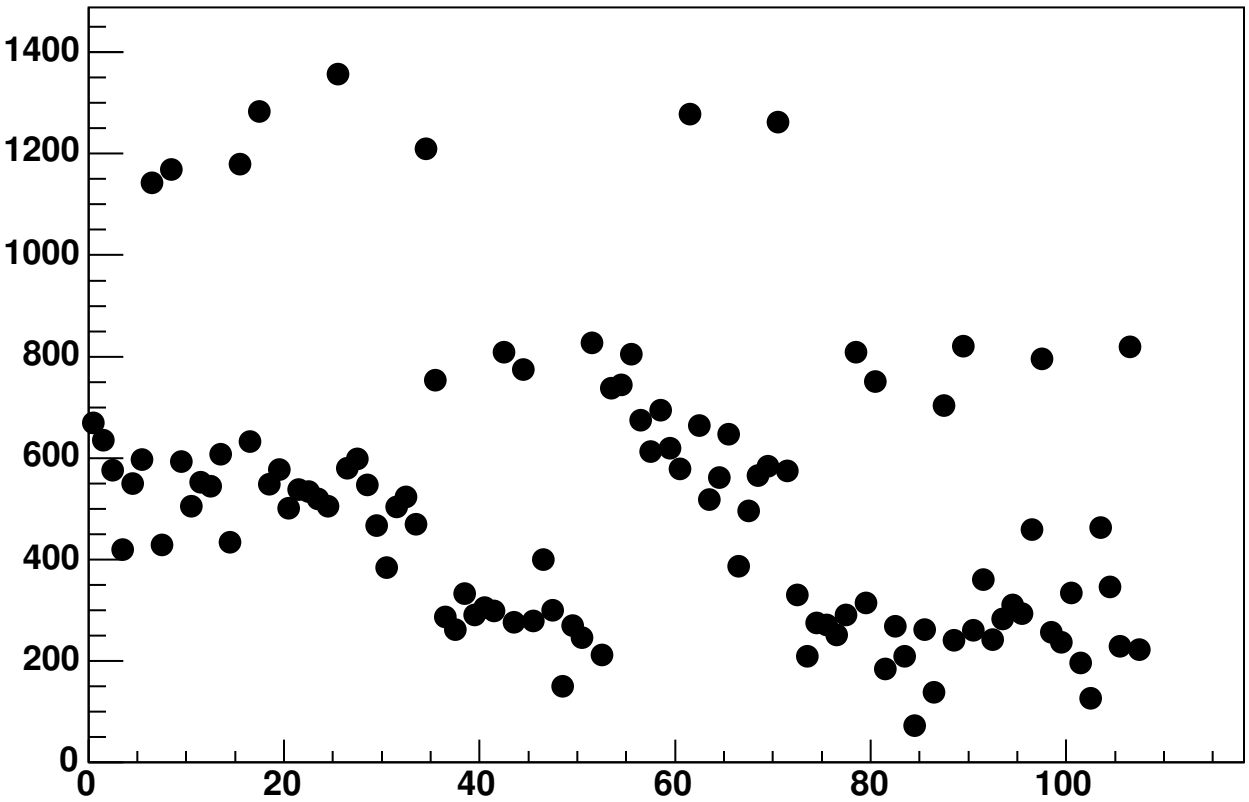
Enable 2, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



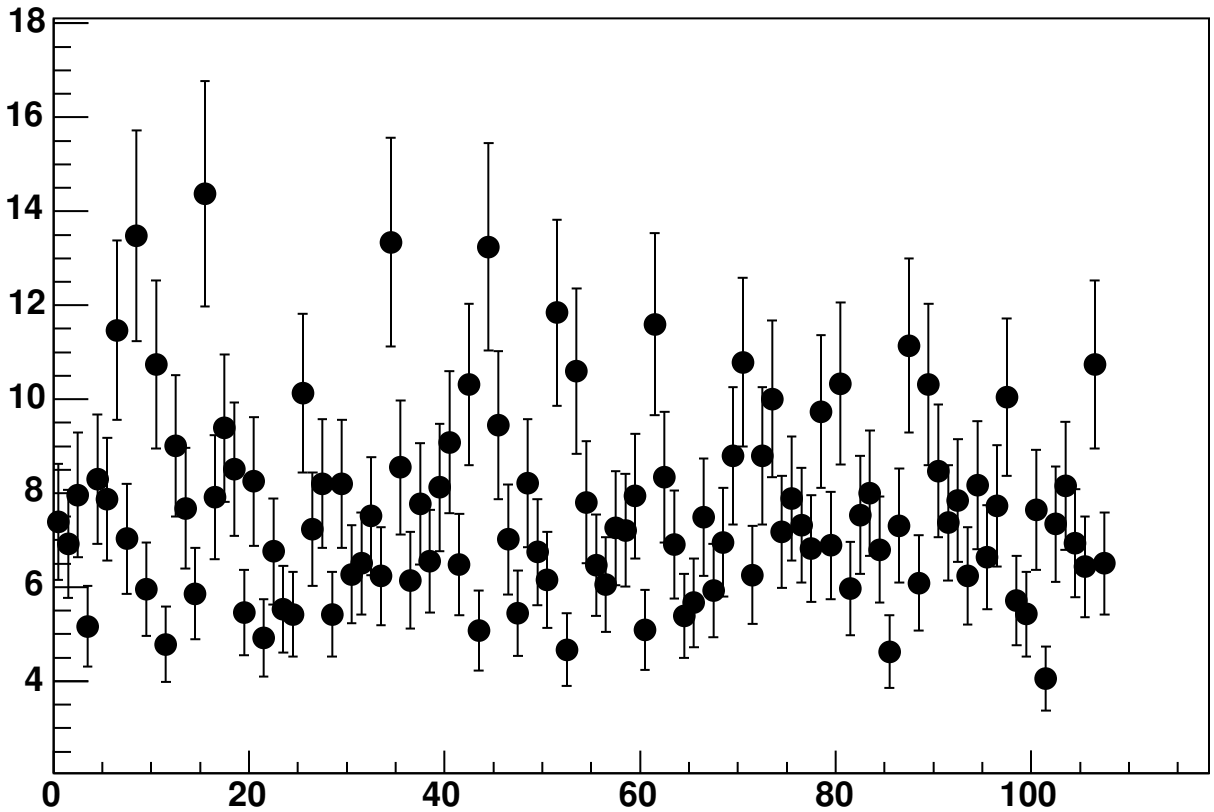
Enable 2, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



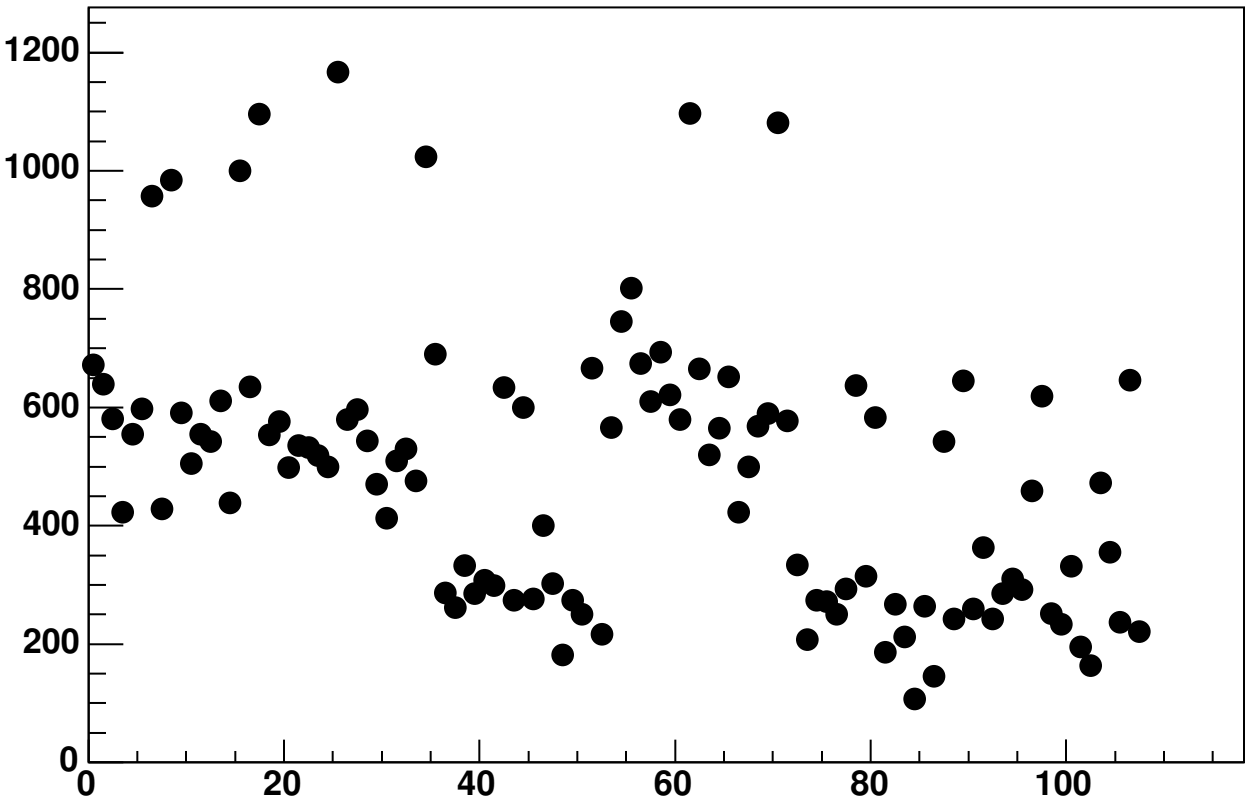
Enable 2, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



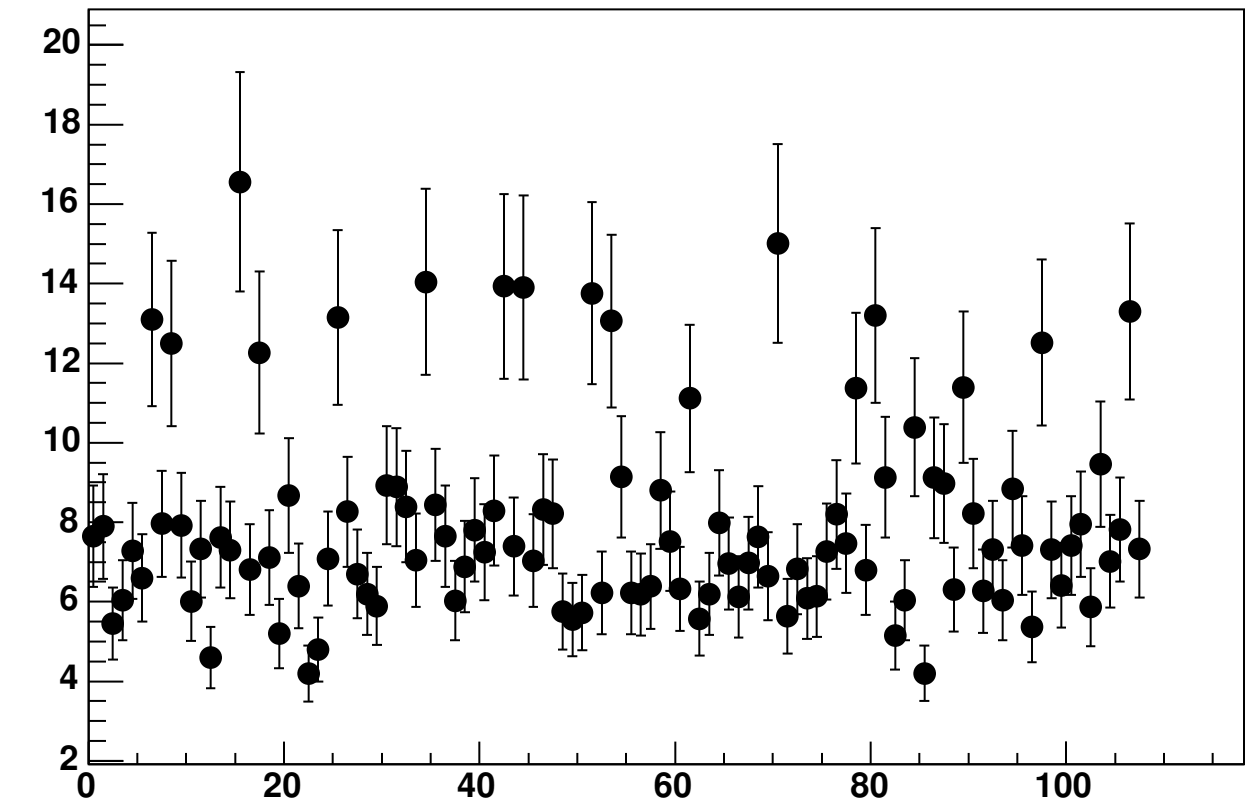
Enable 2, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



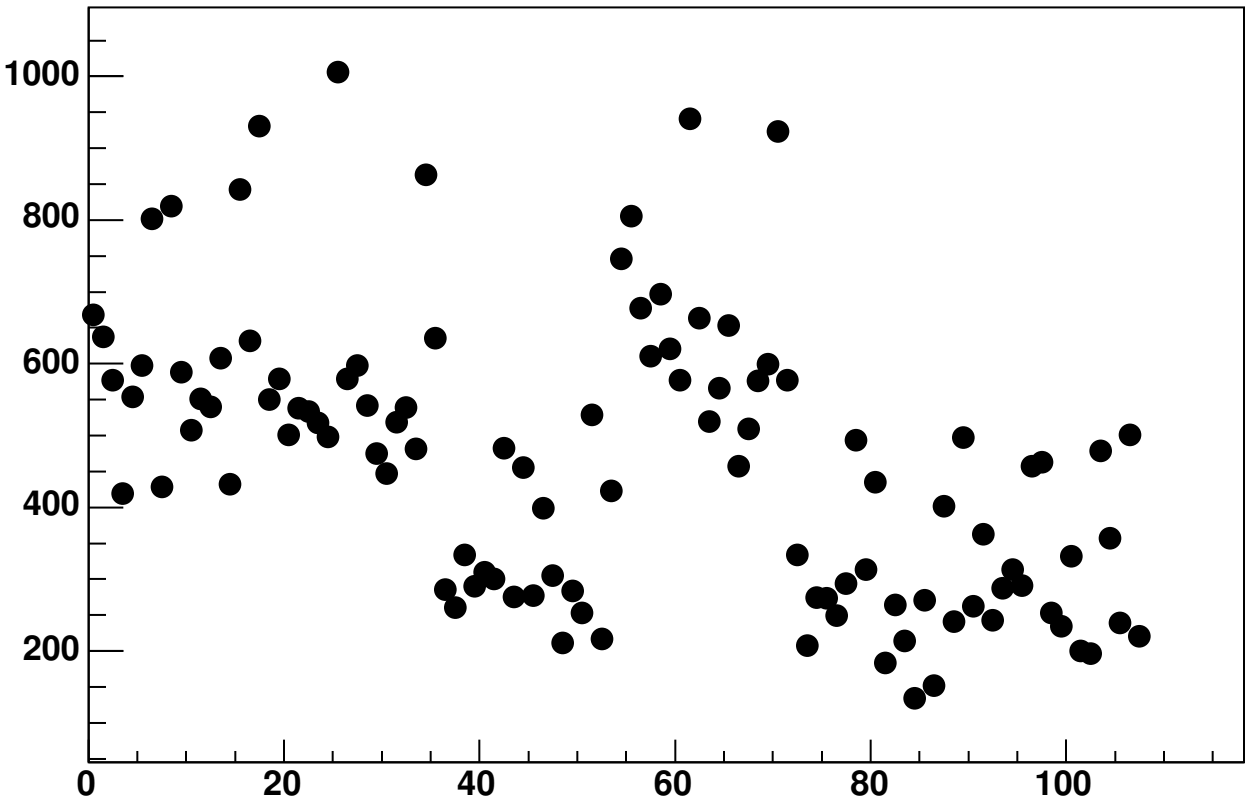
Enable 2, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



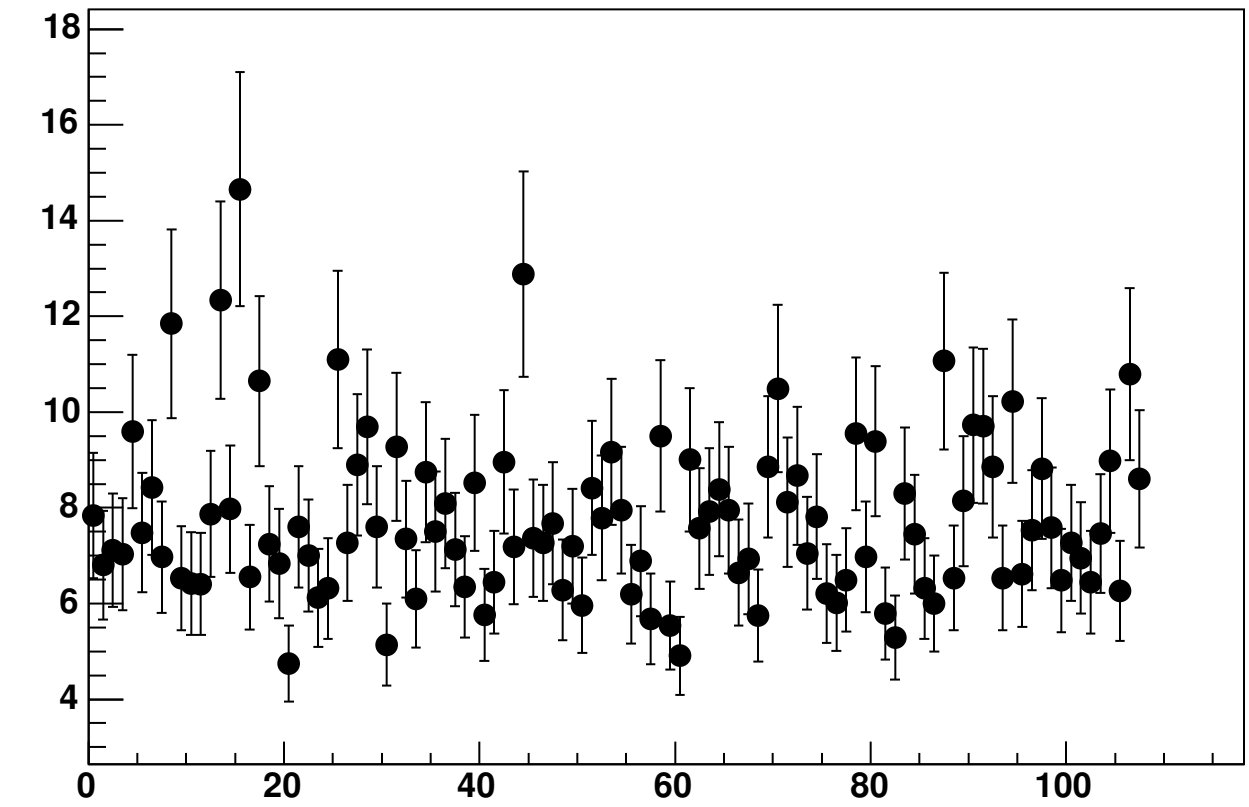
Enable 2, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



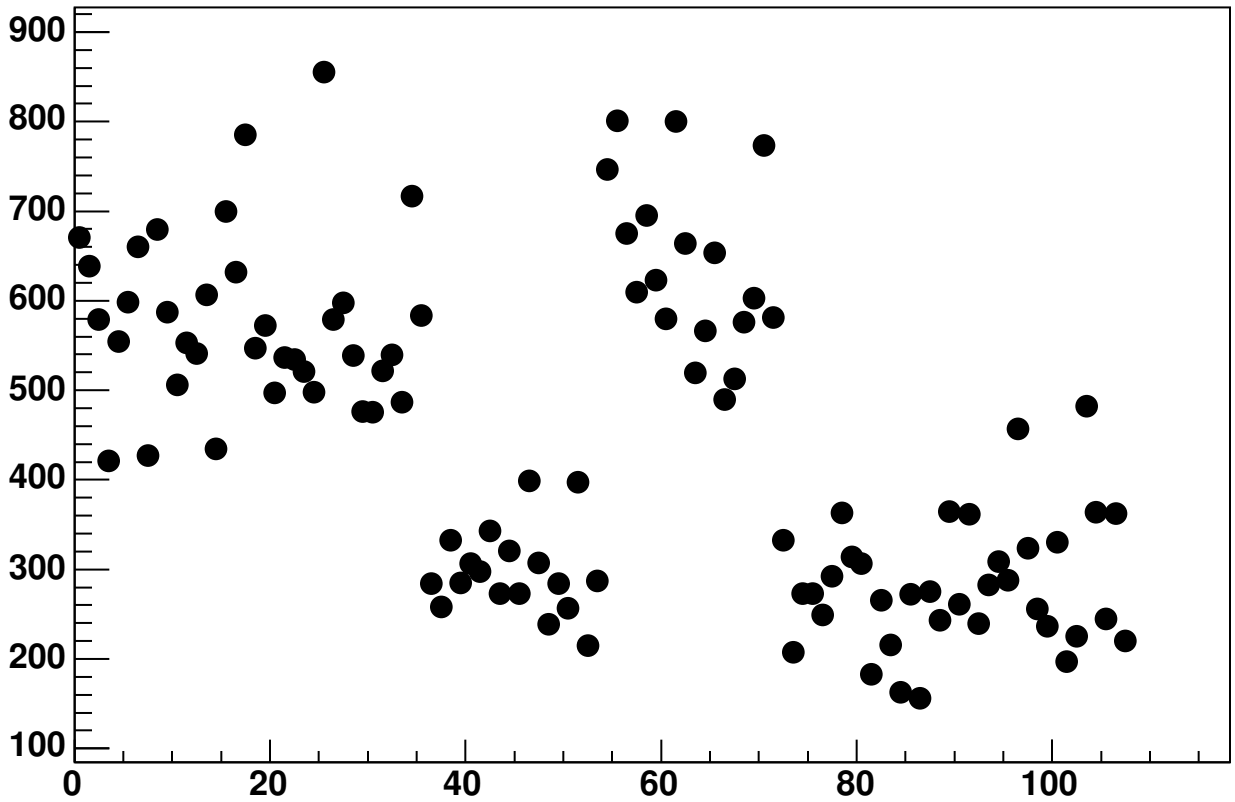
Enable 2, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



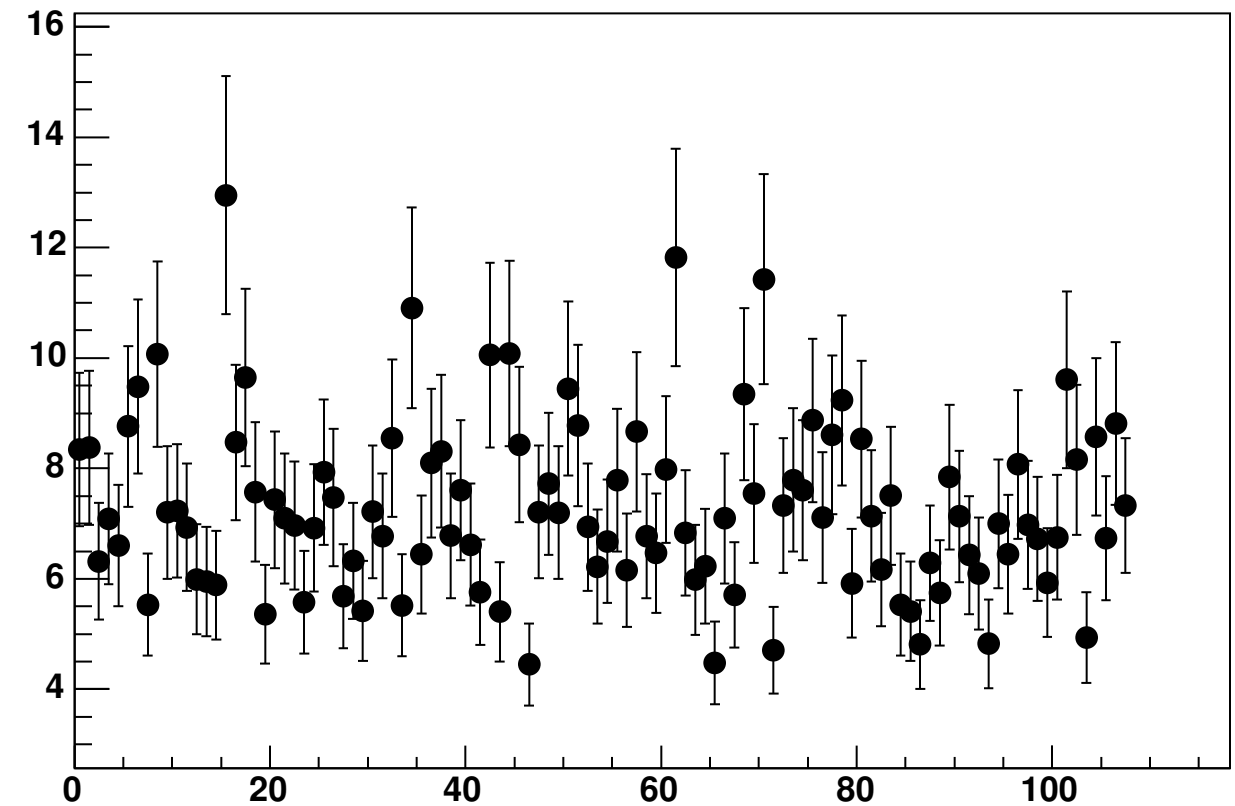
Enable 2, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



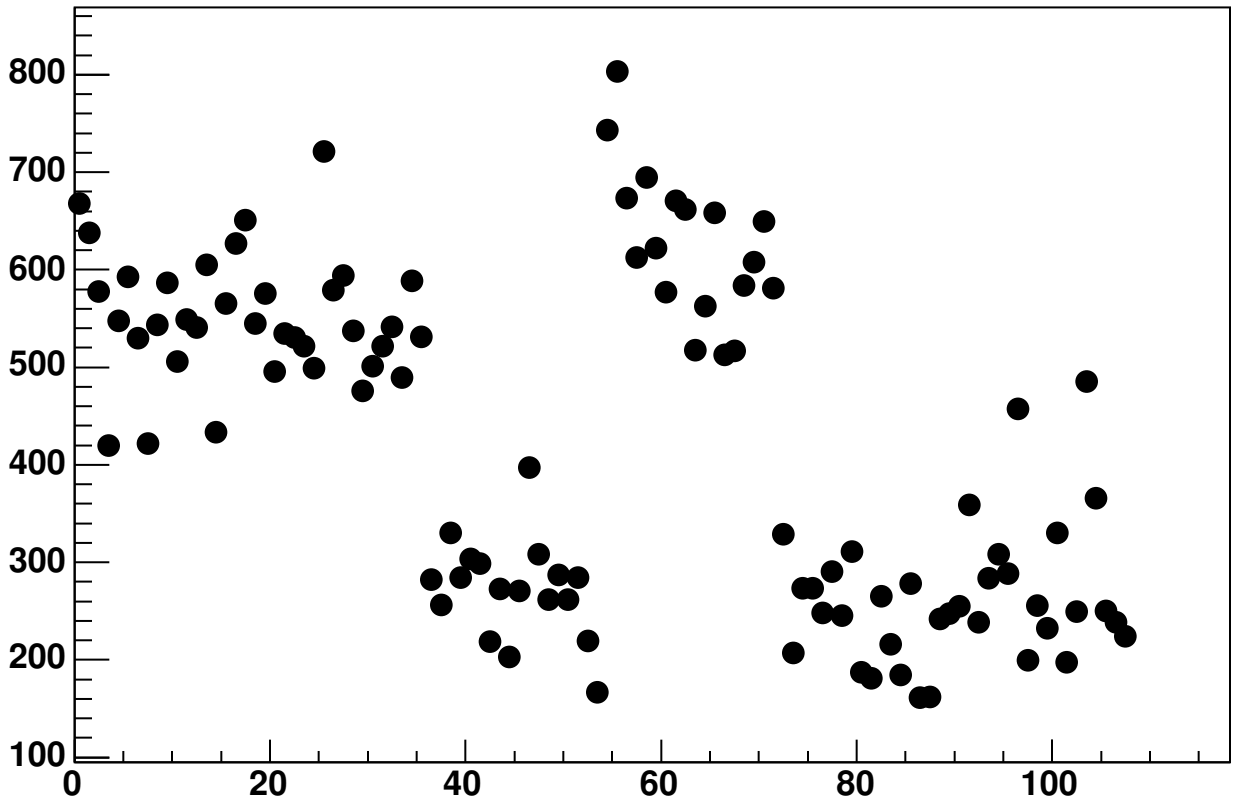
Enable 2, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



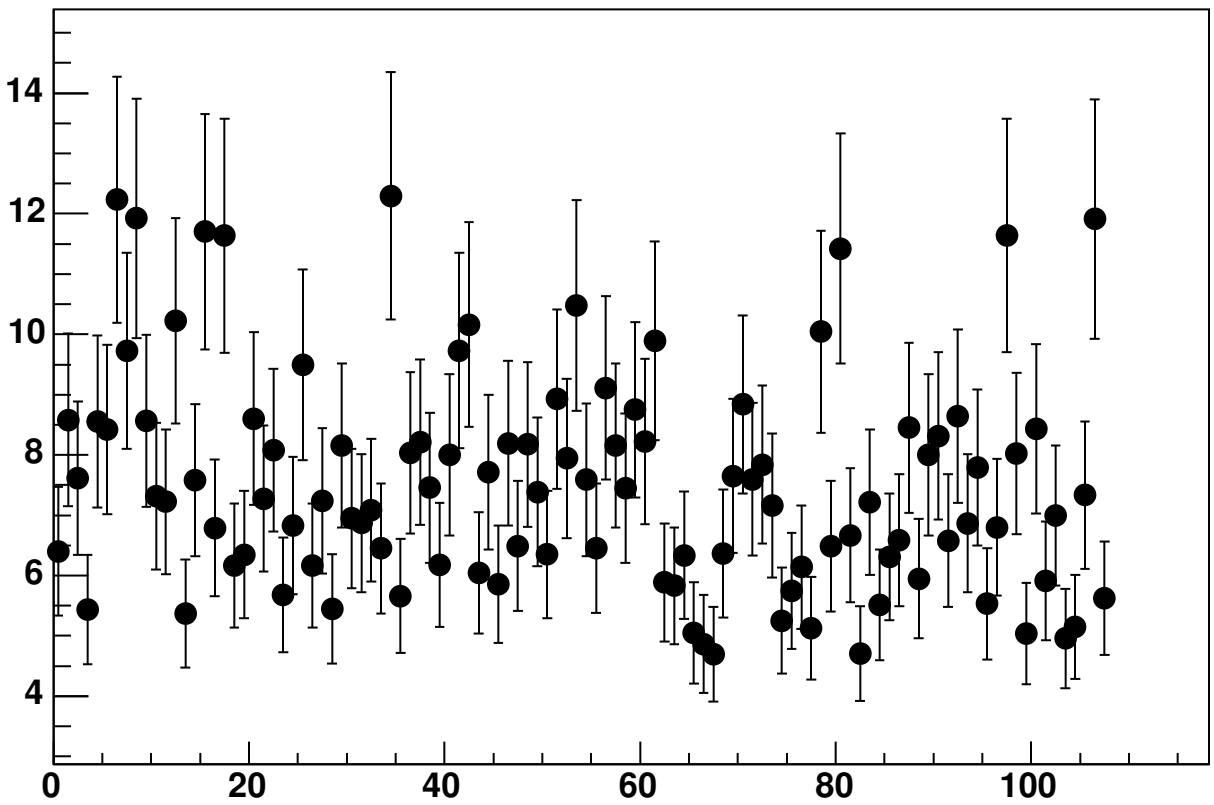
Enable 2, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



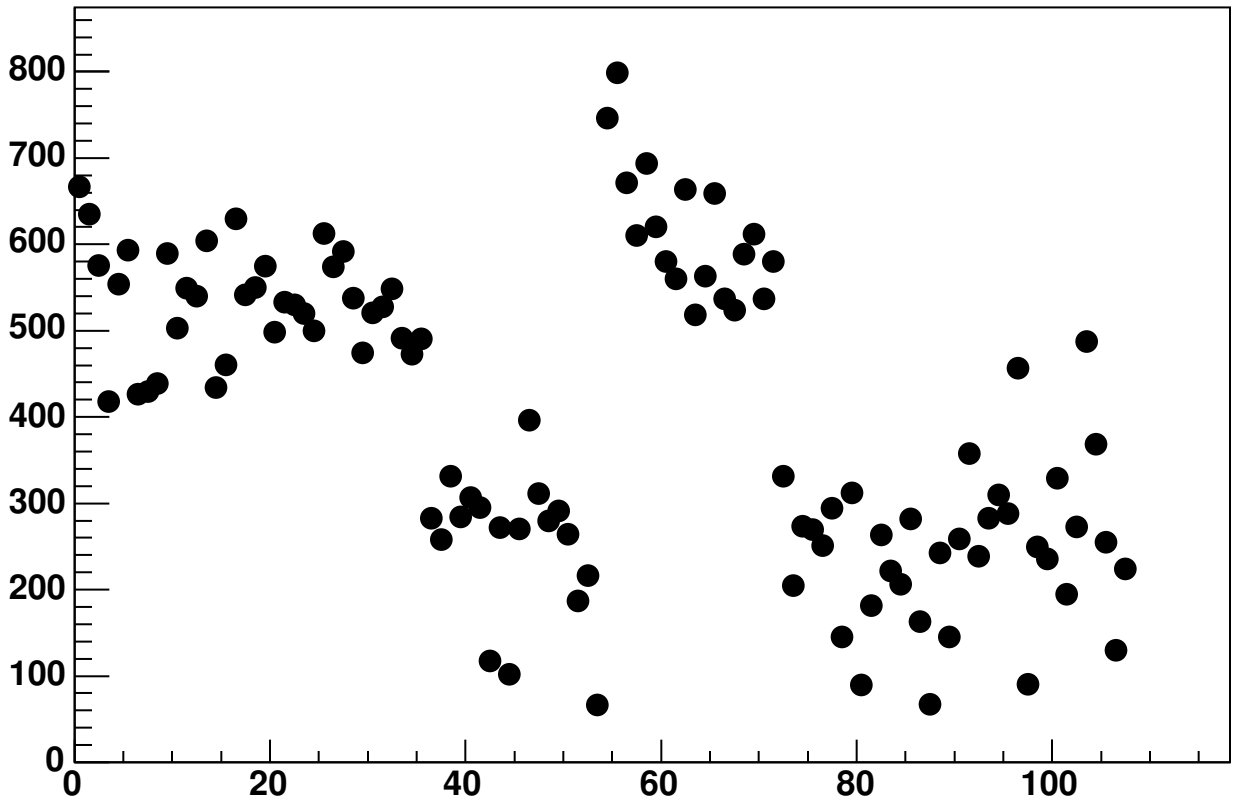
Enable 2, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



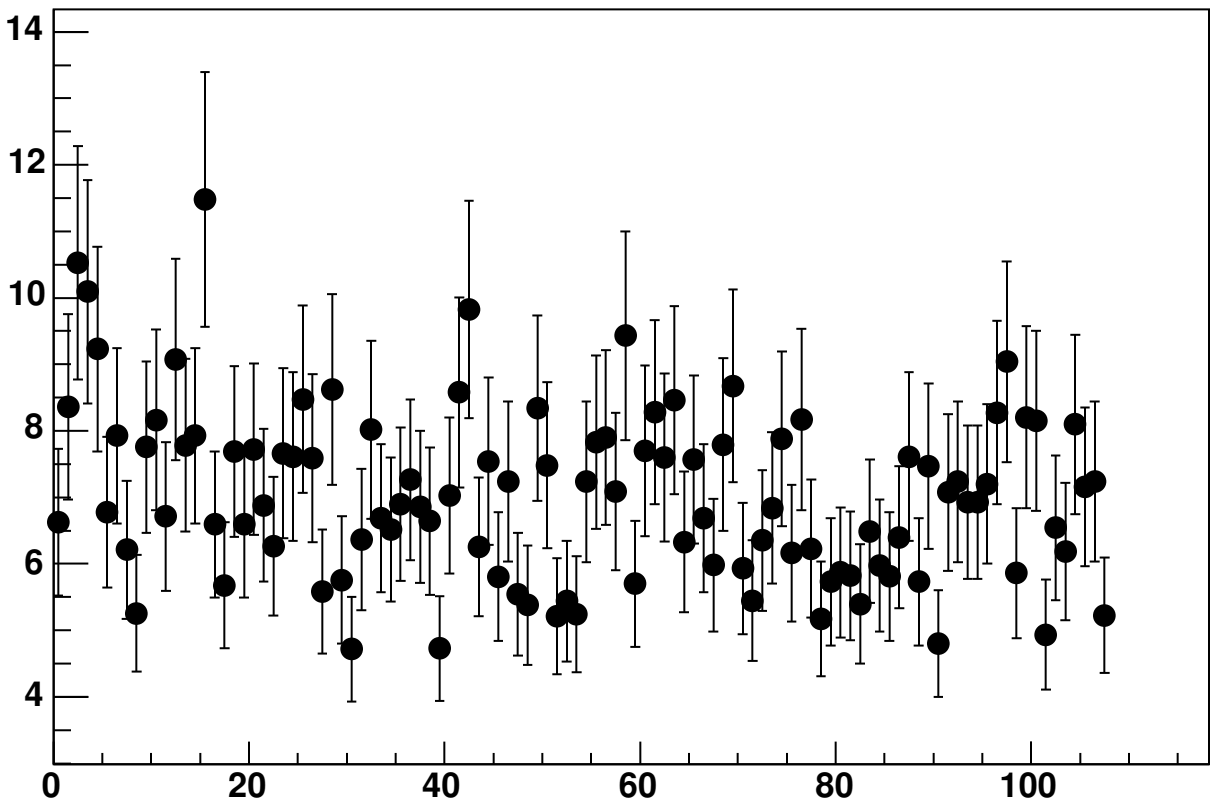
Enable 2, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



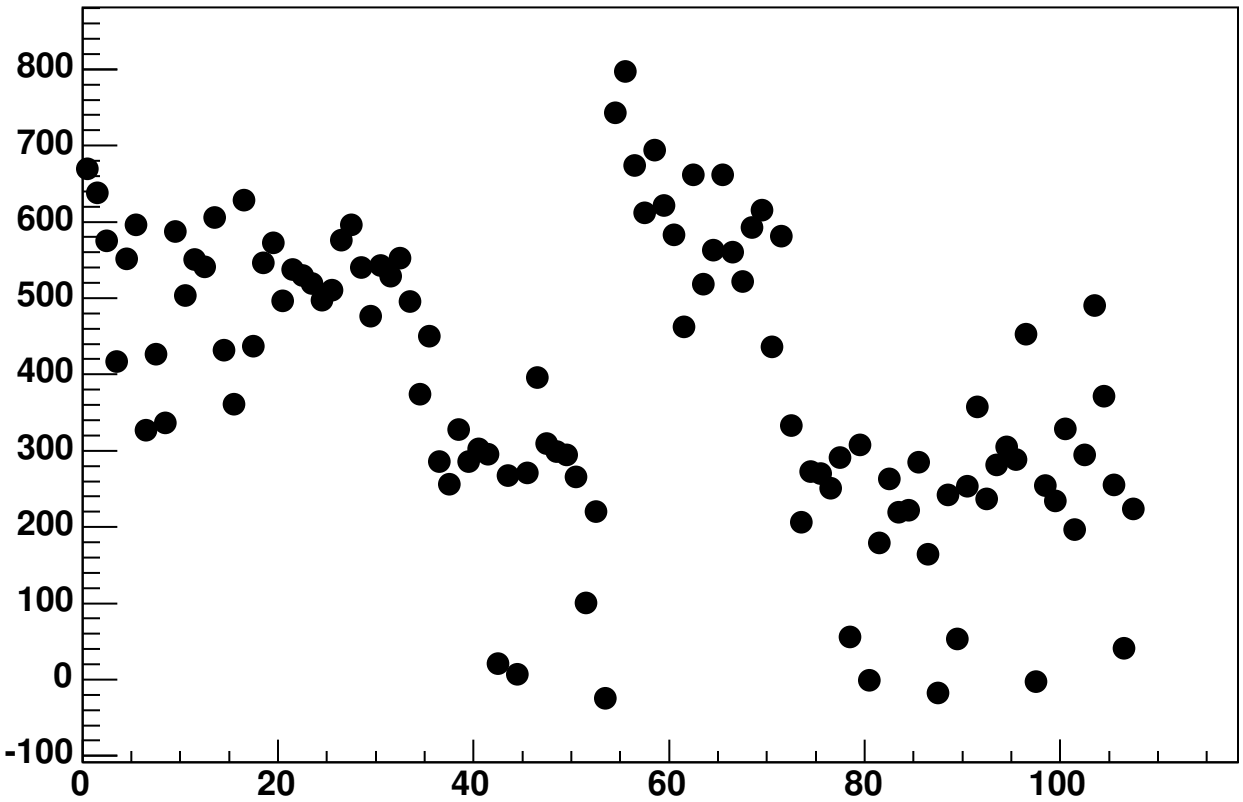
Enable 2, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



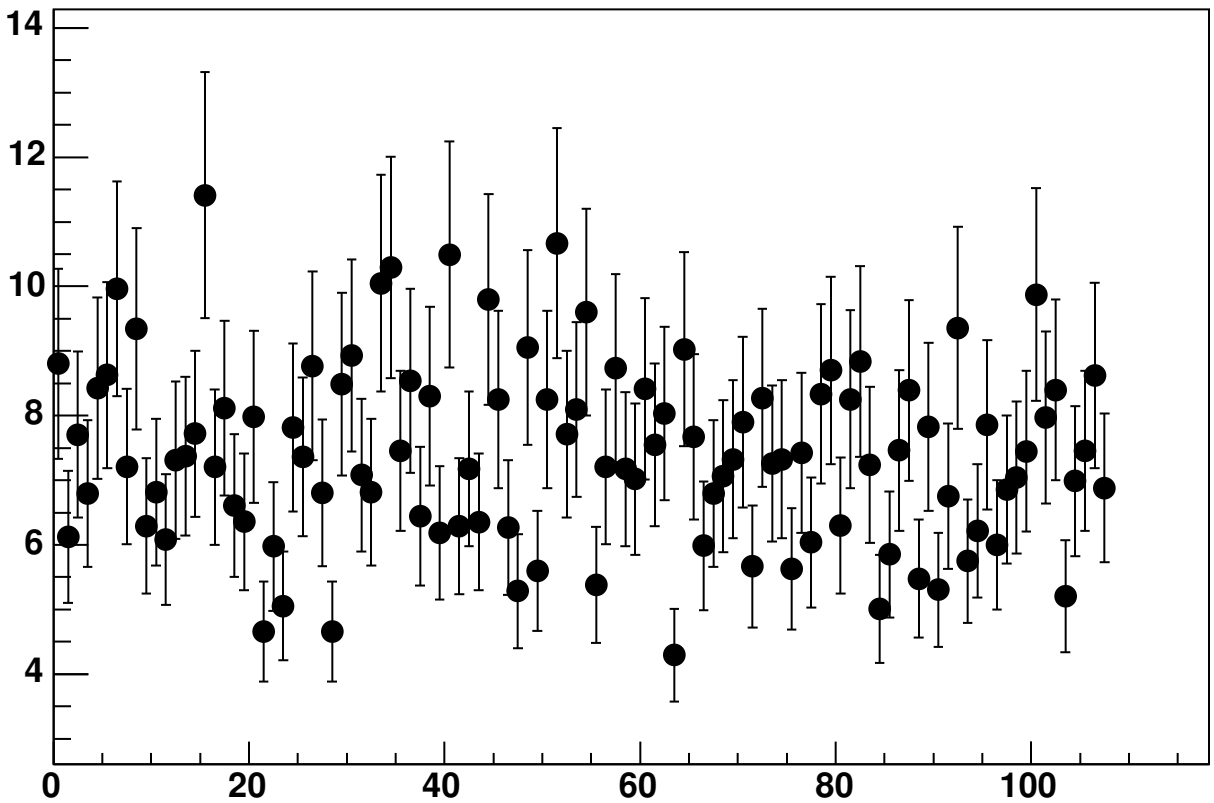
Enable 2, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



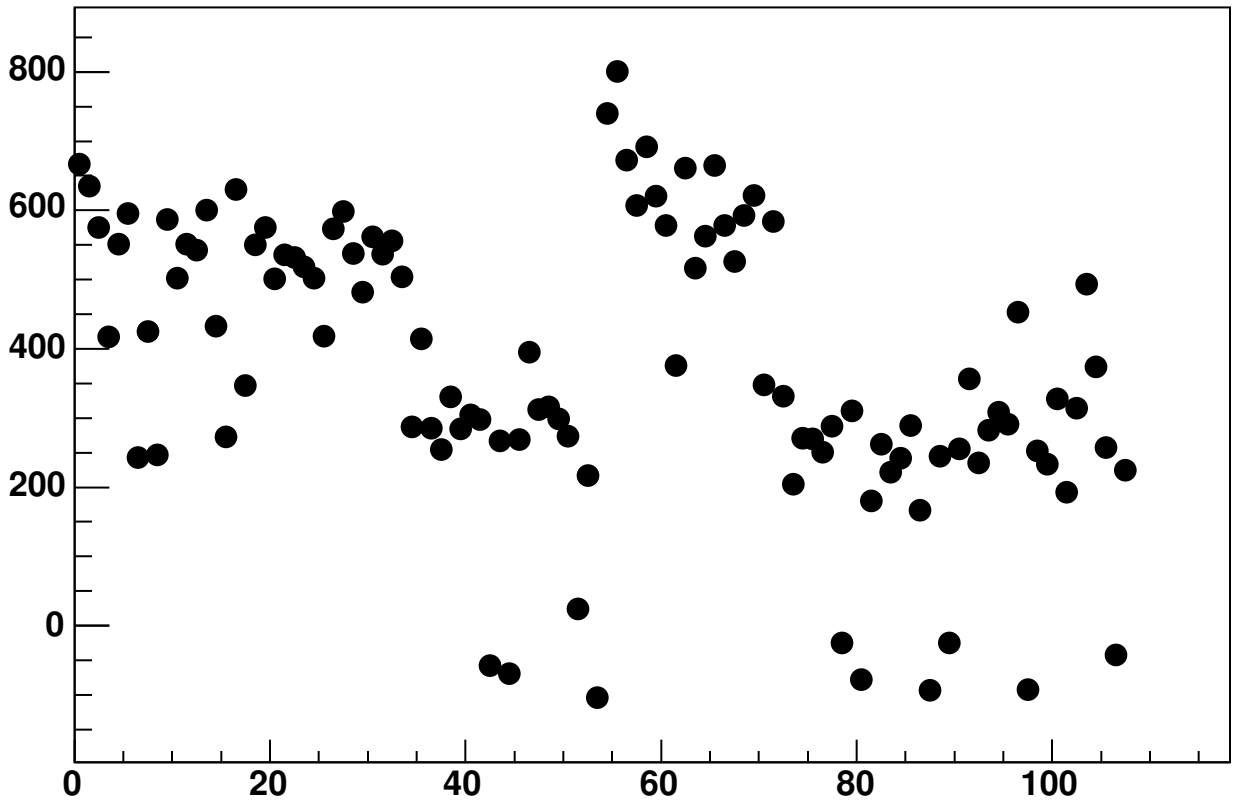
Enable 2, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



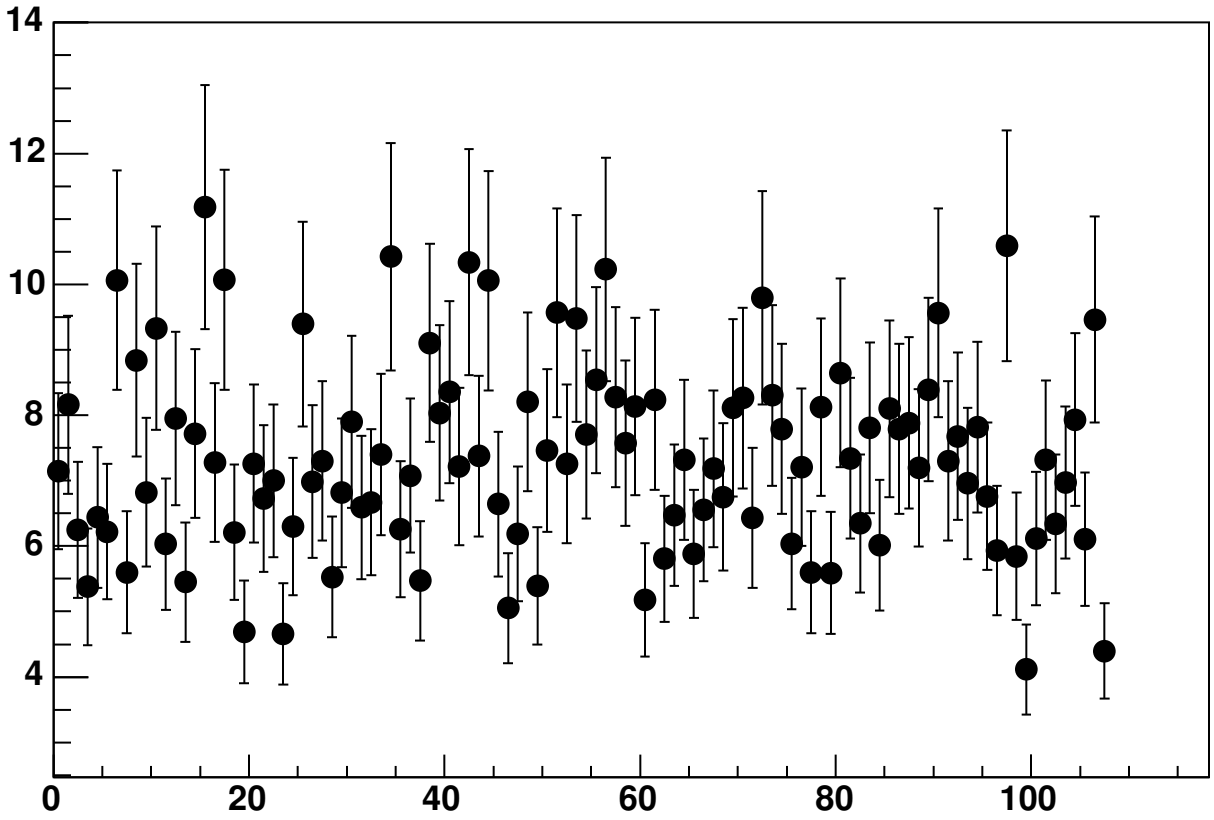
Enable 2, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



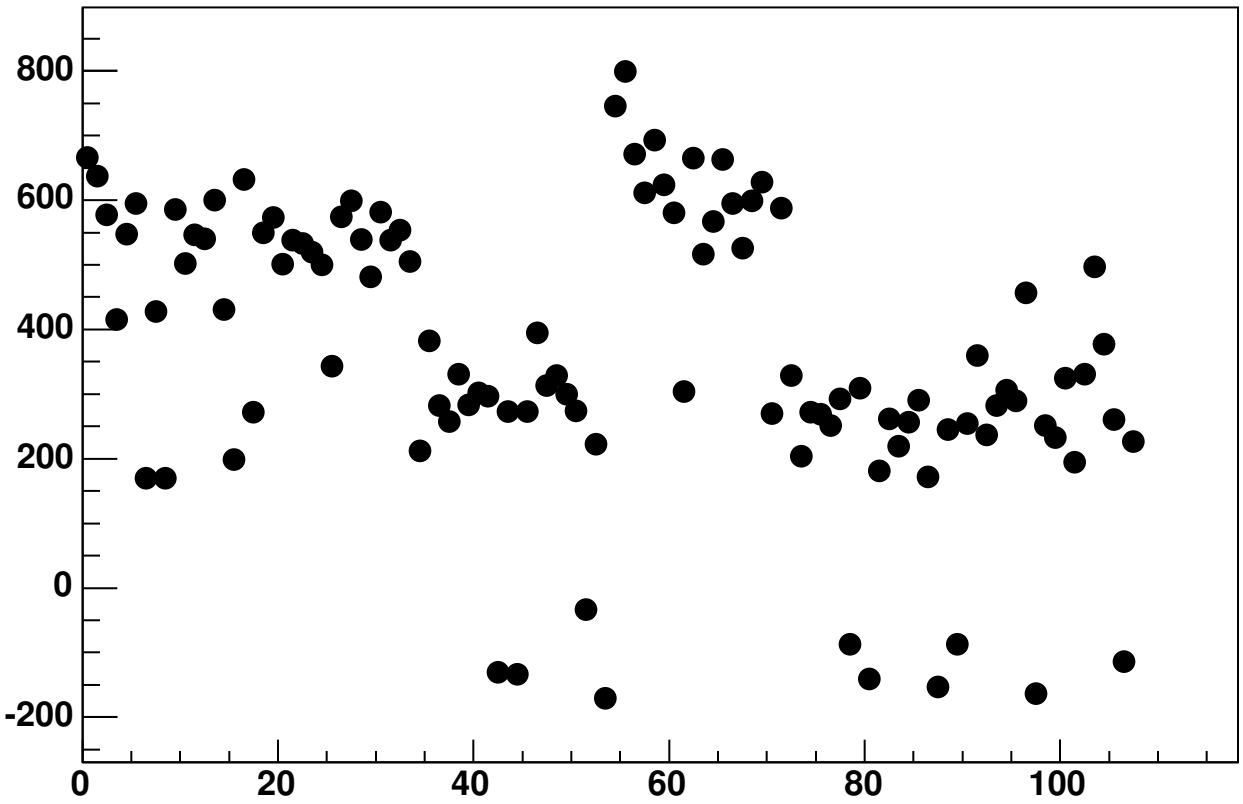
Enable 2, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



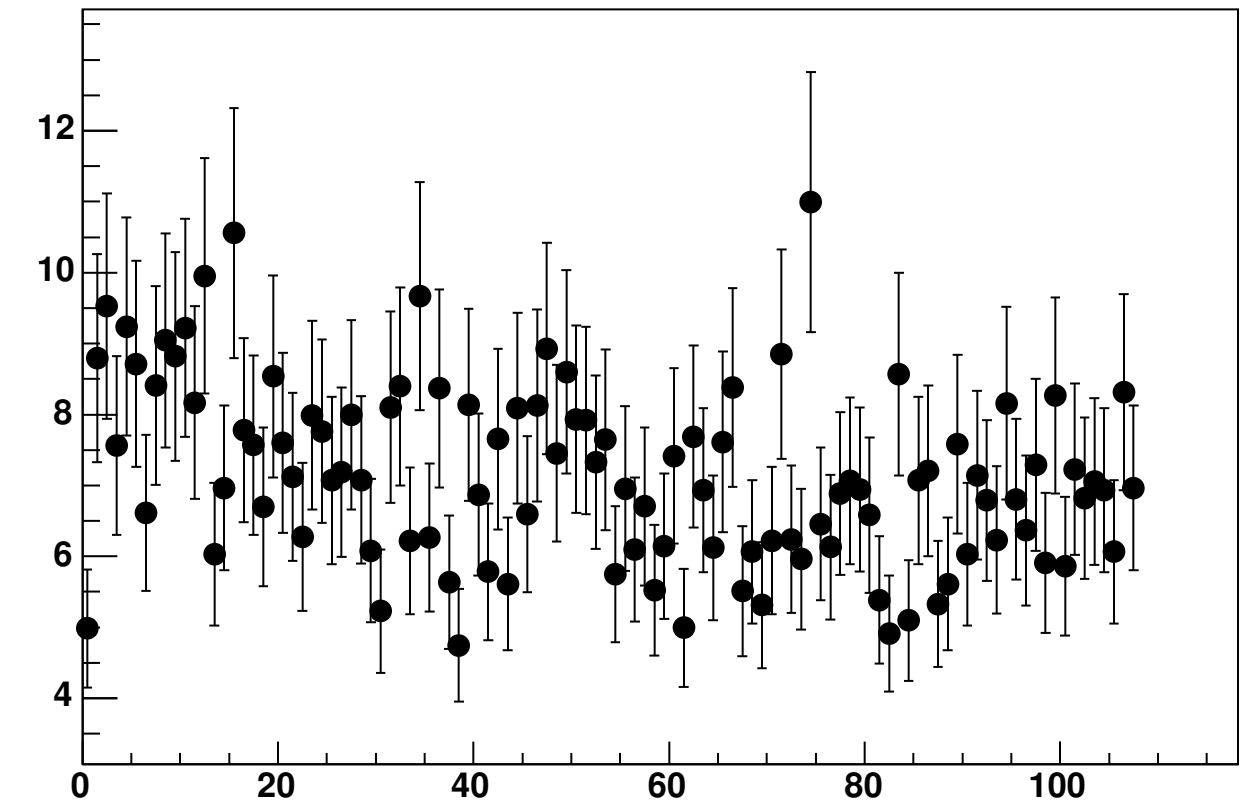
Enable 2, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



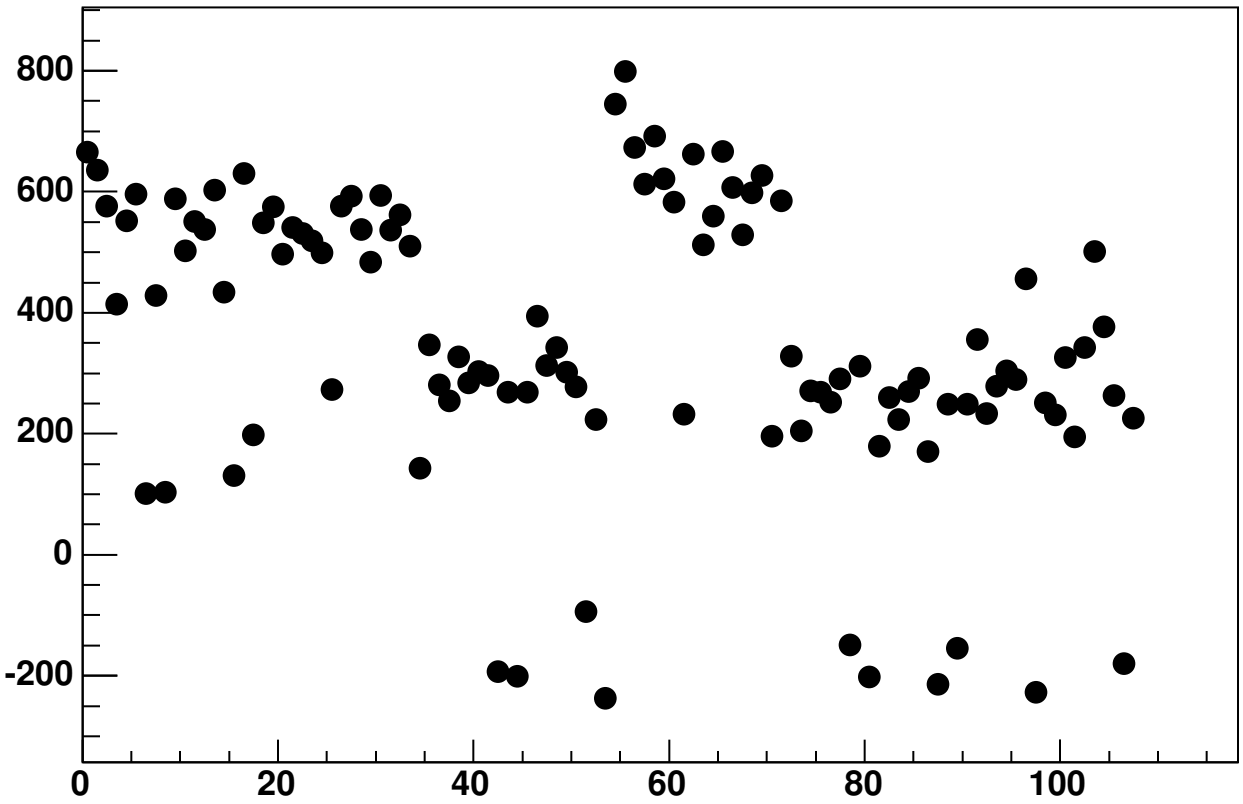
Enable 2, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



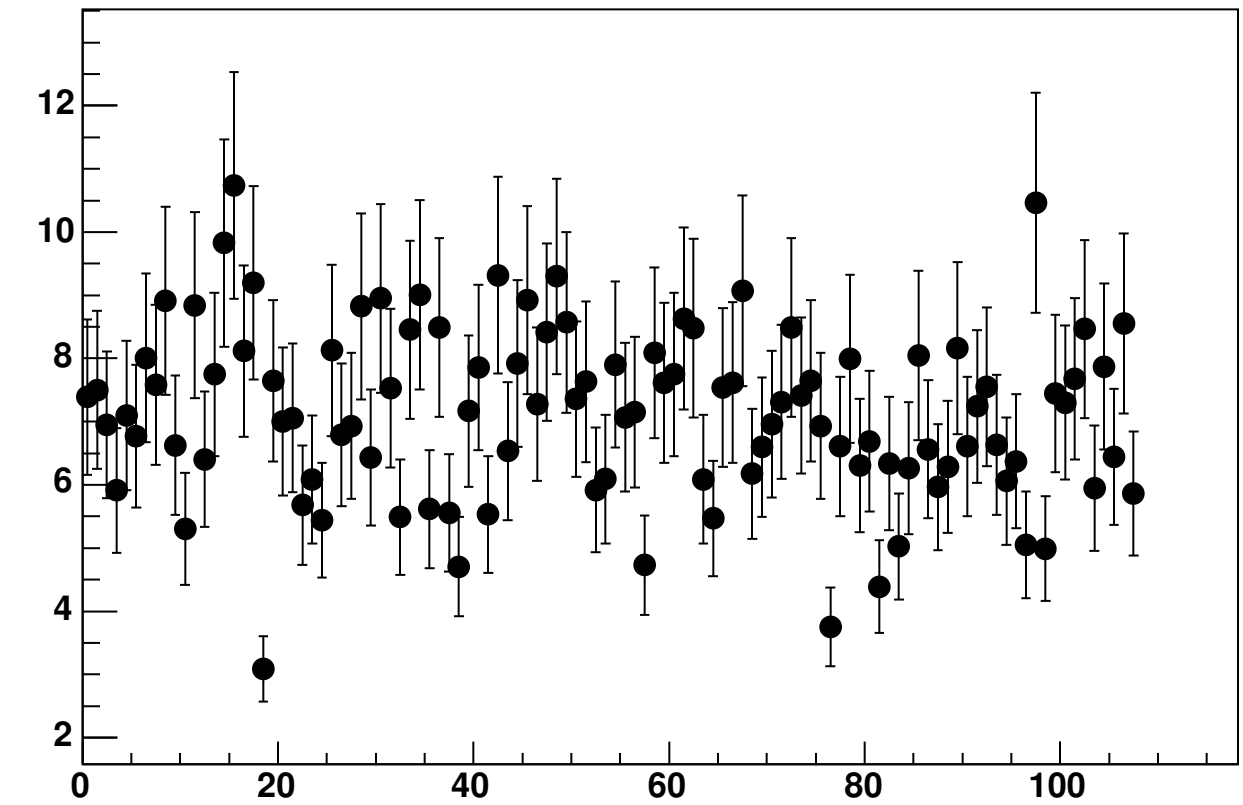
Enable 2, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



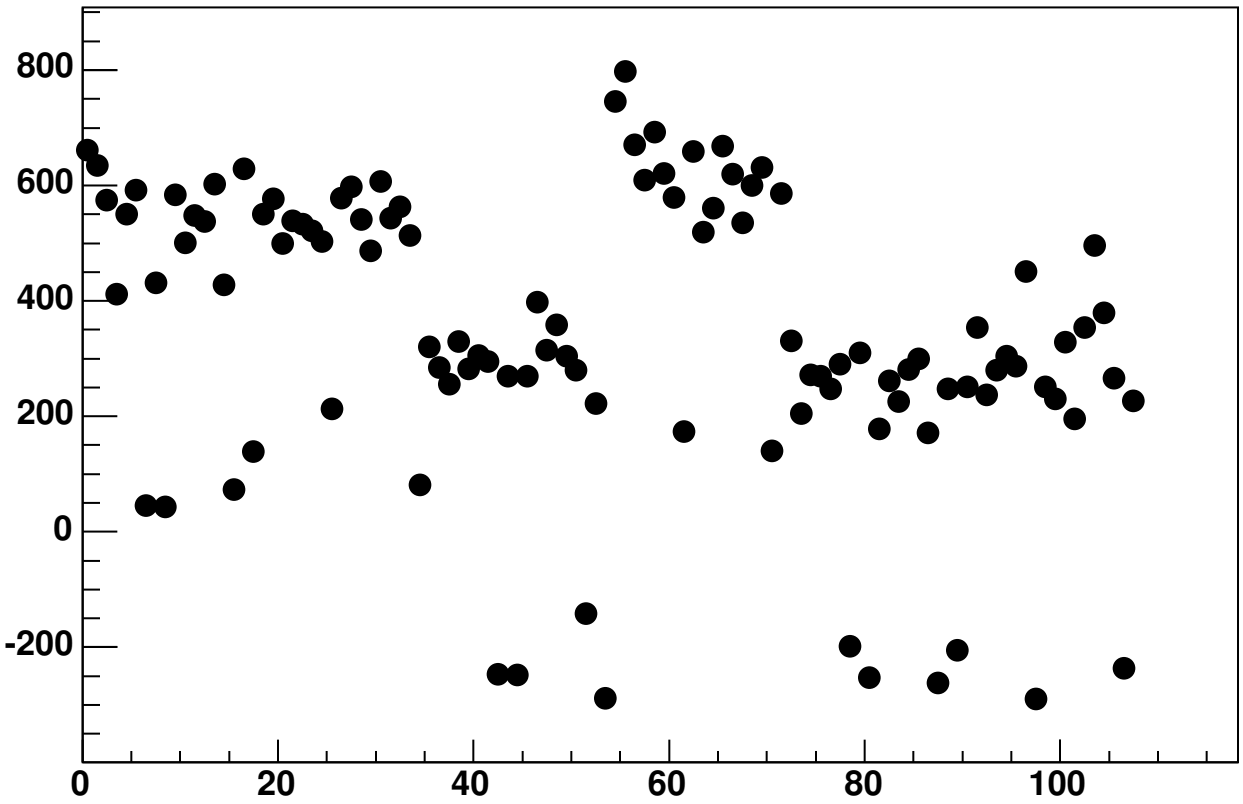
Enable 2, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



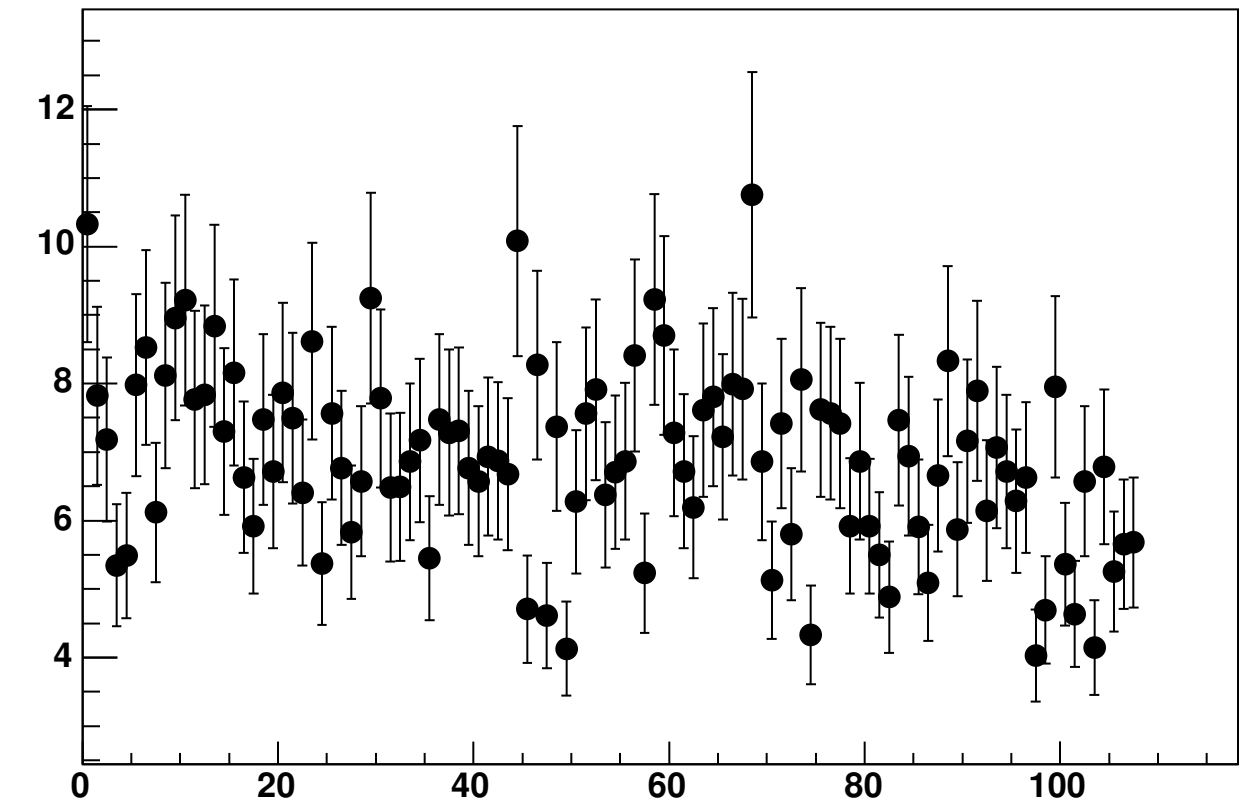
Enable 2, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



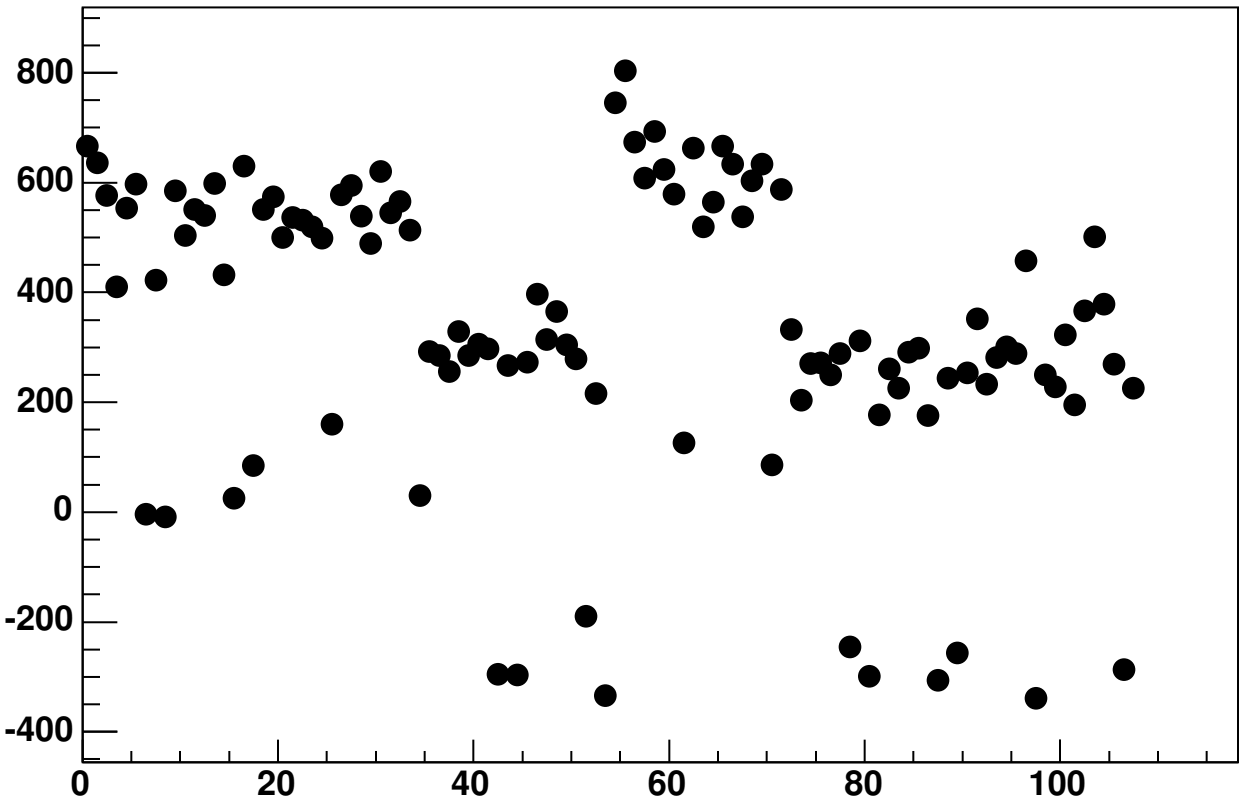
Enable 2, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



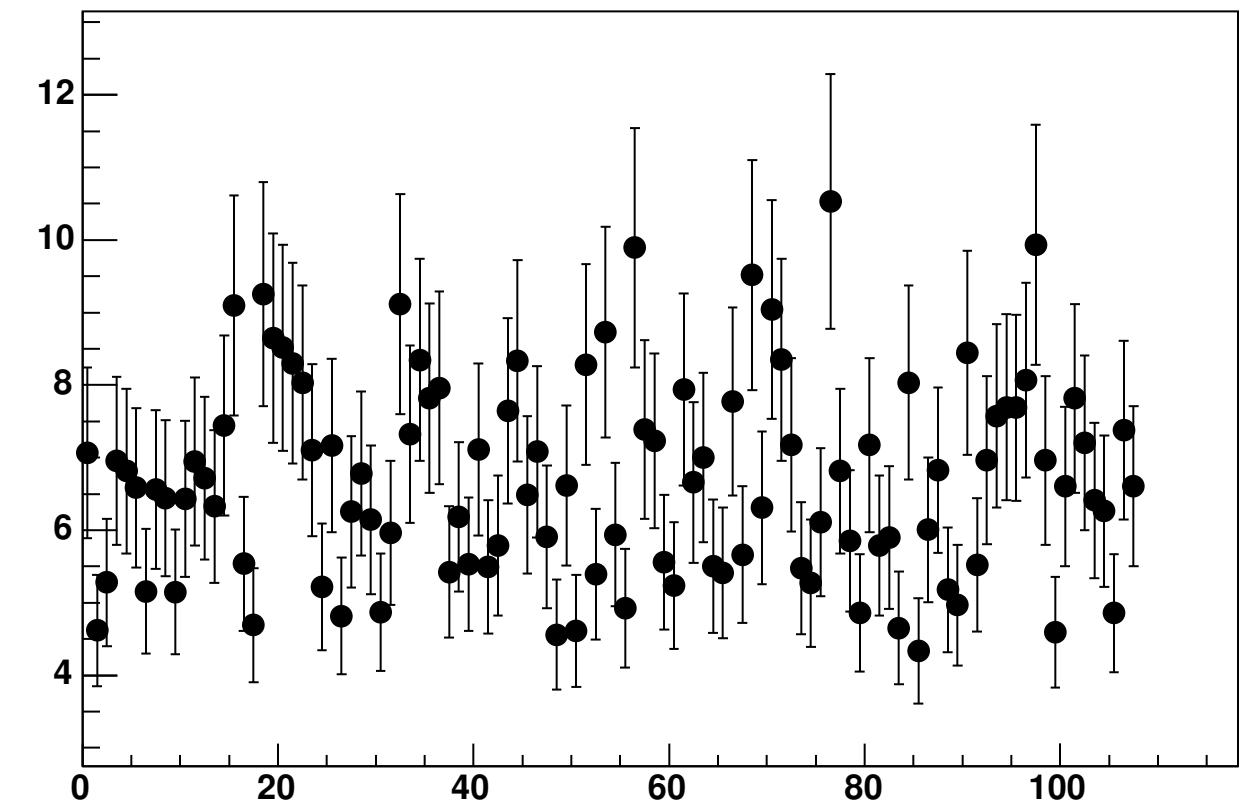
Enable 2, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



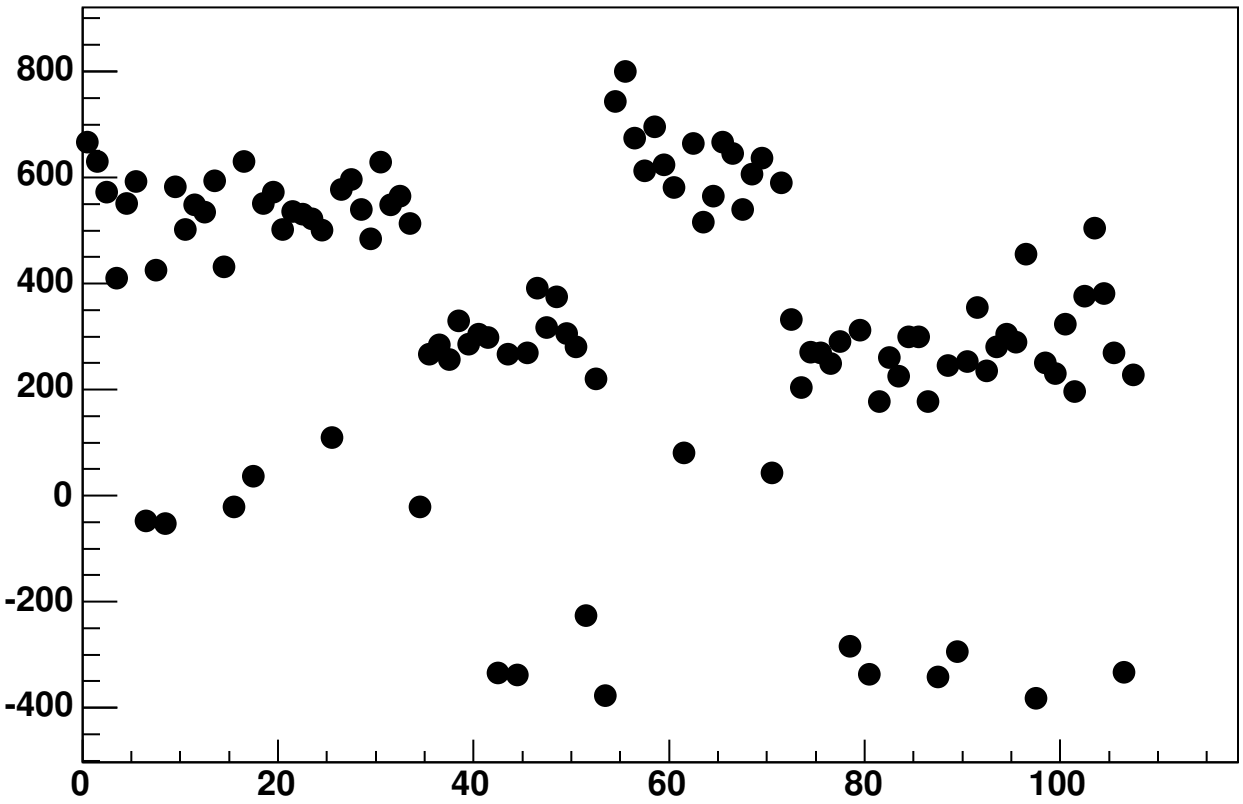
Enable 2, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



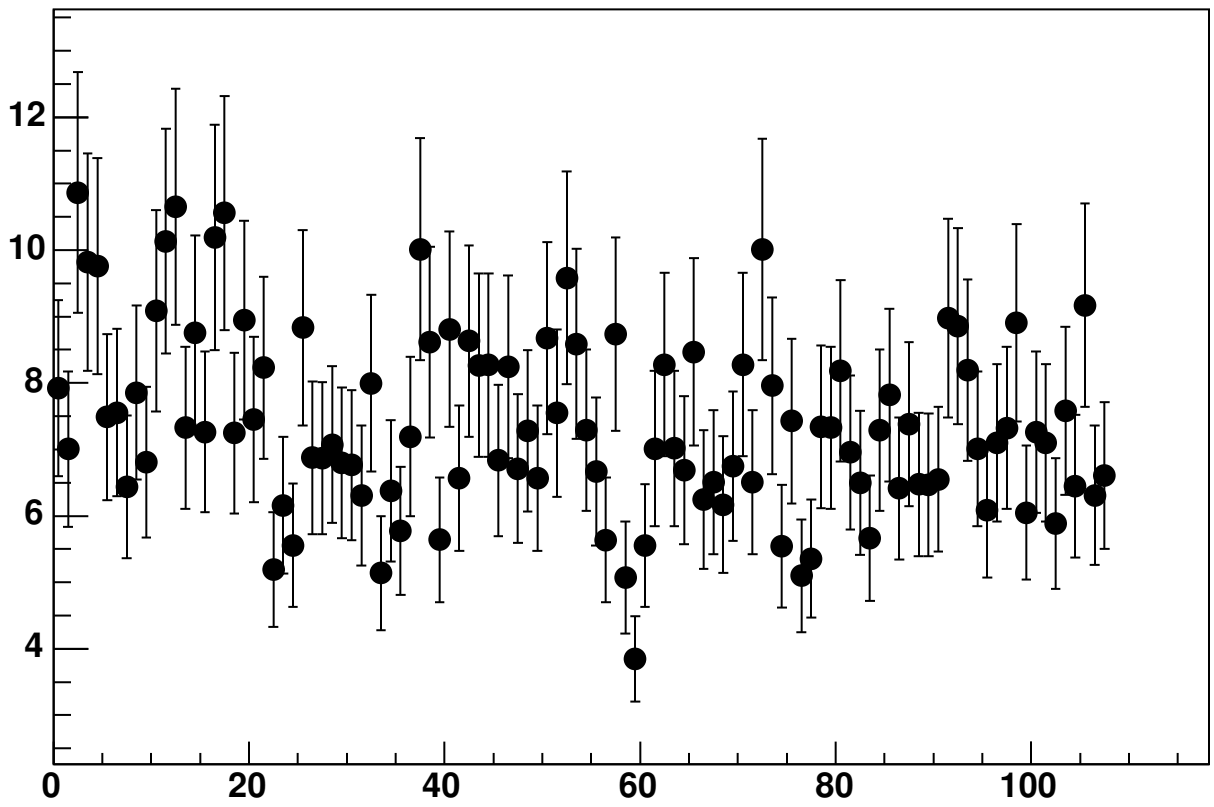
Enable 2, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



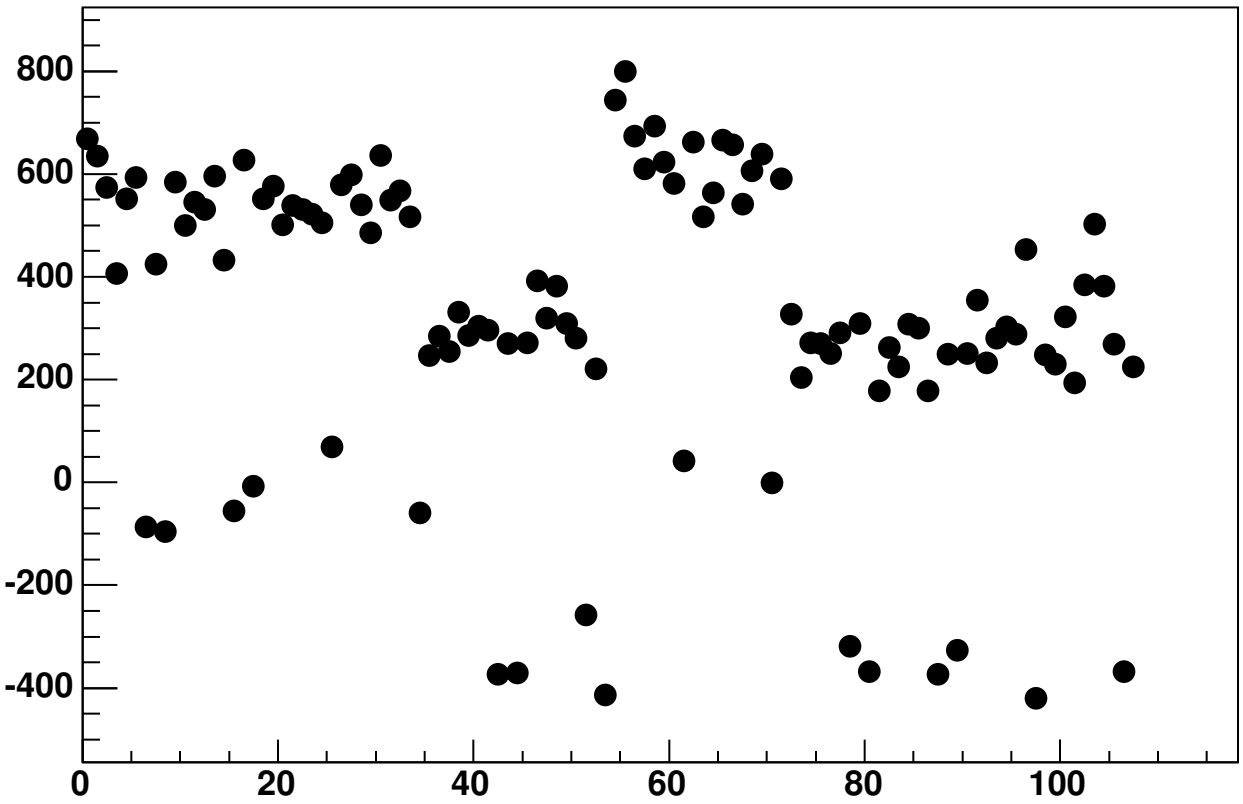
Enable 2, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



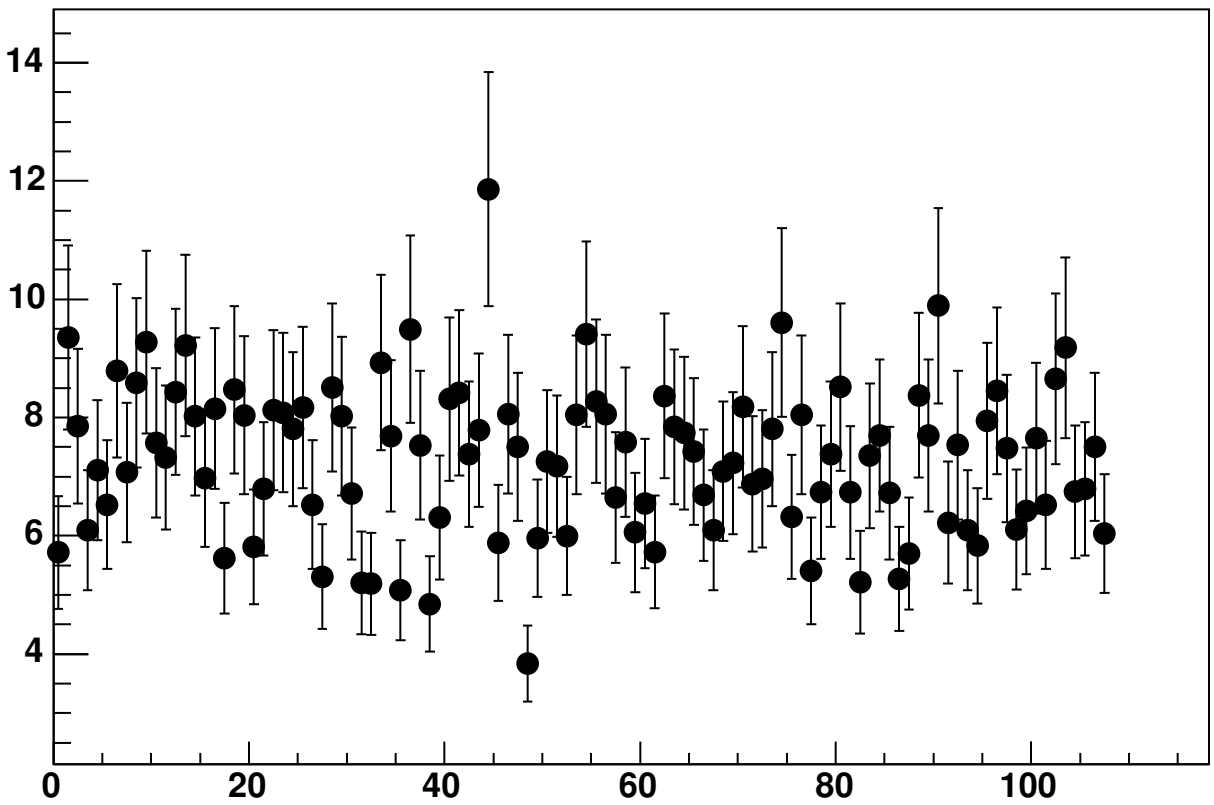
Enable 2, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



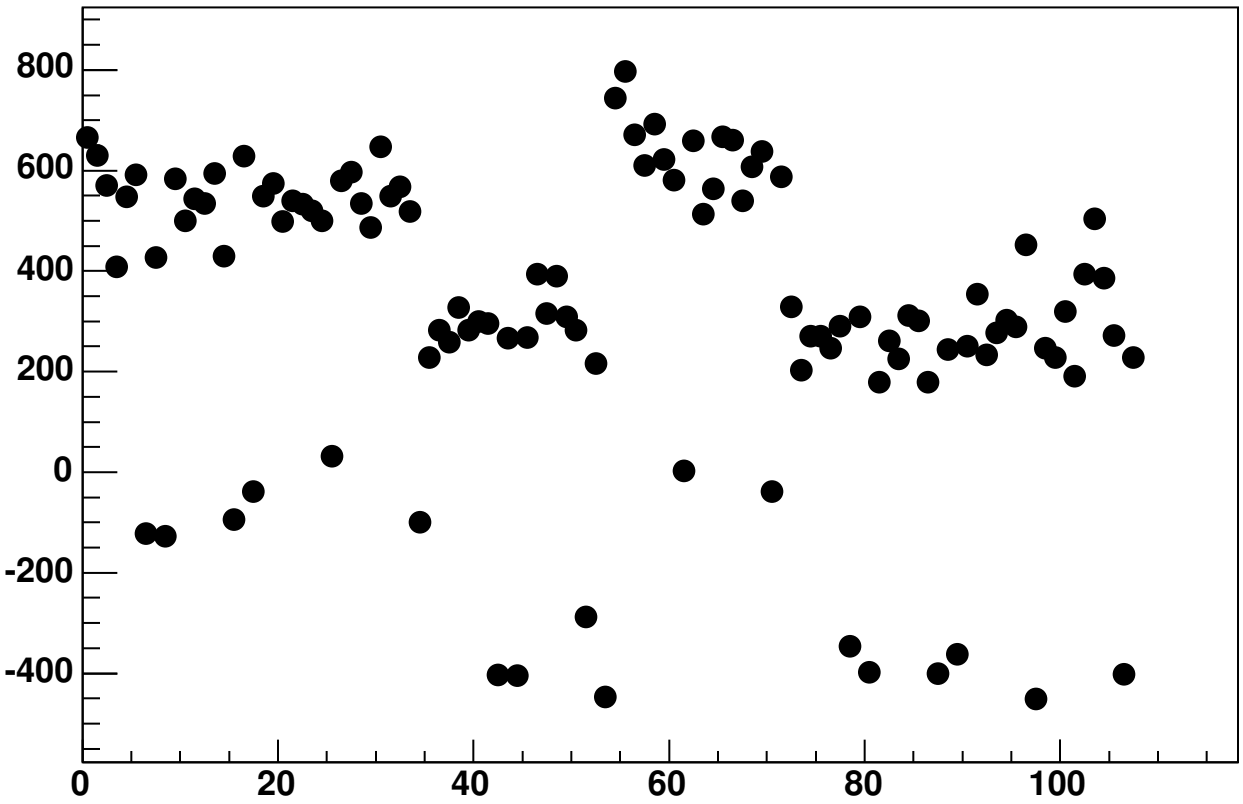
Enable 2, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



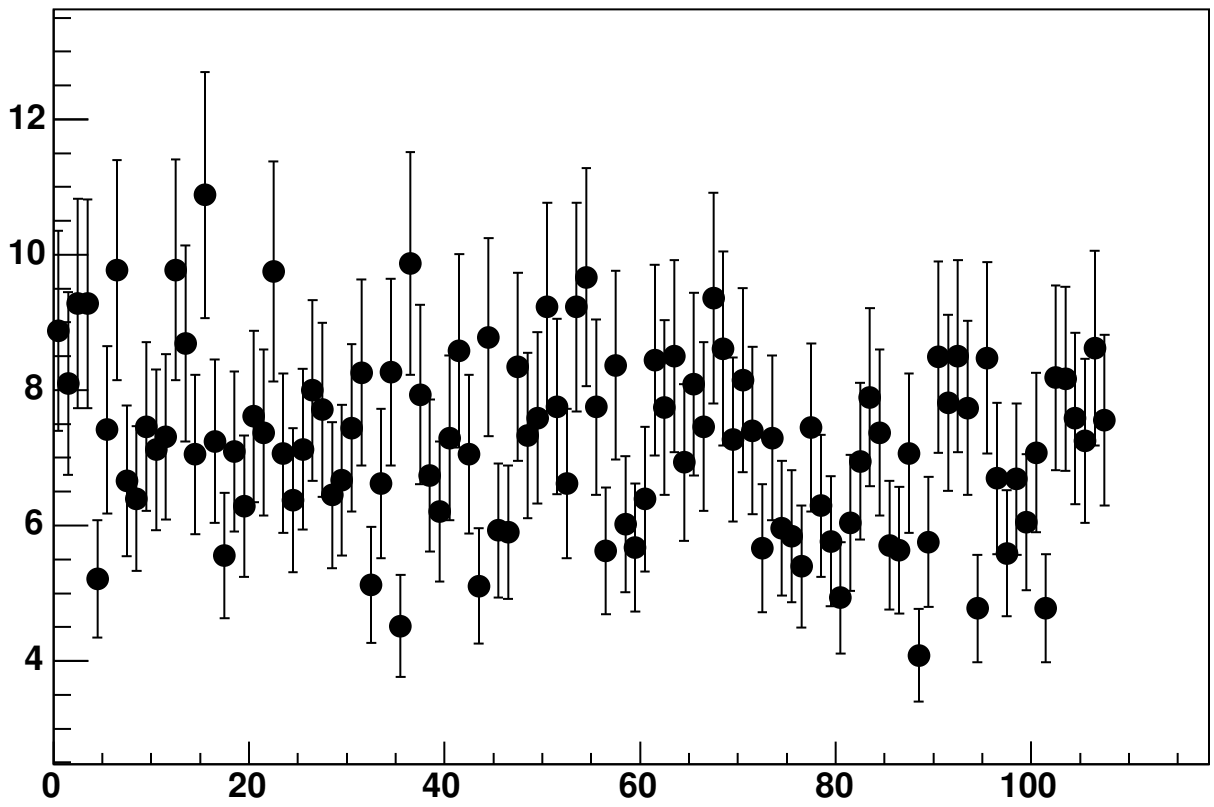
Enable 2, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



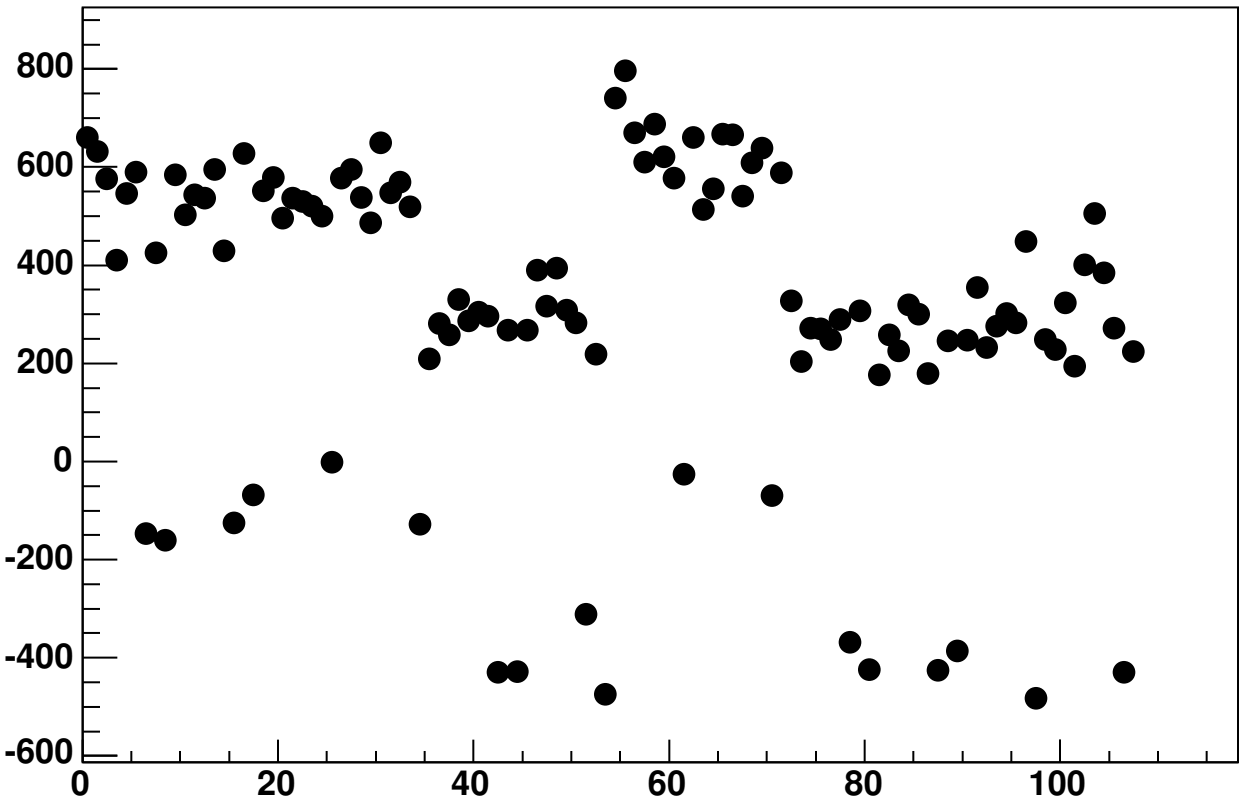
Enable 2, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



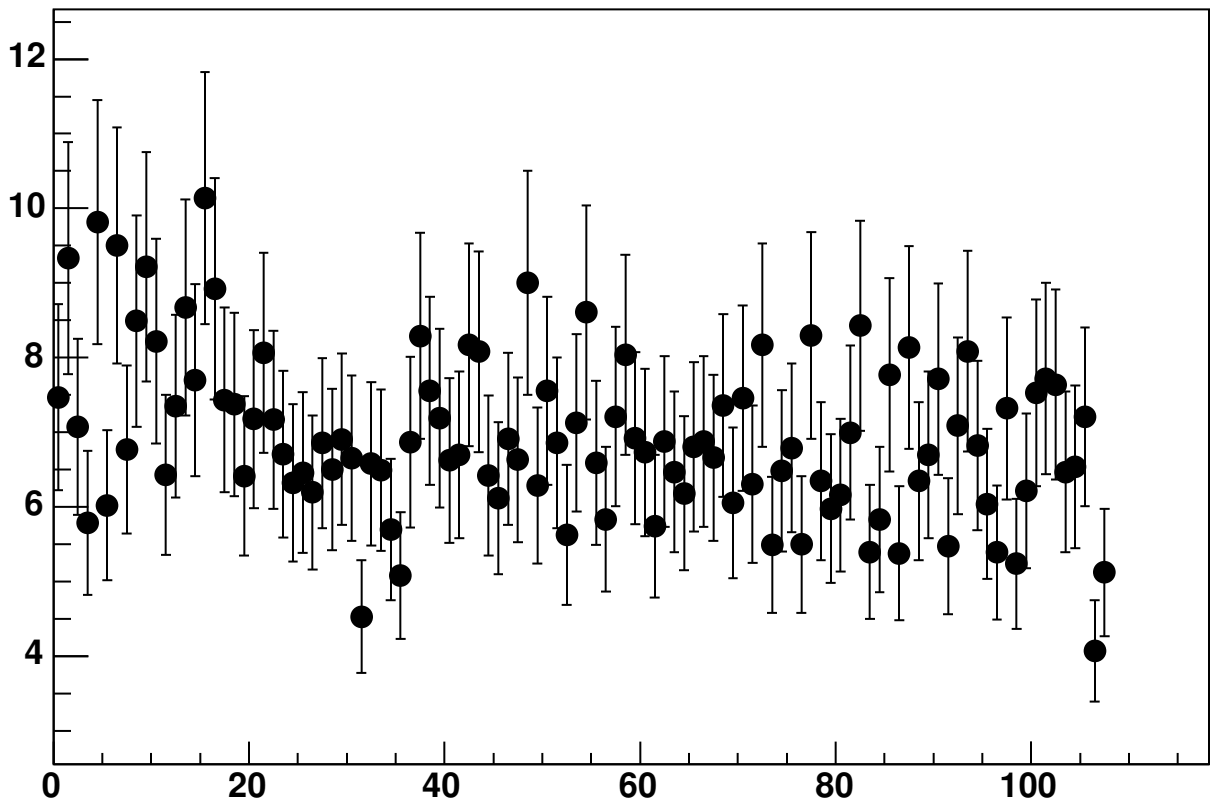
Enable 2, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



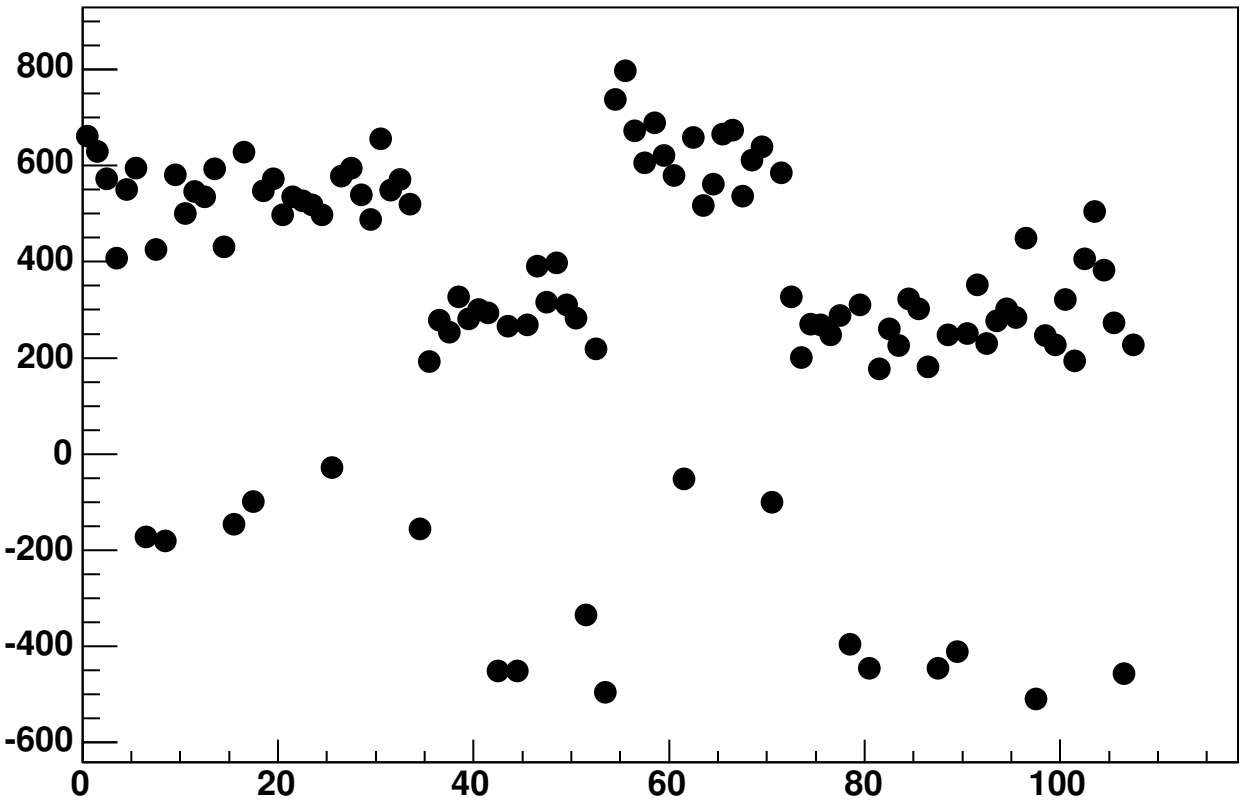
Enable 2, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



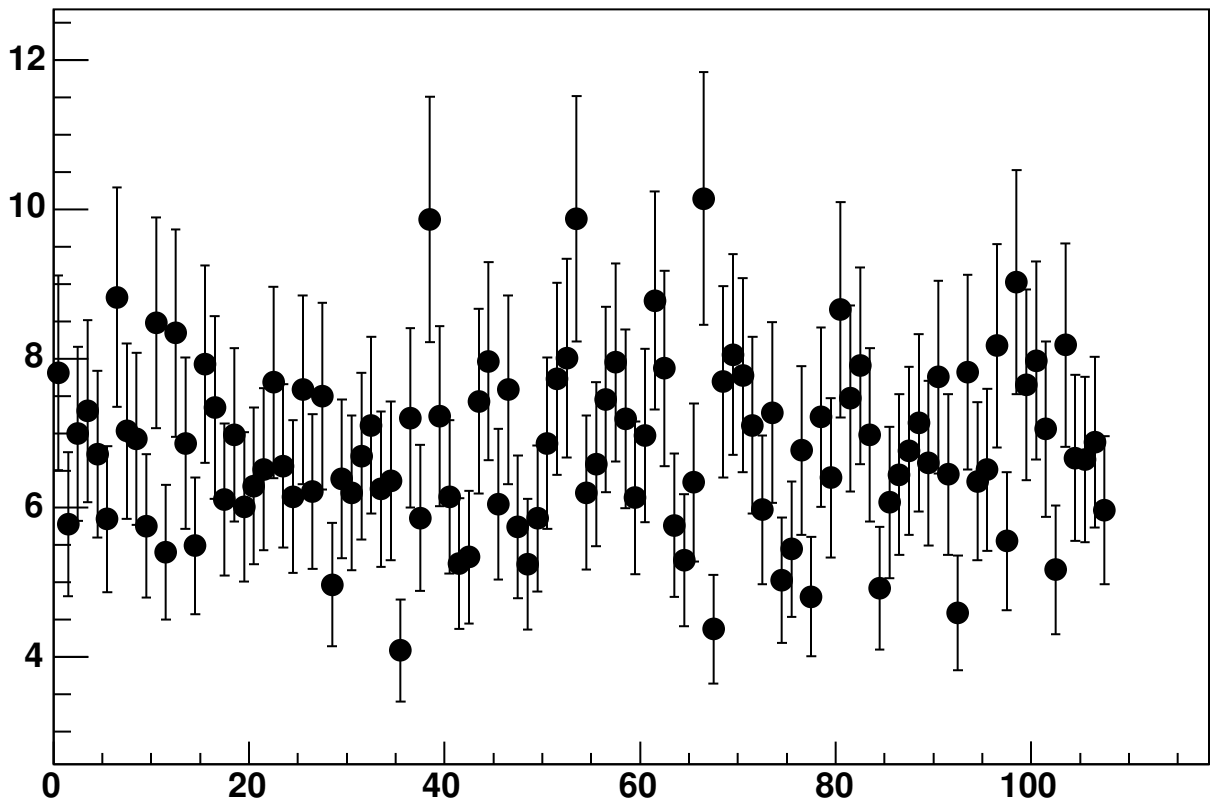
Enable 2, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



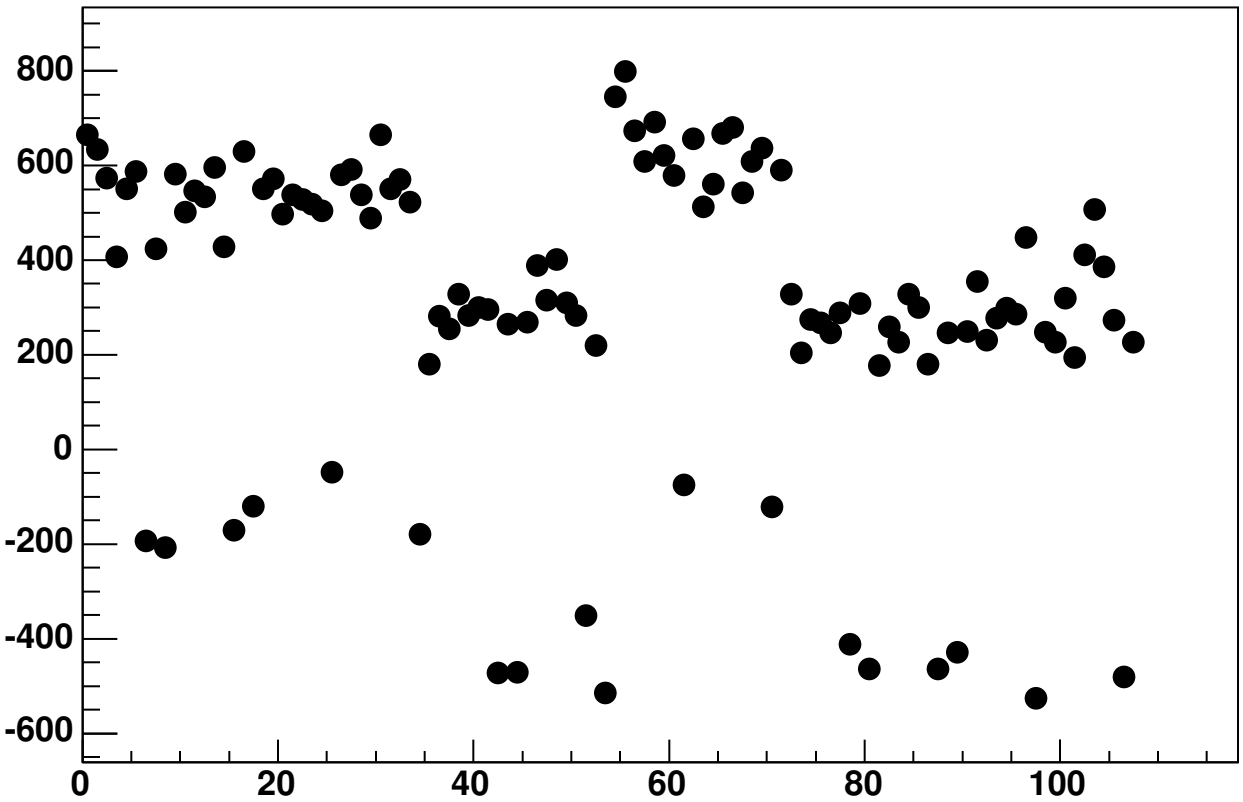
Enable 2, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



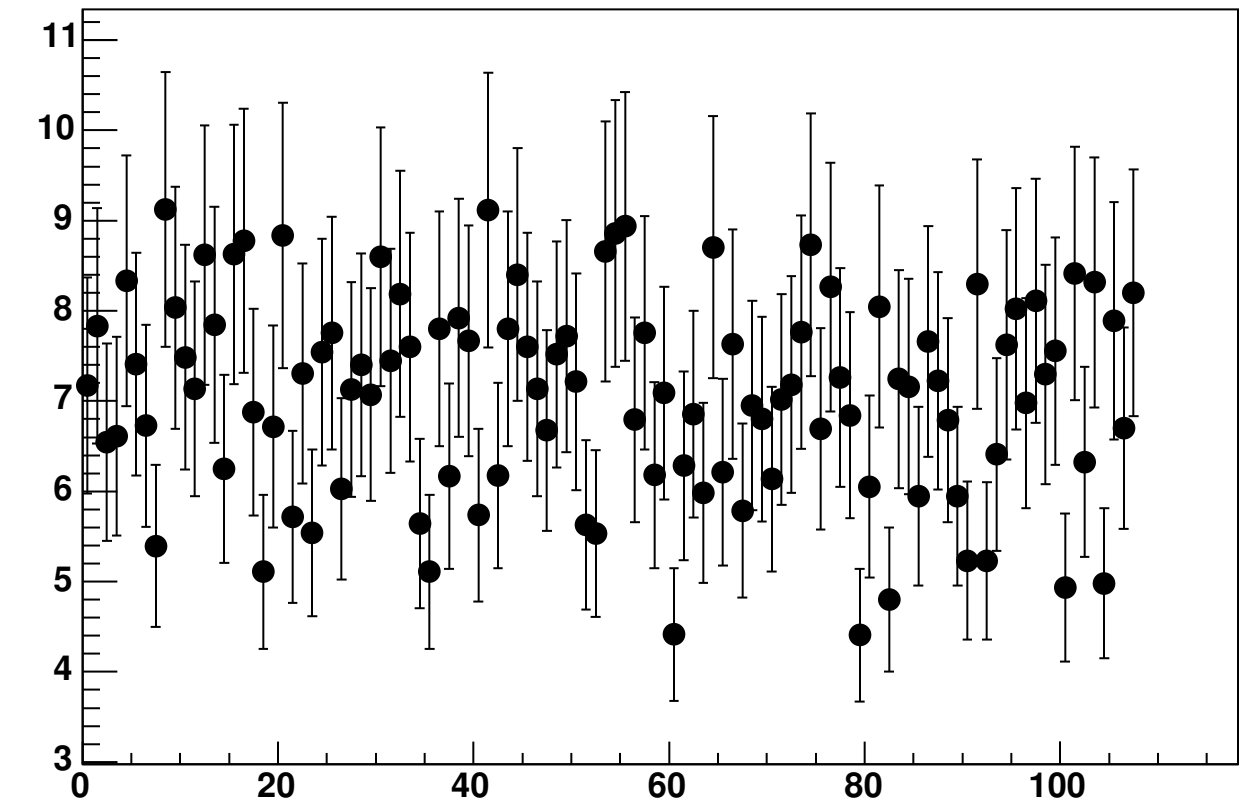
Enable 2, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



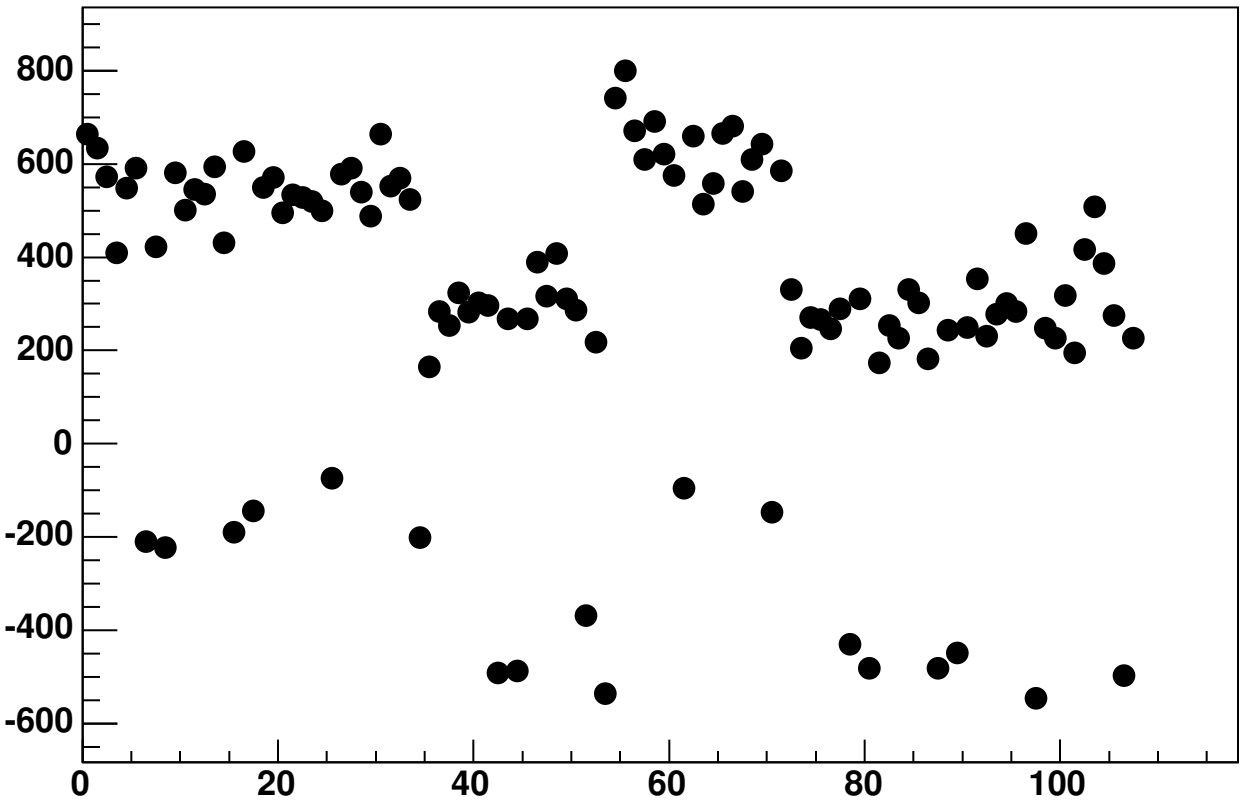
Enable 2, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



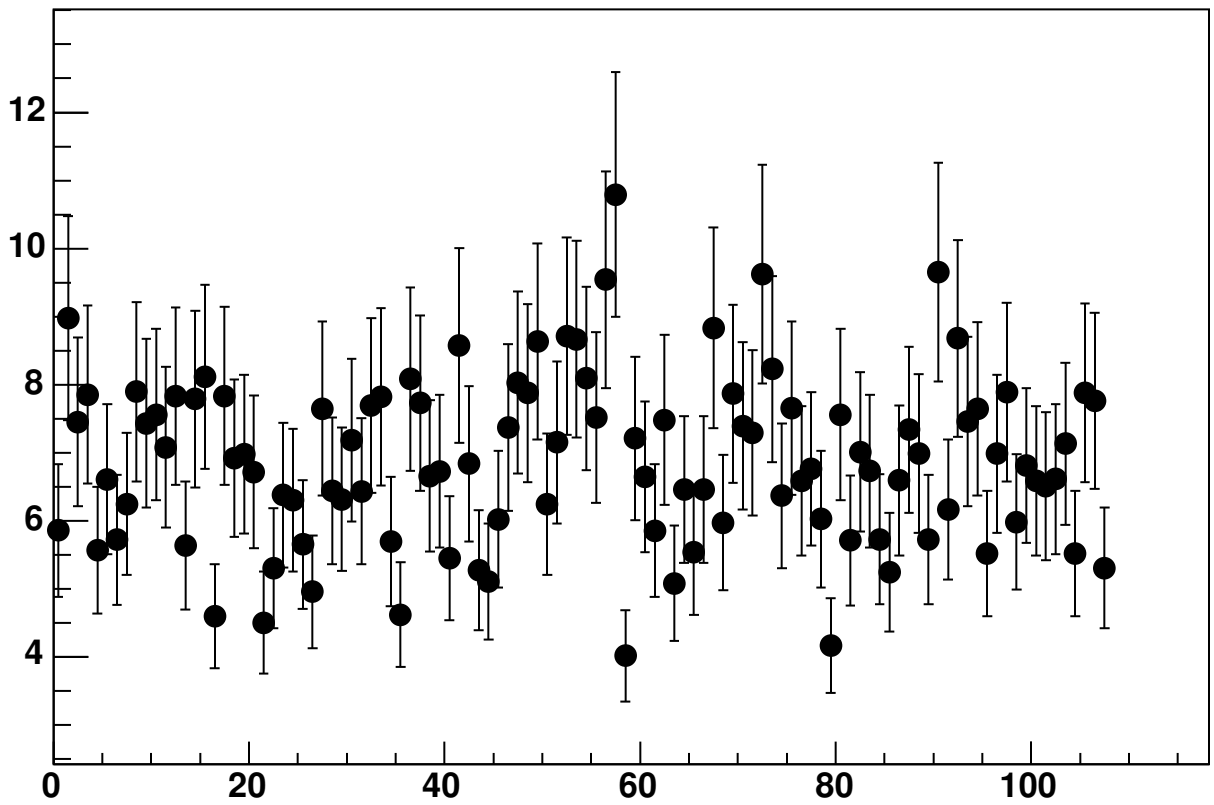
Enable 2, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



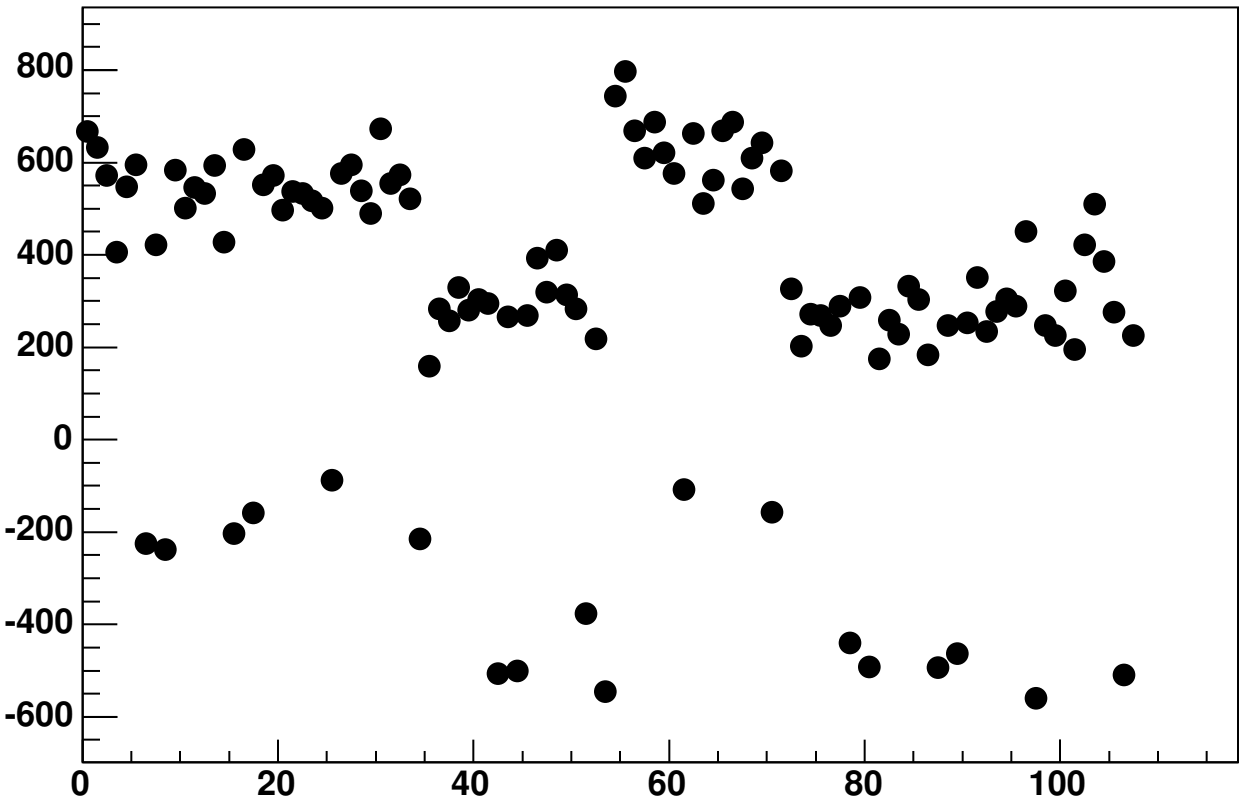
Enable 2, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



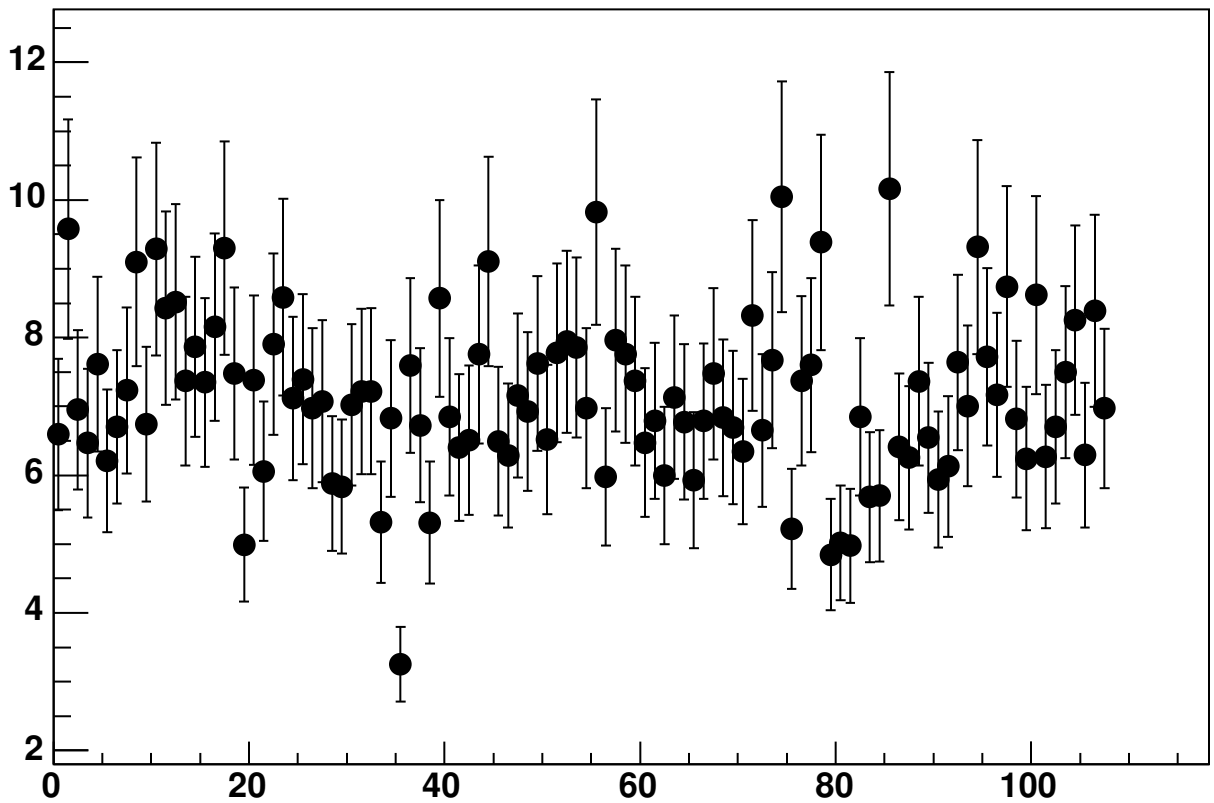
Enable 2, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



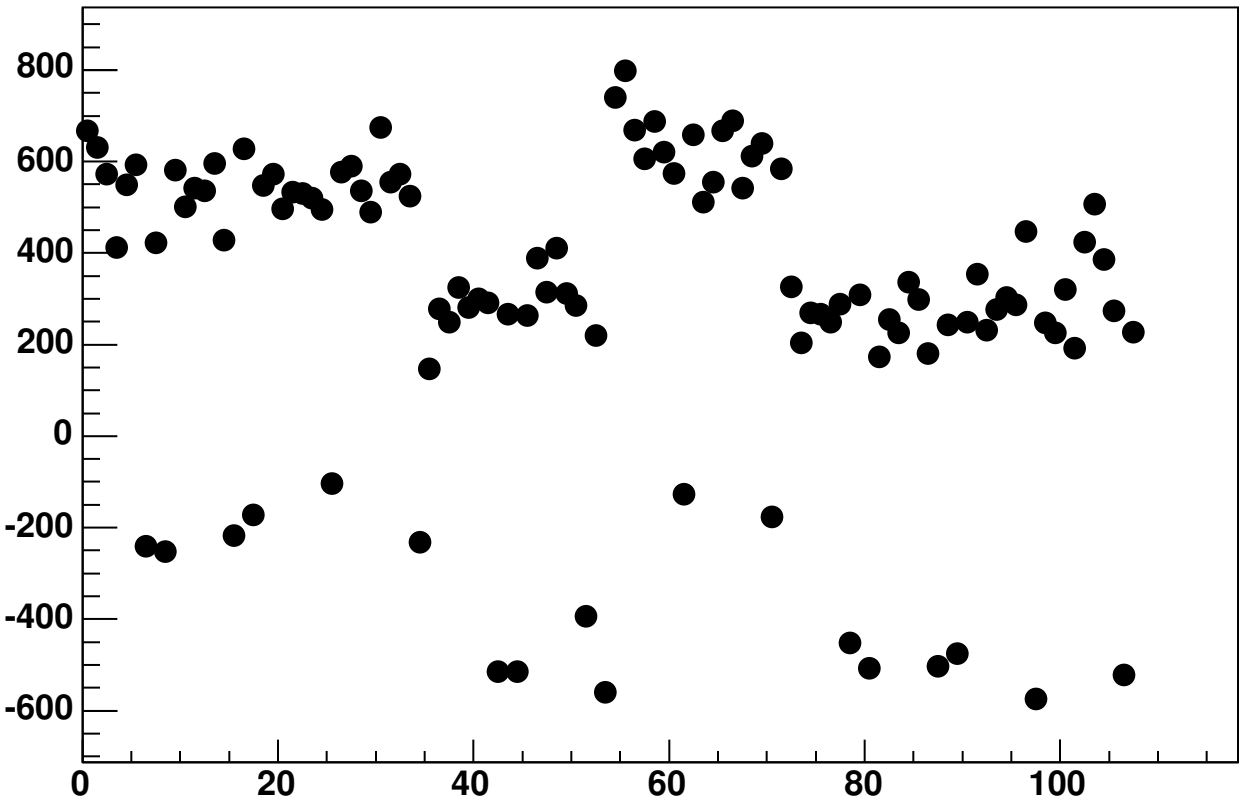
Enable 2, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



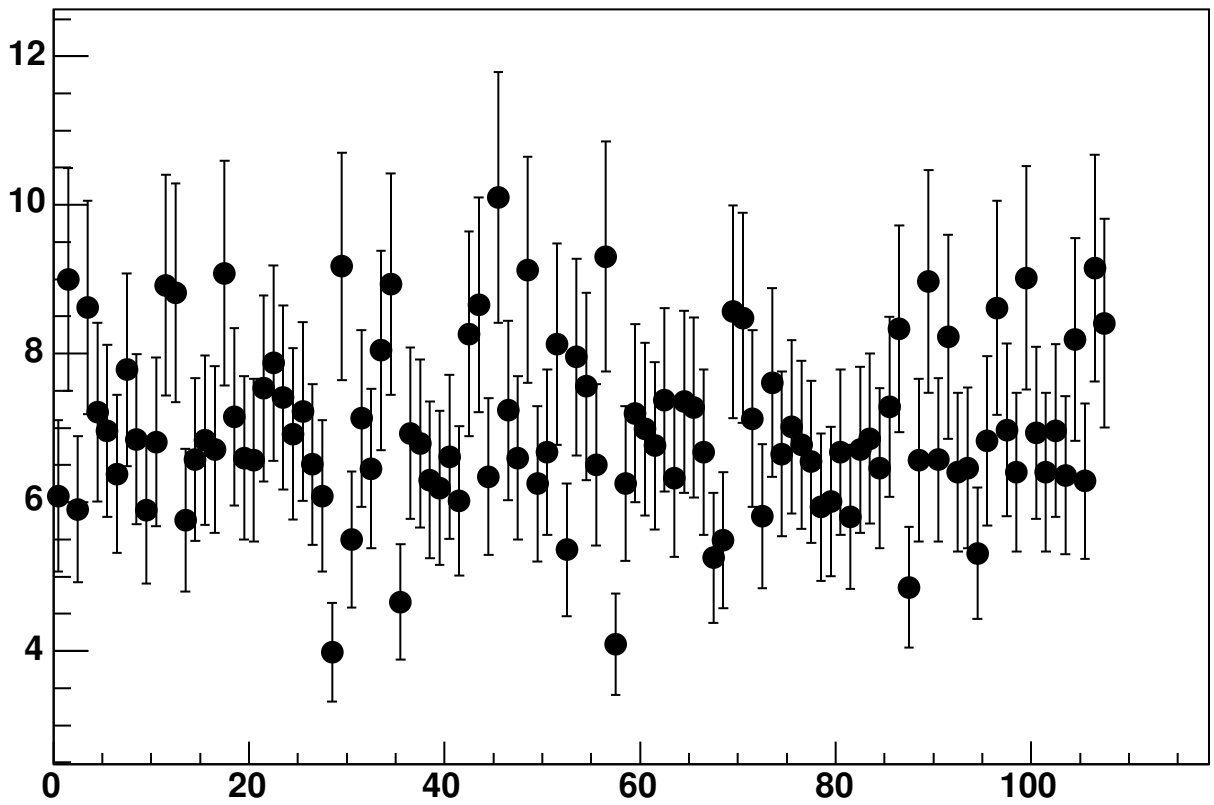
Enable 2, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan



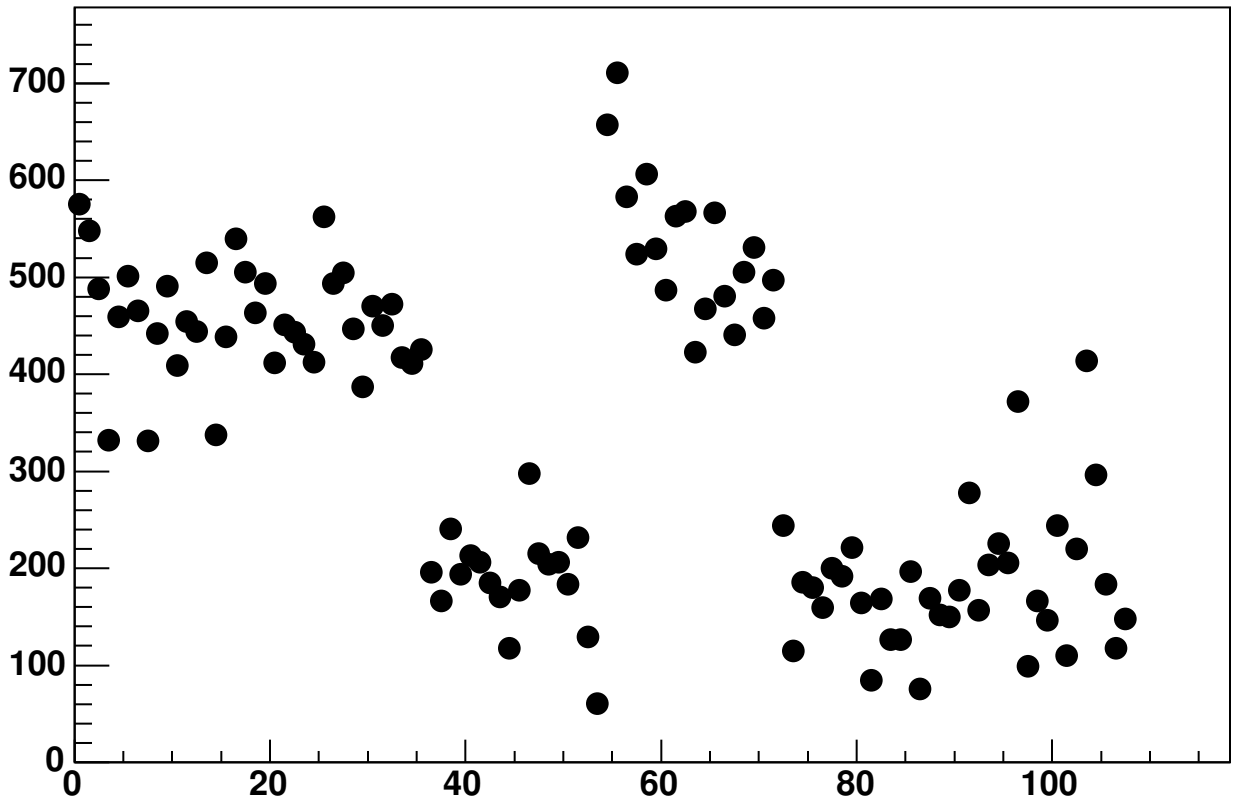
Enable 2, DAC=4000, Hold=245, ADC Mean vs 18*Chip+Chan



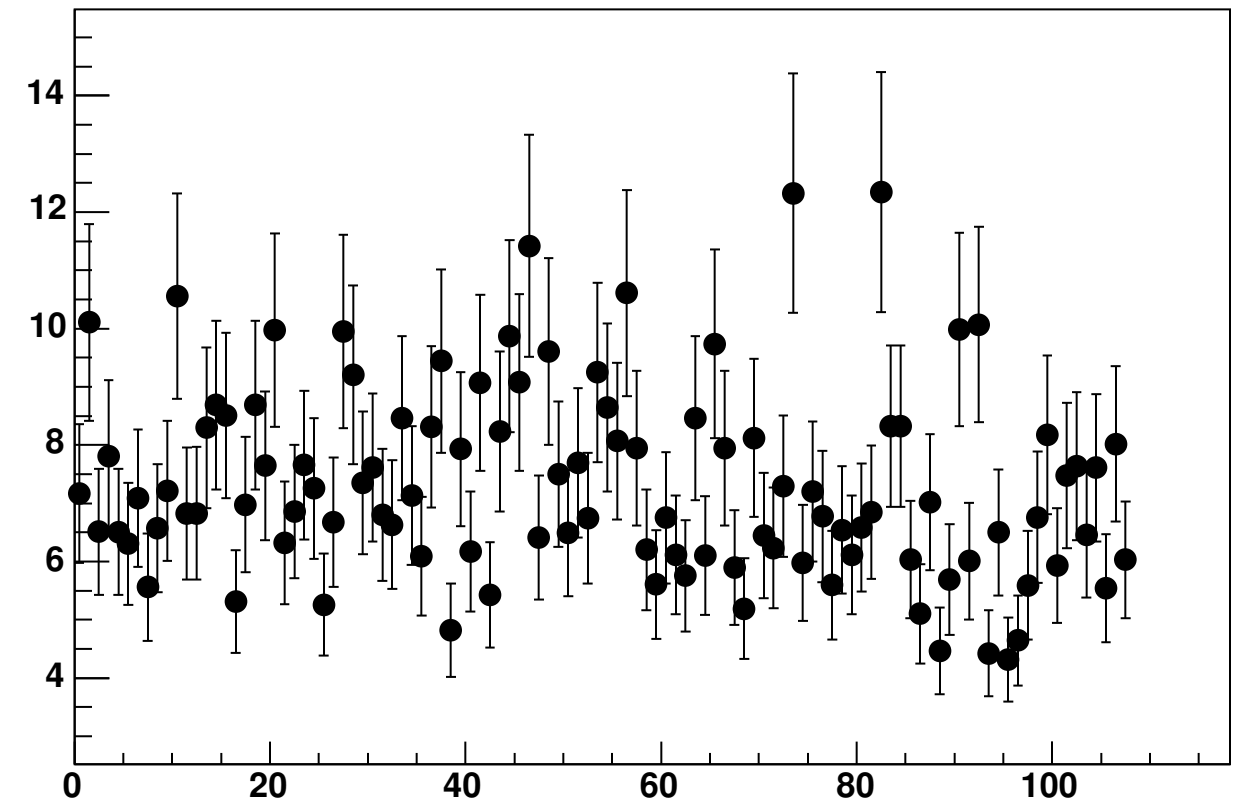
Enable 2, DAC=4000, Hold=245, ADC Noise vs 18*Chip+Chan



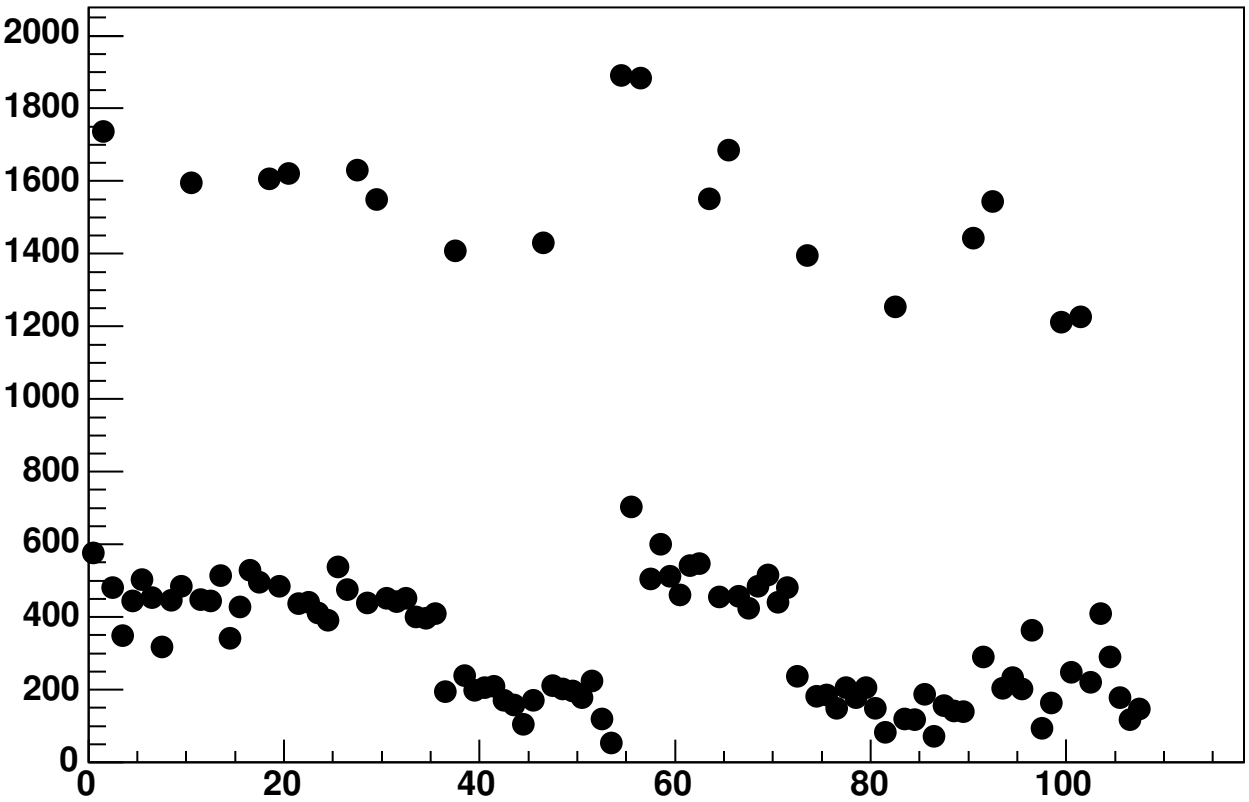
Enable 3, DAC=4000, Hold=0, ADC Mean vs 18*Chip+Chan



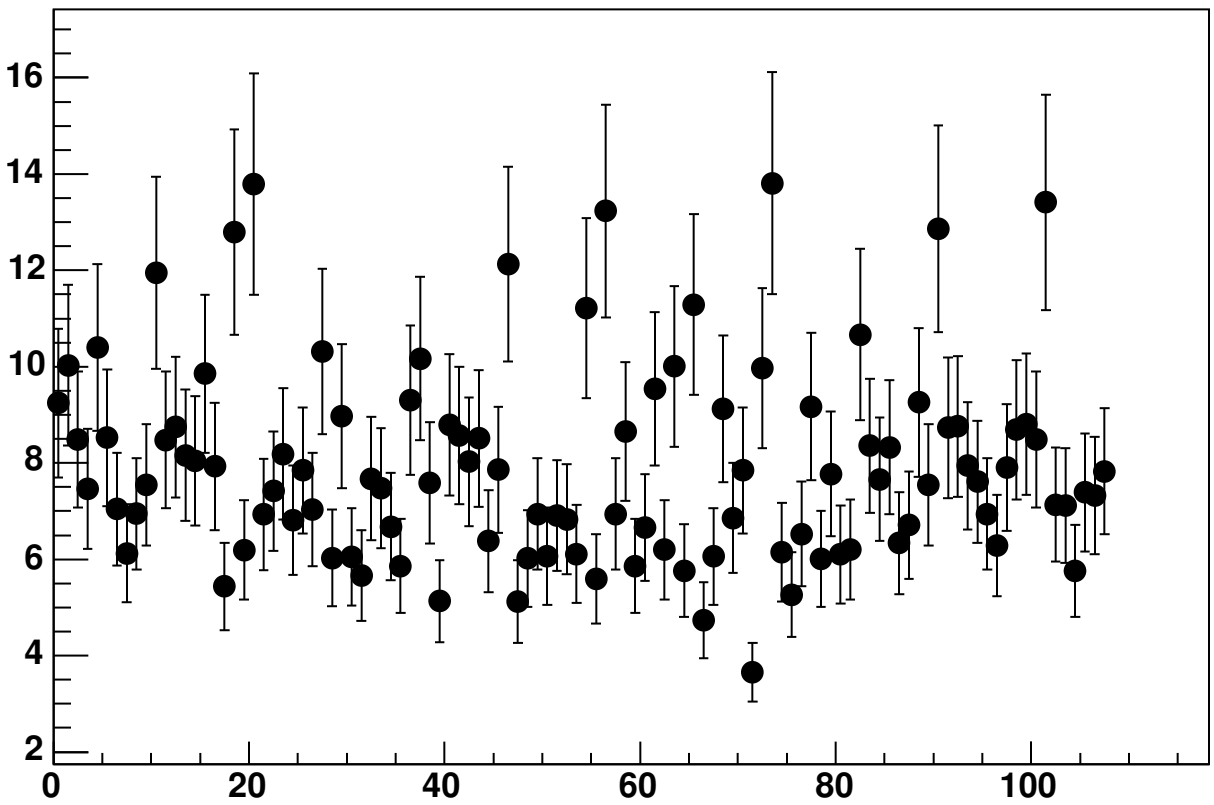
Enable 3, DAC=4000, Hold=0, ADC Noise vs 18*Chip+Chan



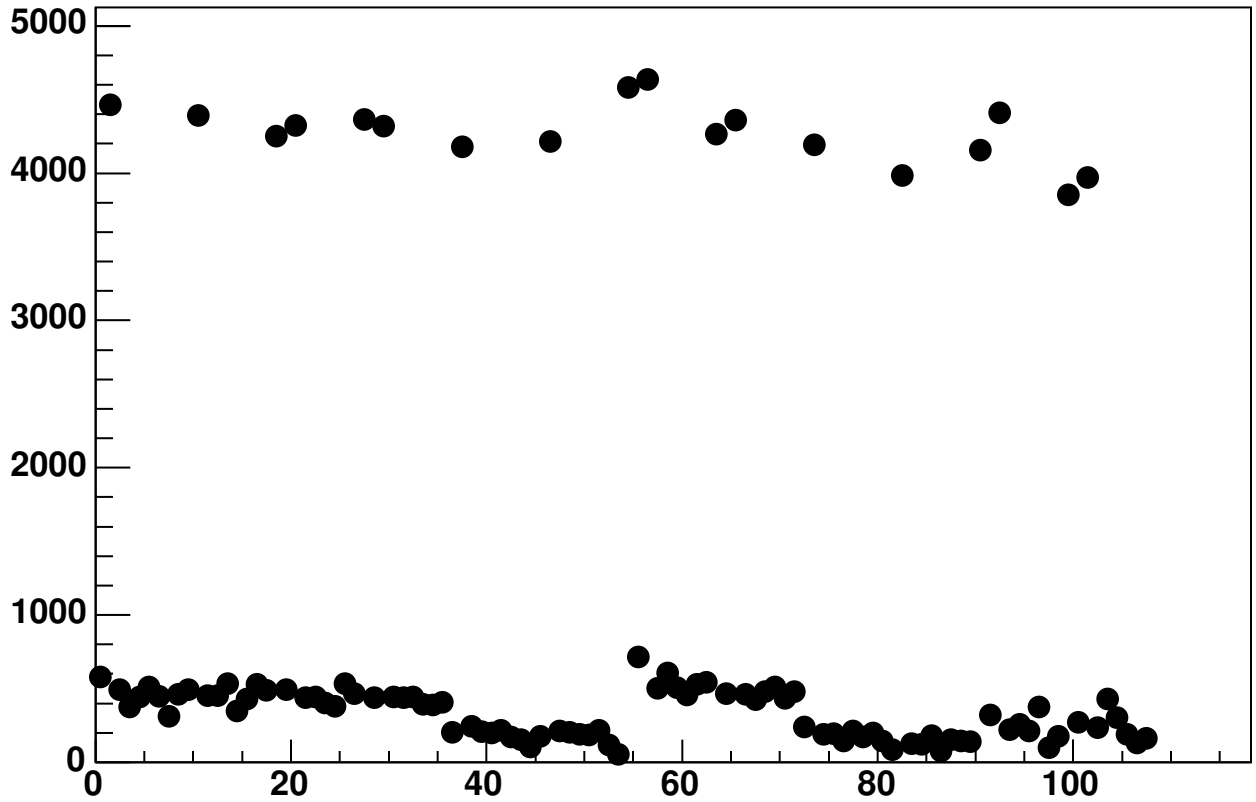
Enable 3, DAC=4000, Hold=5, ADC Mean vs 18*Chip+Chan



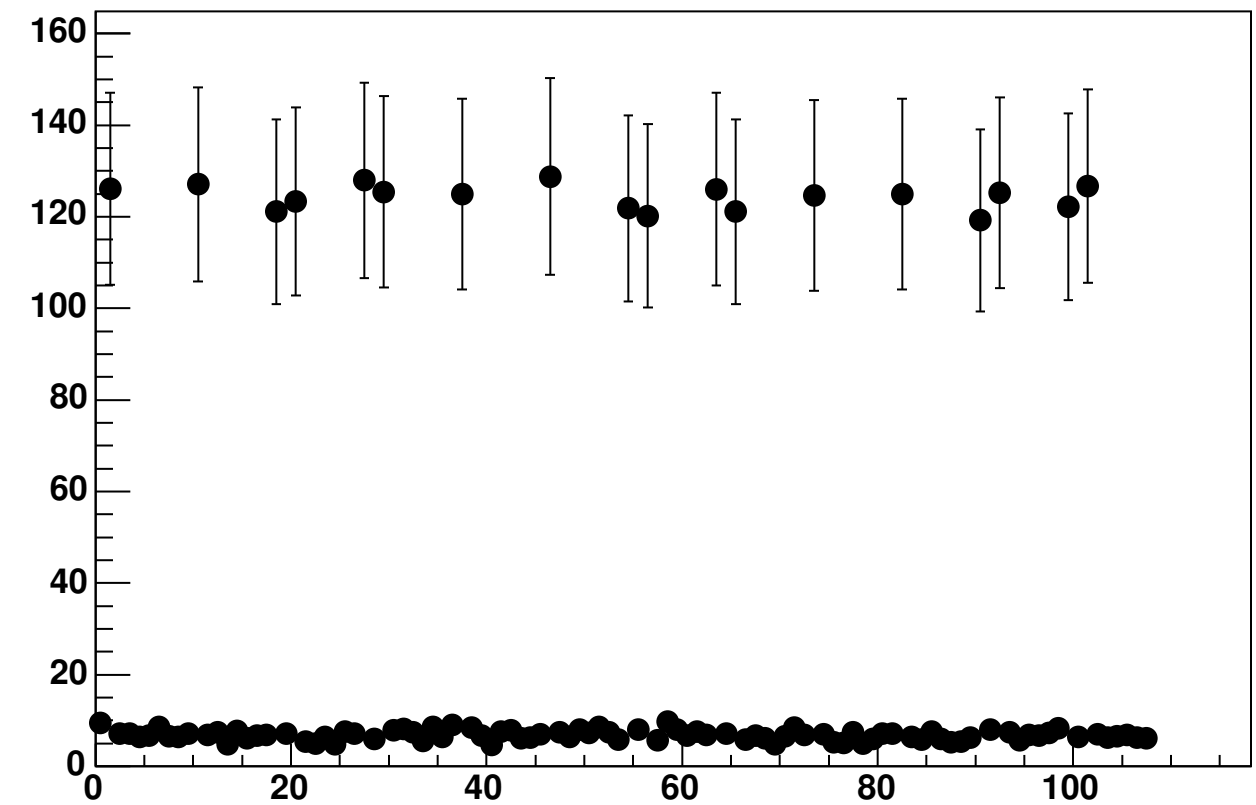
Enable 3, DAC=4000, Hold=5, ADC Noise vs 18*Chip+Chan



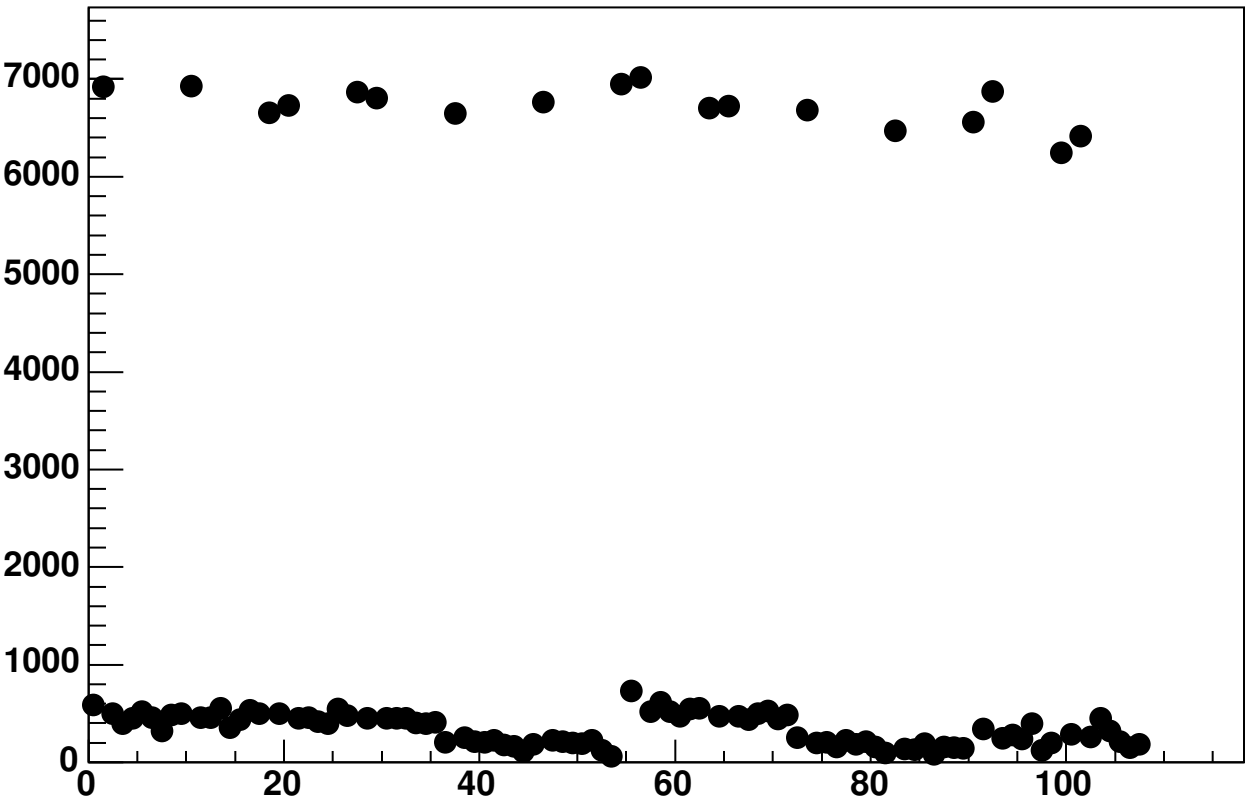
Enable 3, DAC=4000, Hold=10, ADC Mean vs 18*Chip+Chan



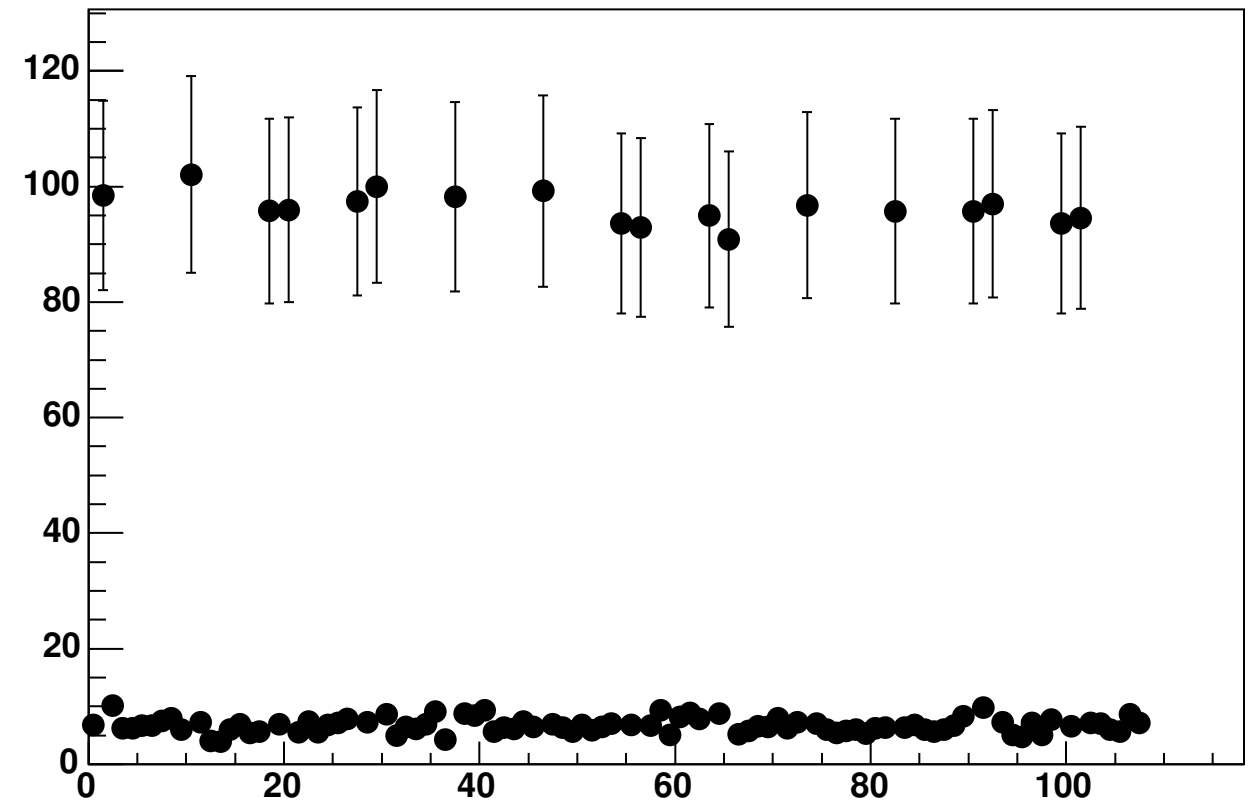
Enable 3, DAC=4000, Hold=10, ADC Noise vs 18*Chip+Chan



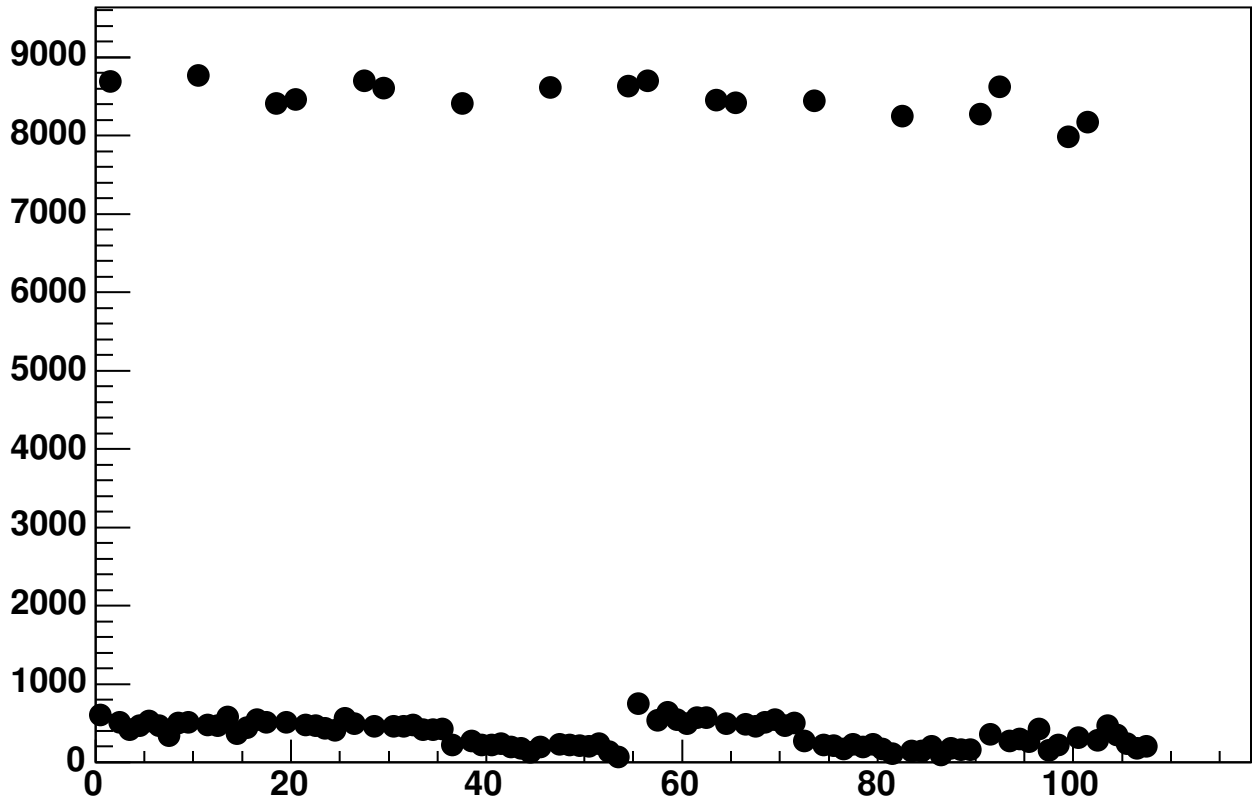
Enable 3, DAC=4000, Hold=15, ADC Mean vs 18*Chip+Chan



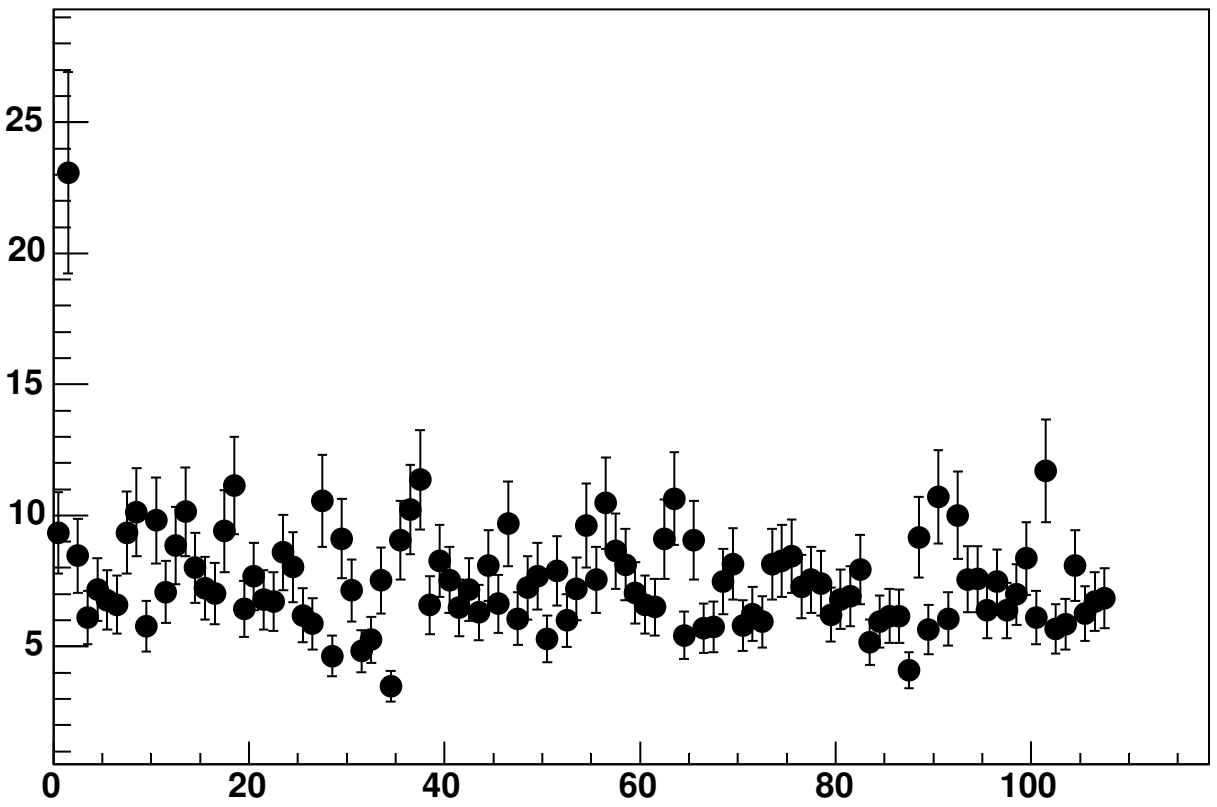
Enable 3, DAC=4000, Hold=15, ADC Noise vs 18*Chip+Chan



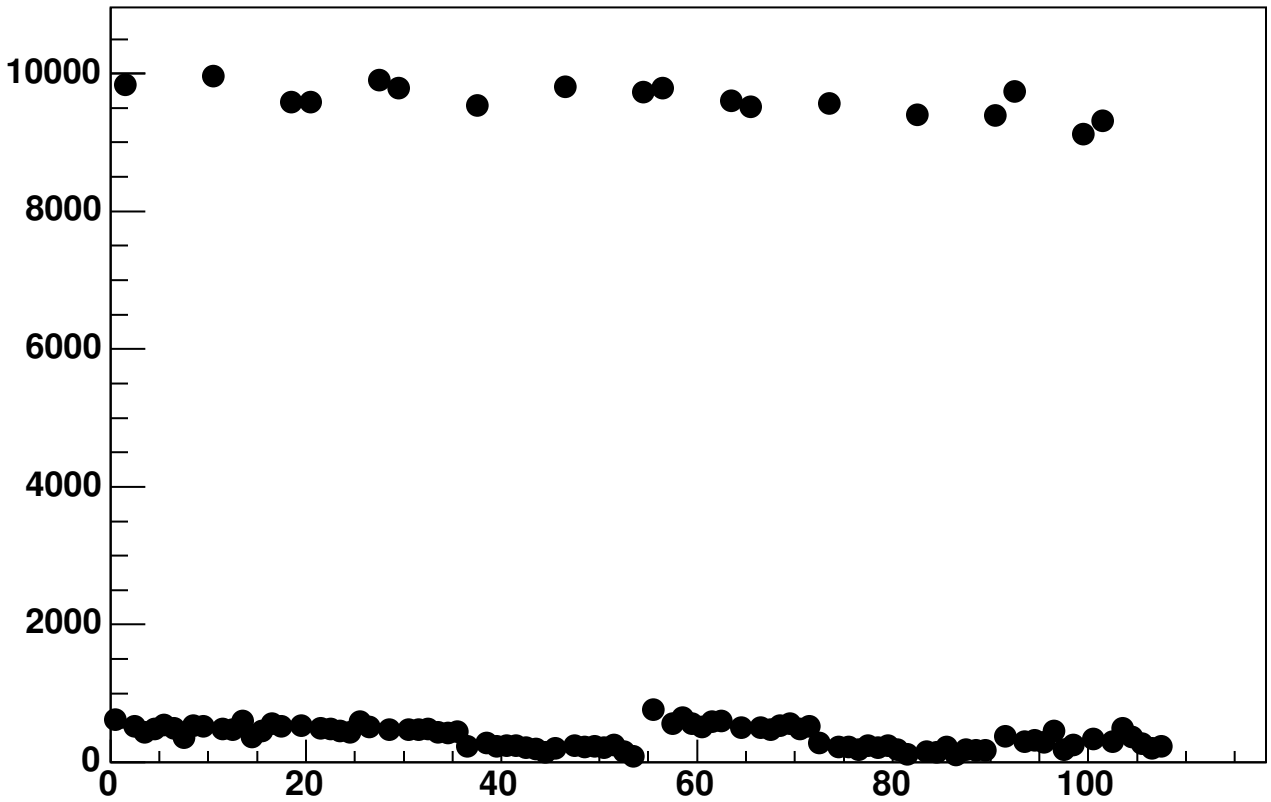
Enable 3, DAC=4000, Hold=20, ADC Mean vs 18*Chip+Chan



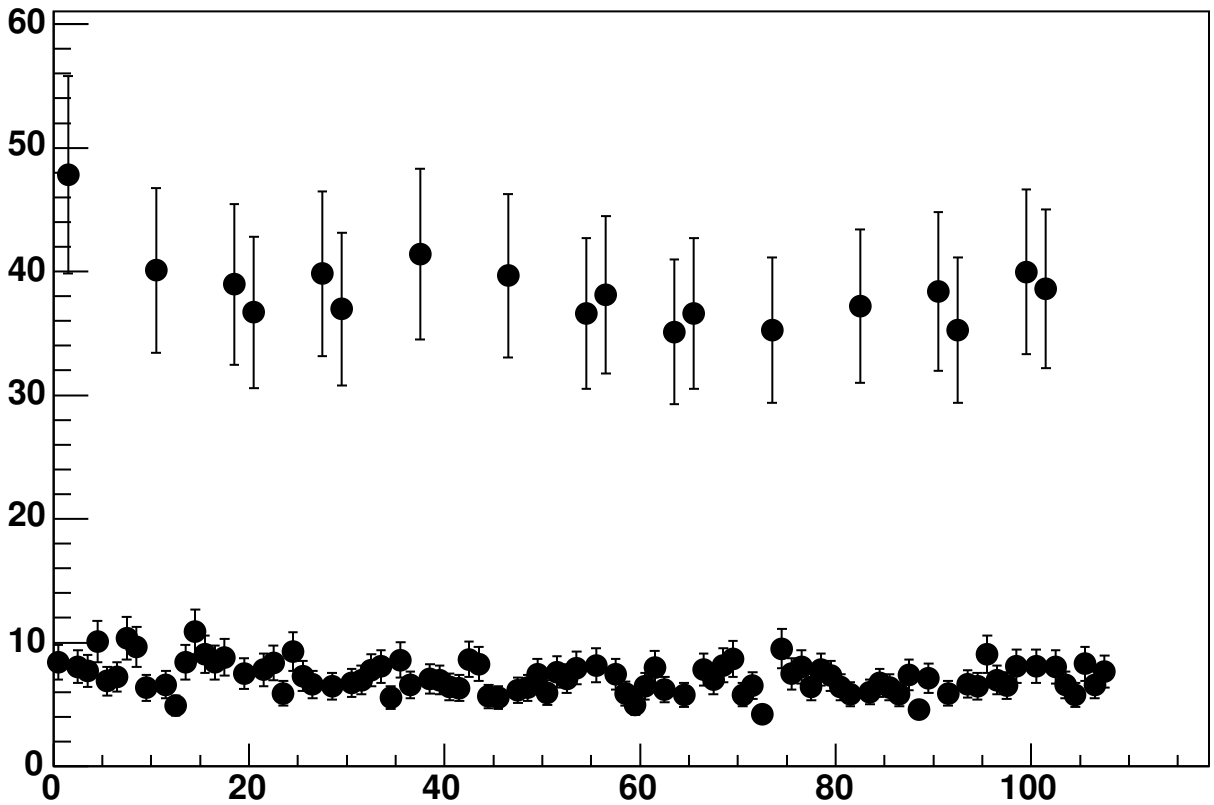
Enable 3, DAC=4000, Hold=20, ADC Noise vs 18*Chip+Chan



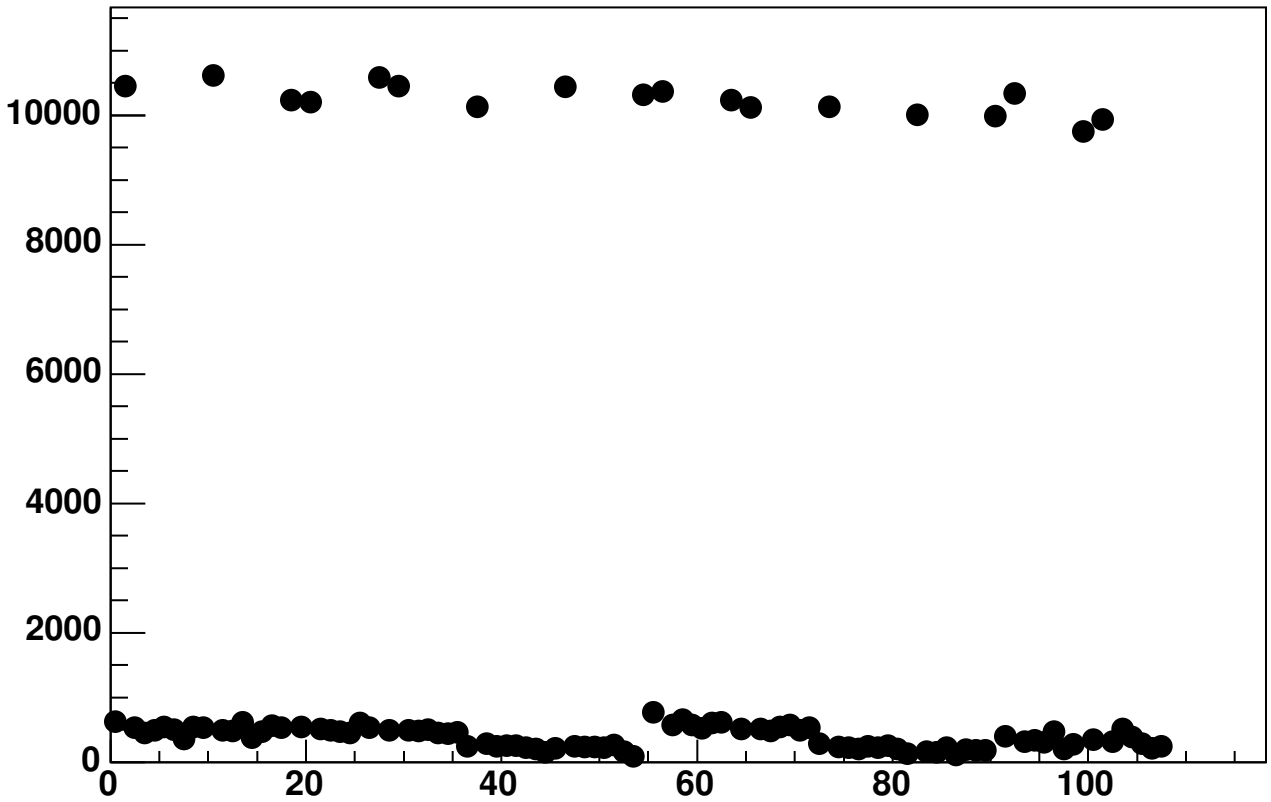
Enable 3, DAC=4000, Hold=25, ADC Mean vs 18*Chip+Chan



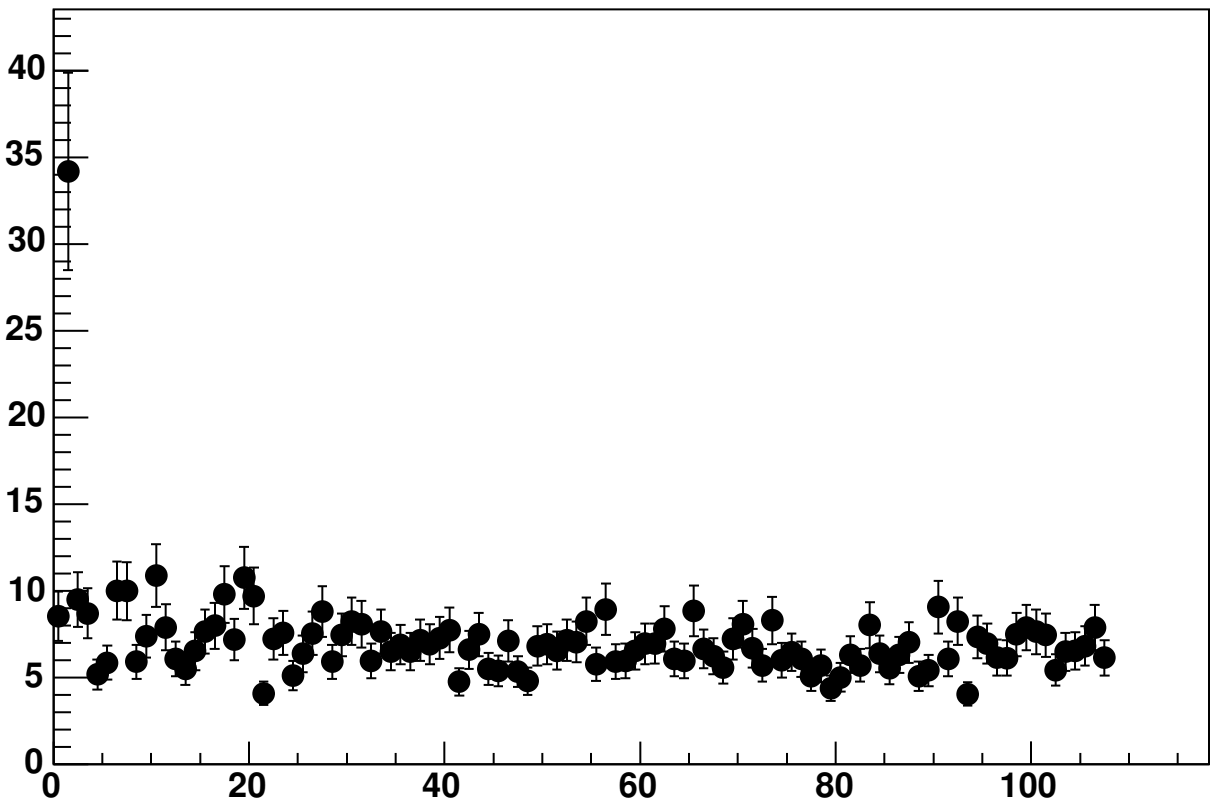
Enable 3, DAC=4000, Hold=25, ADC Noise vs 18*Chip+Chan



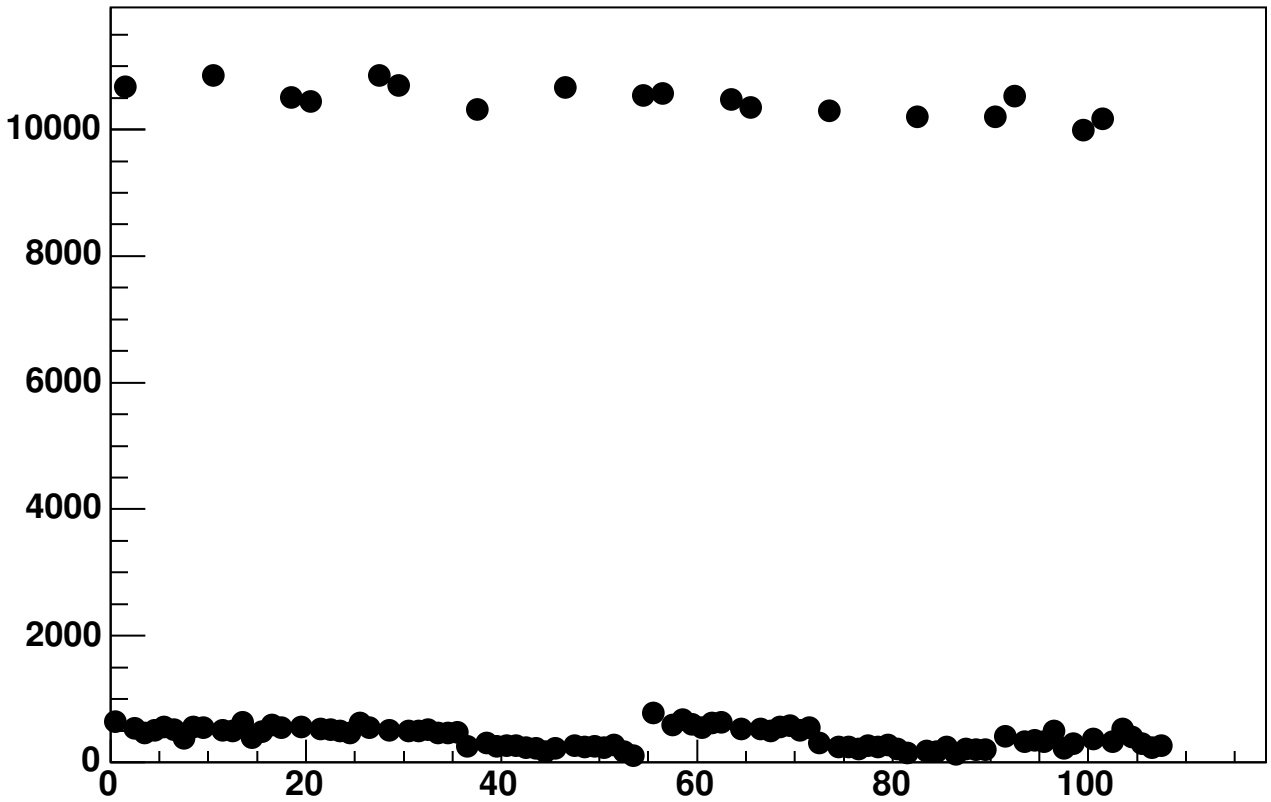
Enable 3, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



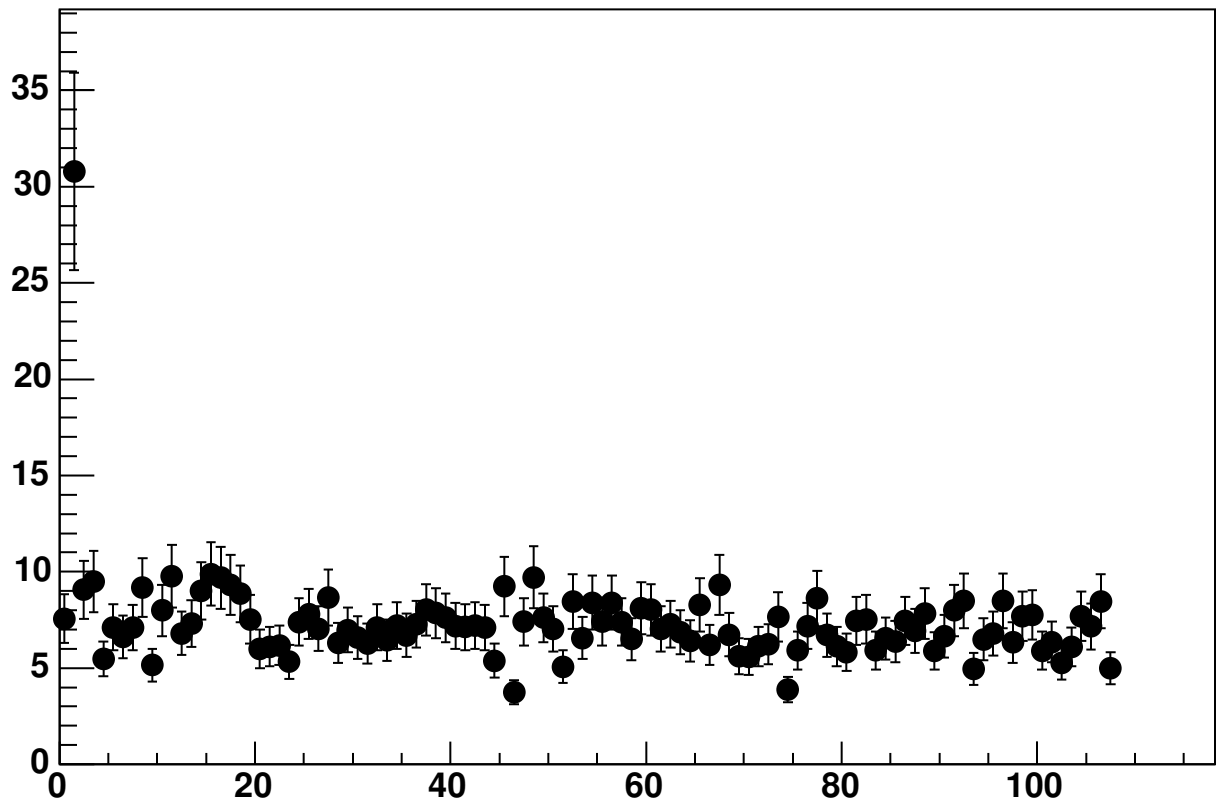
Enable 3, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



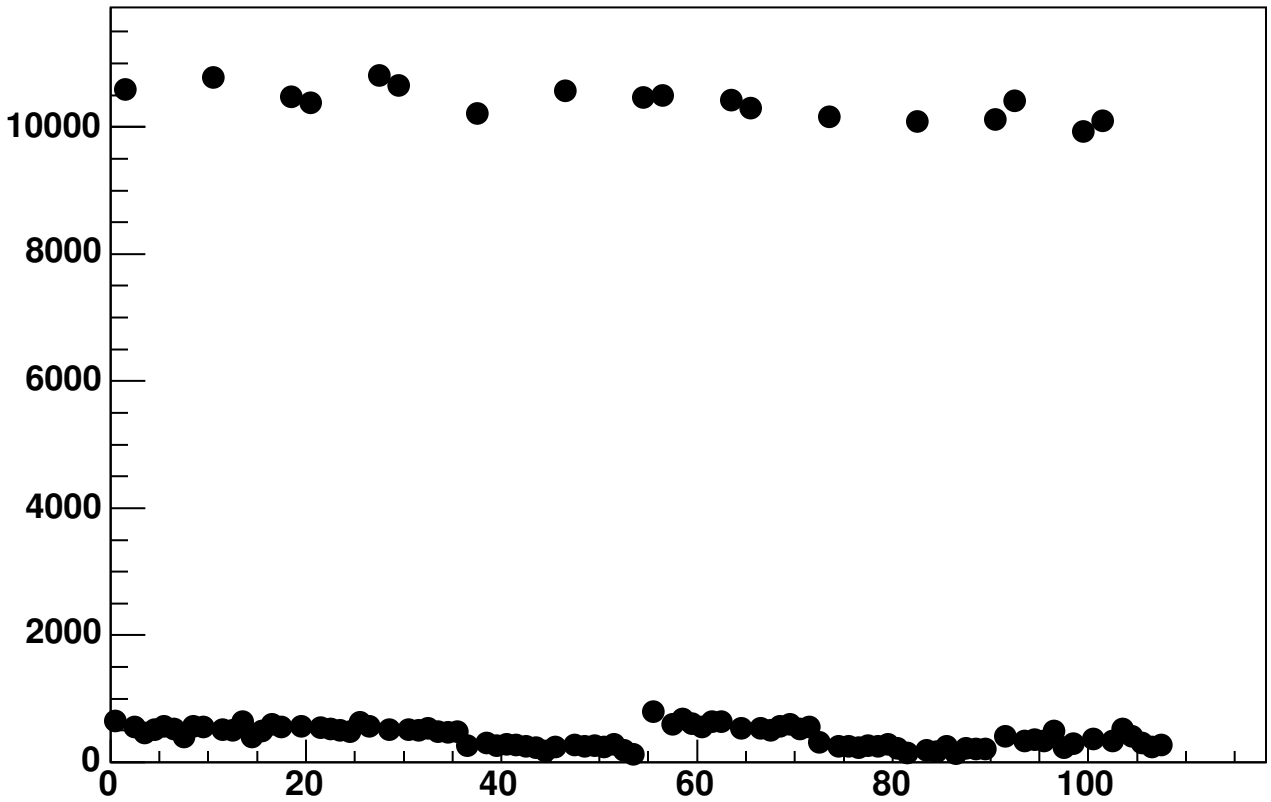
Enable 3, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



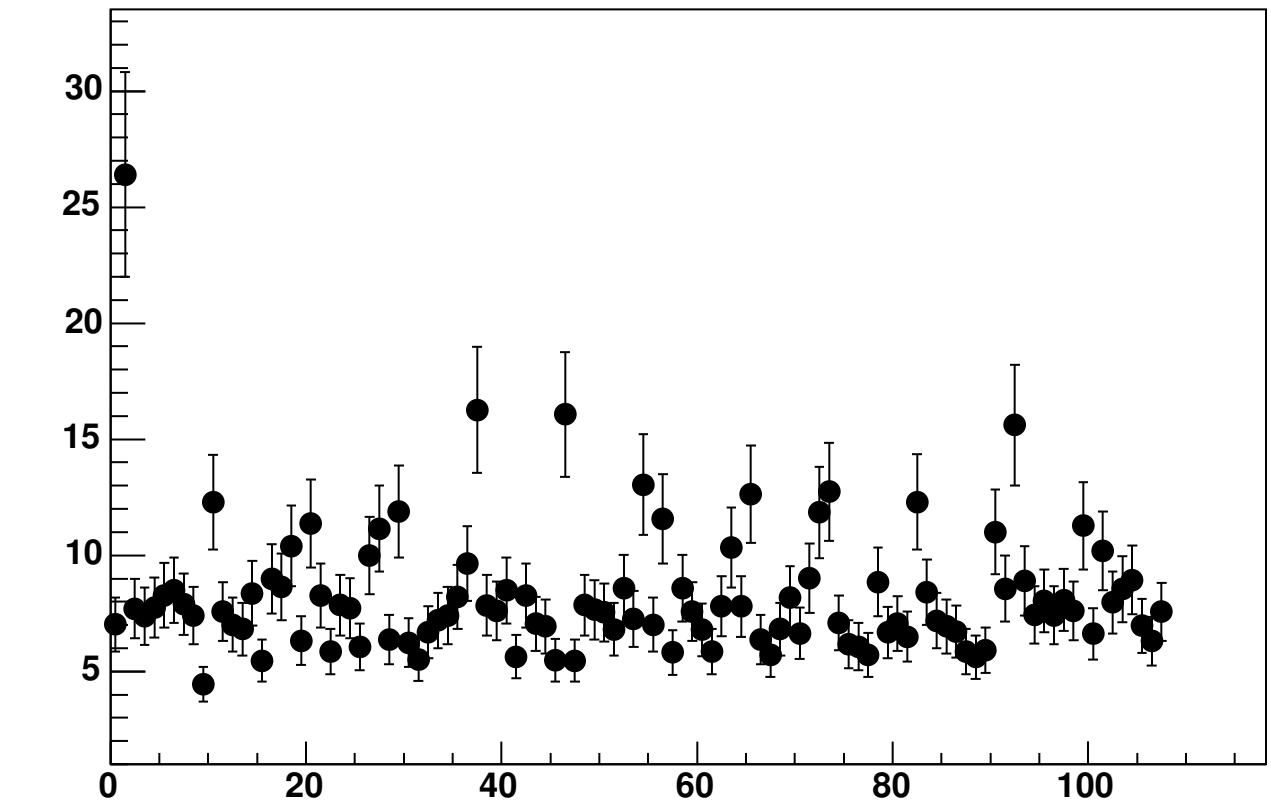
Enable 3, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



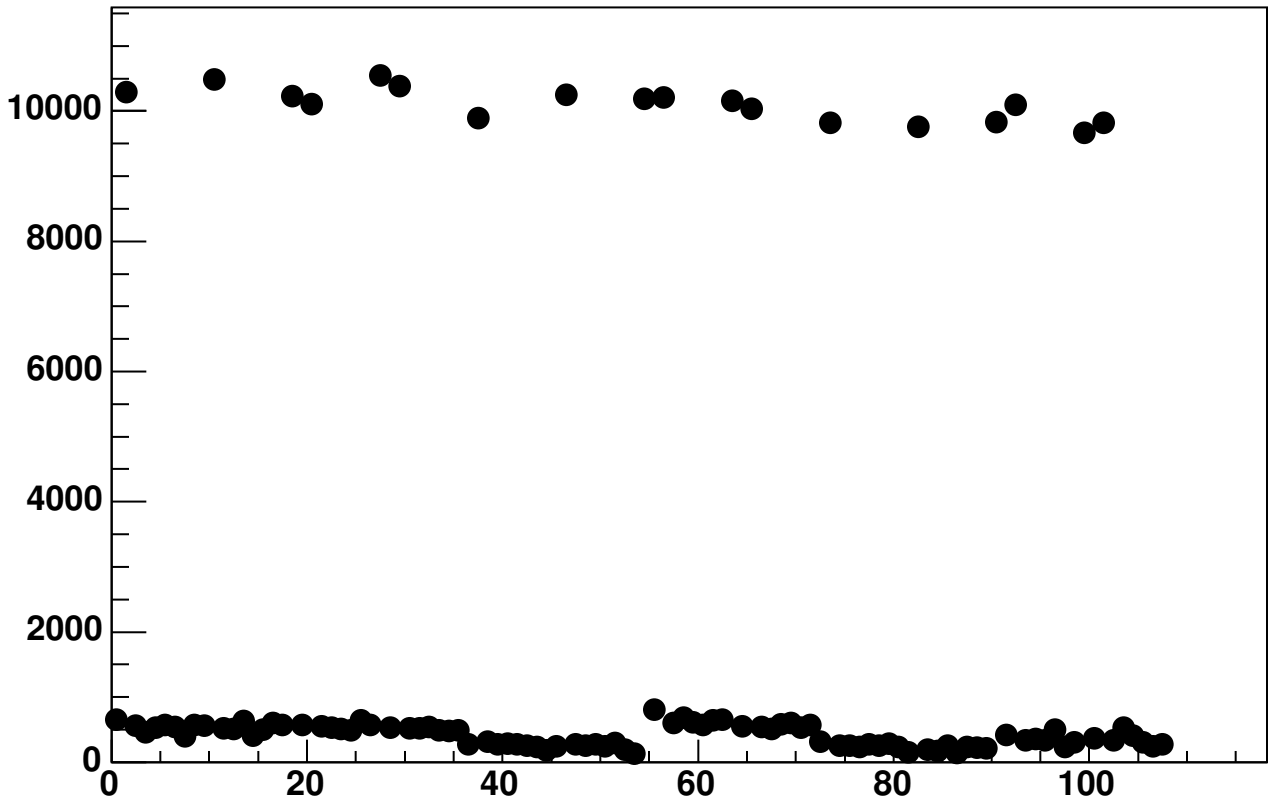
Enable 3, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



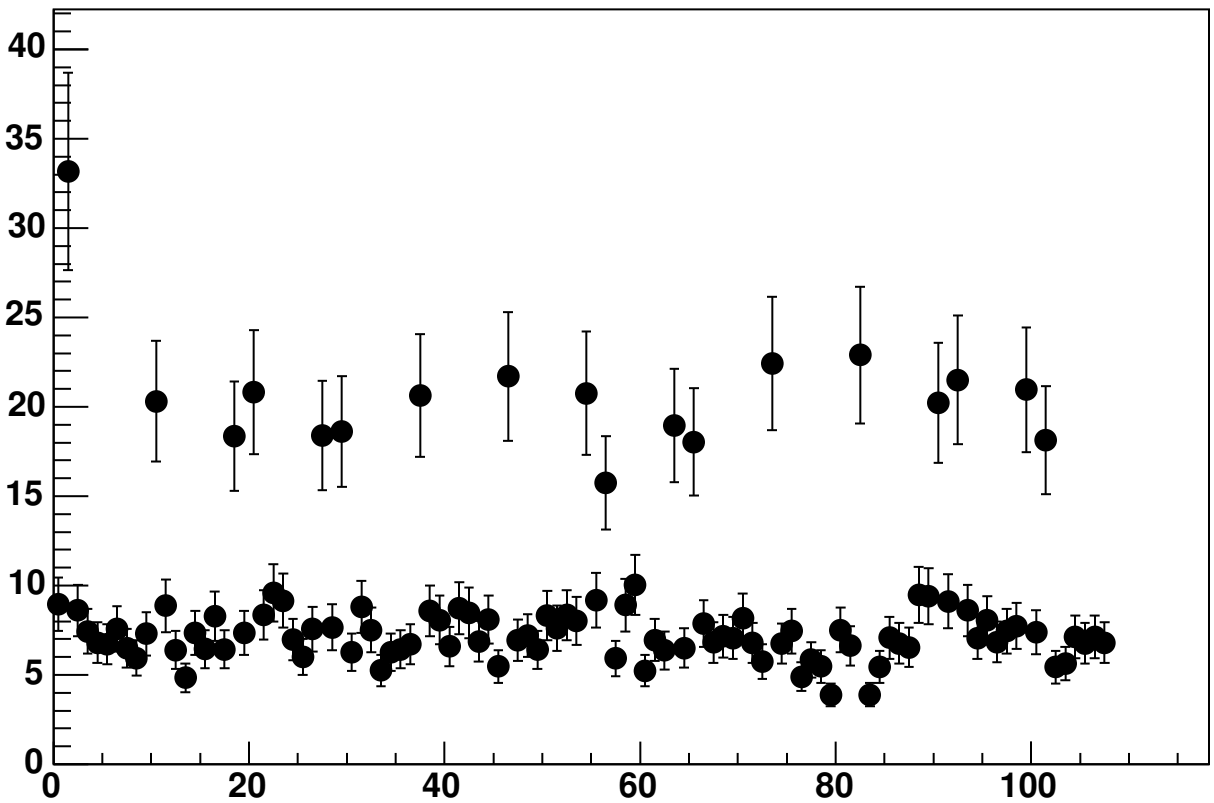
Enable 3, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



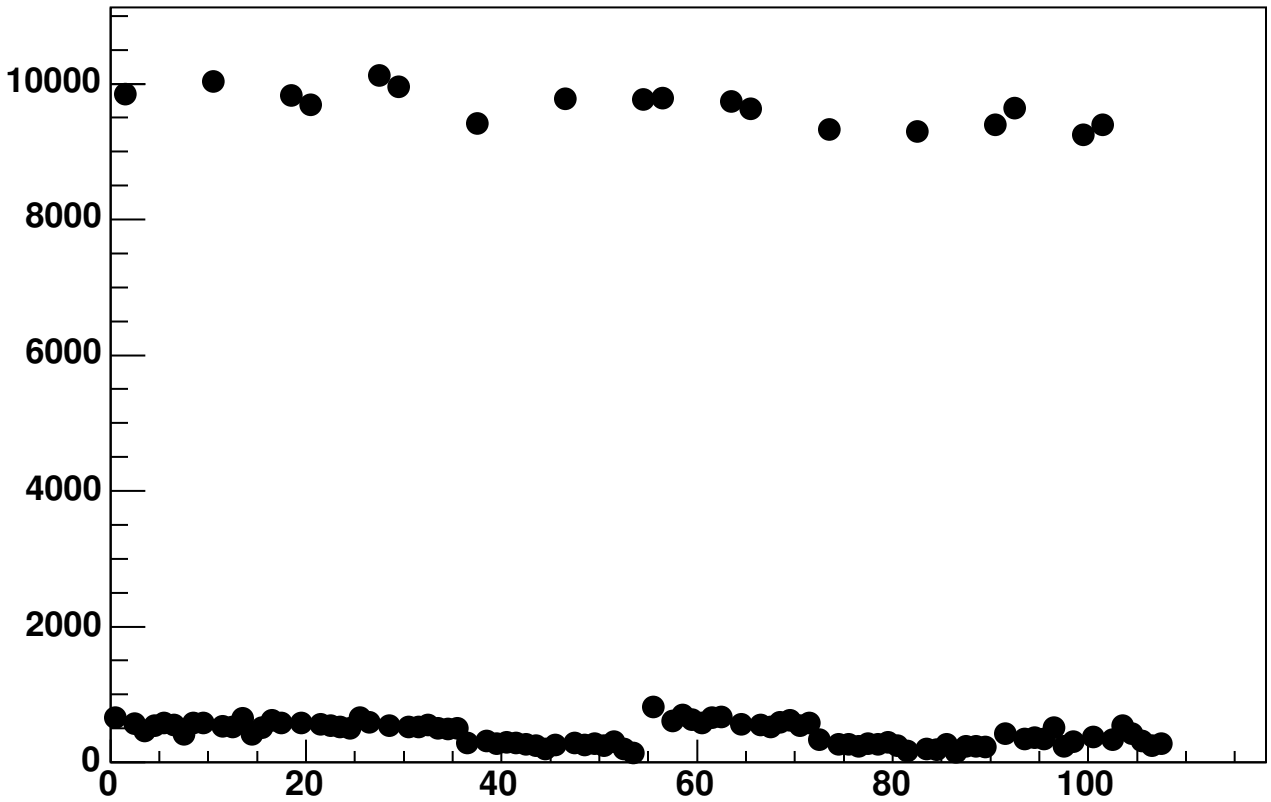
Enable 3, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



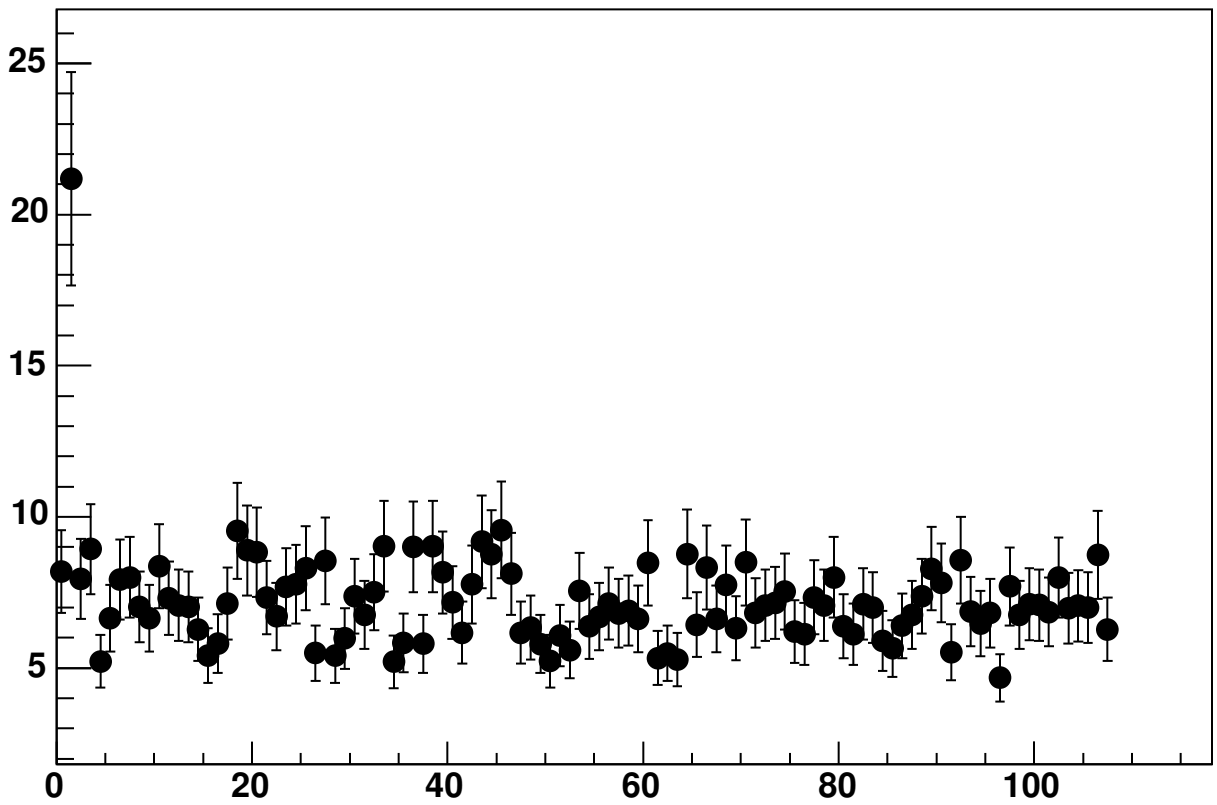
Enable 3, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



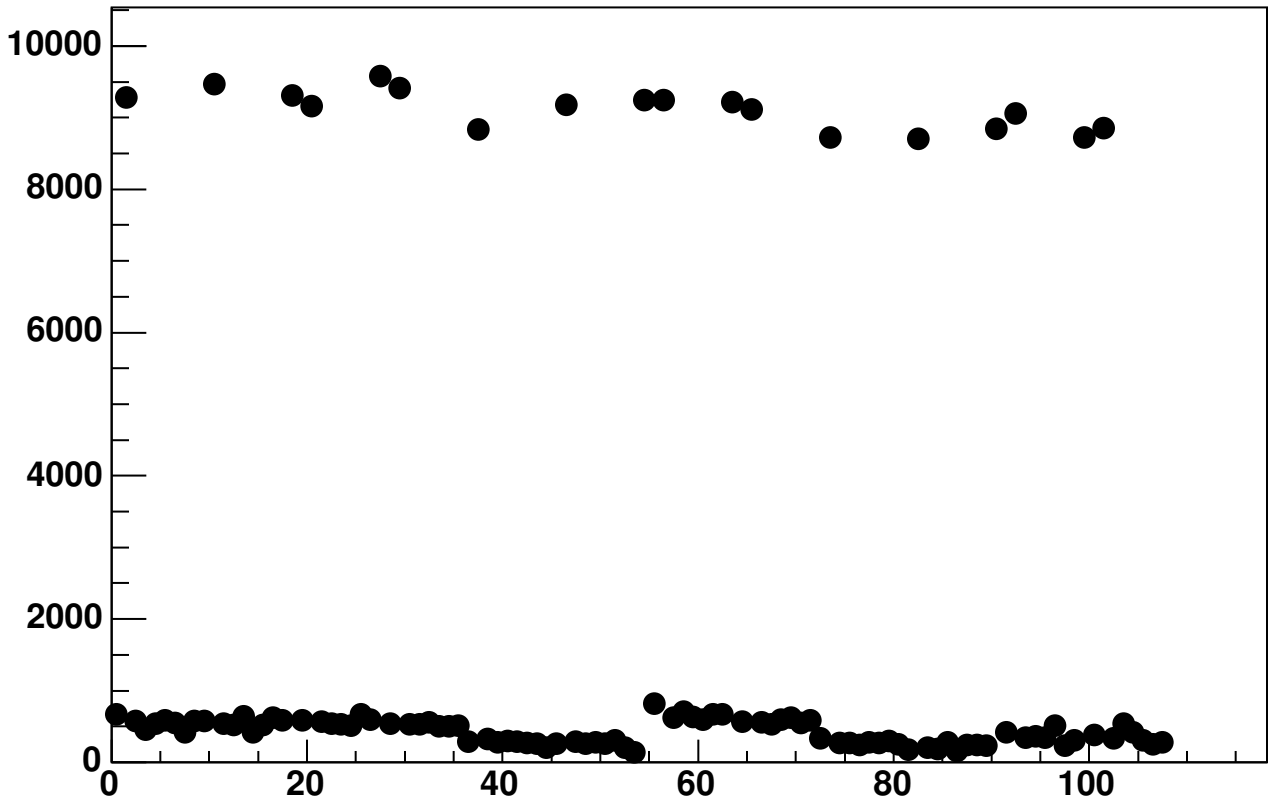
Enable 3, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



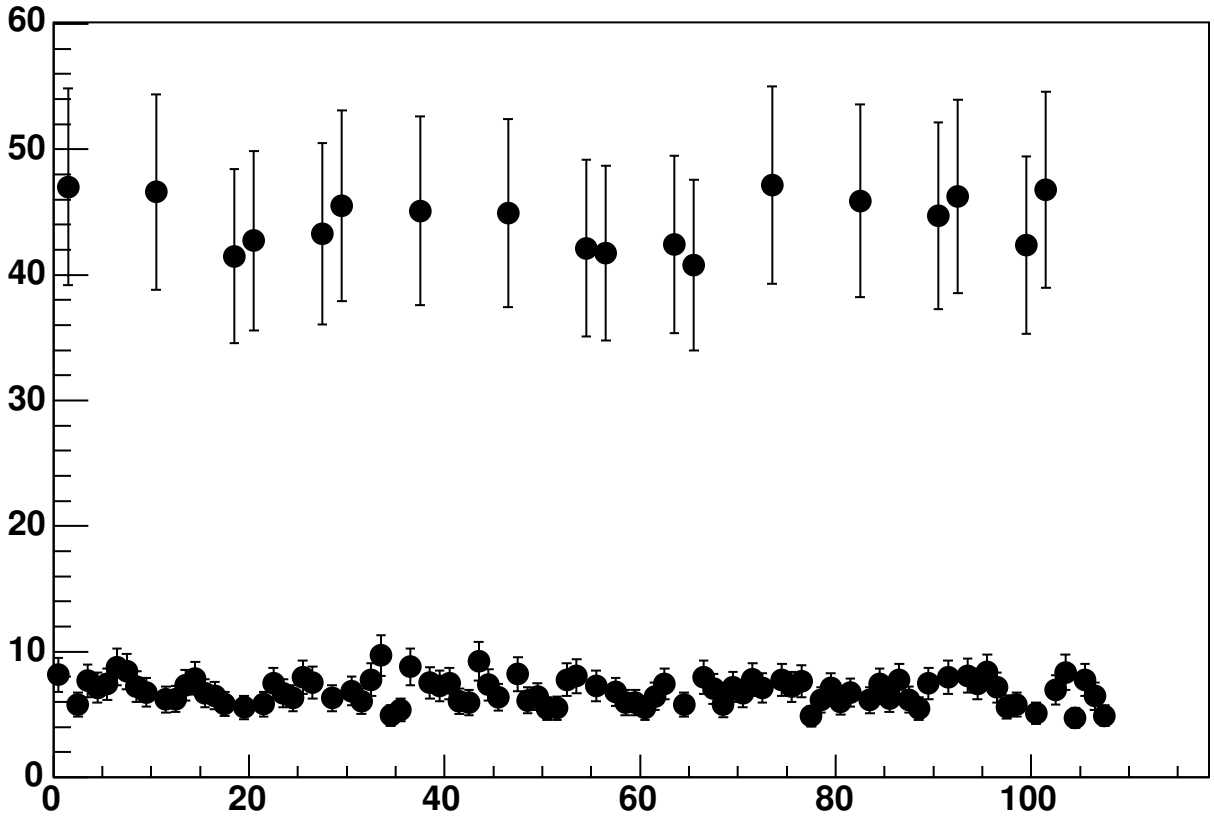
Enable 3, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



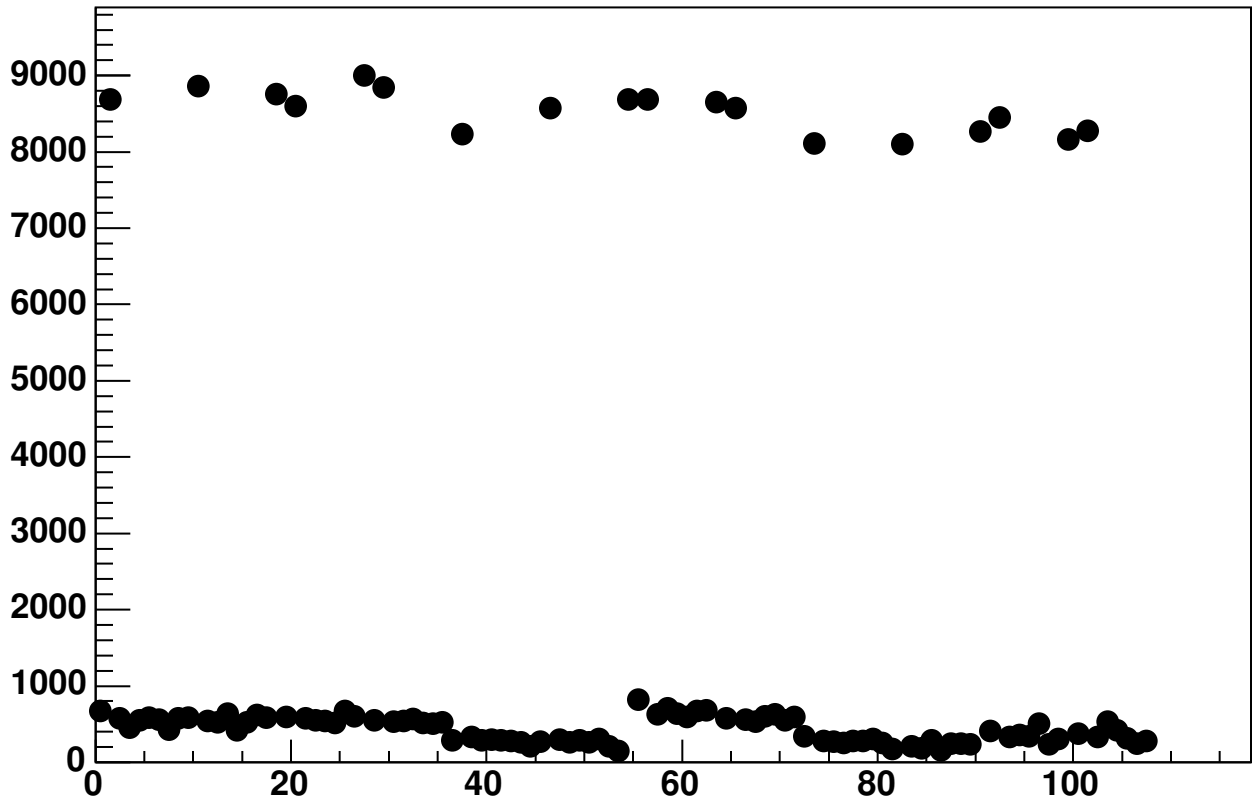
Enable 3, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



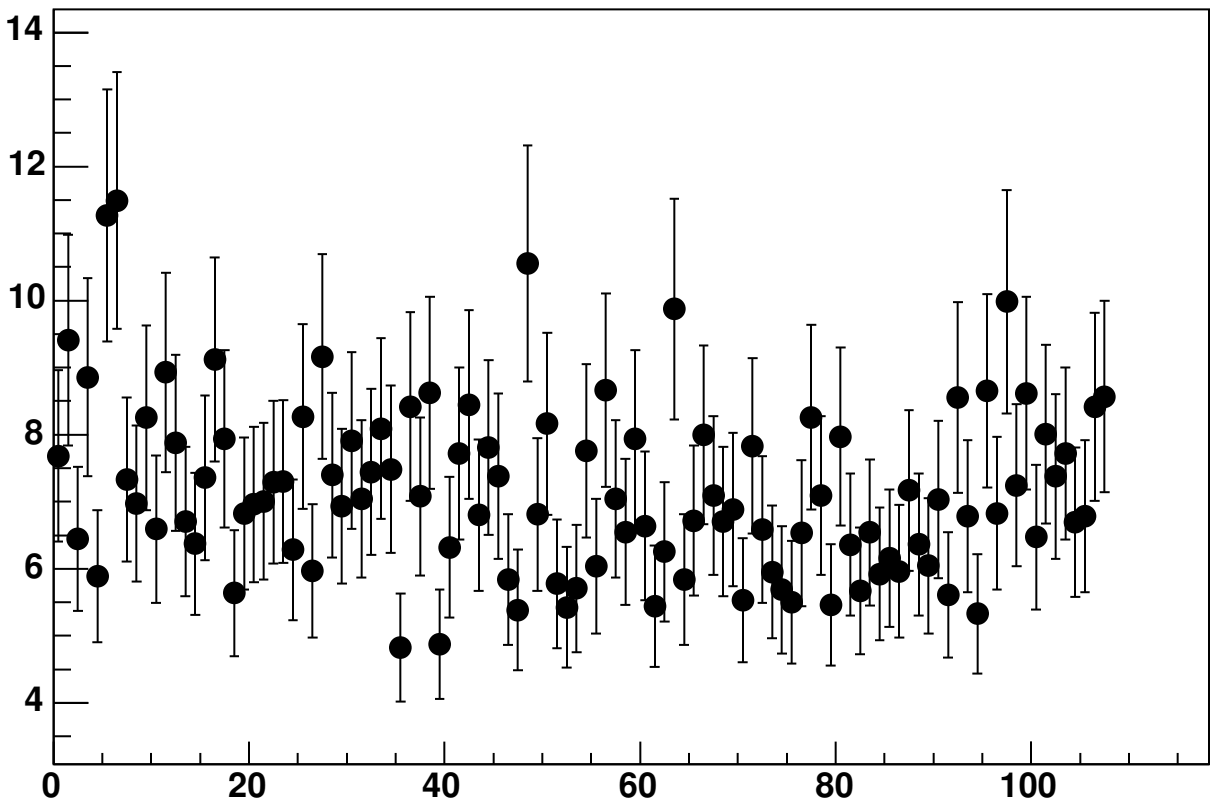
Enable 3, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



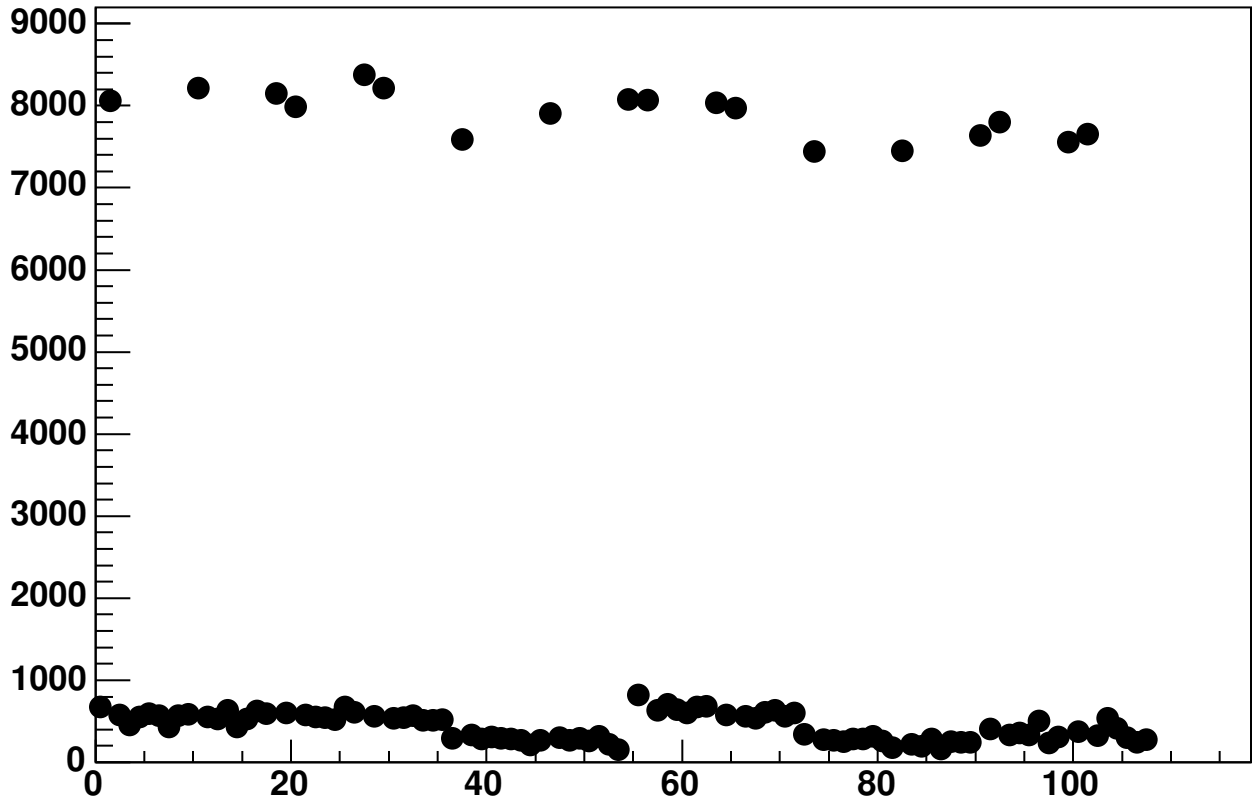
Enable 3, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



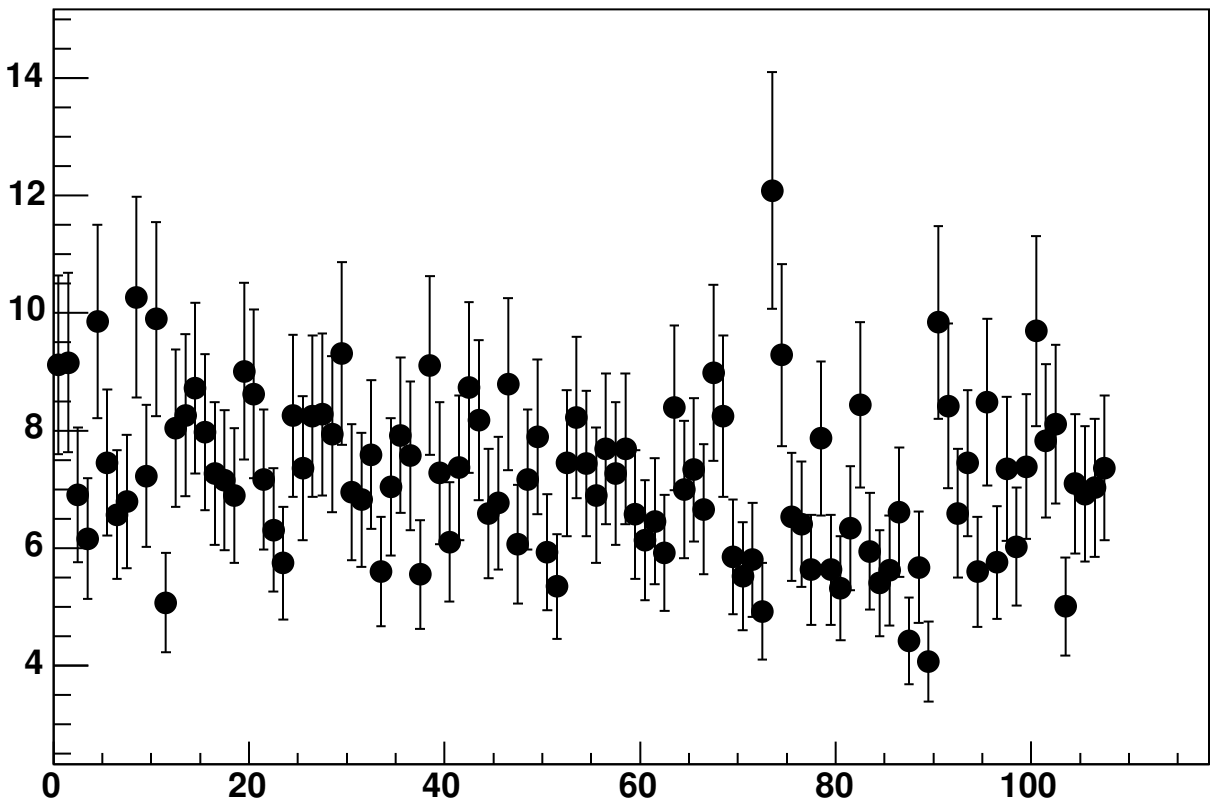
Enable 3, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



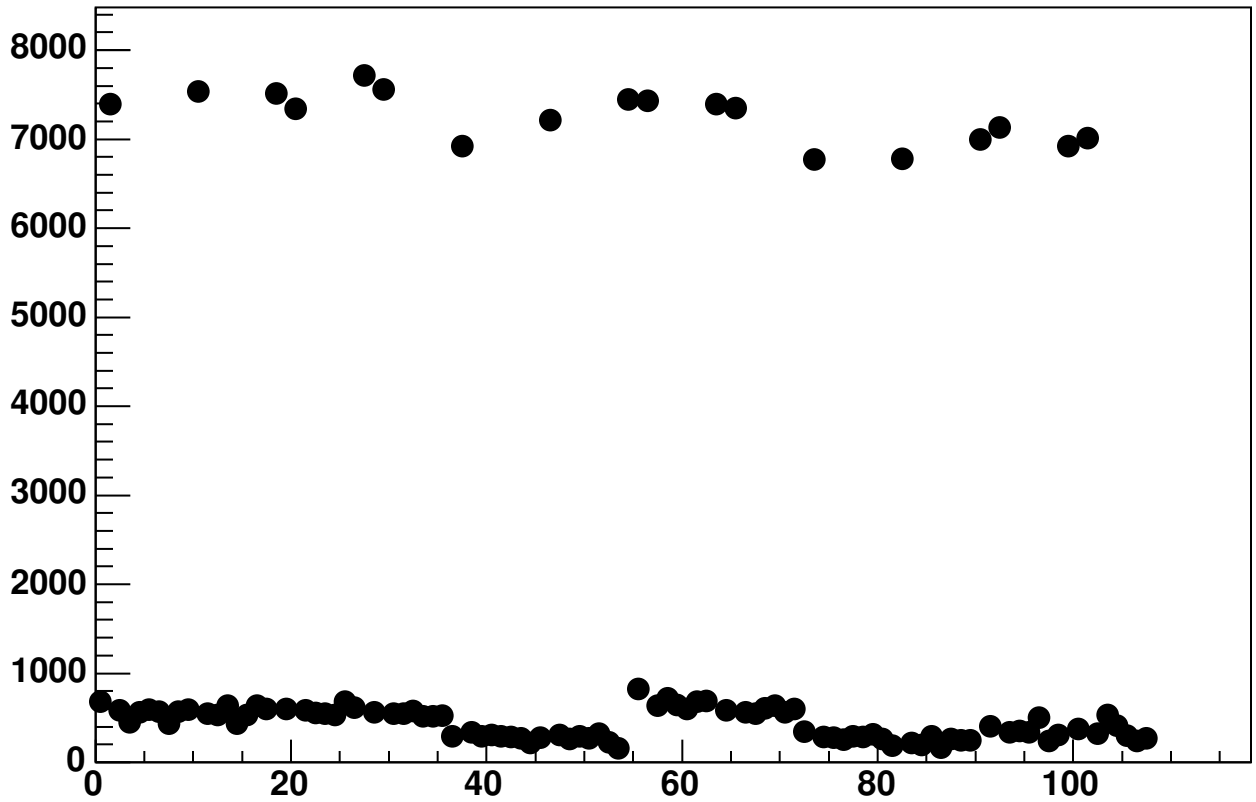
Enable 3, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



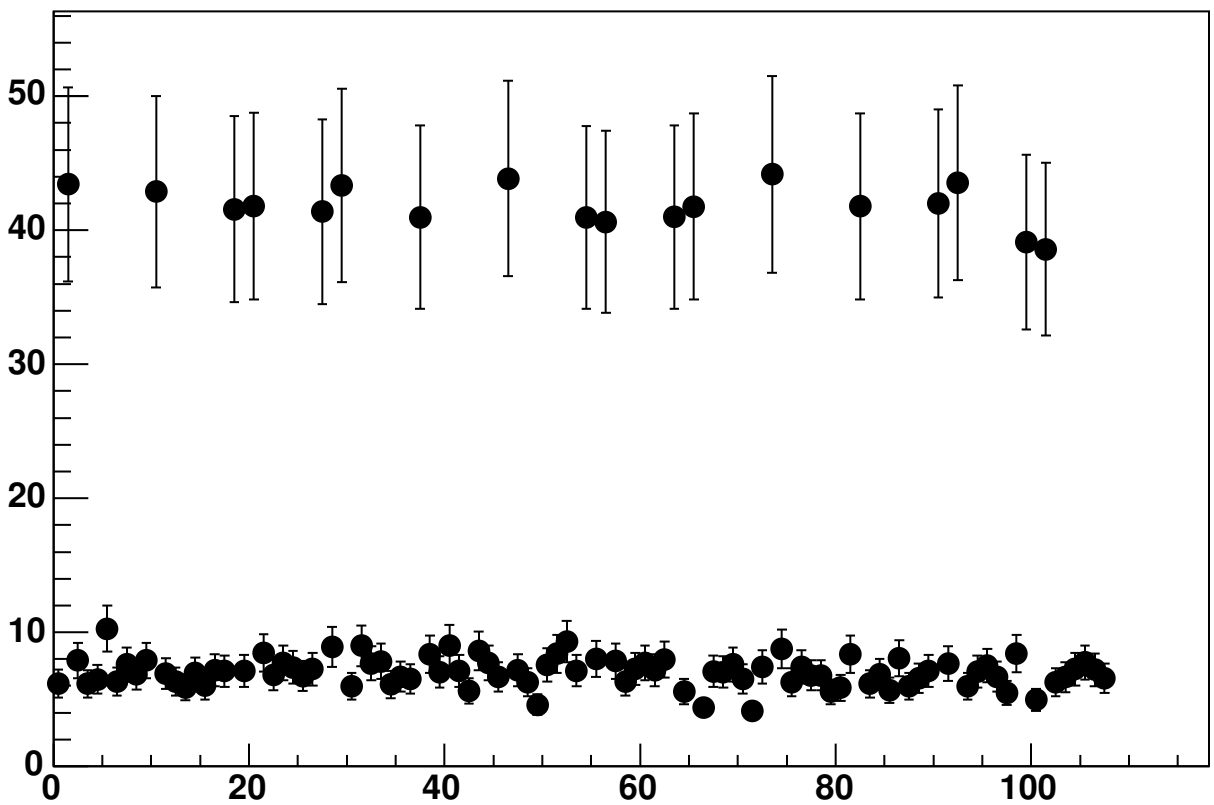
Enable 3, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



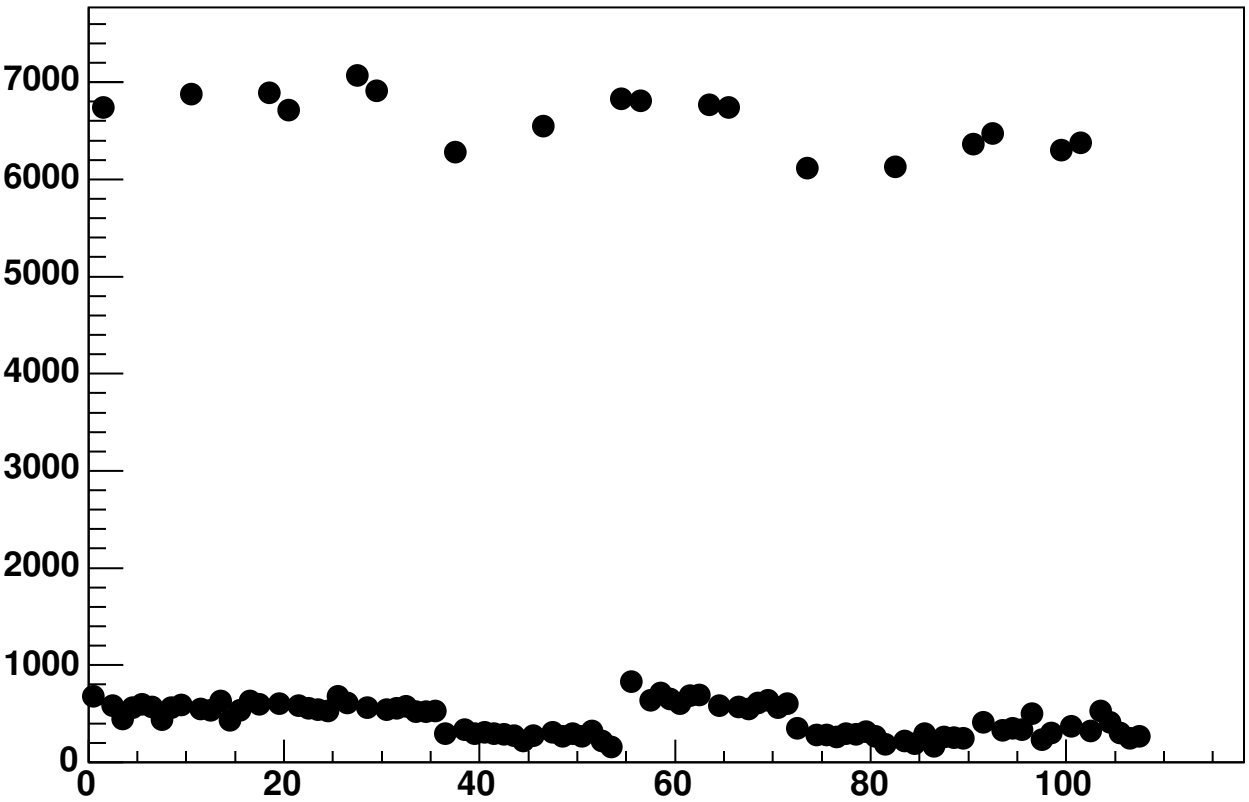
Enable 3, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



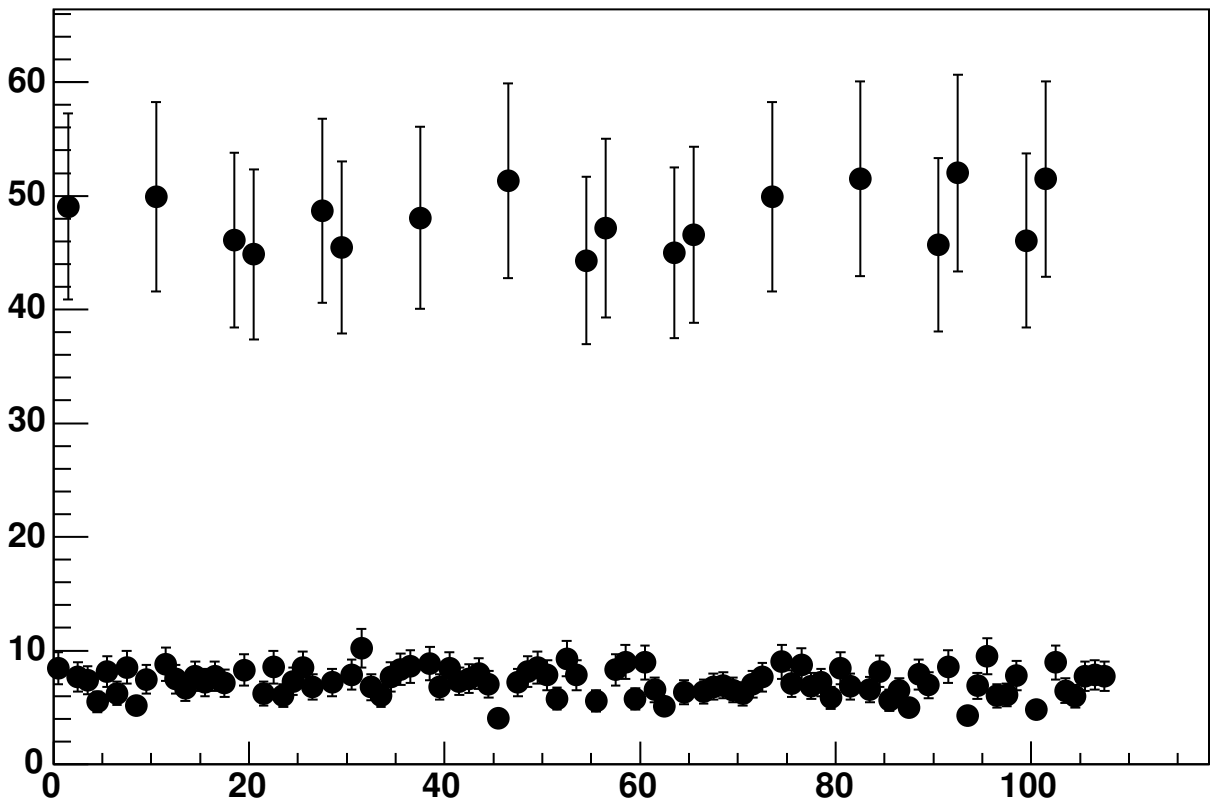
Enable 3, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



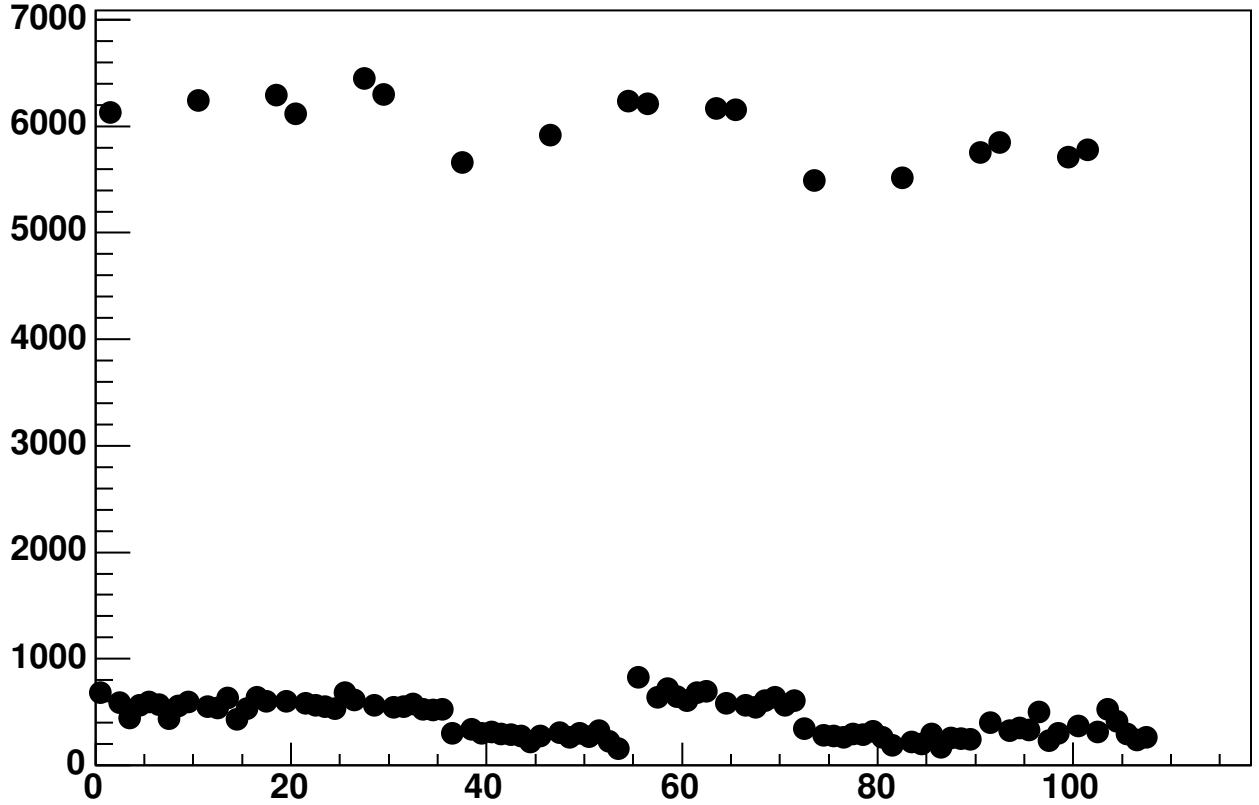
Enable 3, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



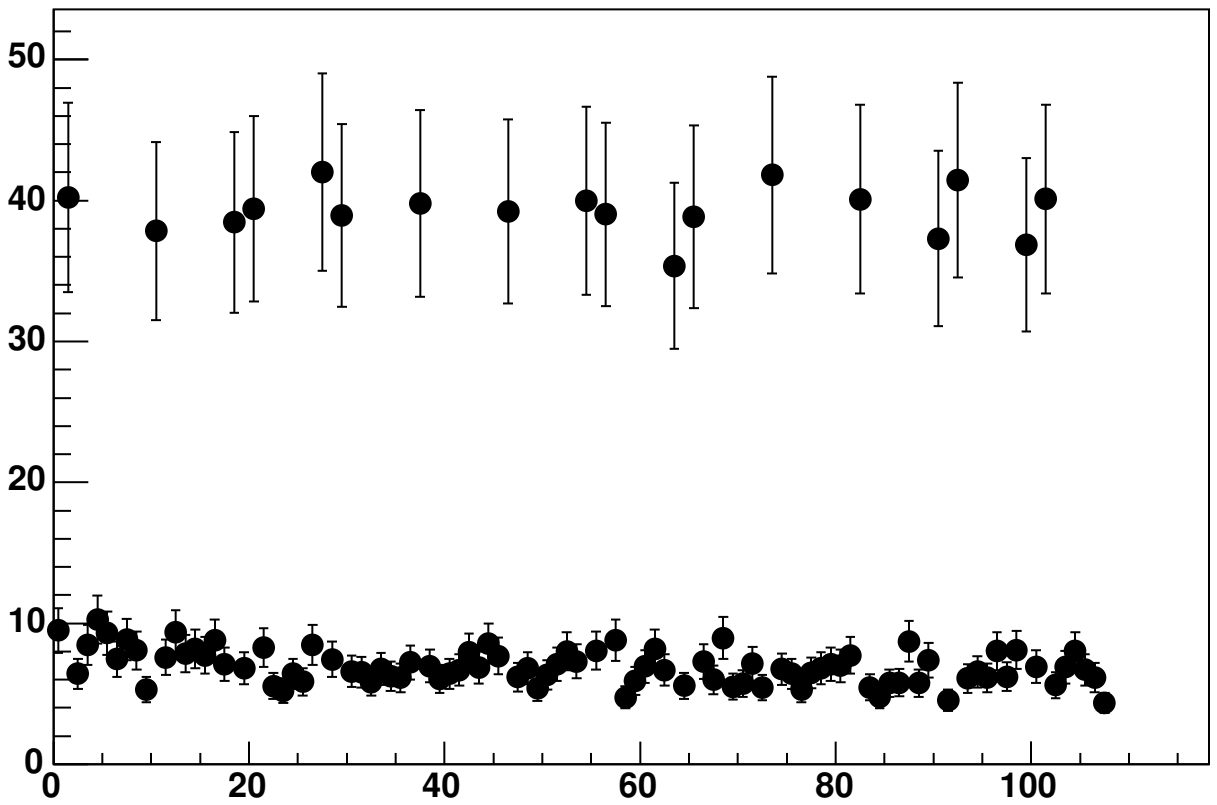
Enable 3, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



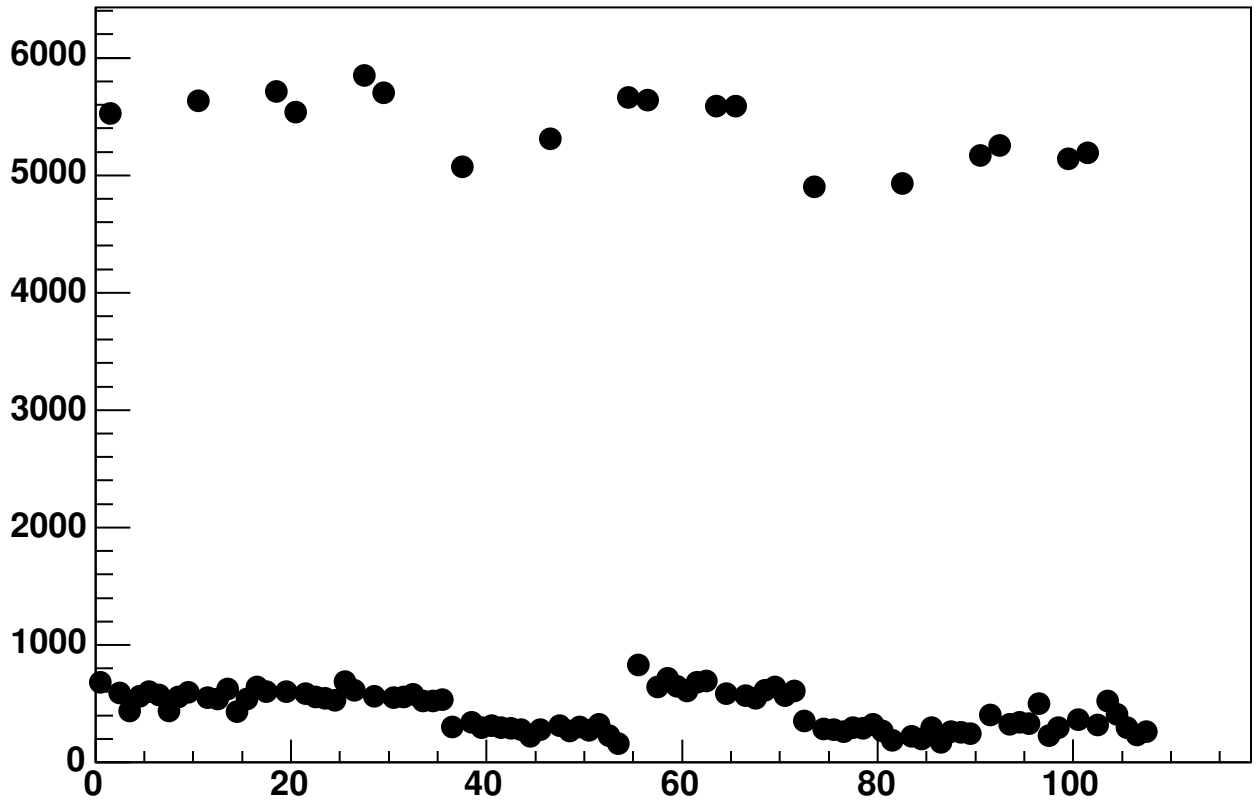
Enable 3, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



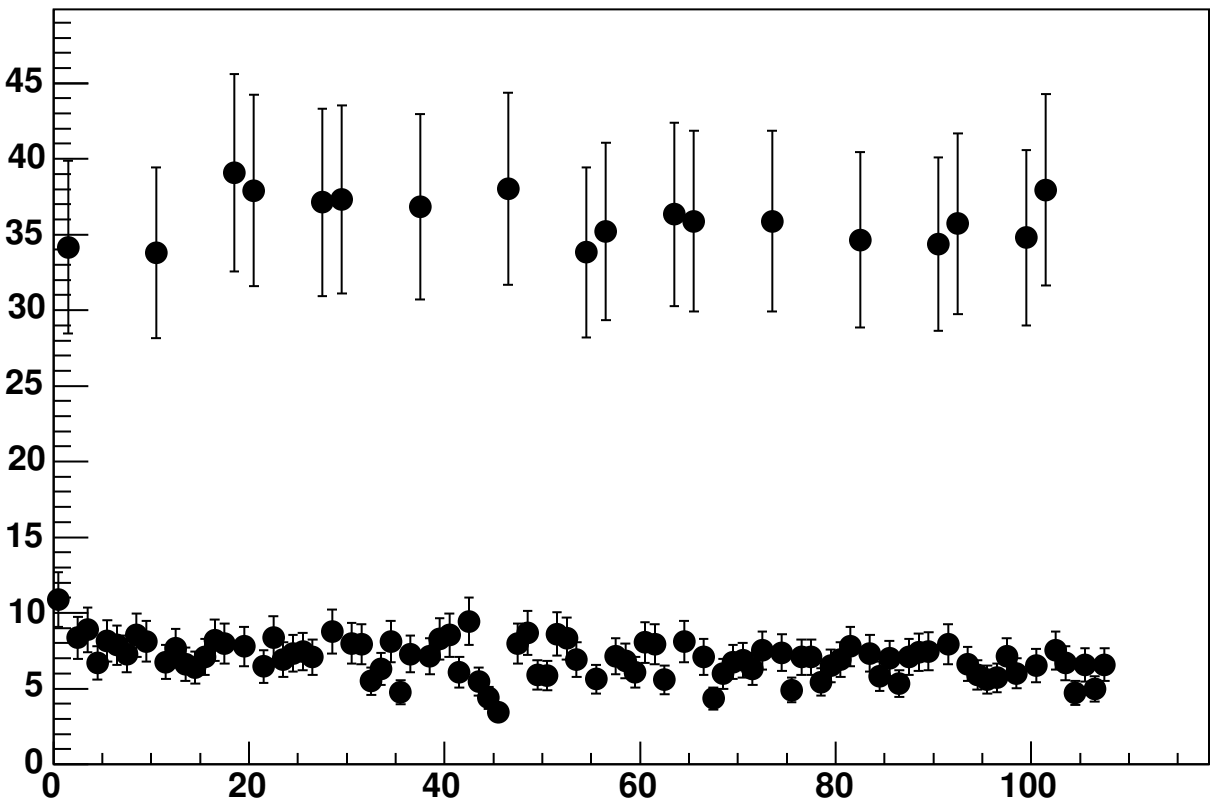
Enable 3, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



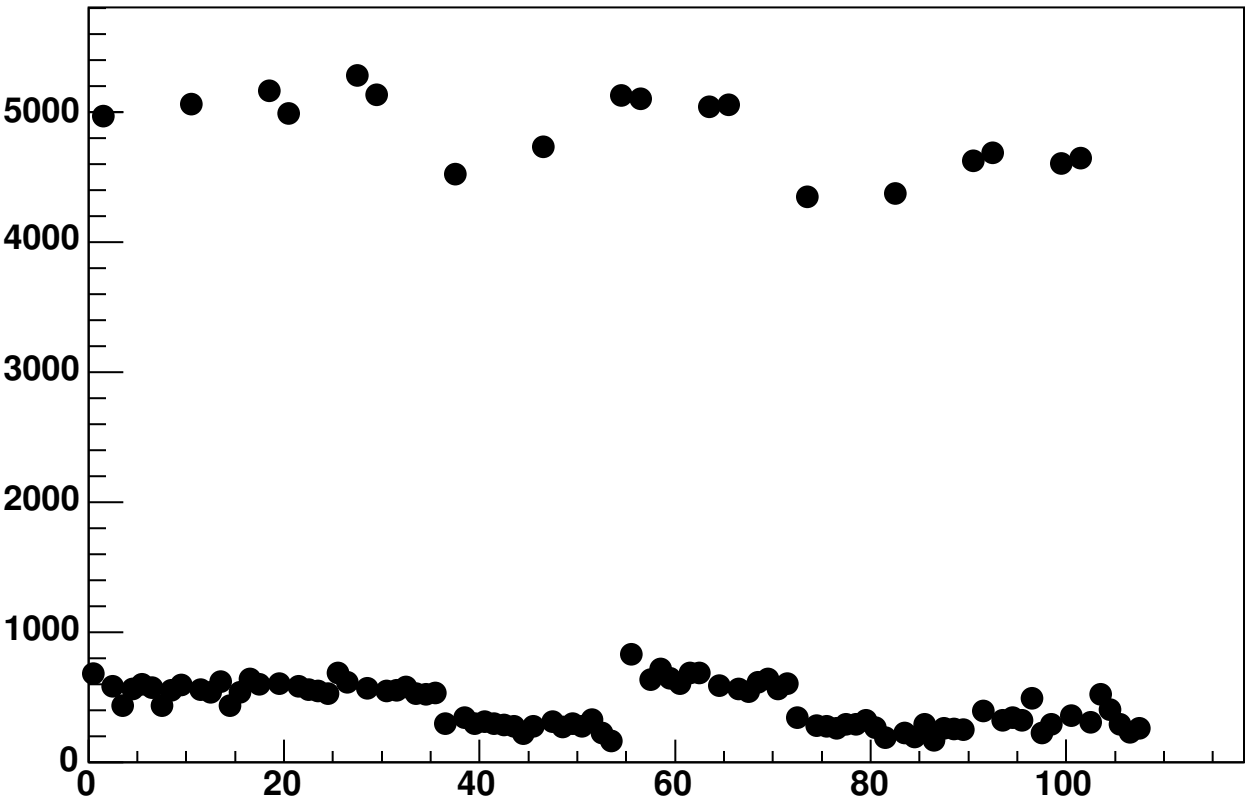
Enable 3, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



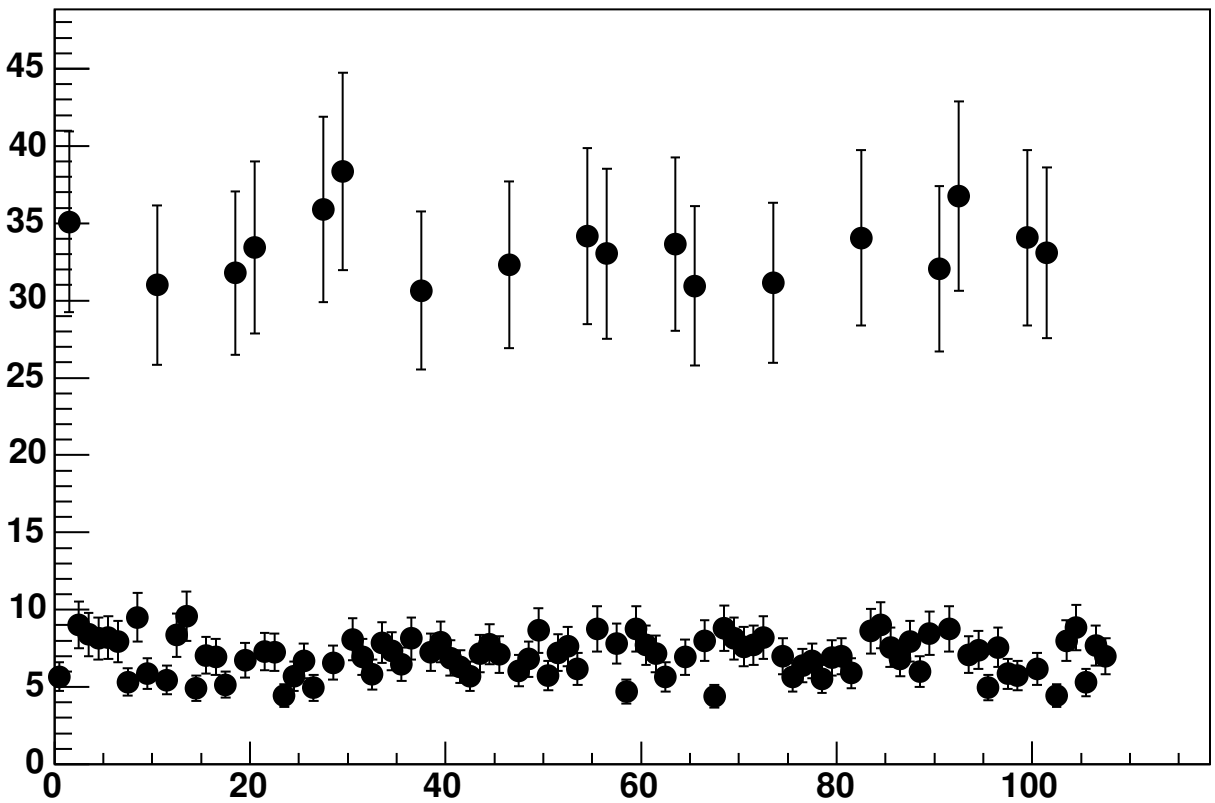
Enable 3, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



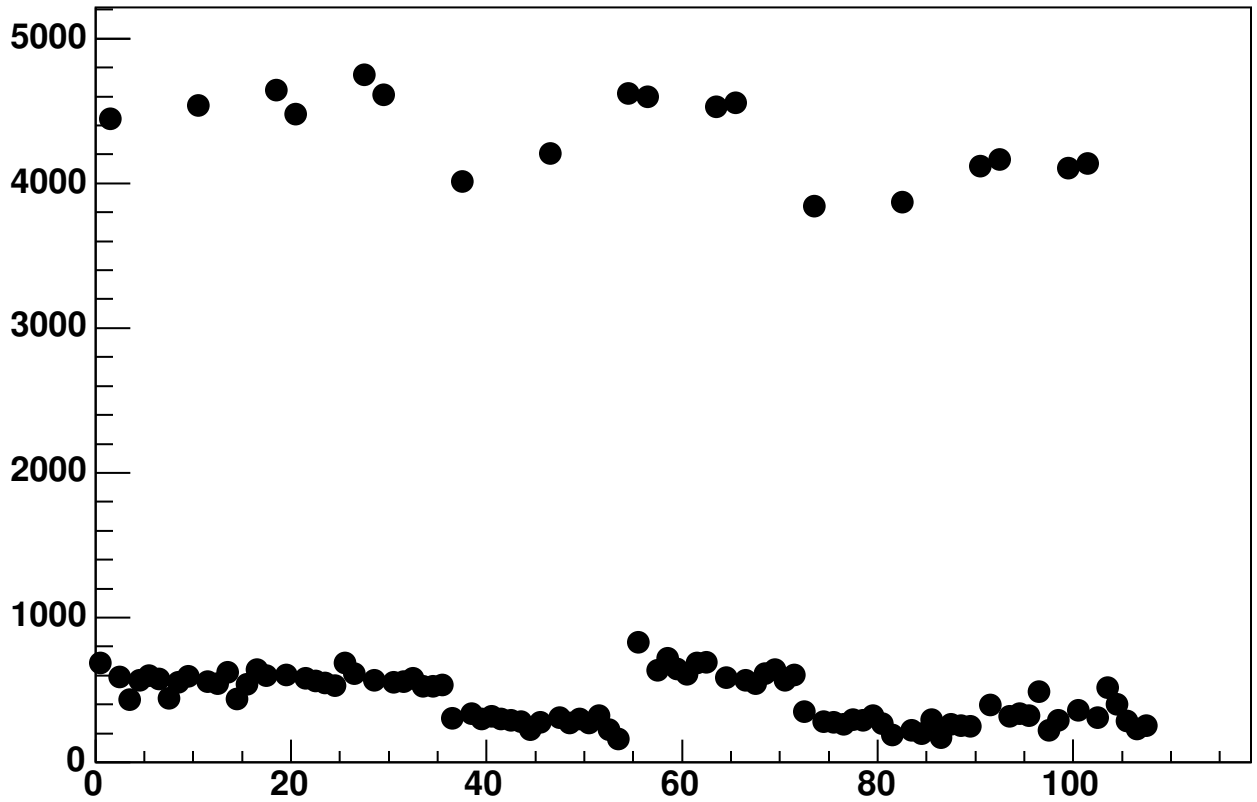
Enable 3, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



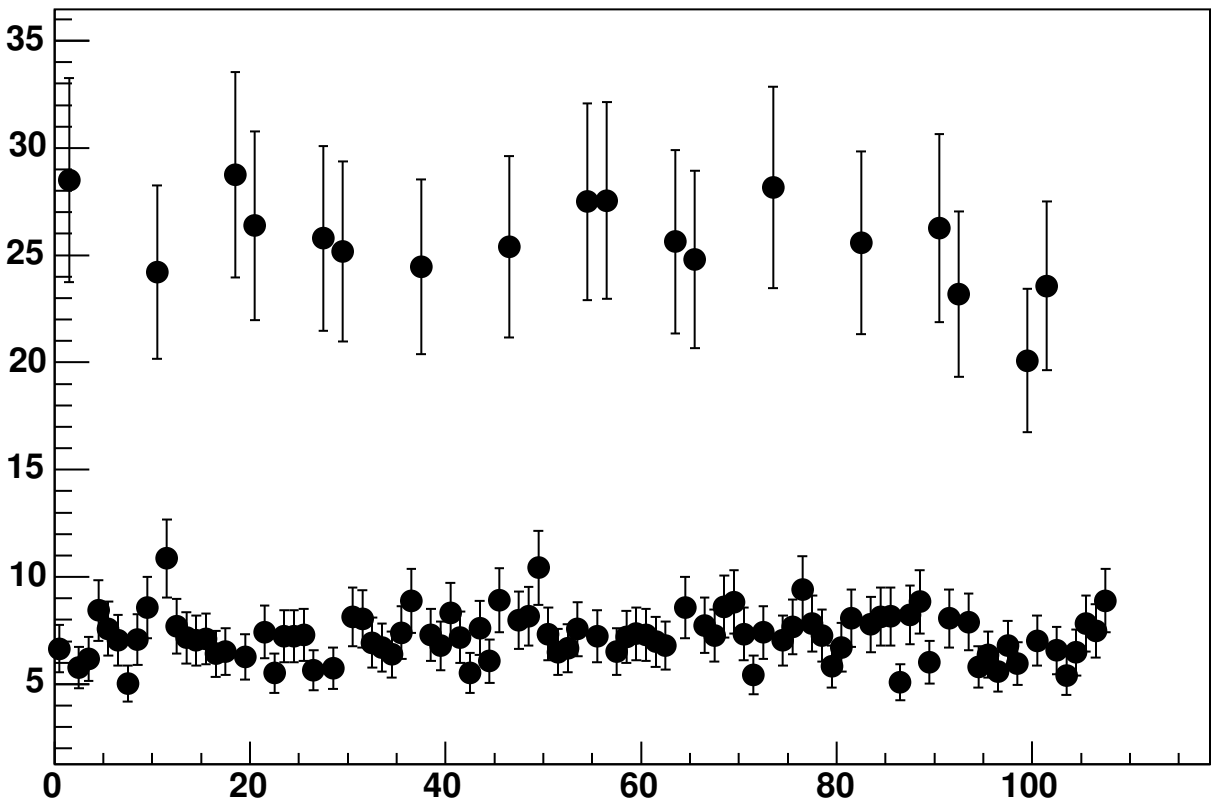
Enable 3, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



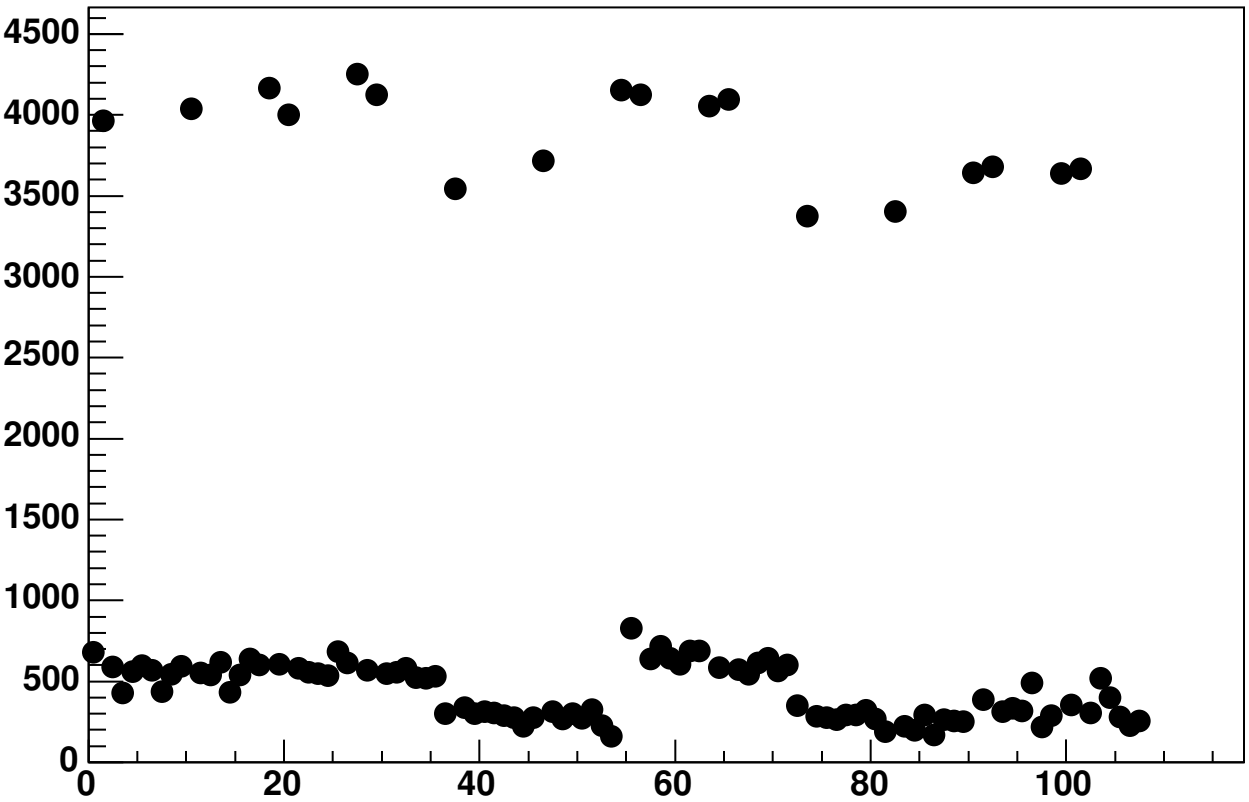
Enable 3, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



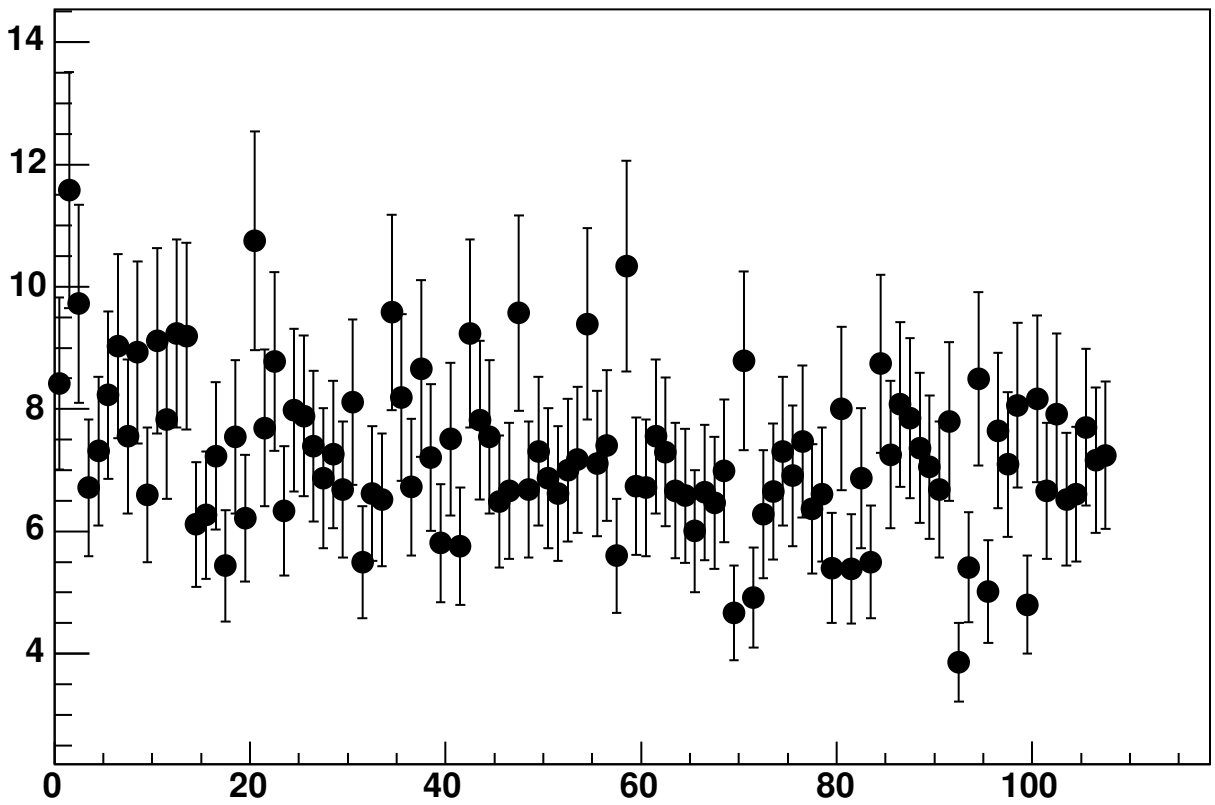
Enable 3, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



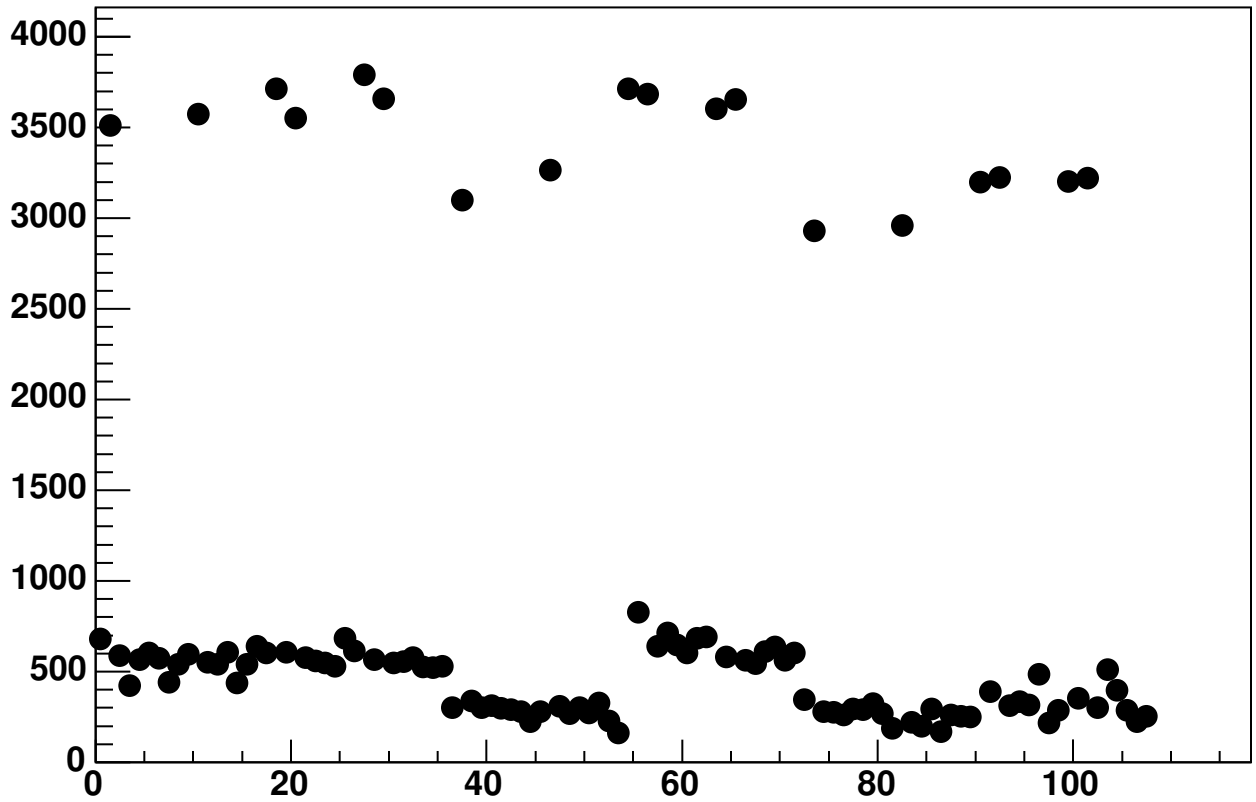
Enable 3, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



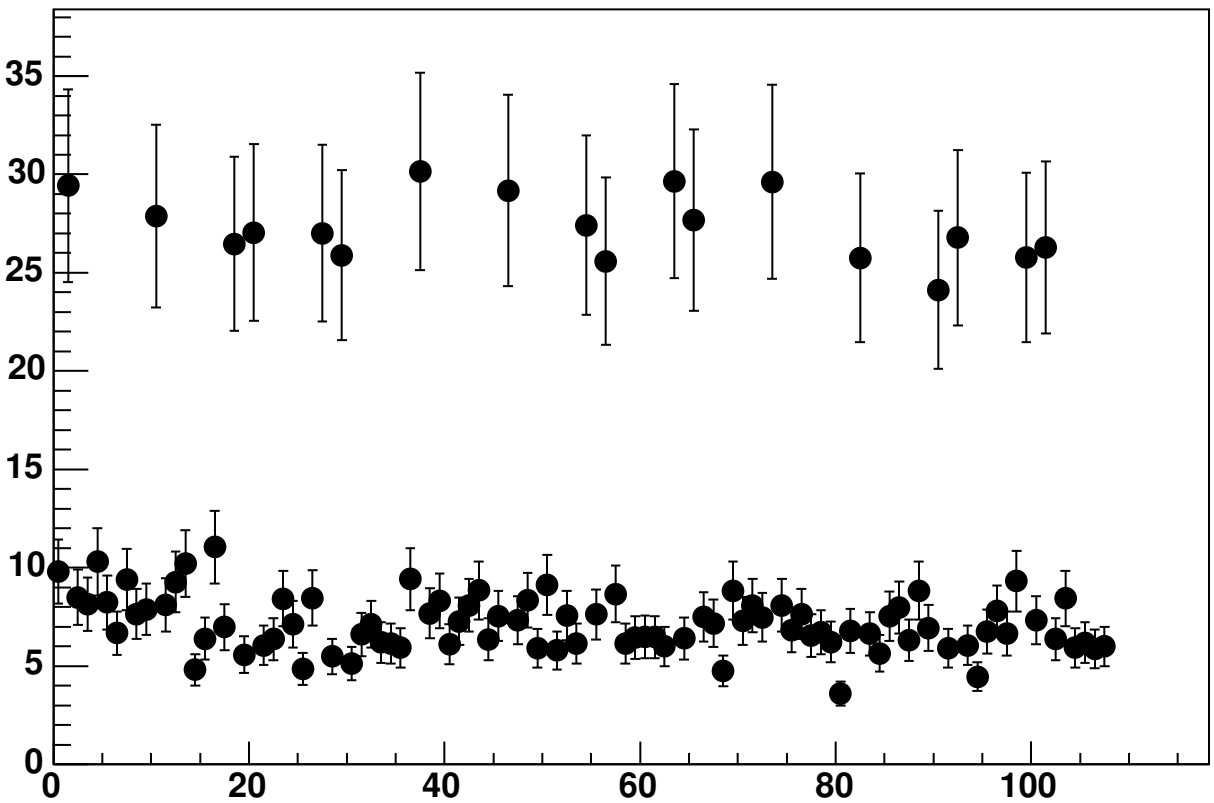
Enable 3, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



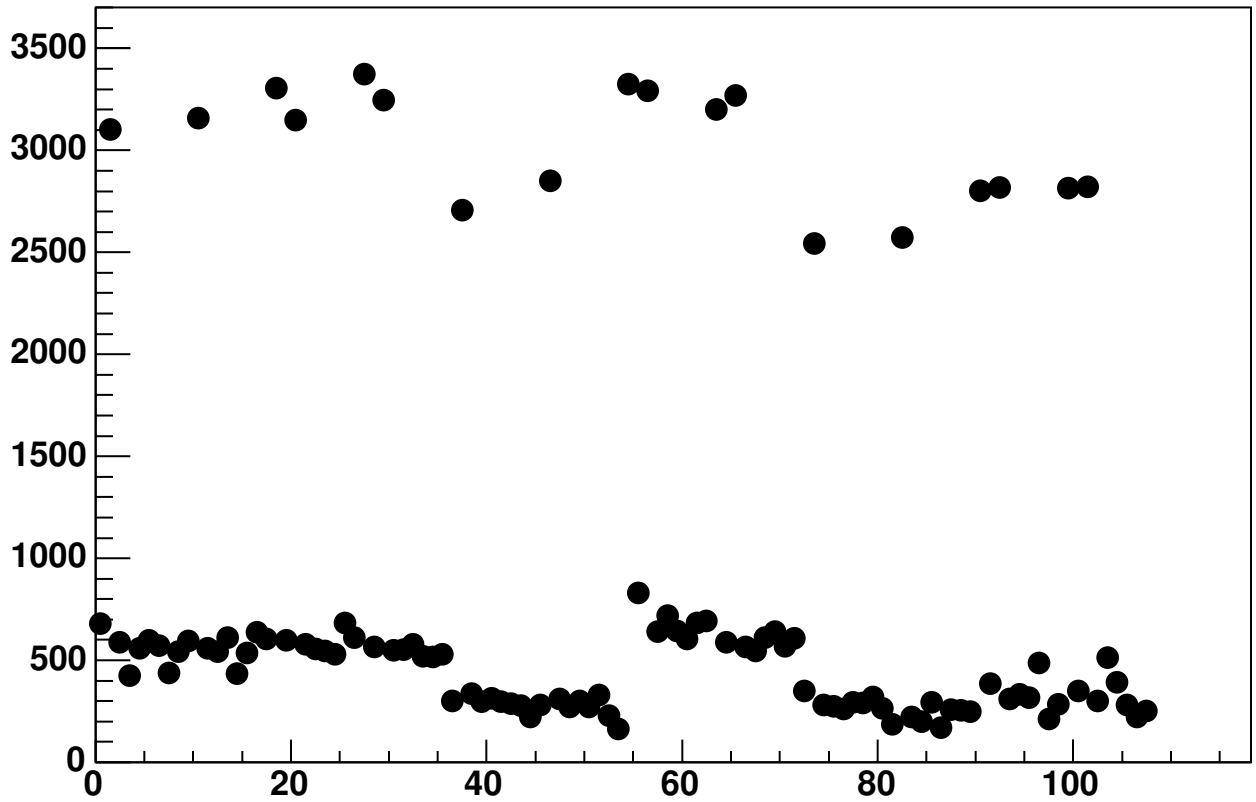
Enable 3, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



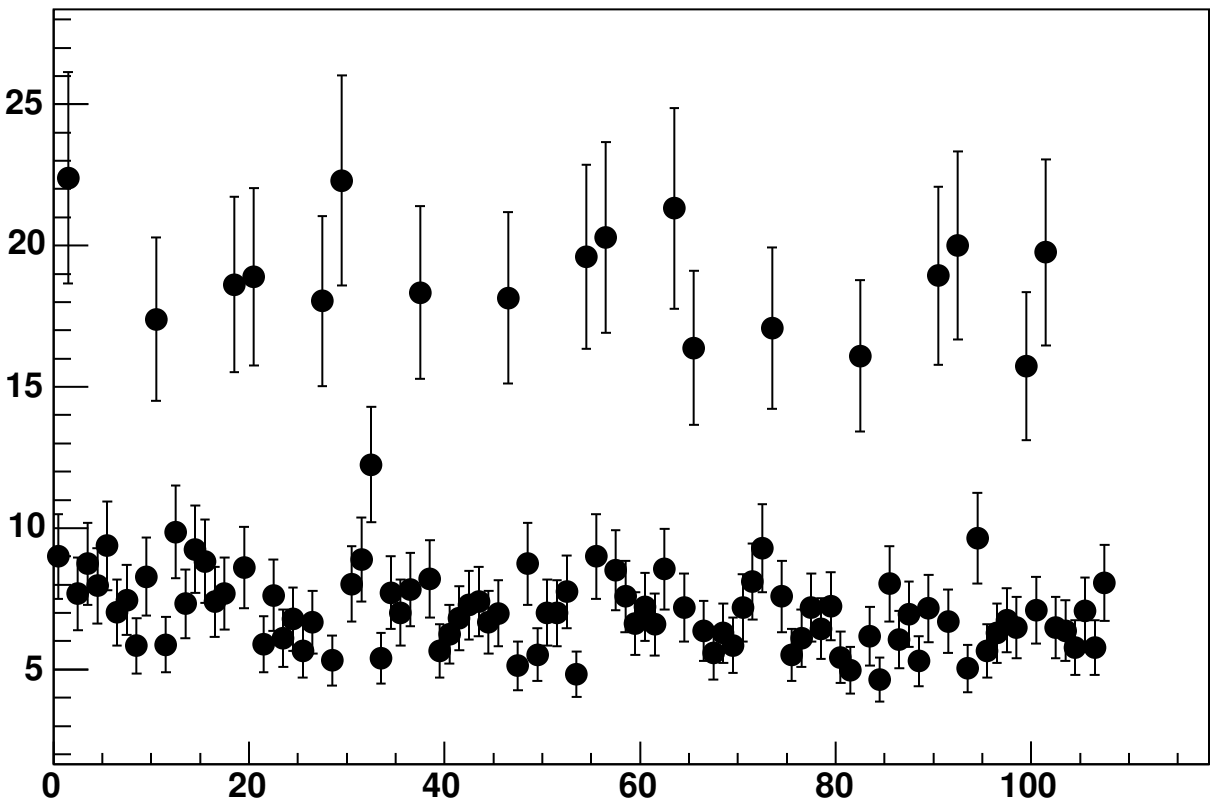
Enable 3, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



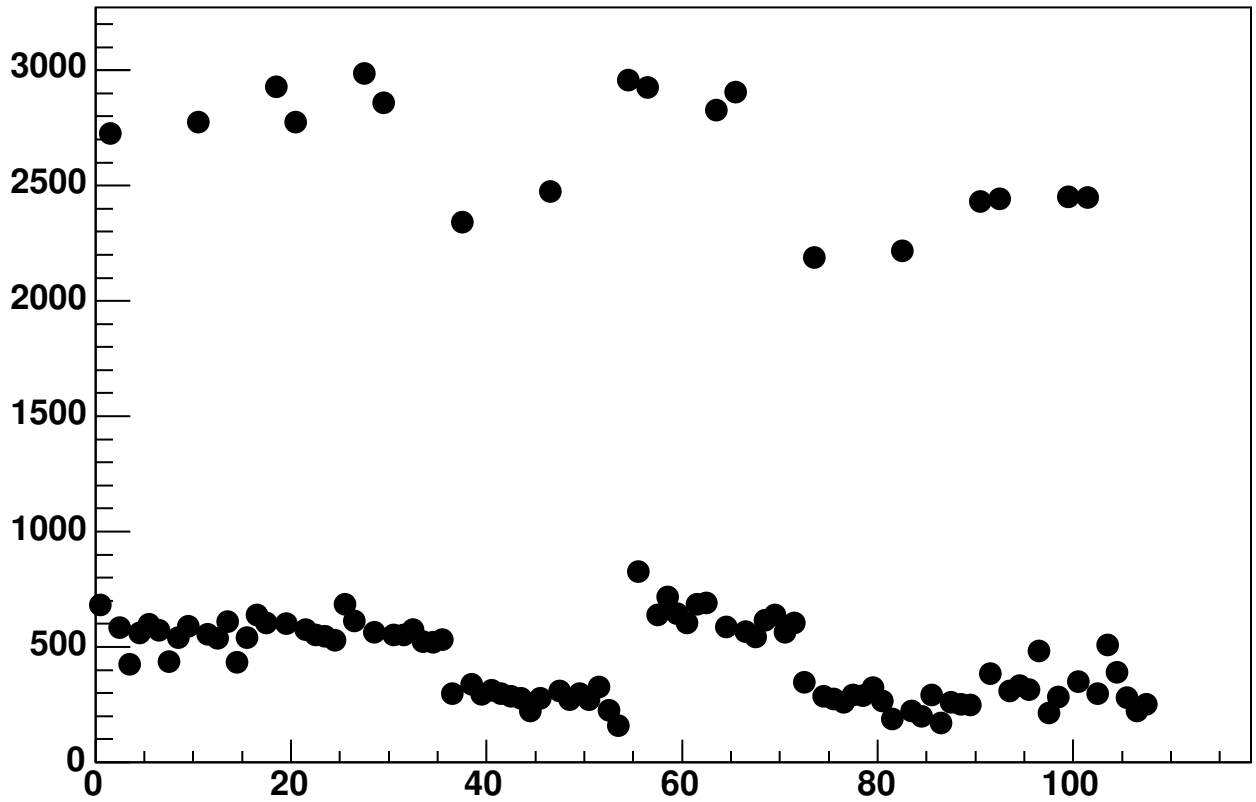
Enable 3, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



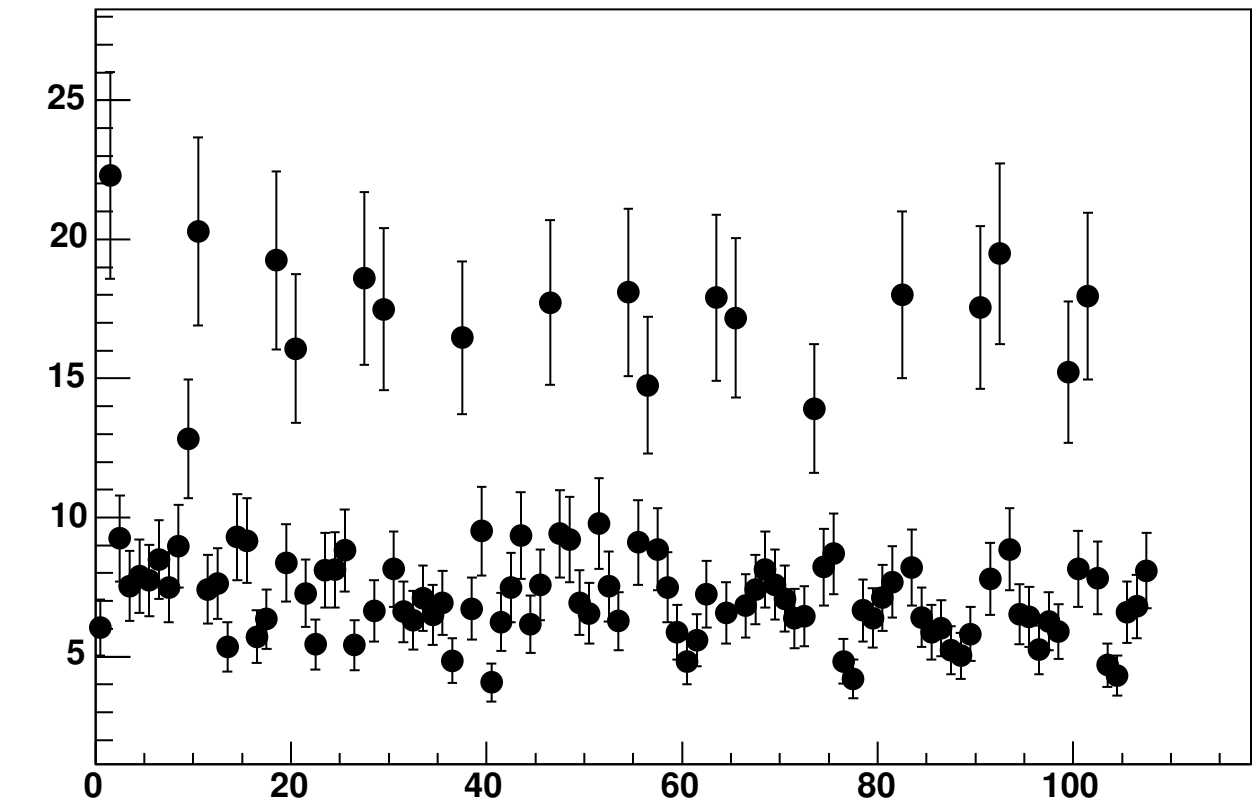
Enable 3, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



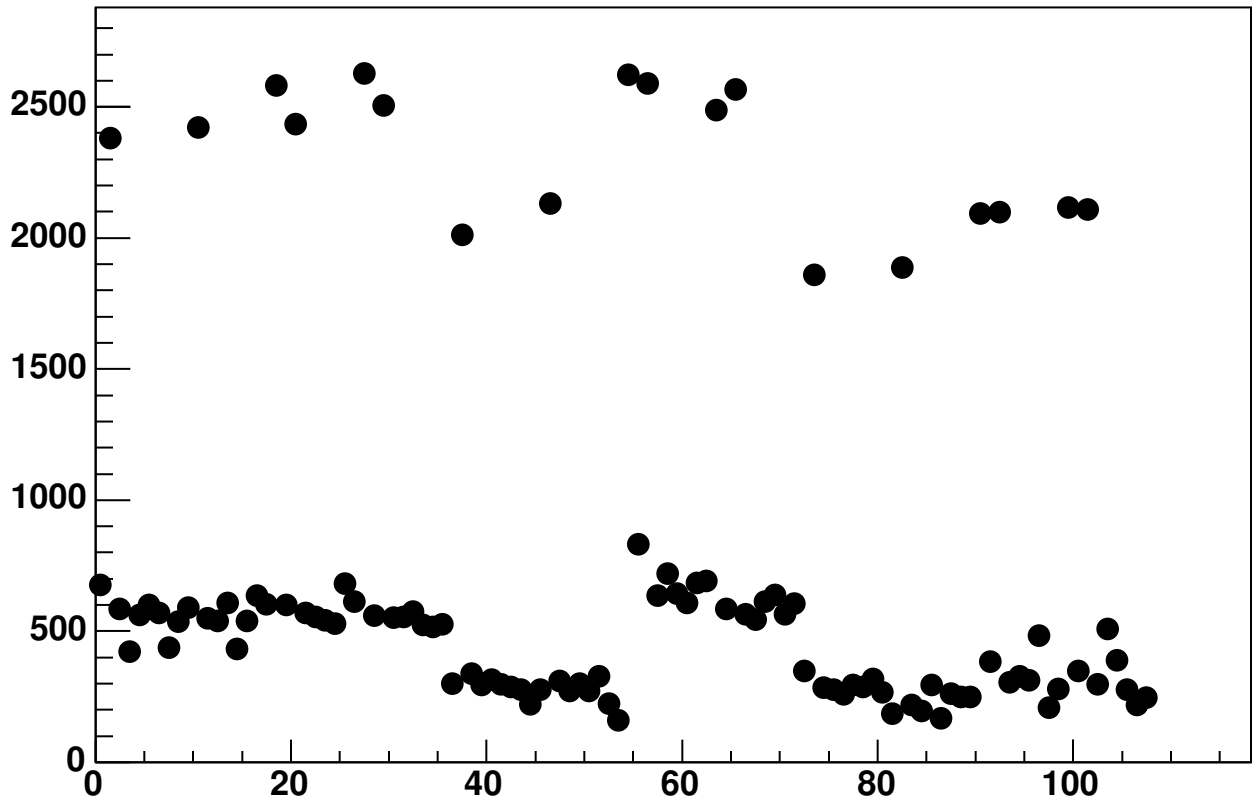
Enable 3, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



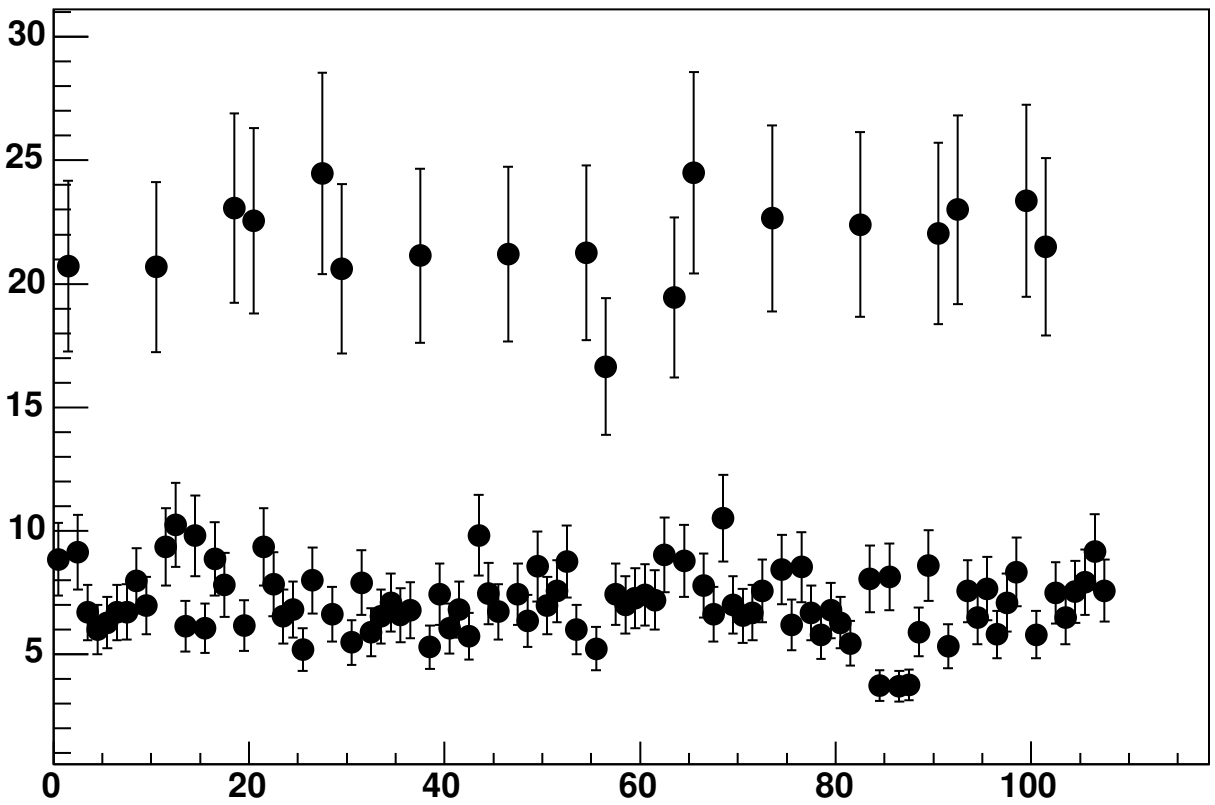
Enable 3, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



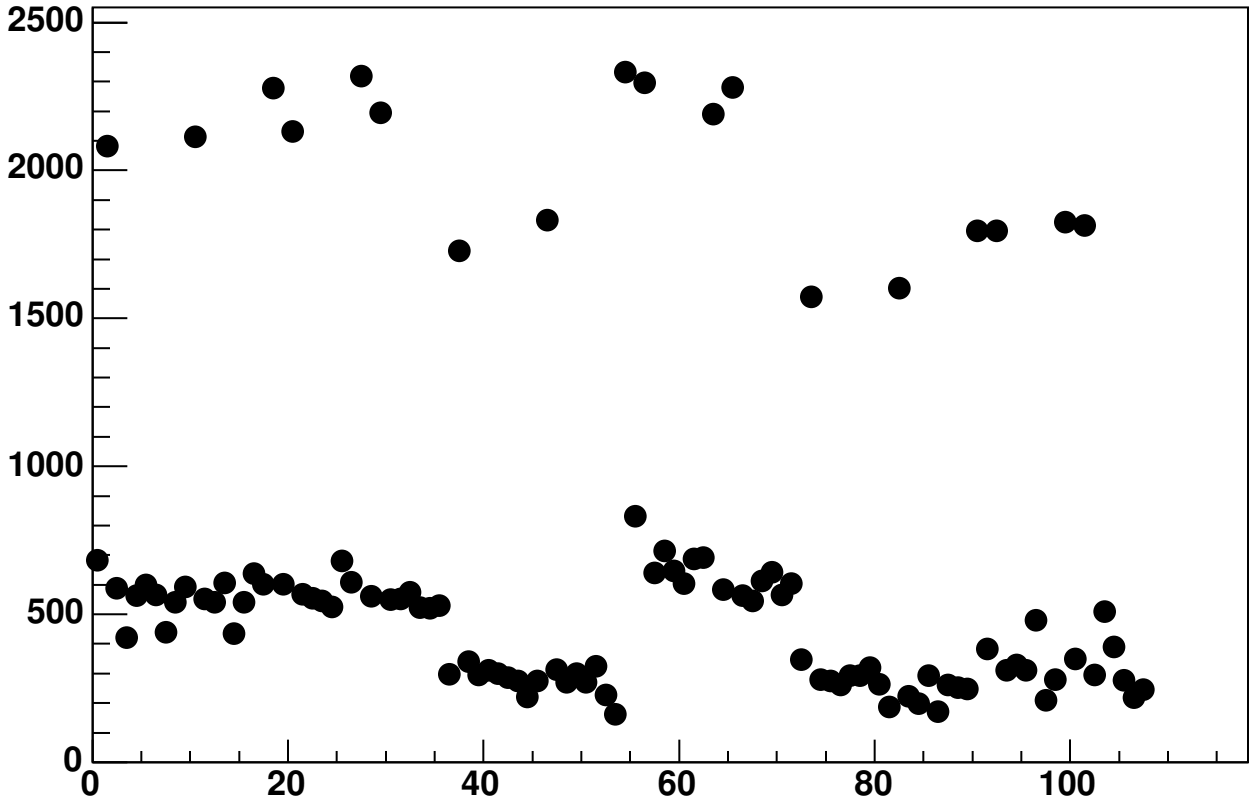
Enable 3, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



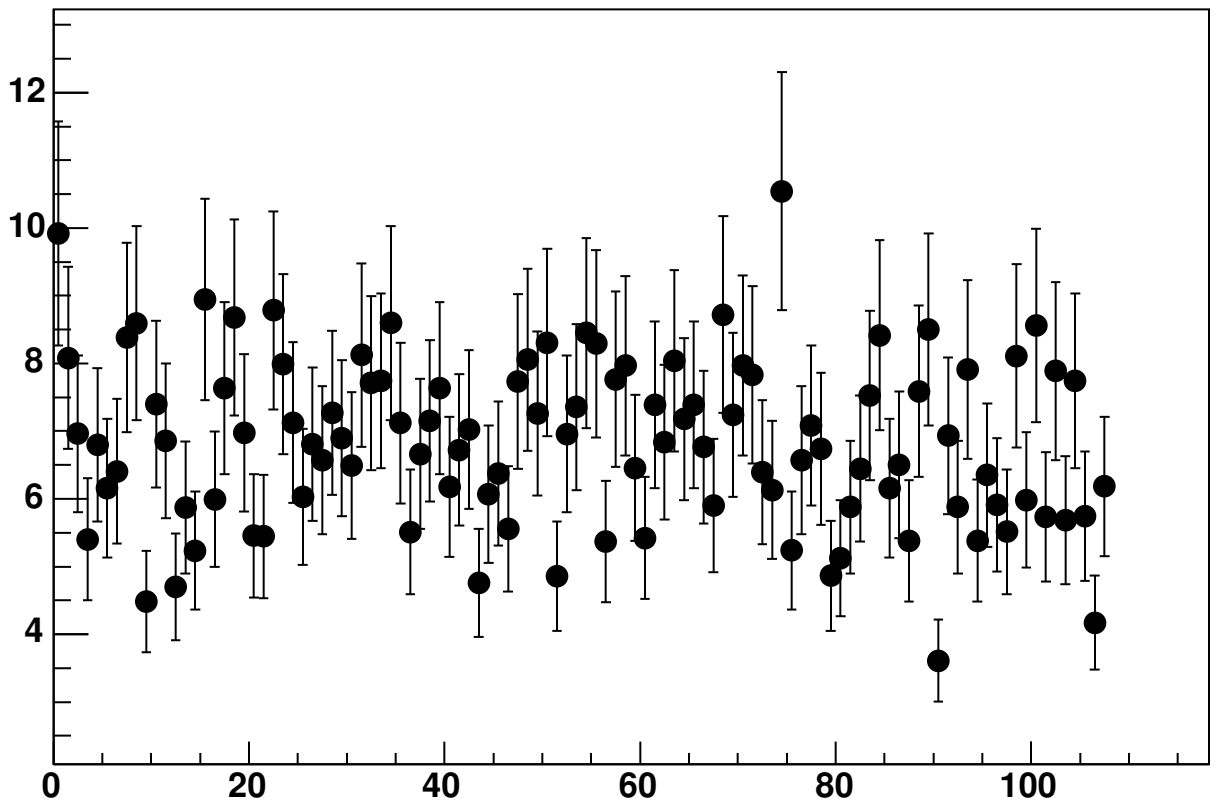
Enable 3, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



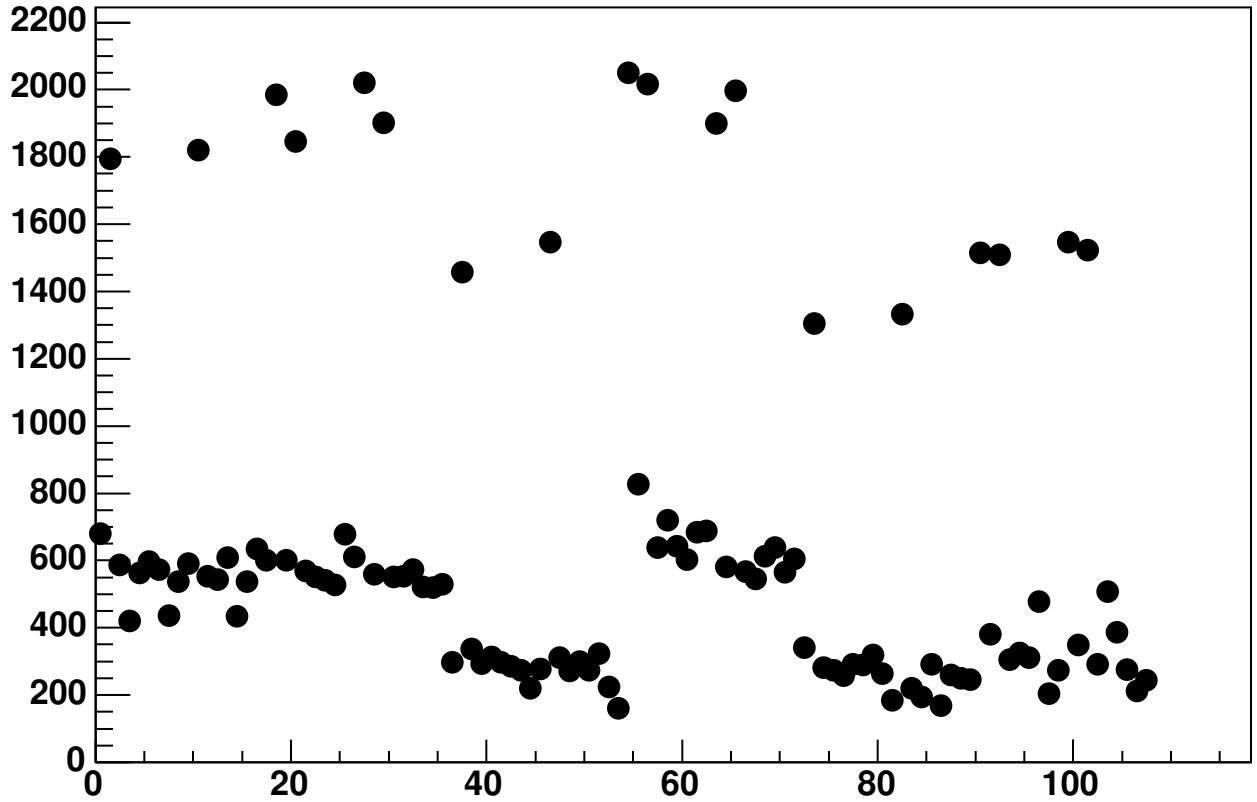
Enable 3, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



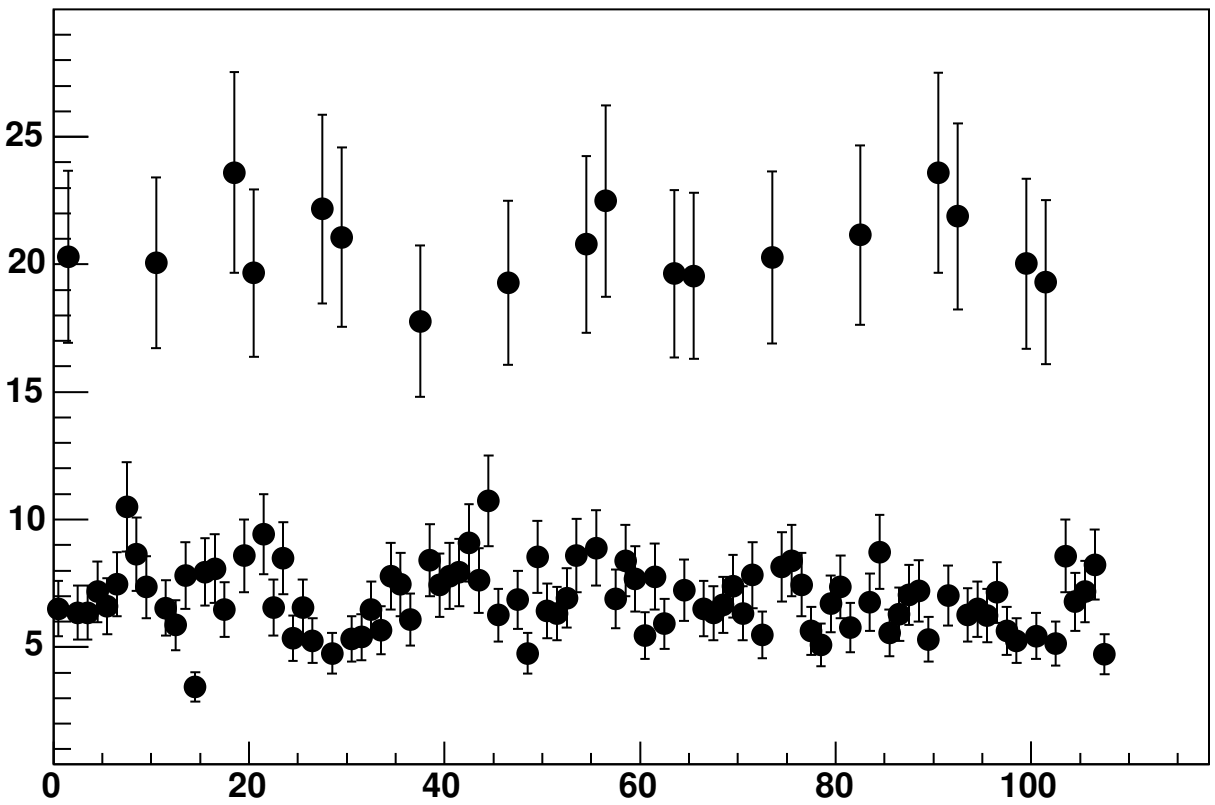
Enable 3, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



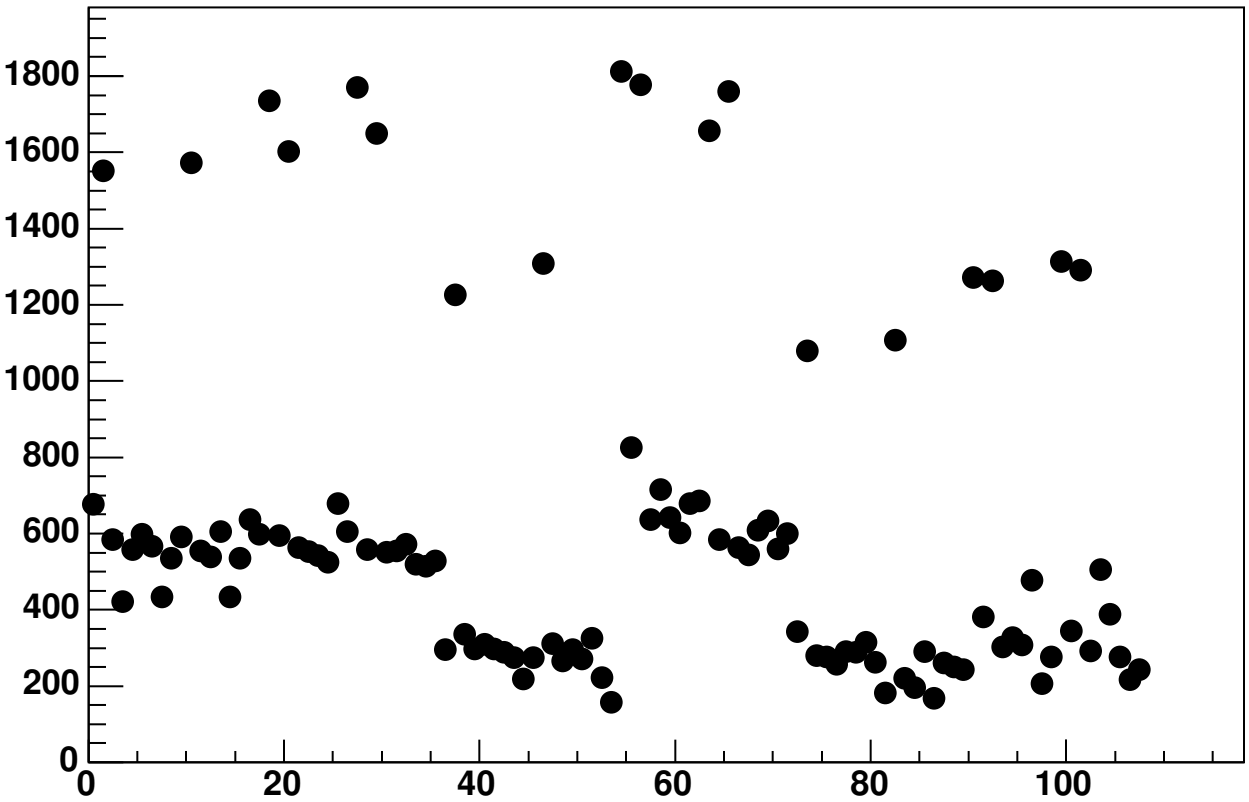
Enable 3, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



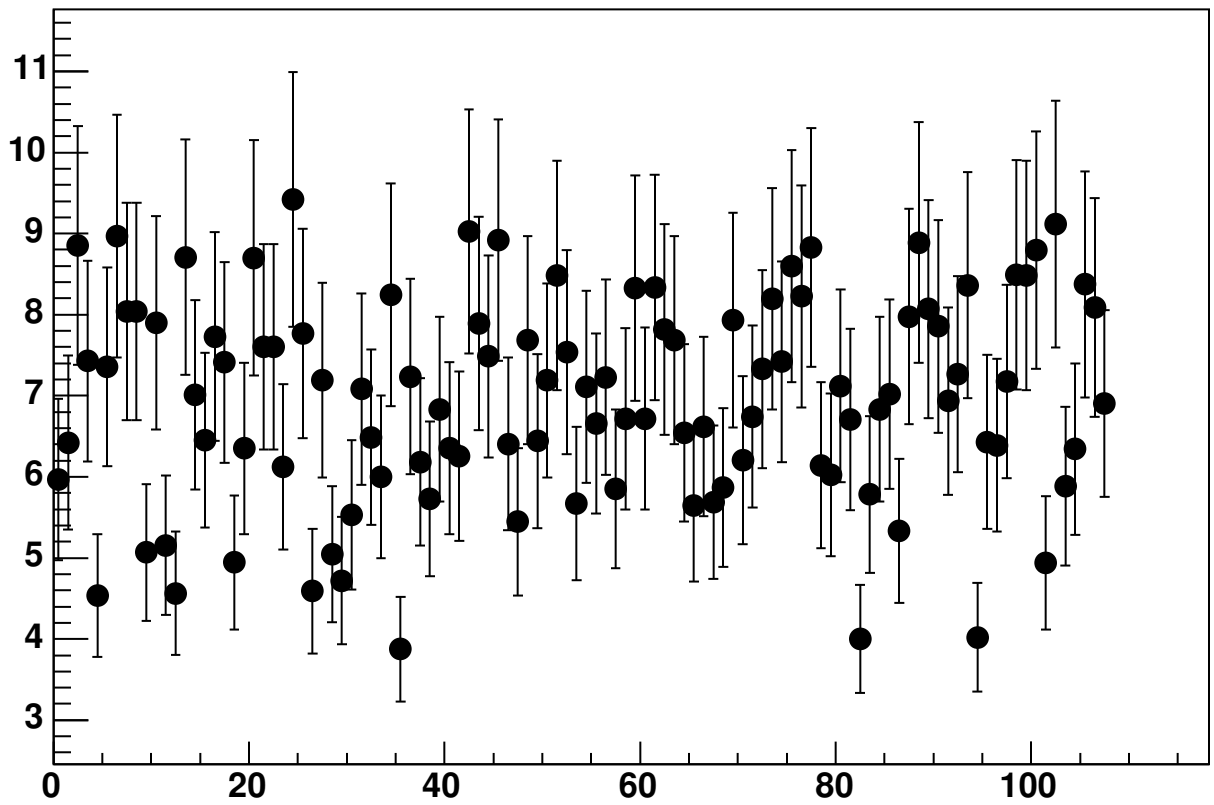
Enable 3, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



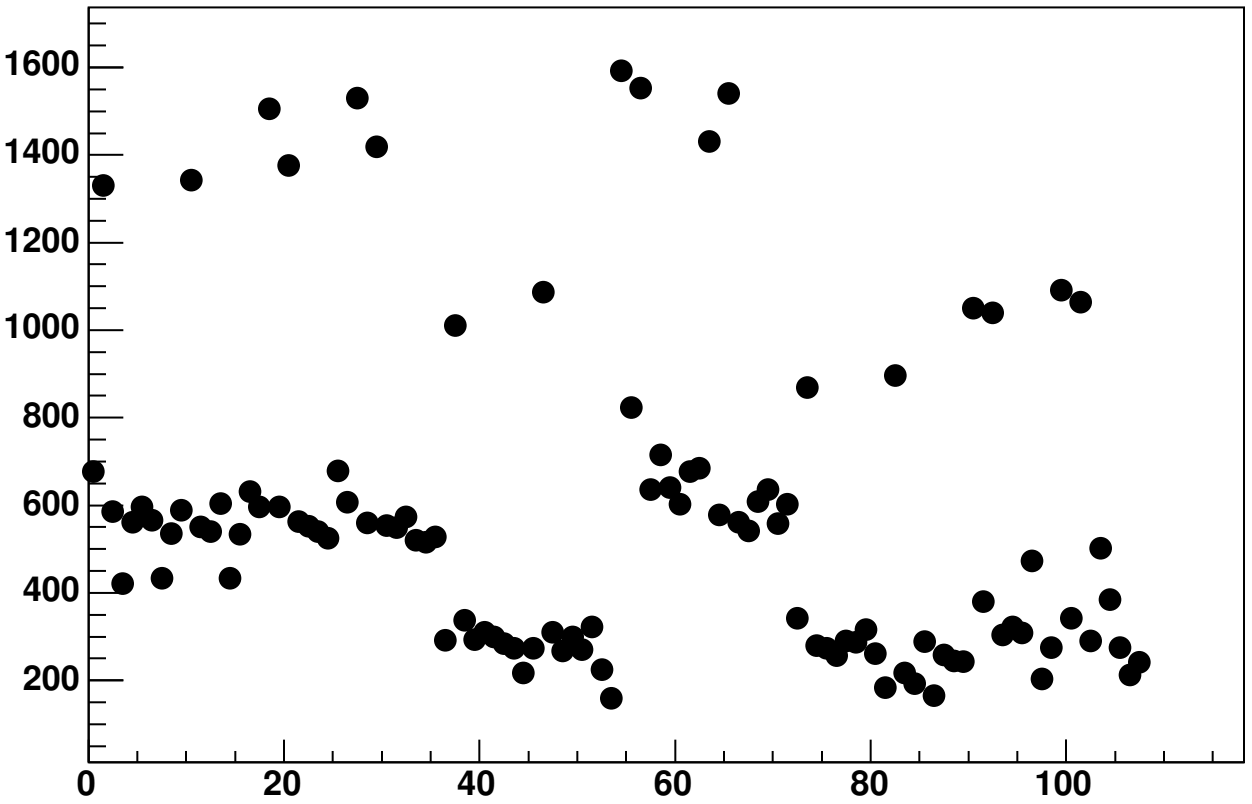
Enable 3, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



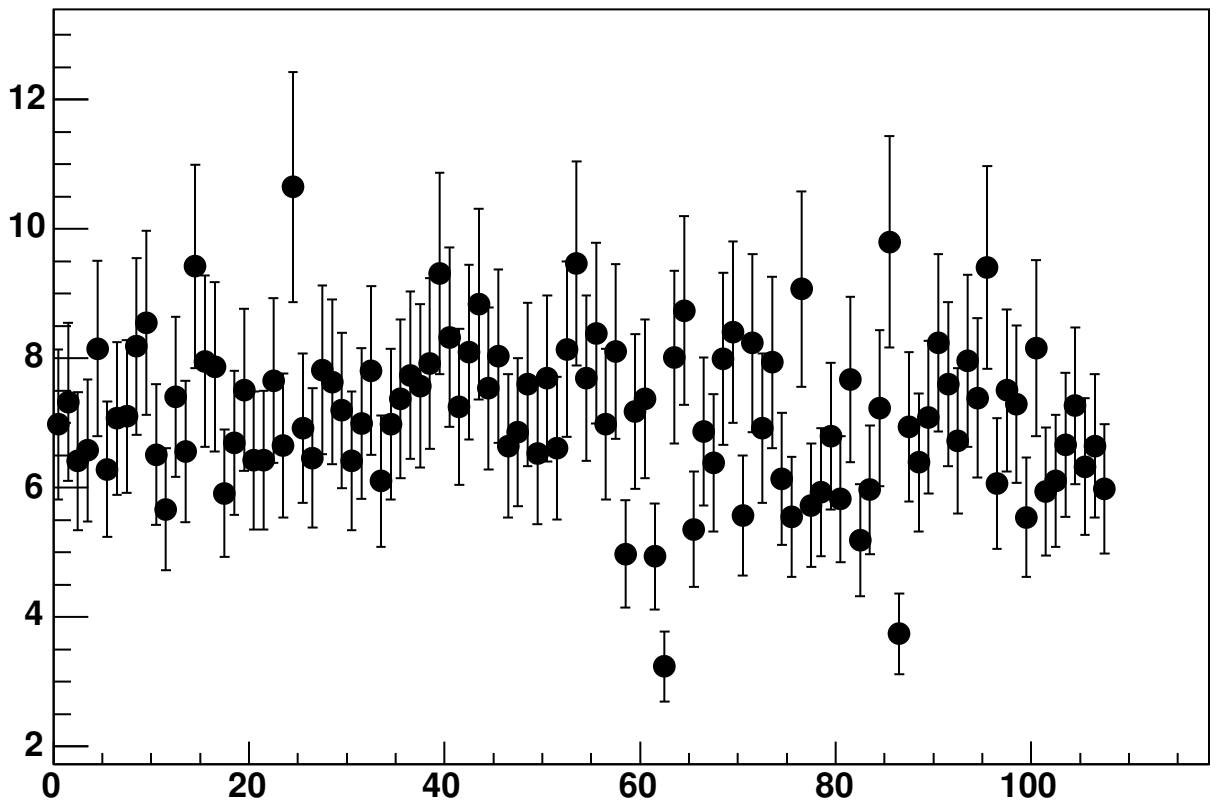
Enable 3, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



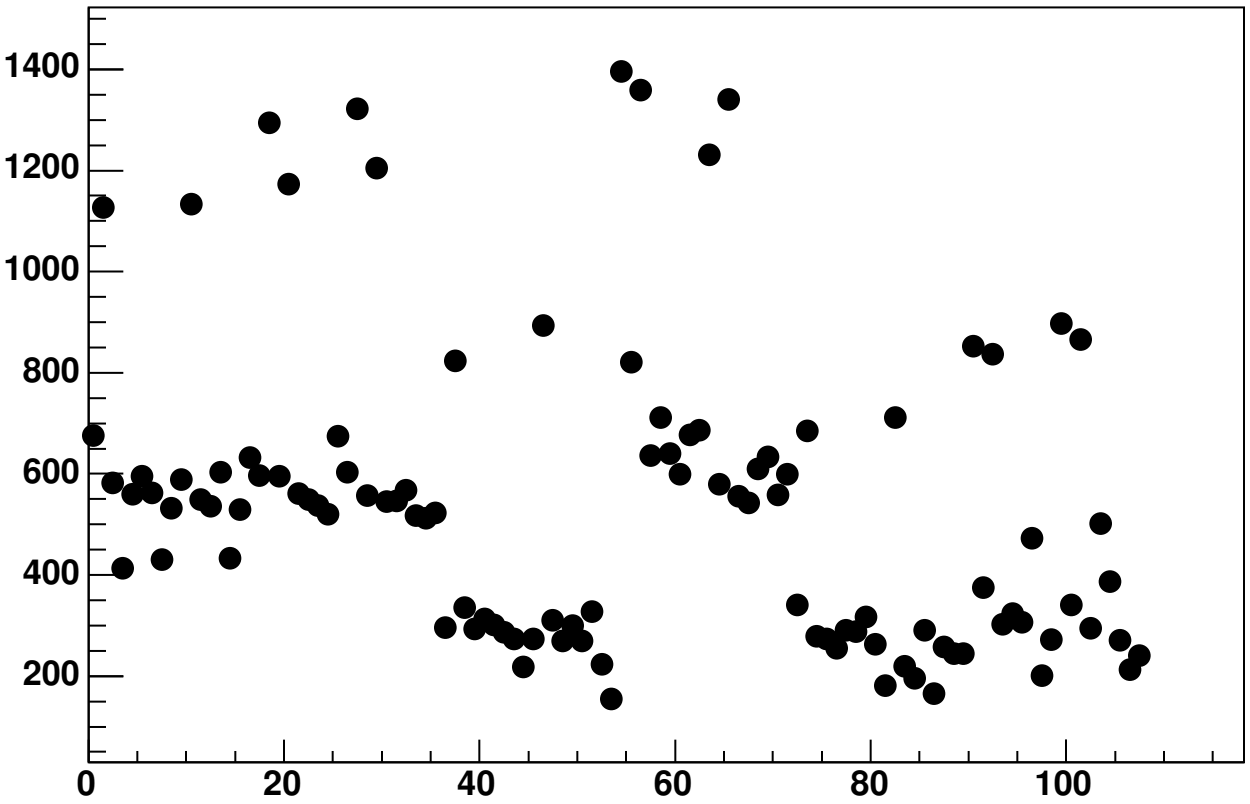
Enable 3, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



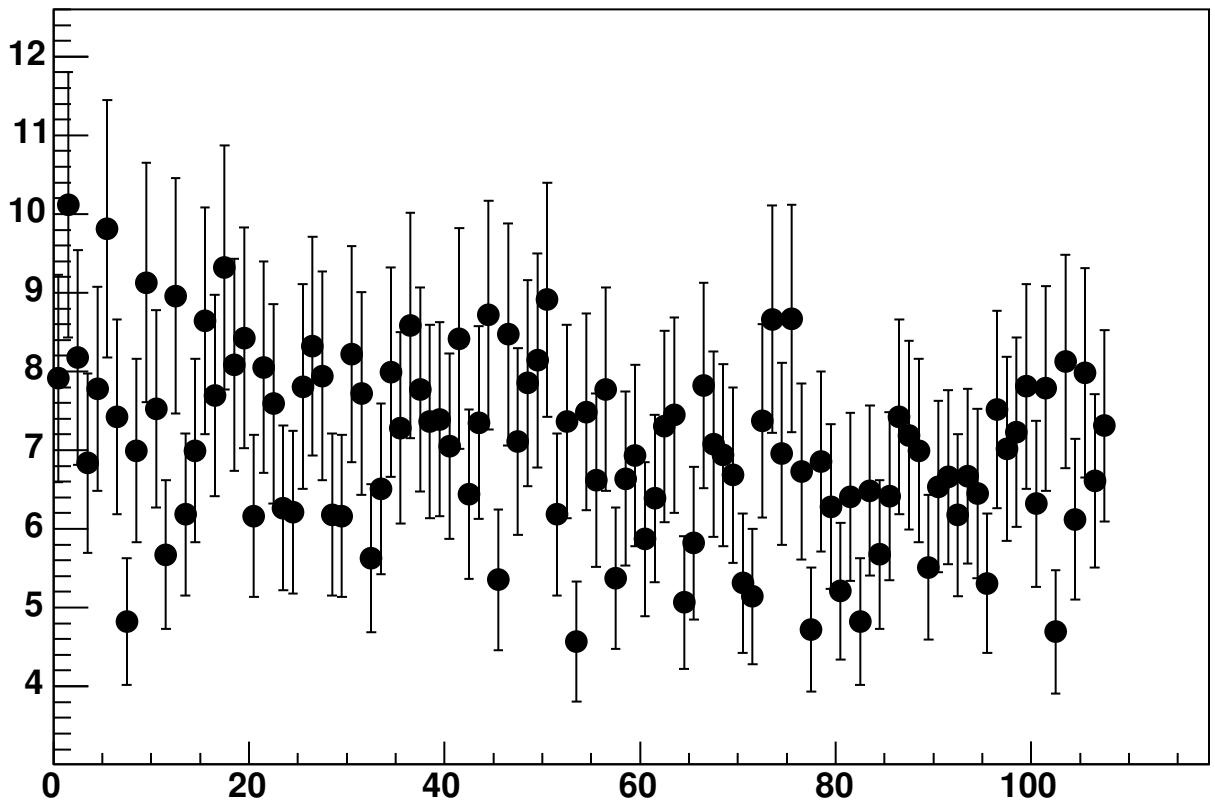
Enable 3, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



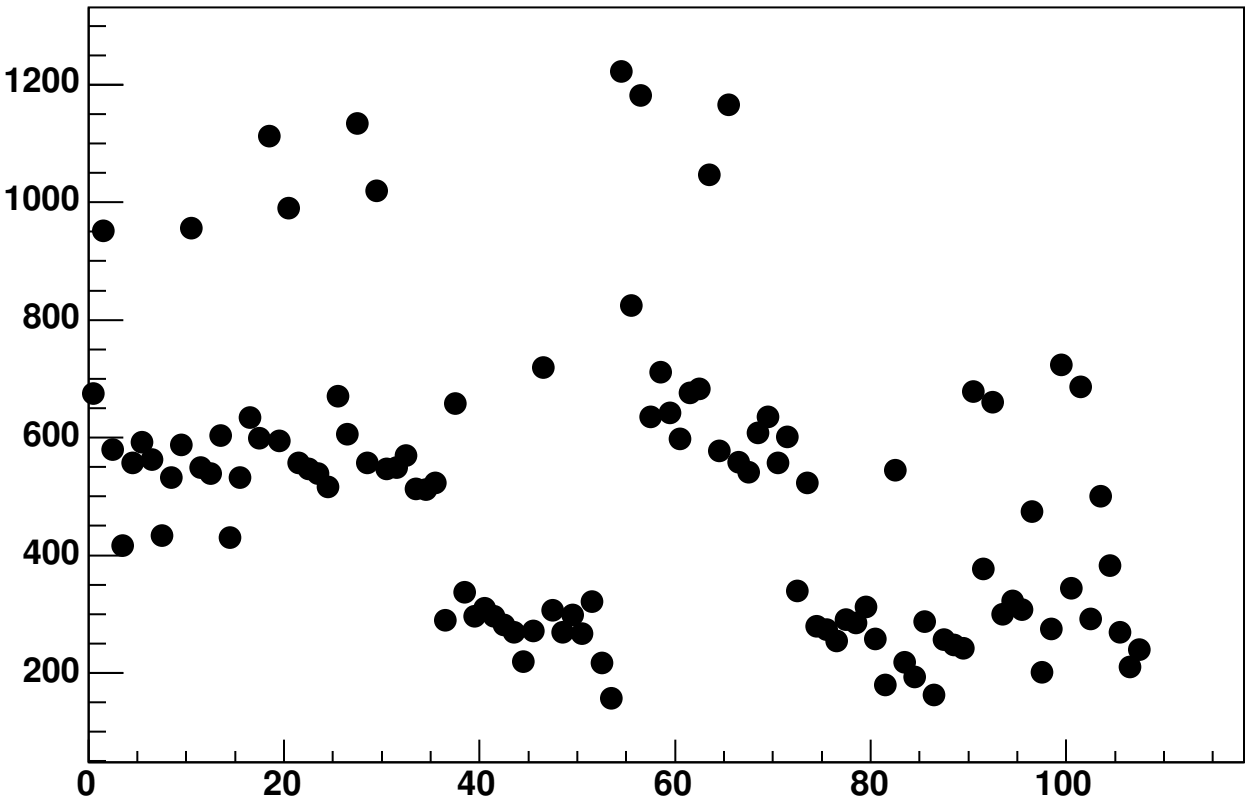
Enable 3, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



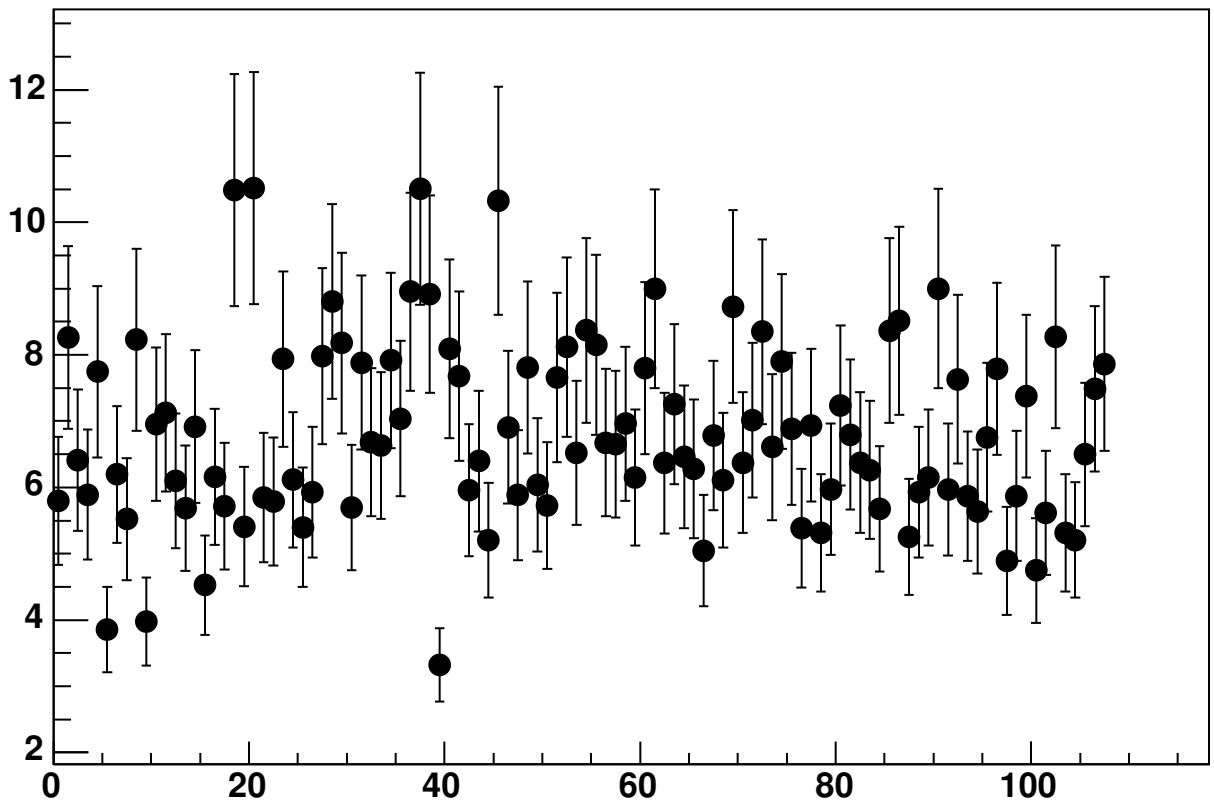
Enable 3, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



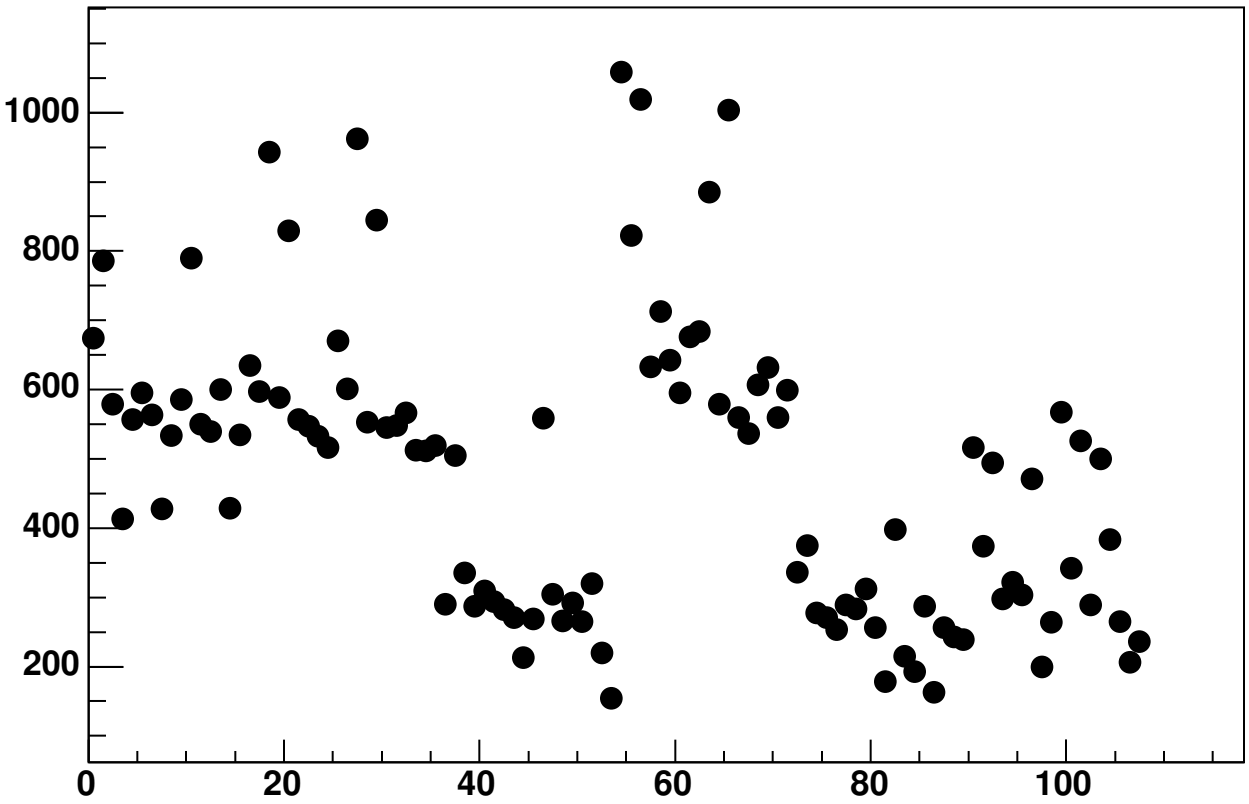
Enable 3, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



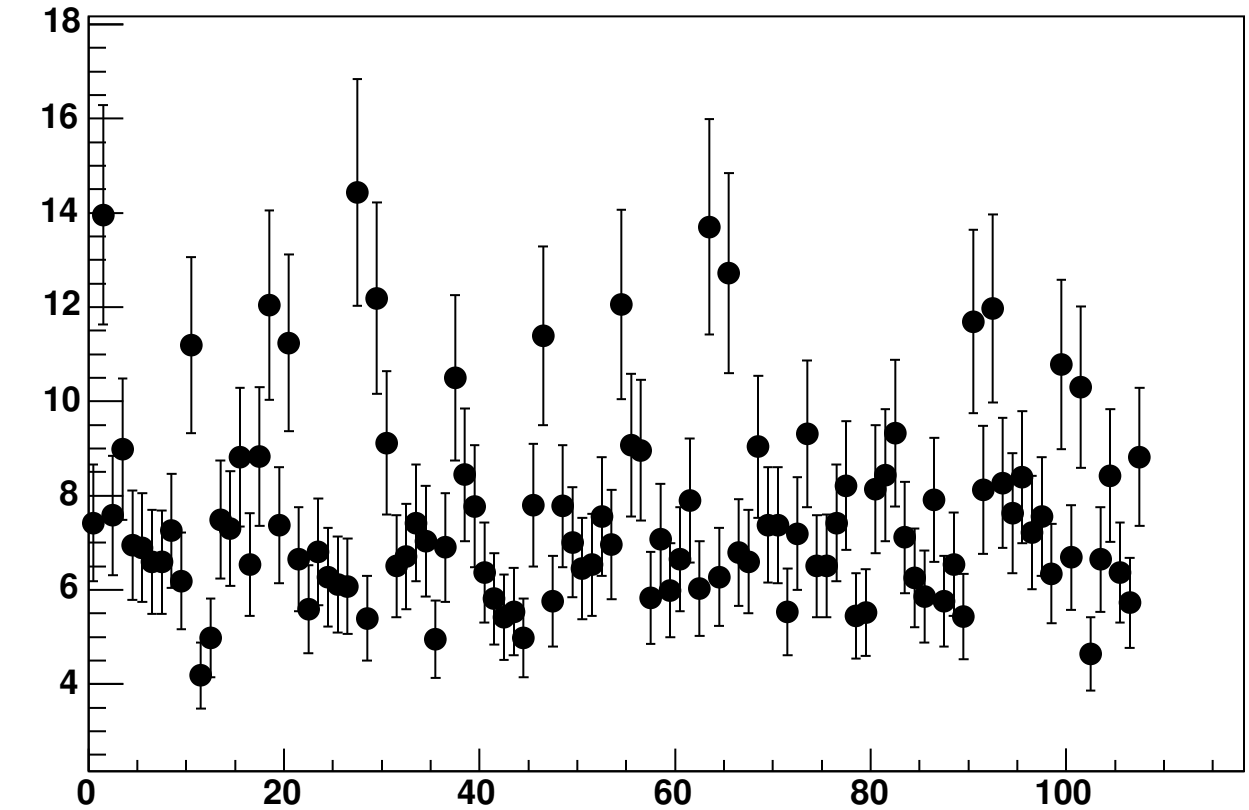
Enable 3, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



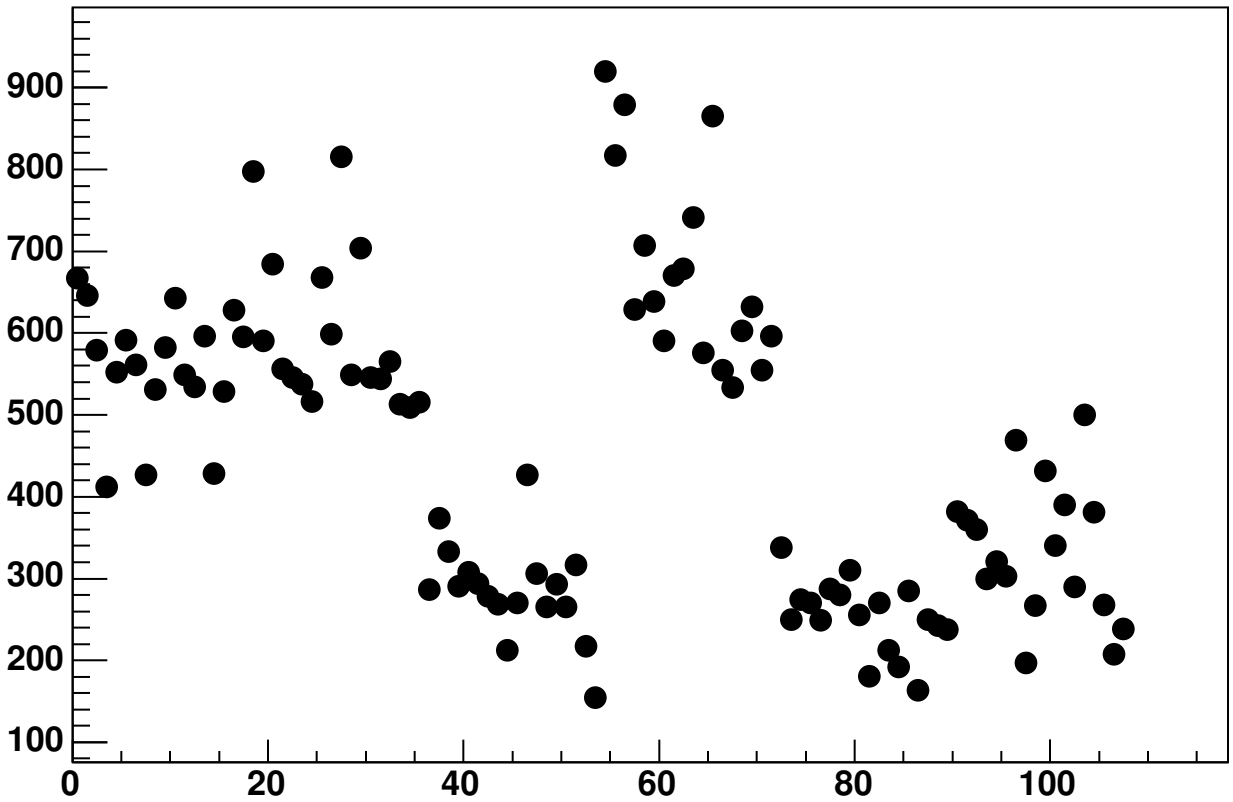
Enable 3, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



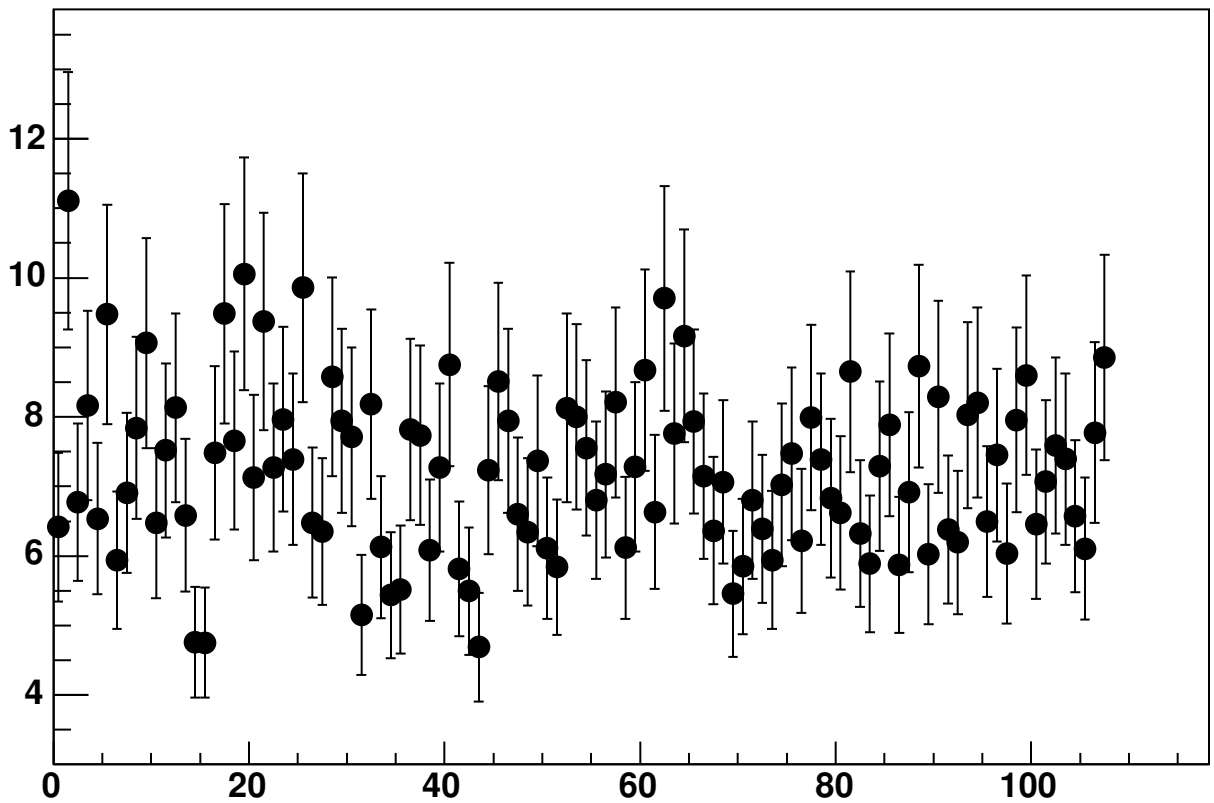
Enable 3, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



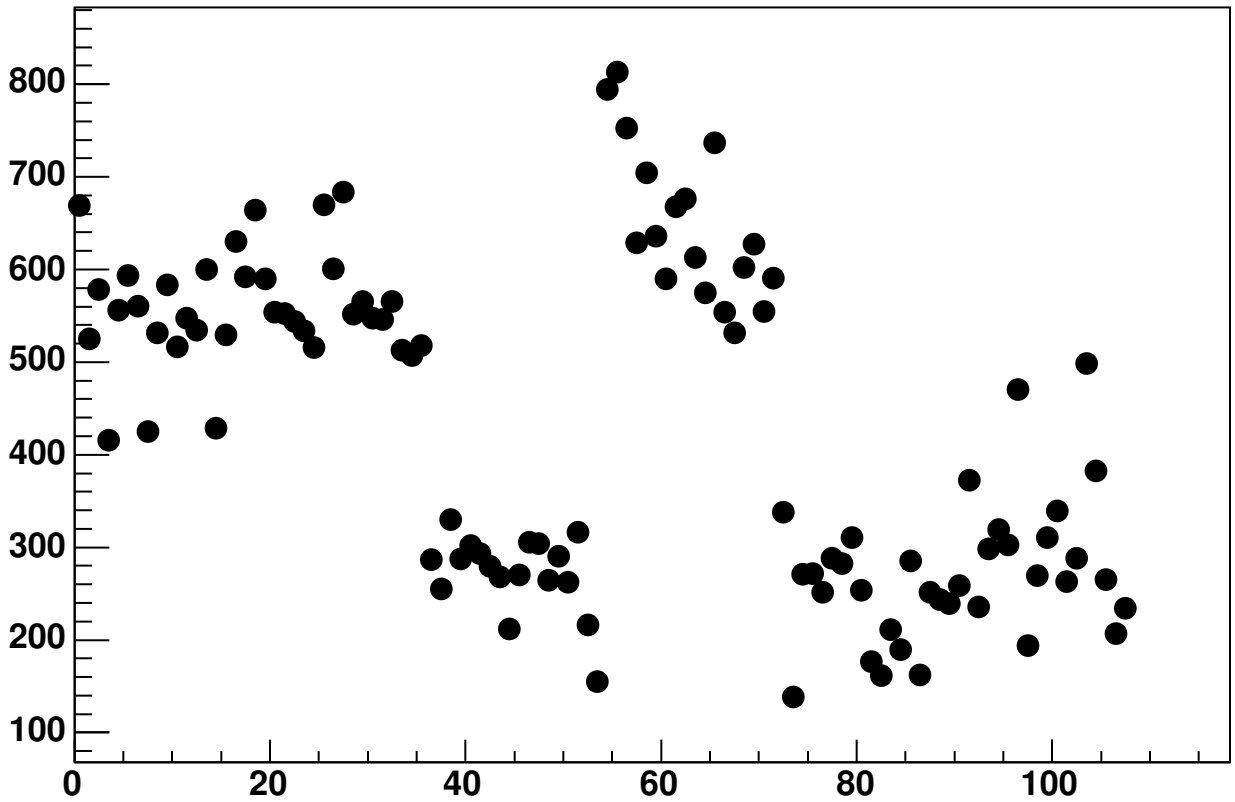
Enable 3, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



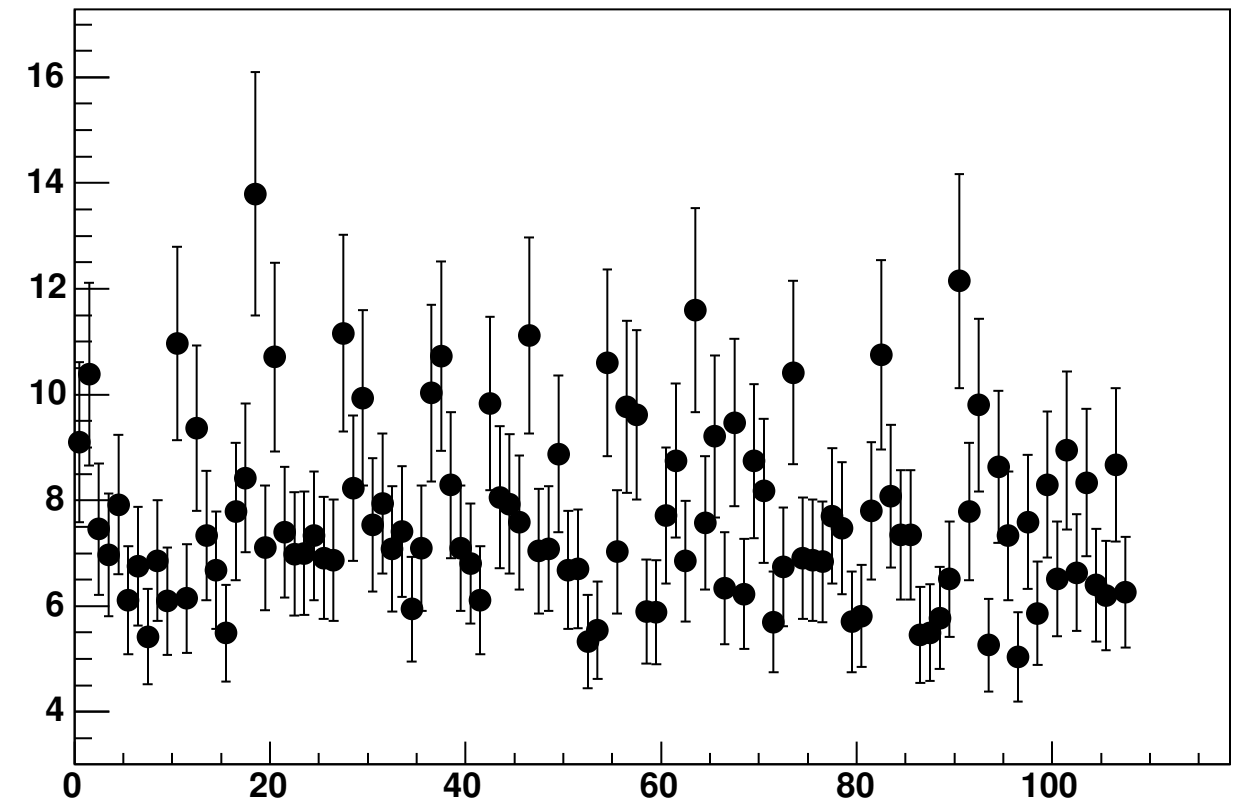
Enable 3, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



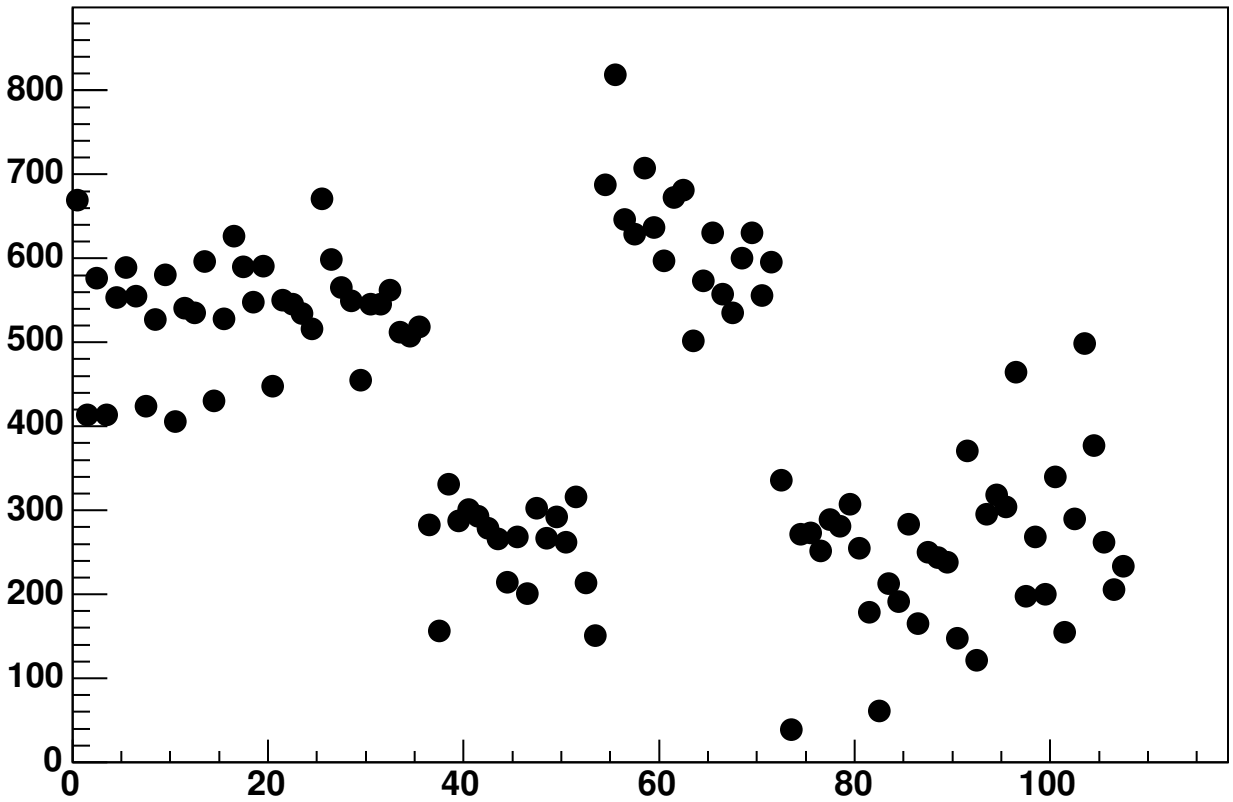
Enable 3, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



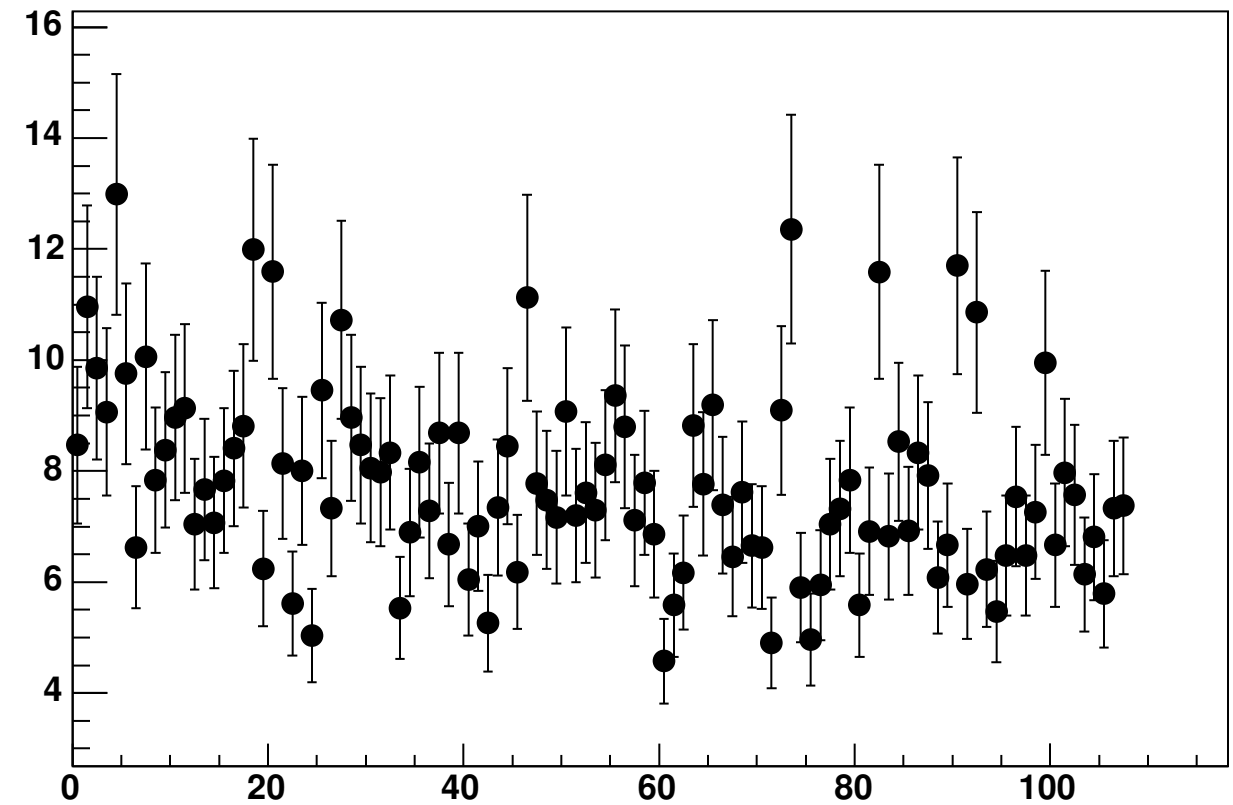
Enable 3, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



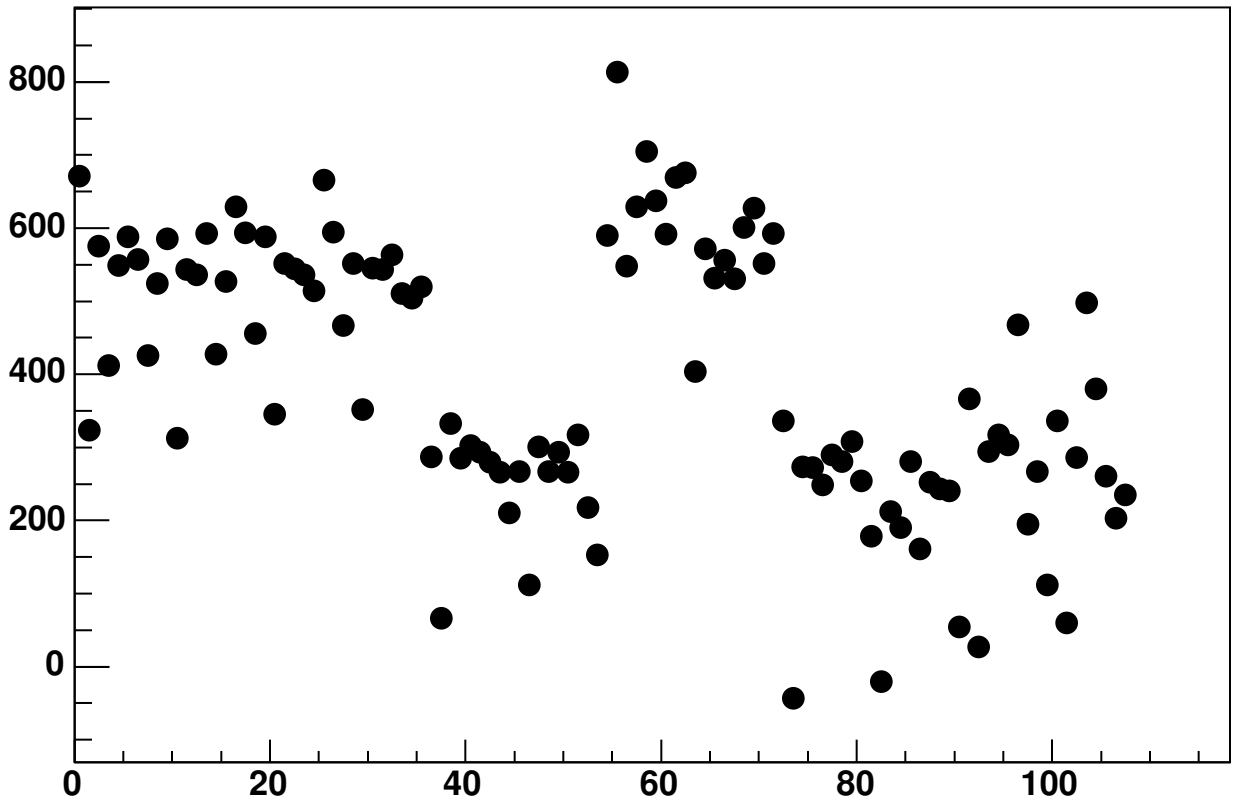
Enable 3, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



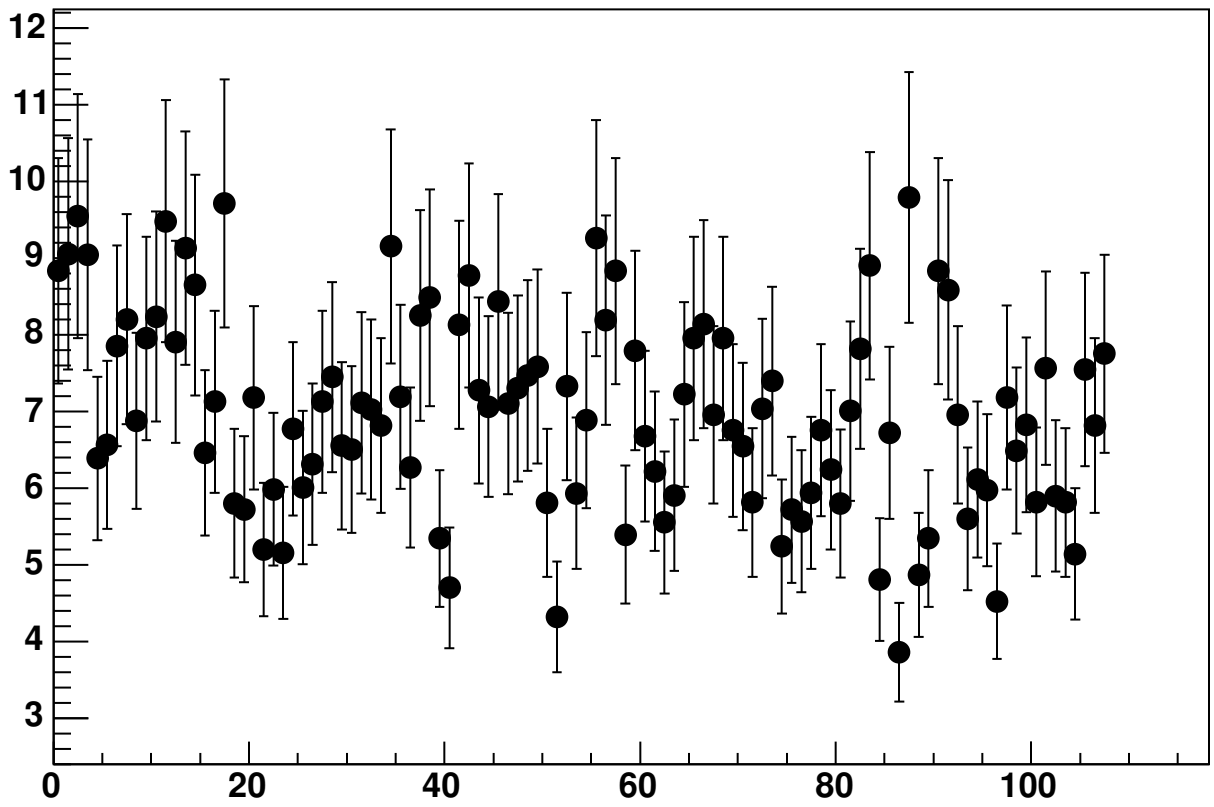
Enable 3, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



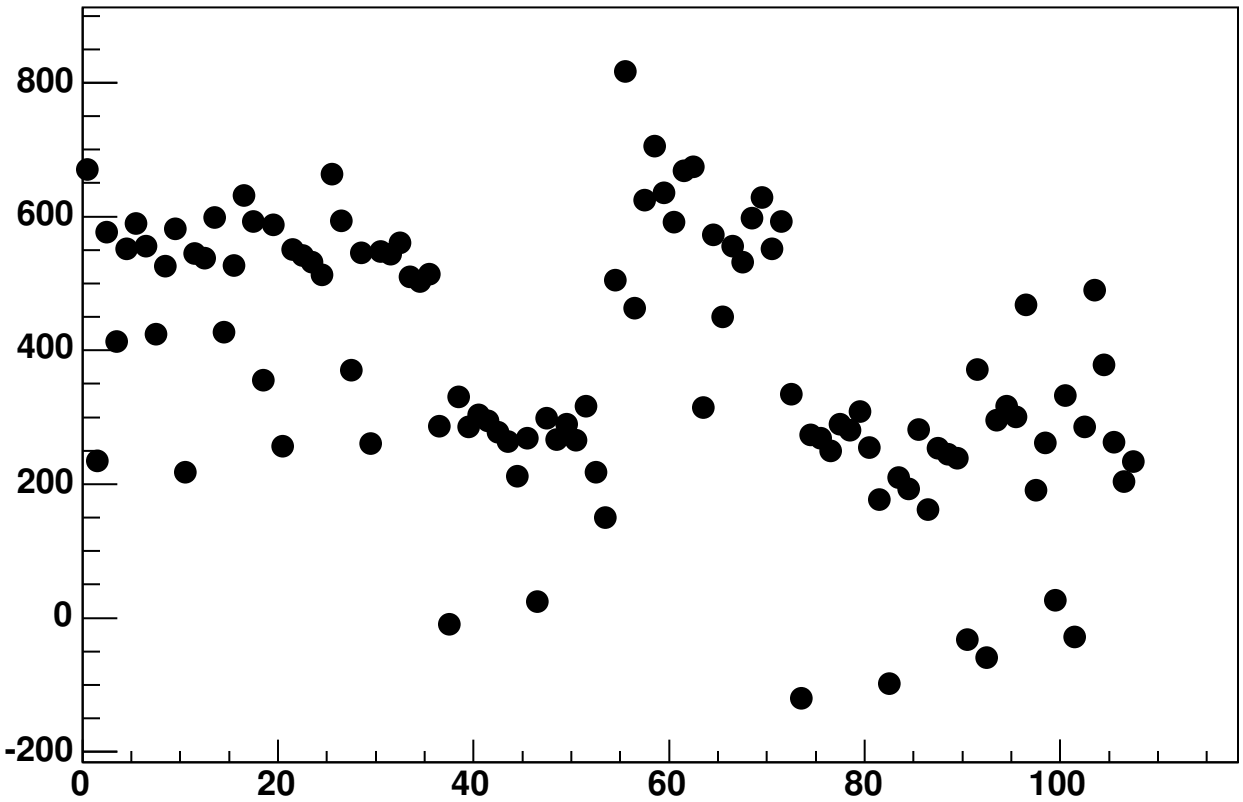
Enable 3, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



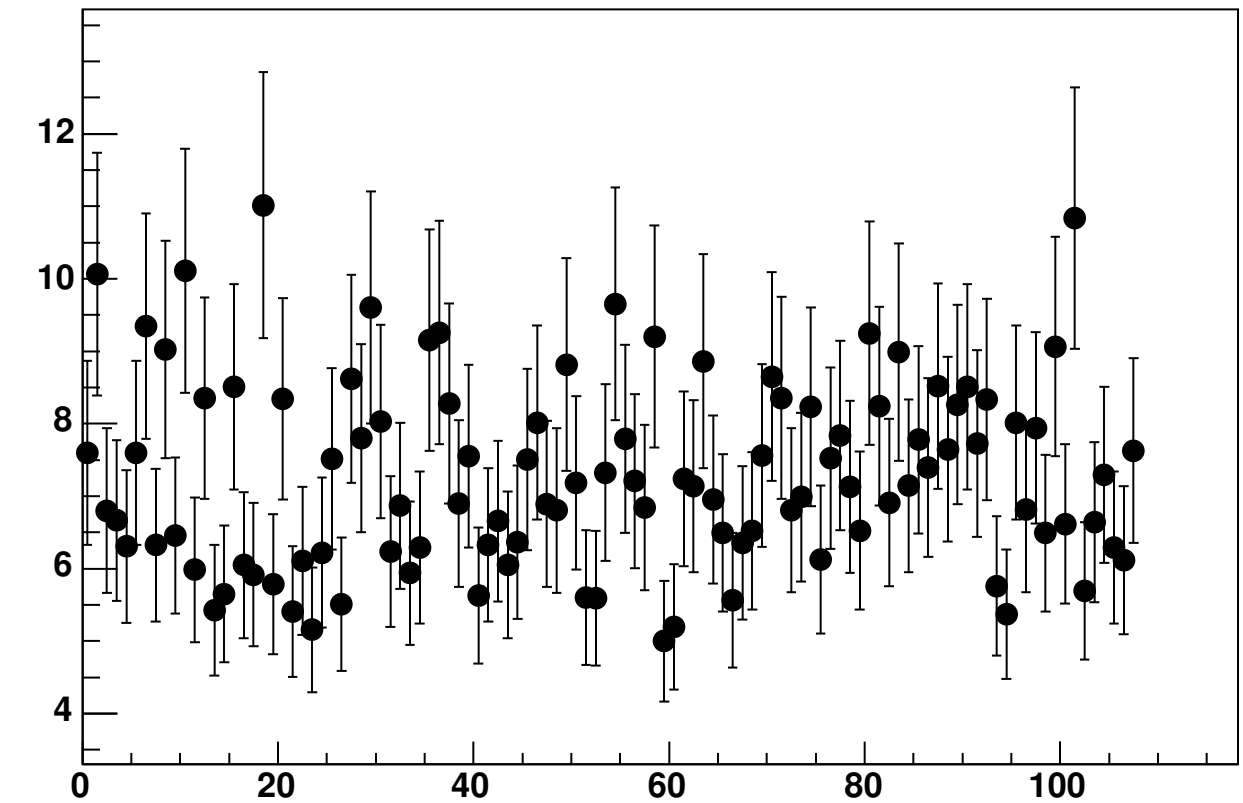
Enable 3, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



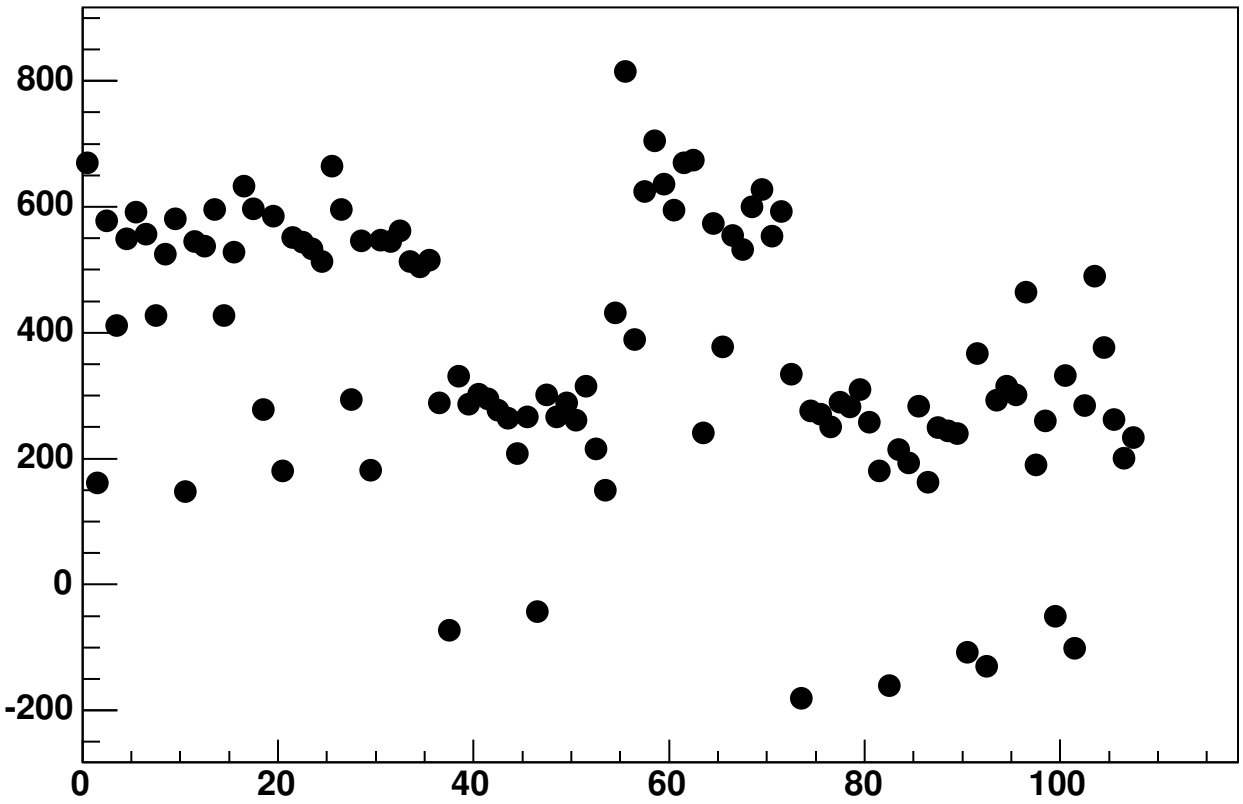
Enable 3, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



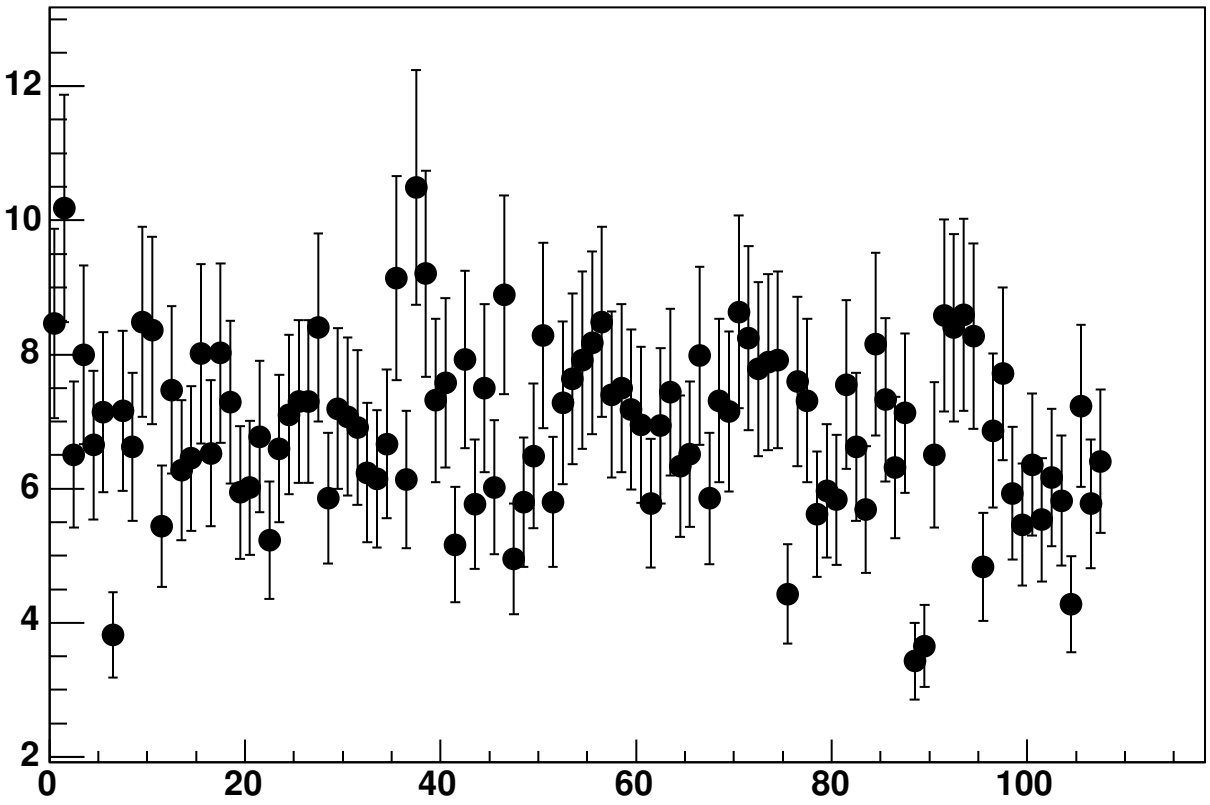
Enable 3, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



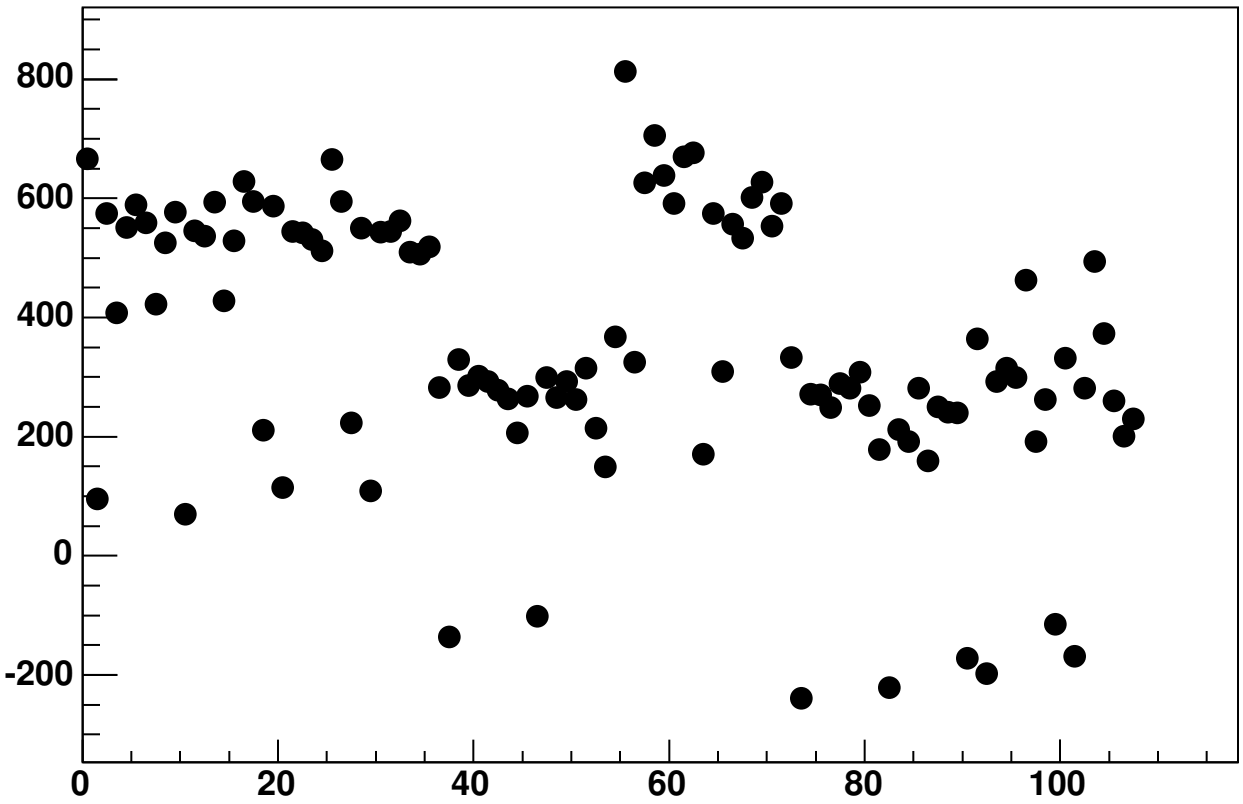
Enable 3, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



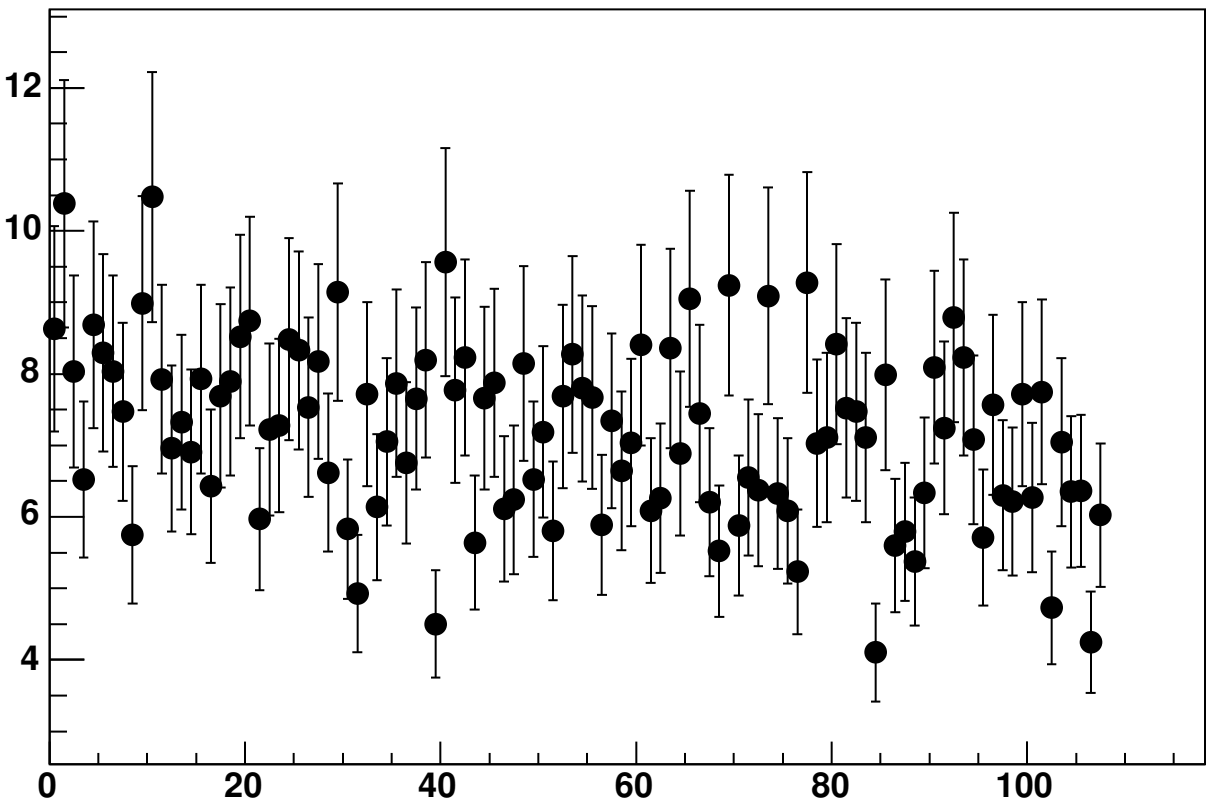
Enable 3, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



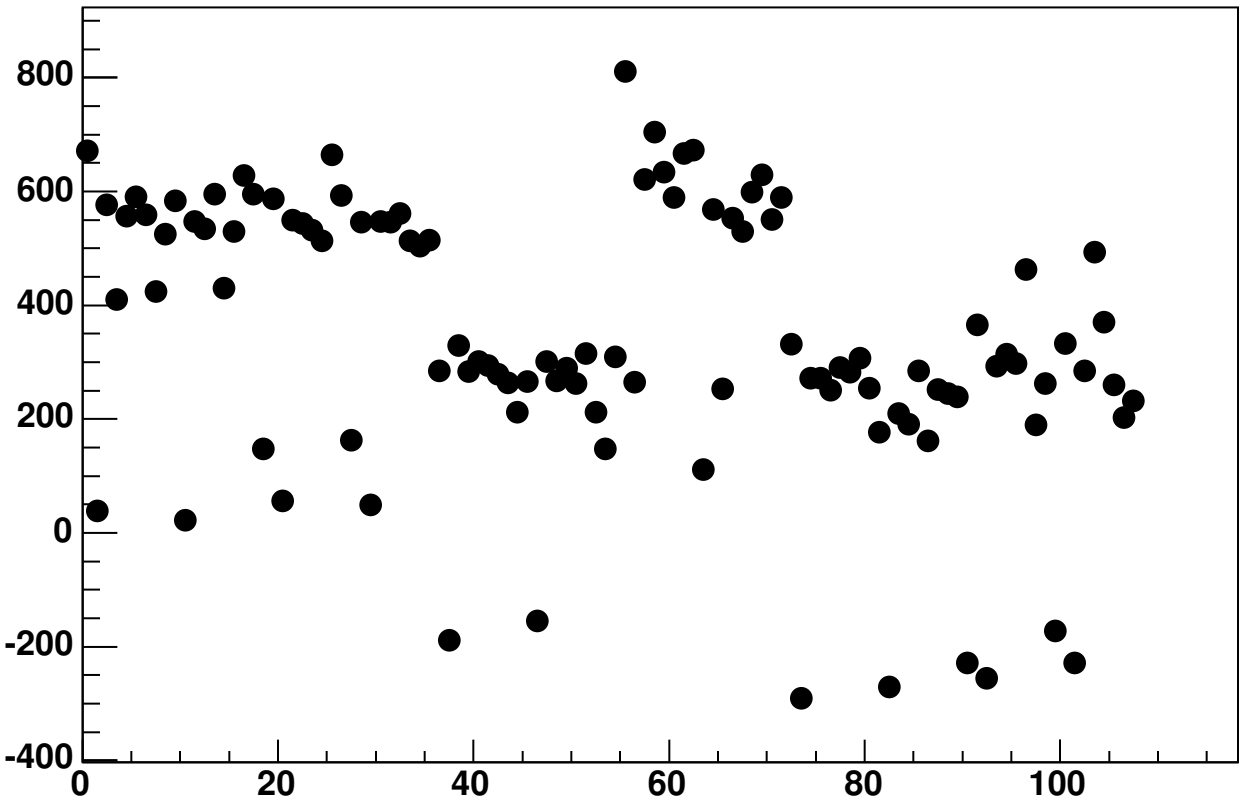
Enable 3, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



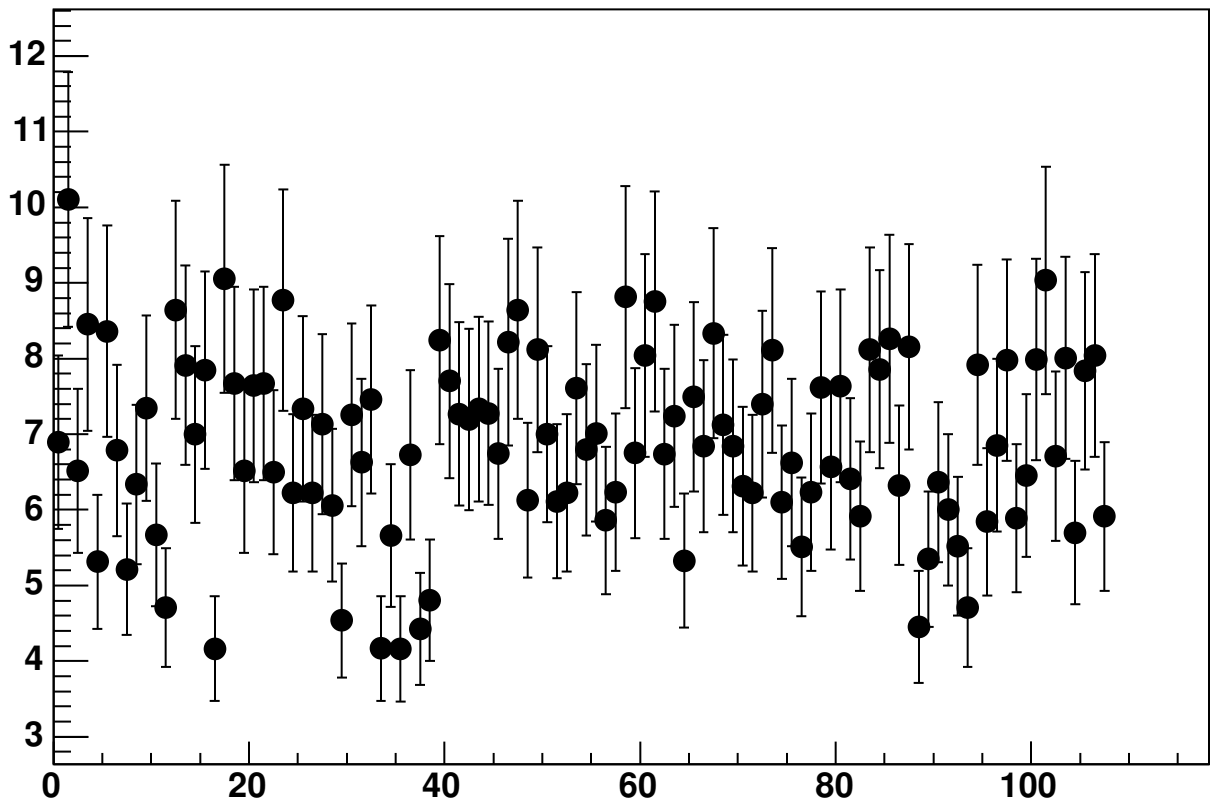
Enable 3, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



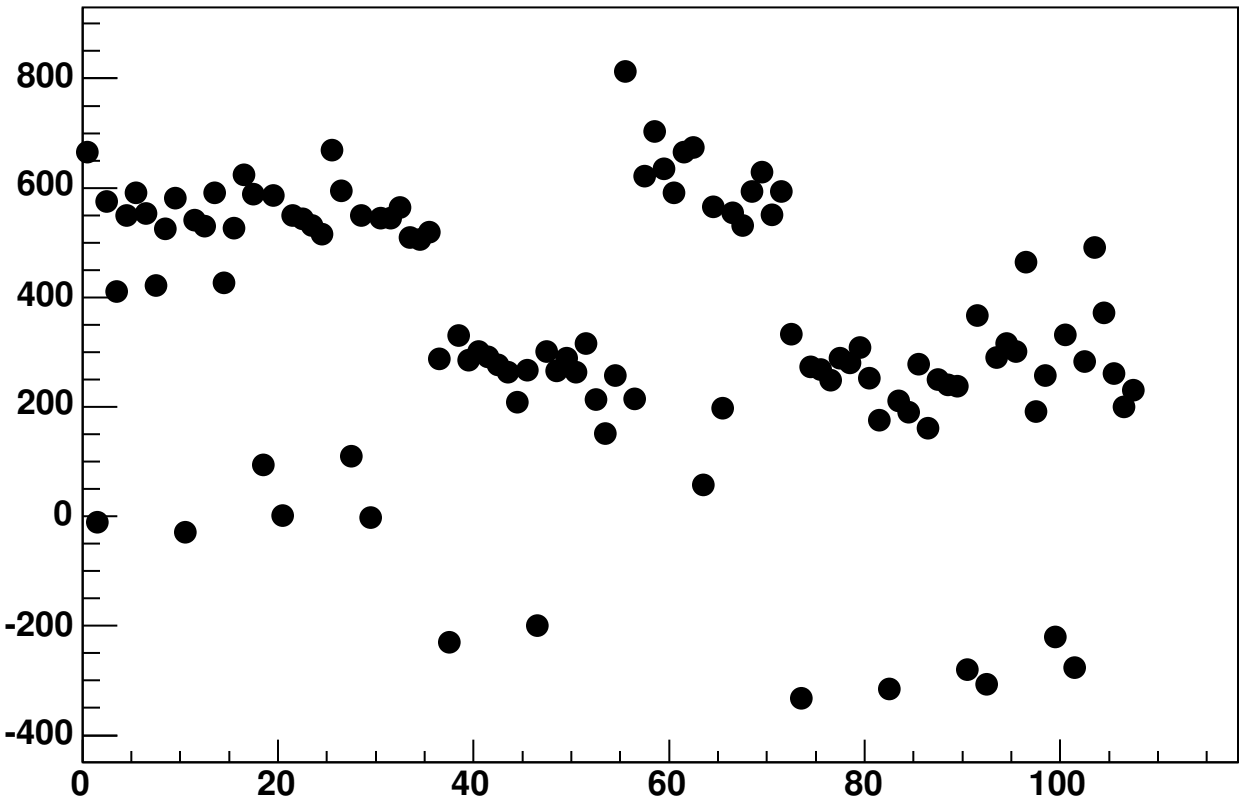
Enable 3, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



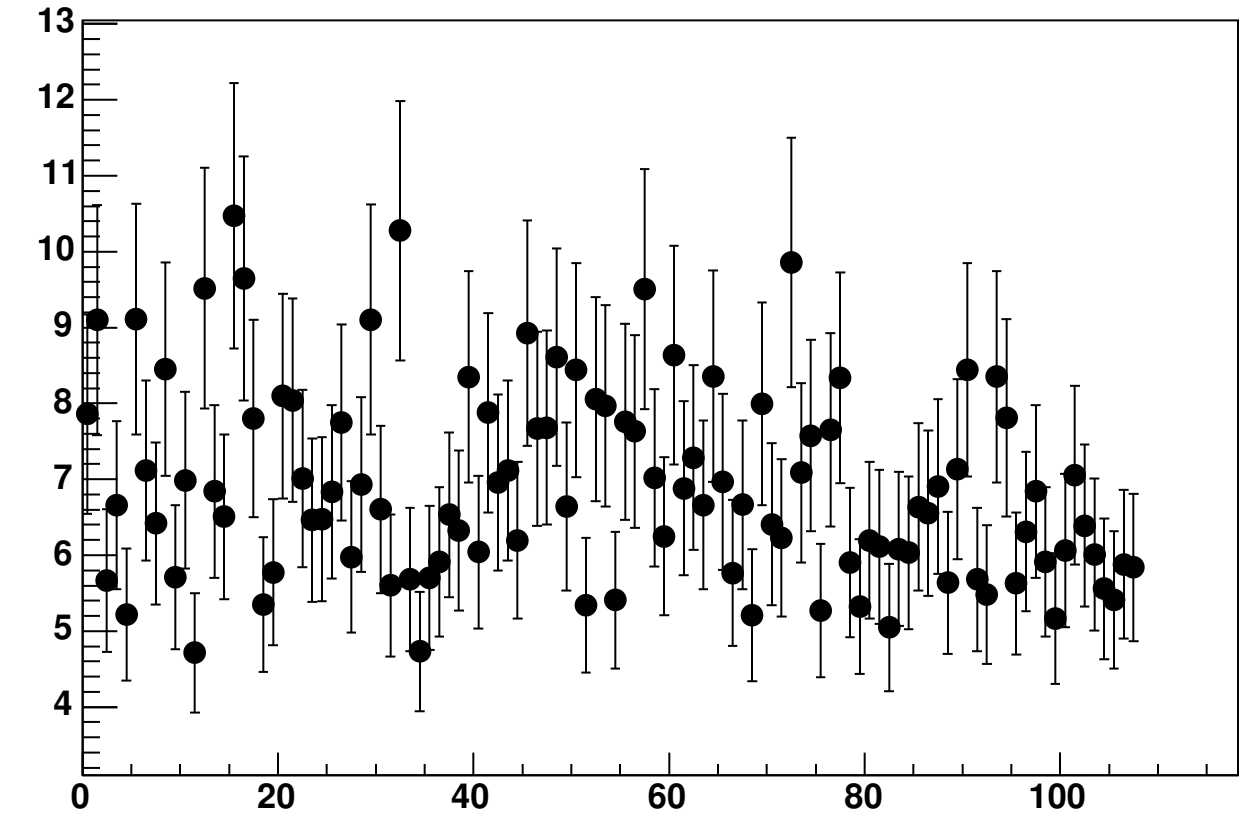
Enable 3, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



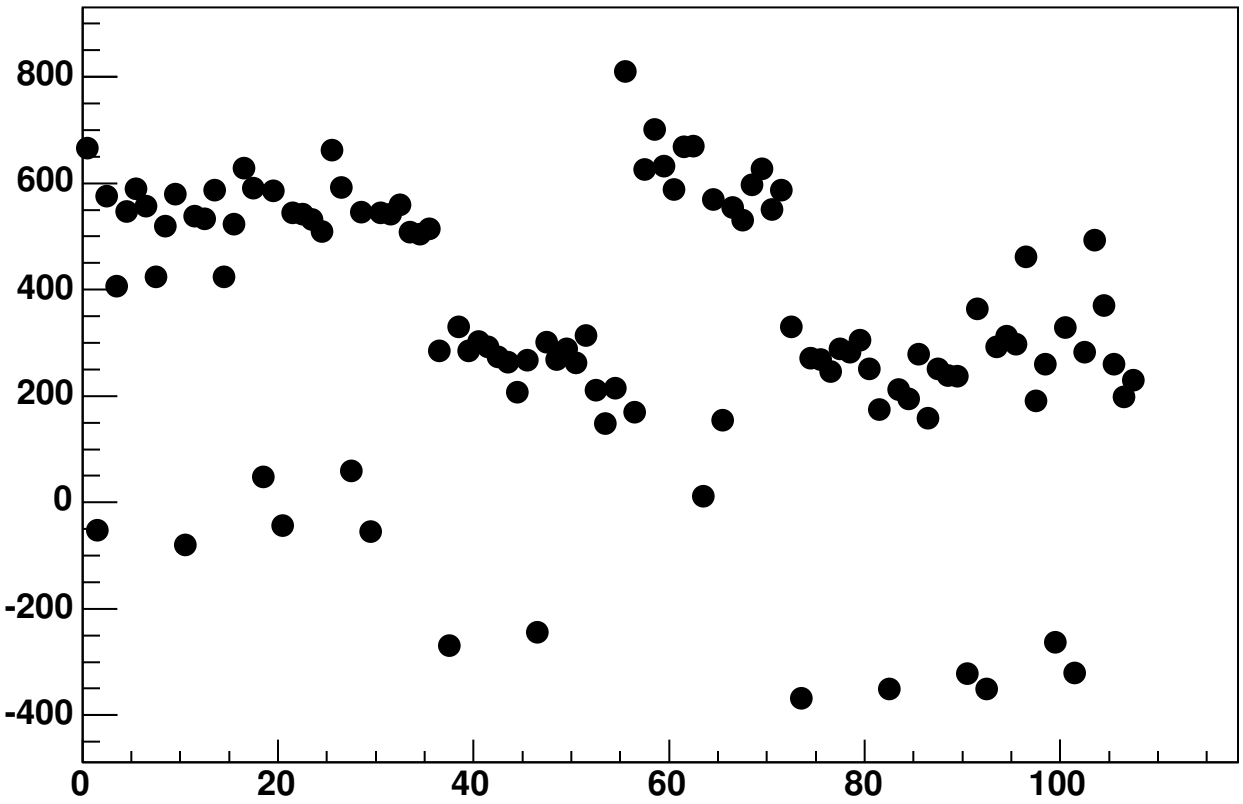
Enable 3, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



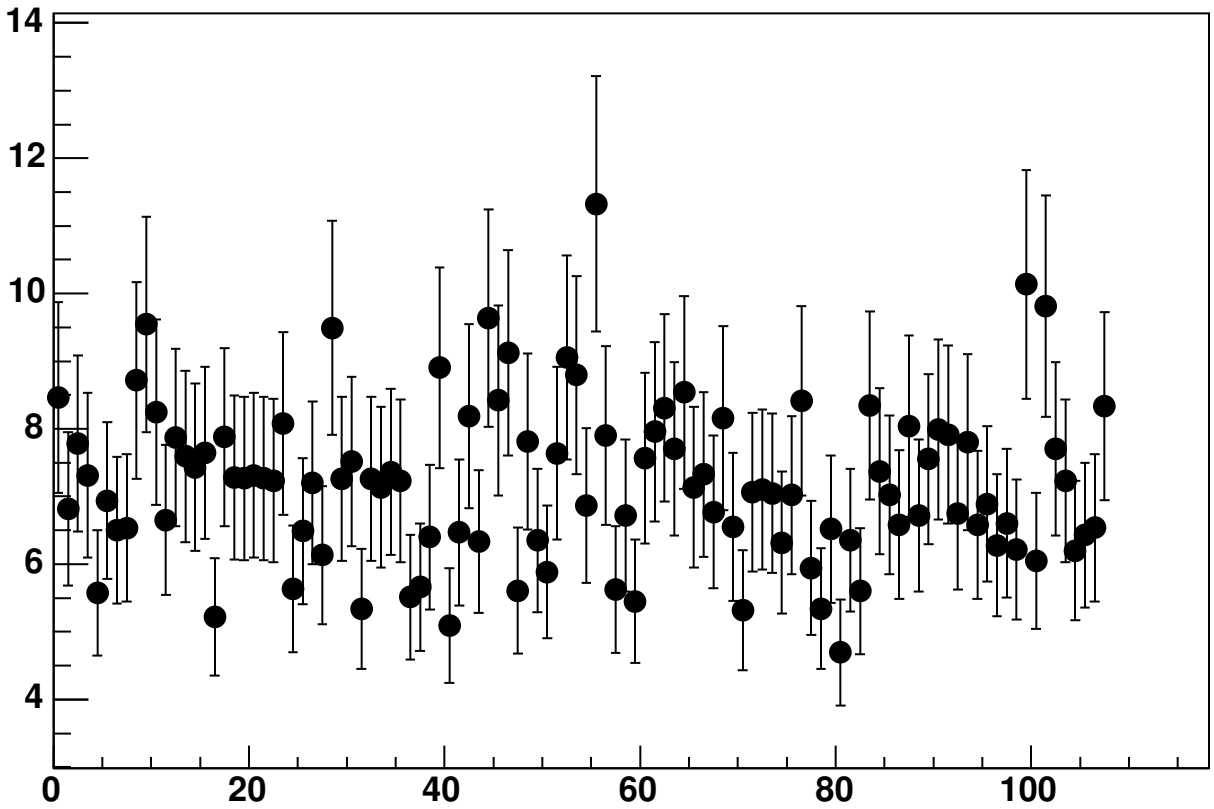
Enable 3, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



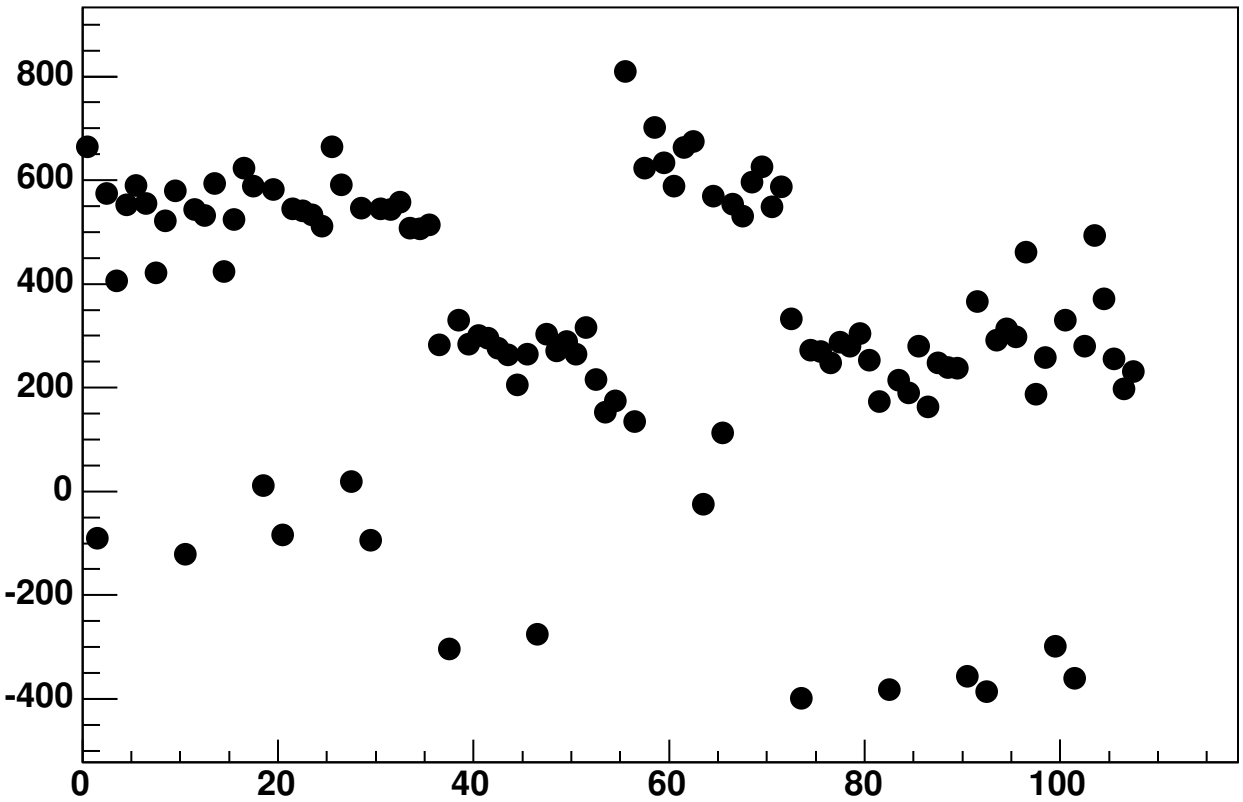
Enable 3, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



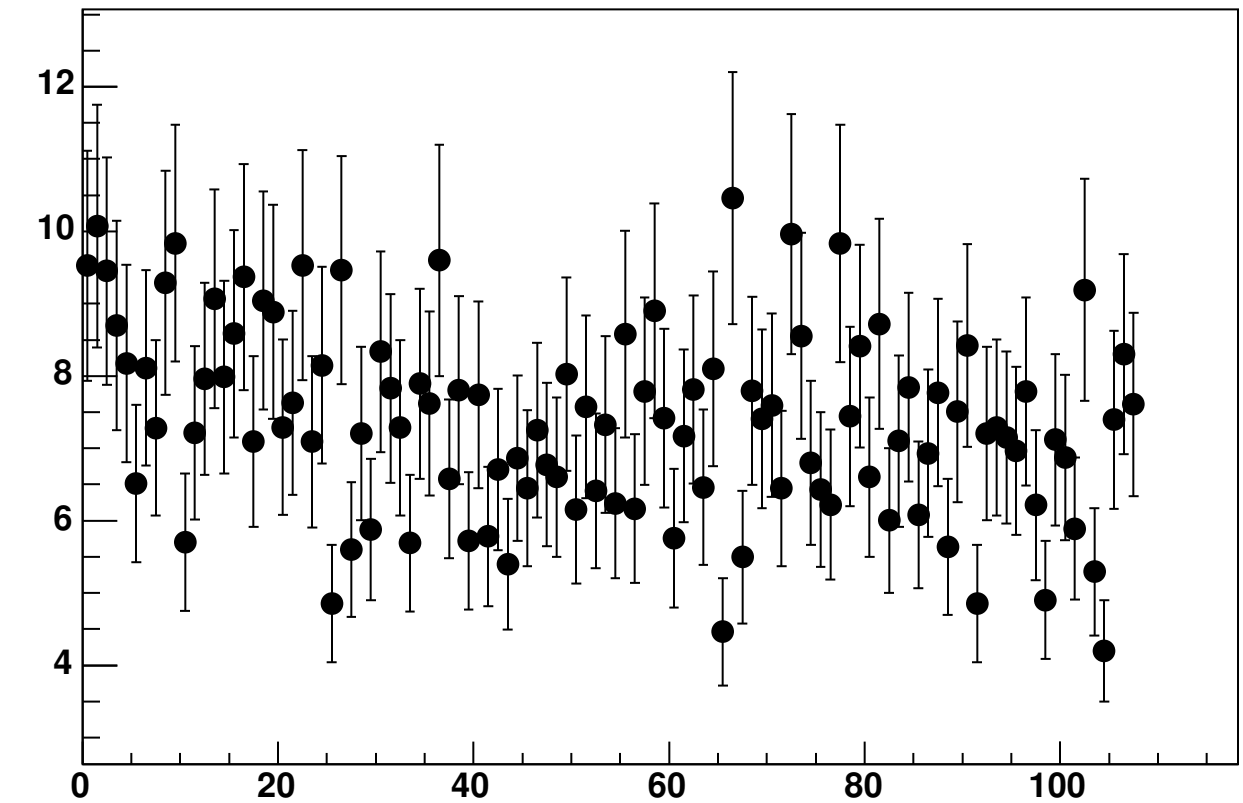
Enable 3, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



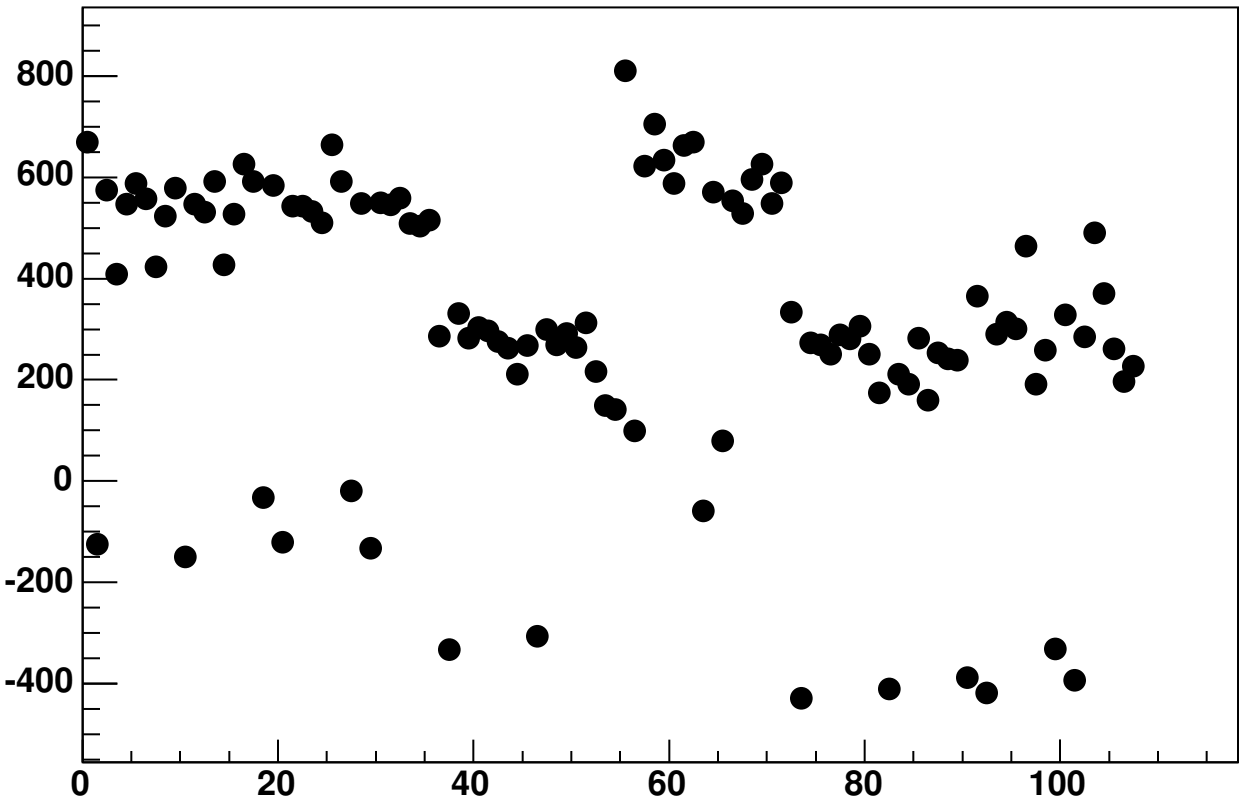
Enable 3, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



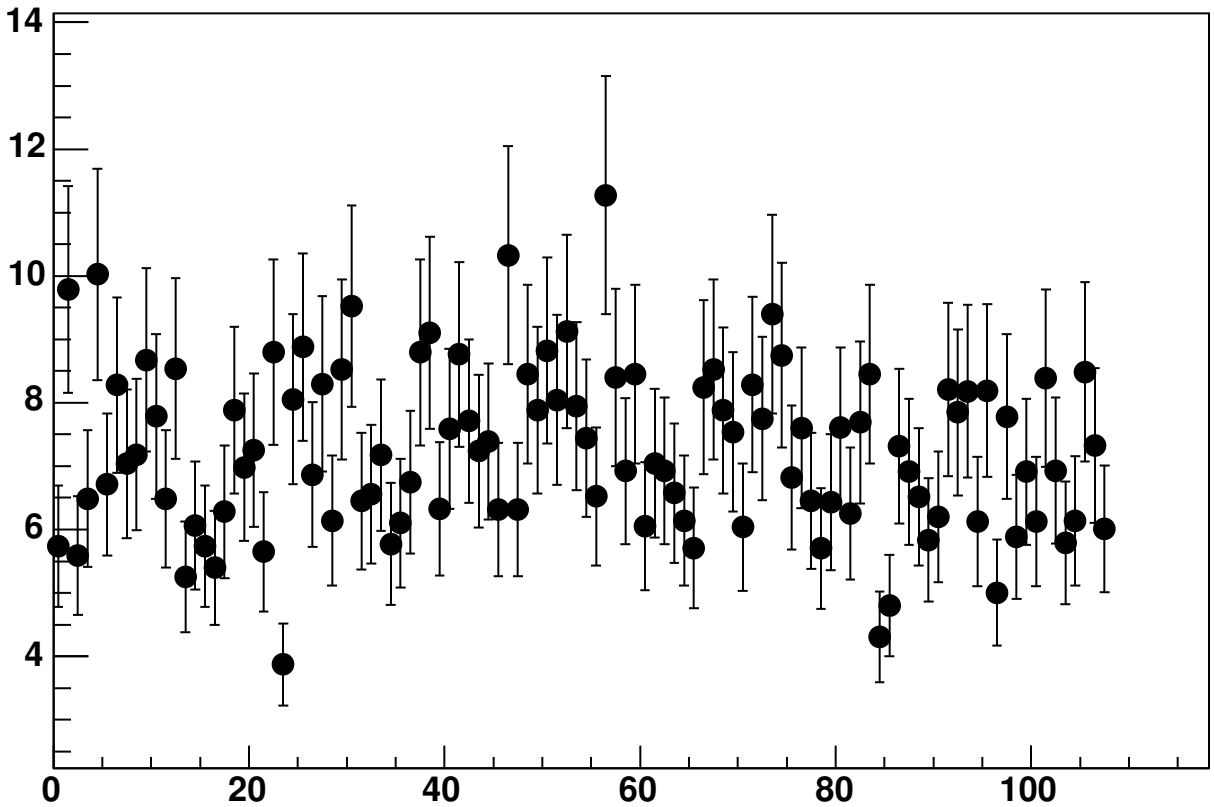
Enable 3, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



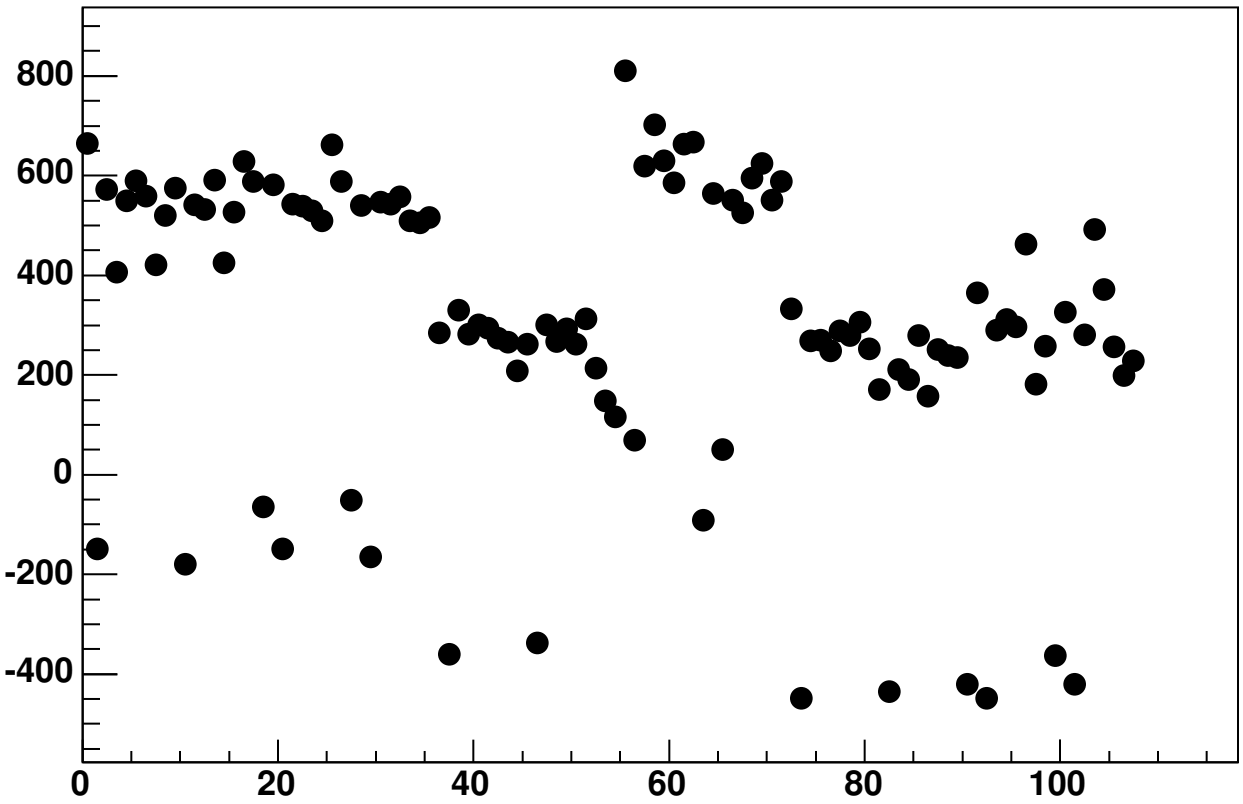
Enable 3, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



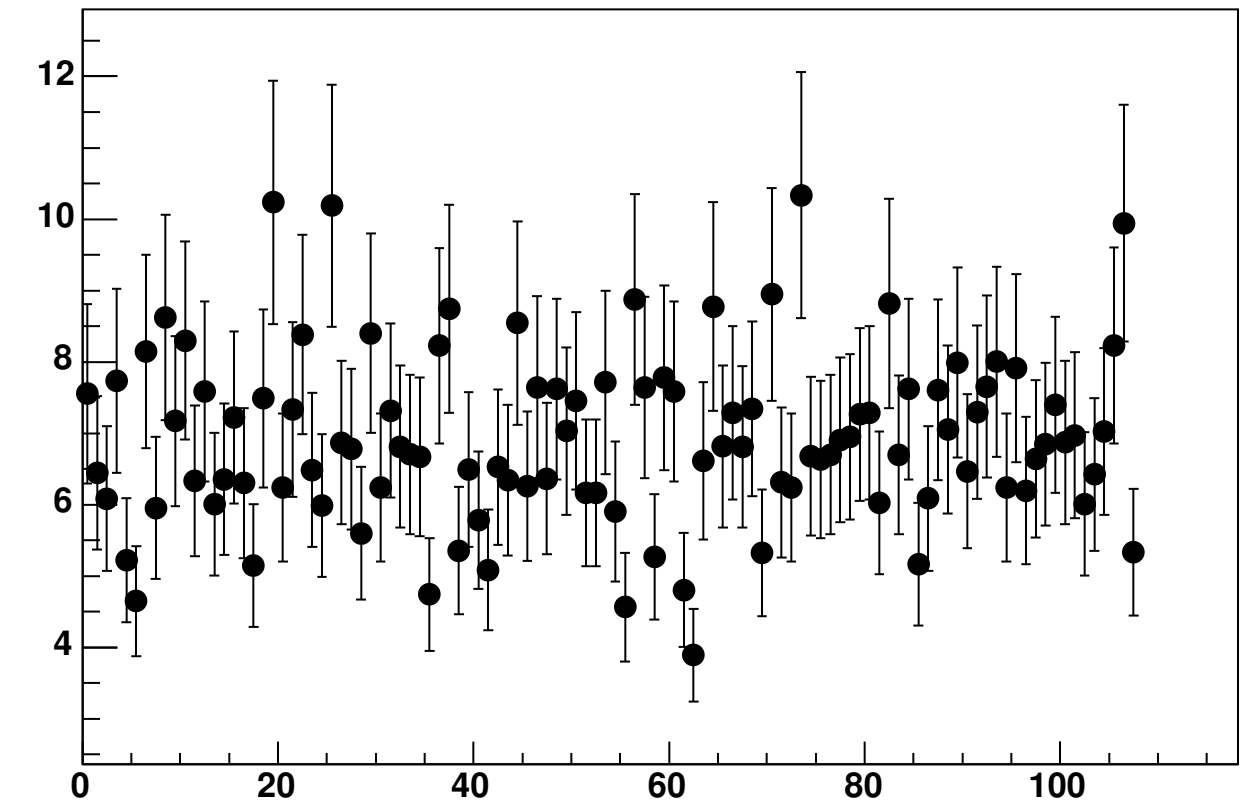
Enable 3, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



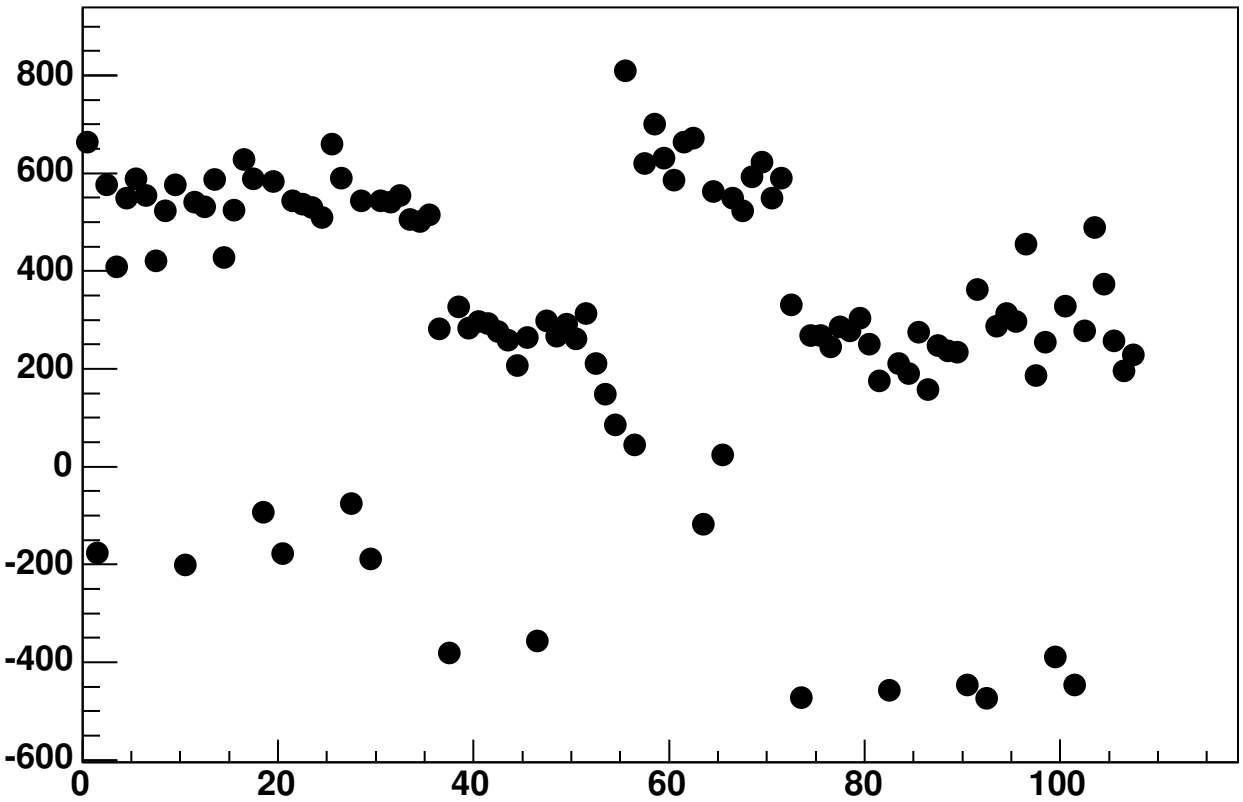
Enable 3, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



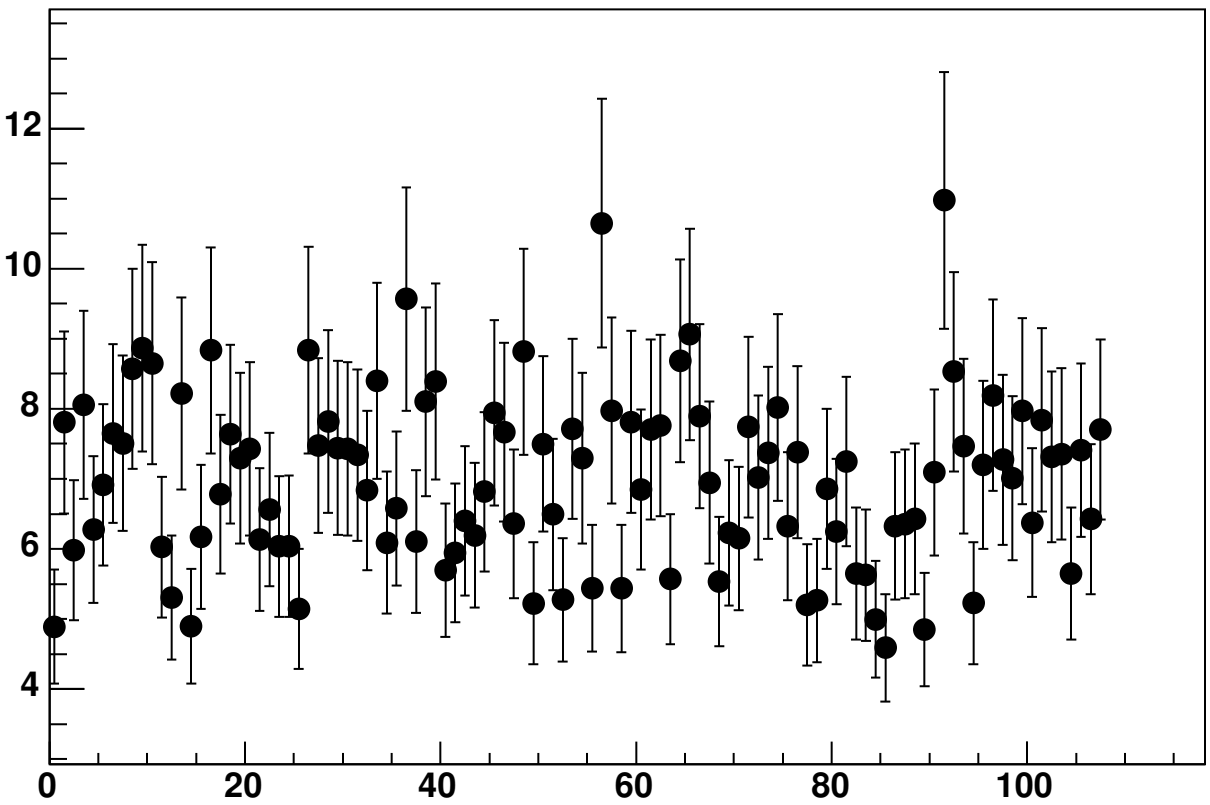
Enable 3, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



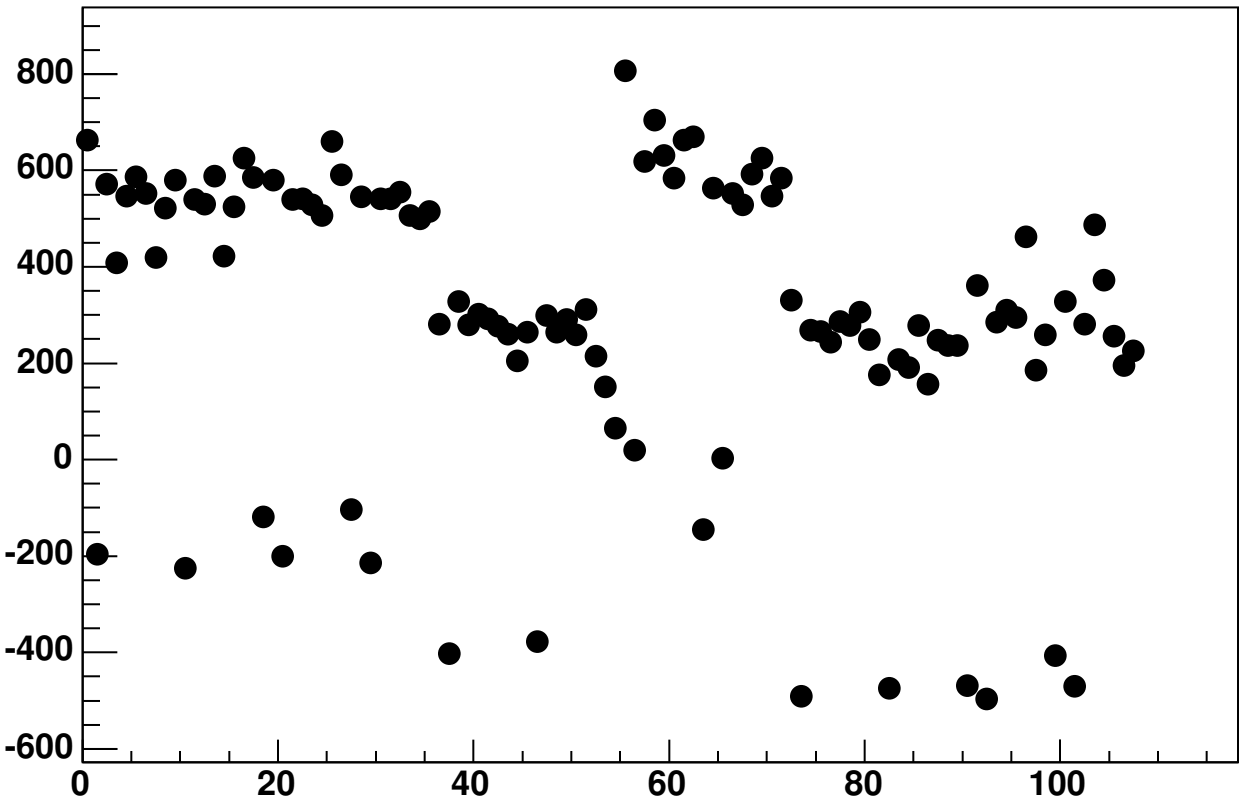
Enable 3, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



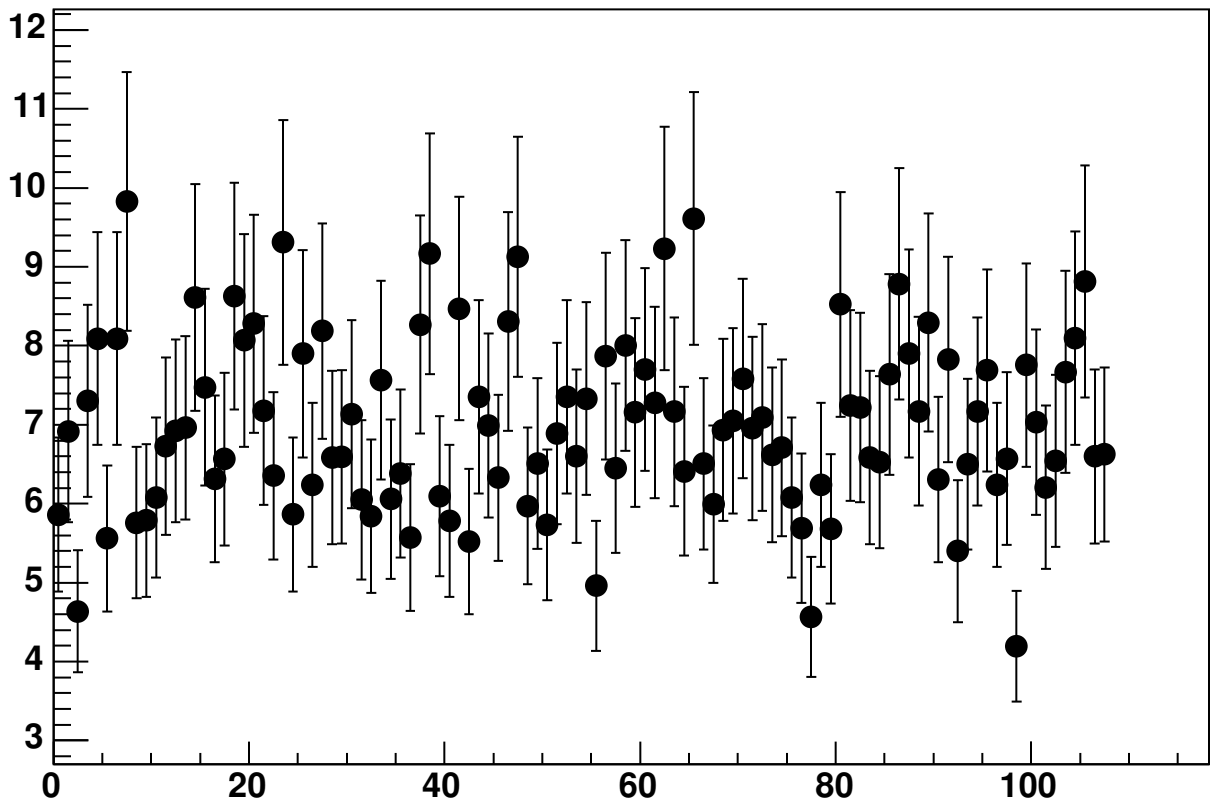
Enable 3, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



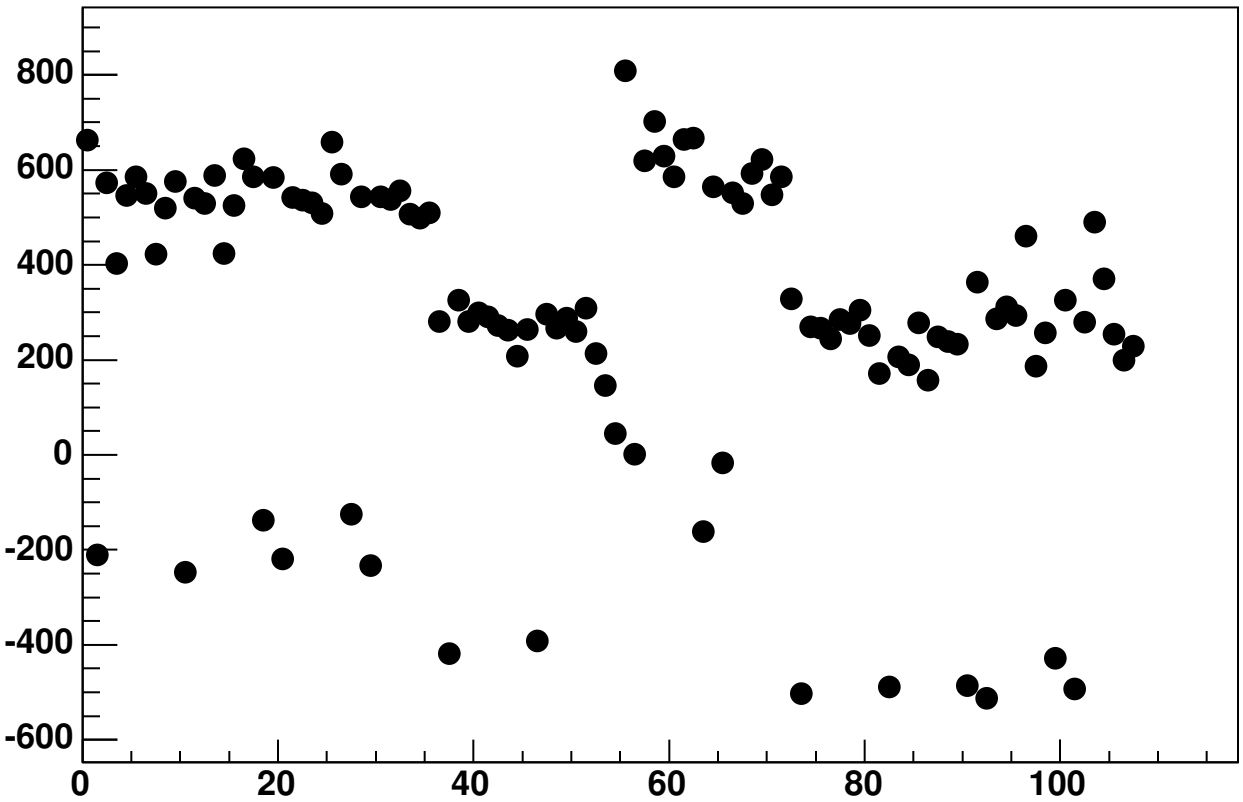
Enable 3, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



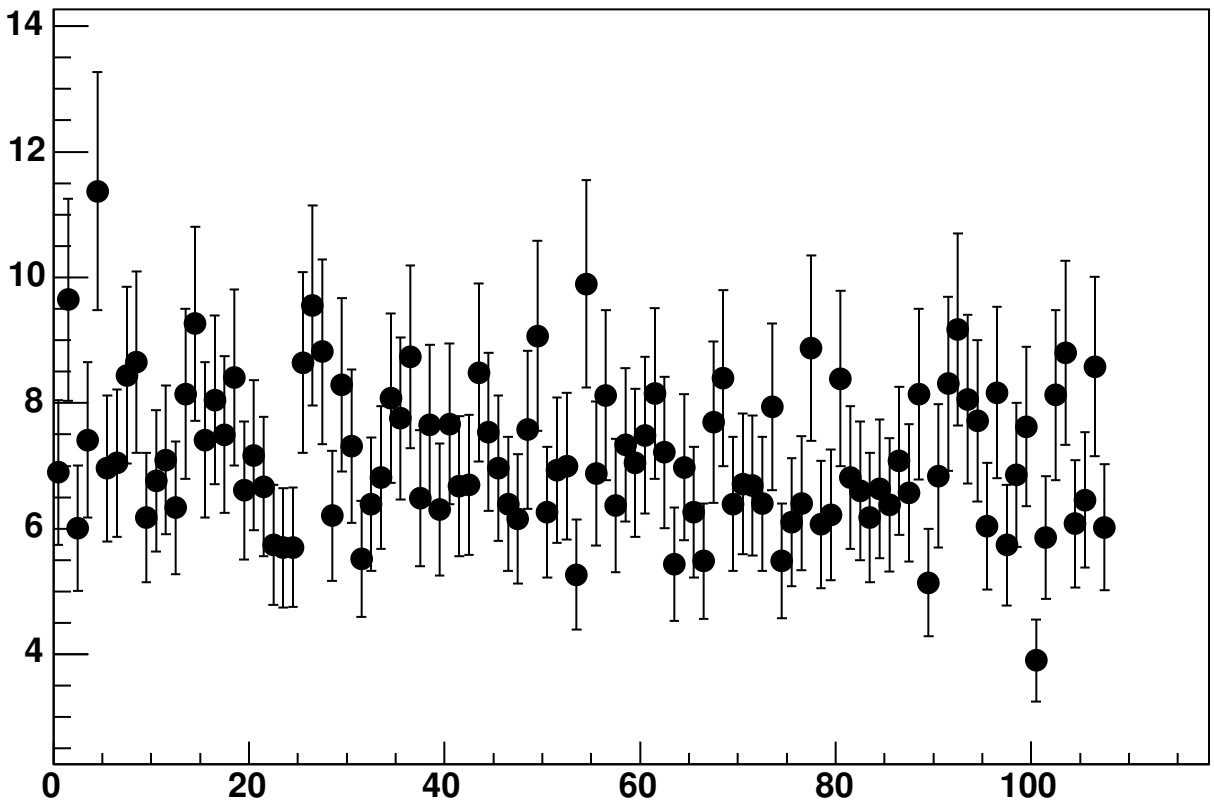
Enable 3, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



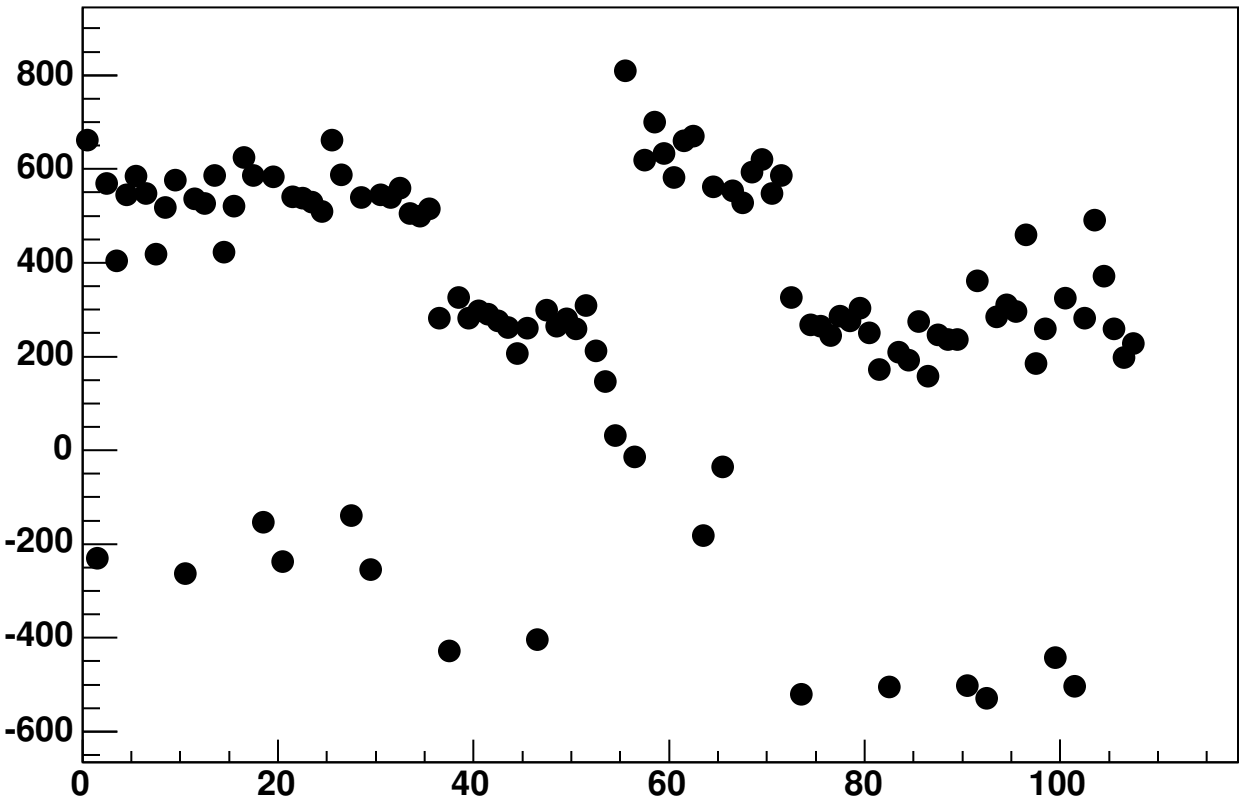
Enable 3, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



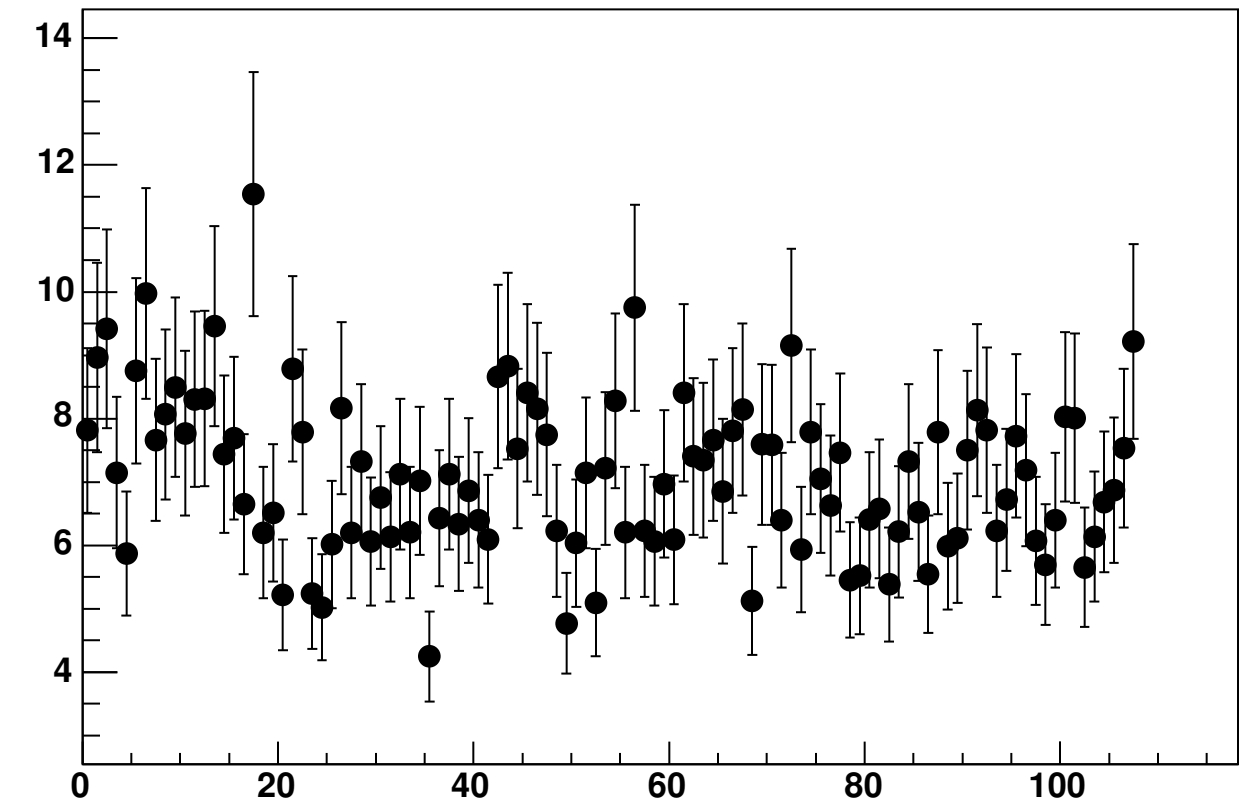
Enable 3, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



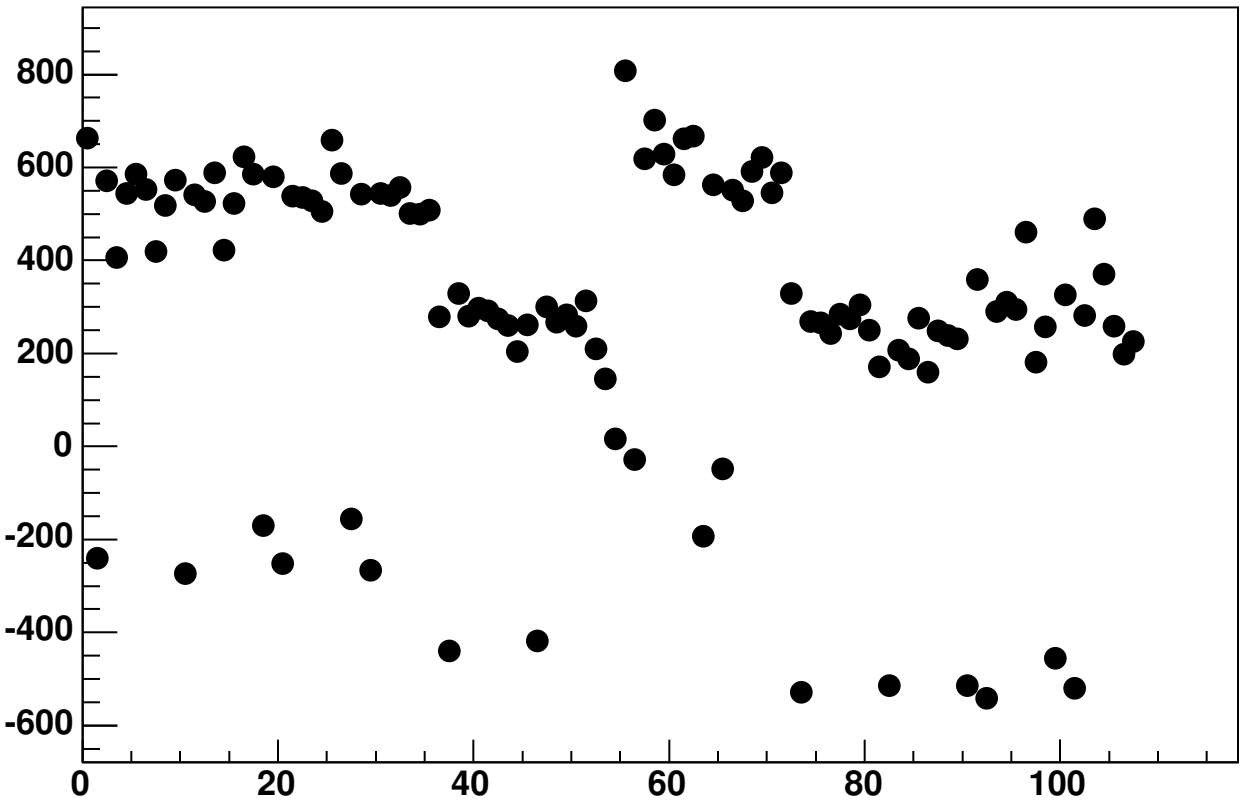
Enable 3, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



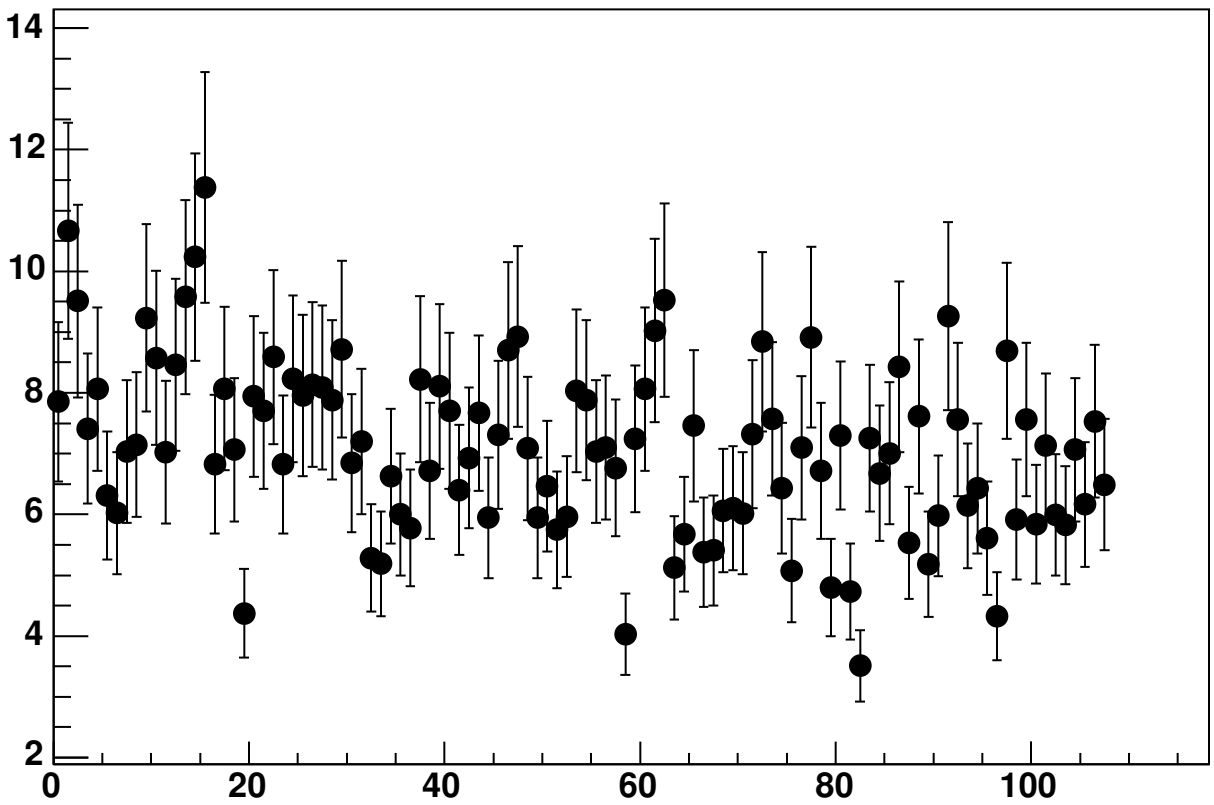
Enable 3, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan



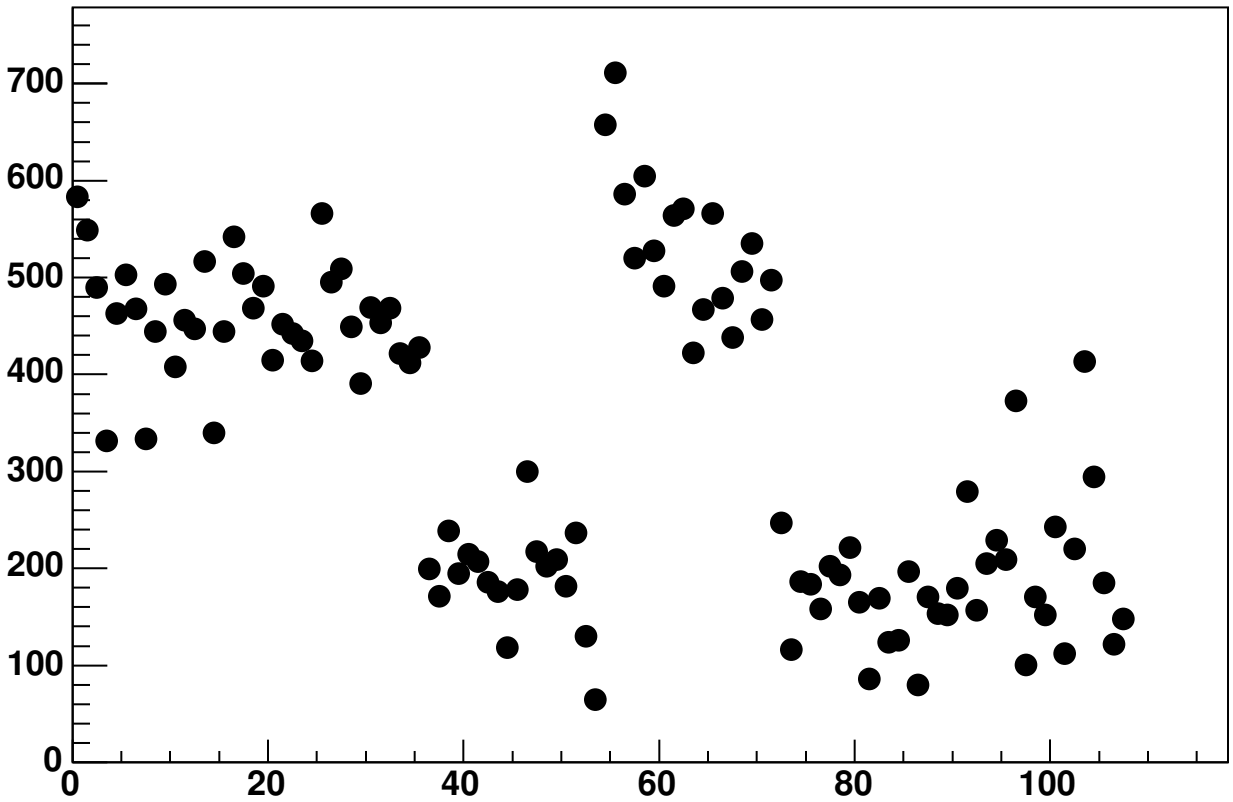
Enable 3, DAC=4000, Hold=245, ADC Mean vs 18*Chip+Chan



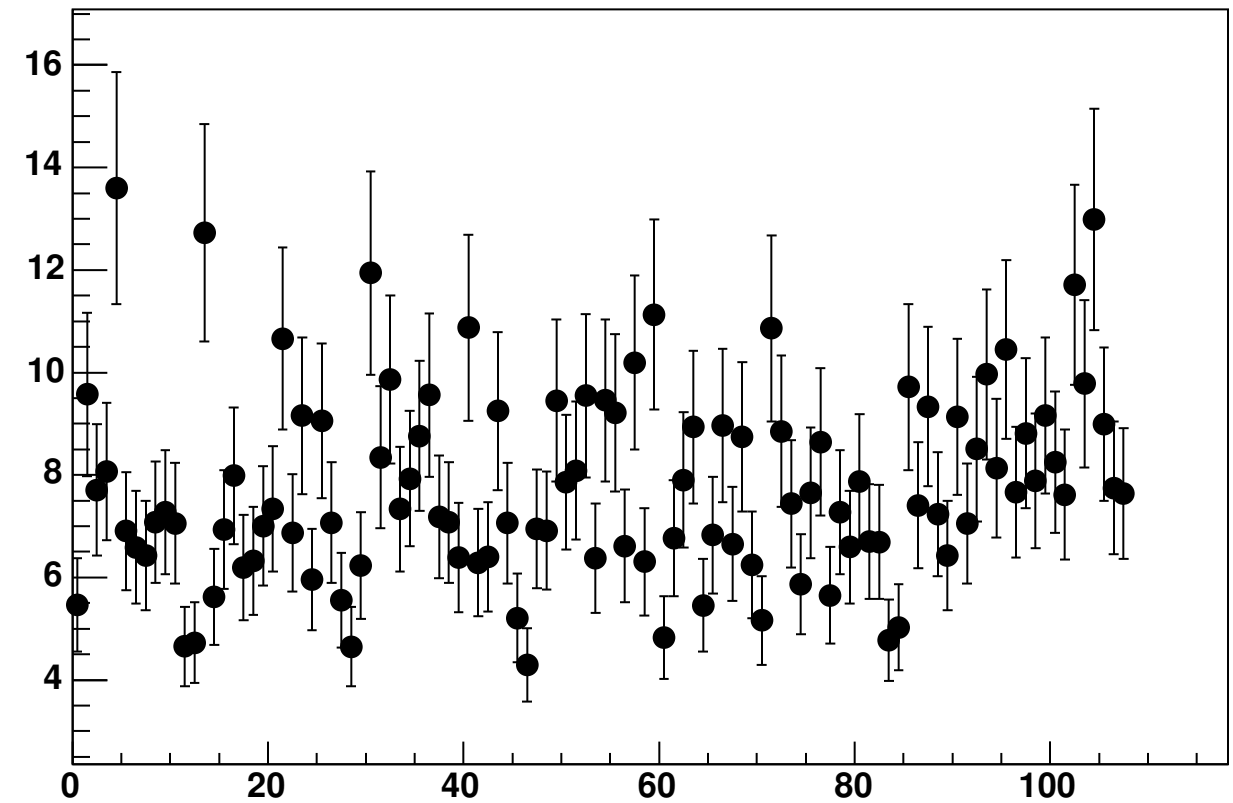
Enable 3, DAC=4000, Hold=245, ADC Noise vs 18*Chip+Chan



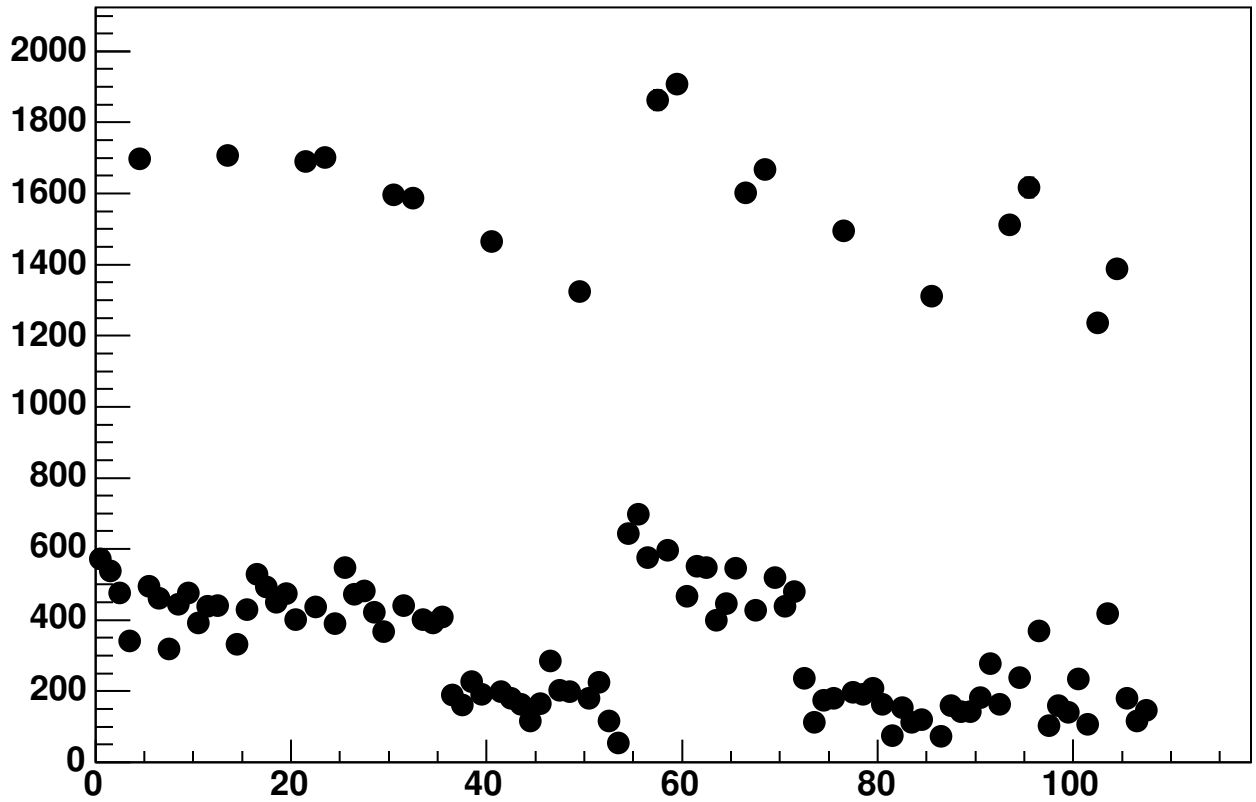
Enable 4, DAC=4000, Hold=0, ADC Mean vs 18*Chip+Chan



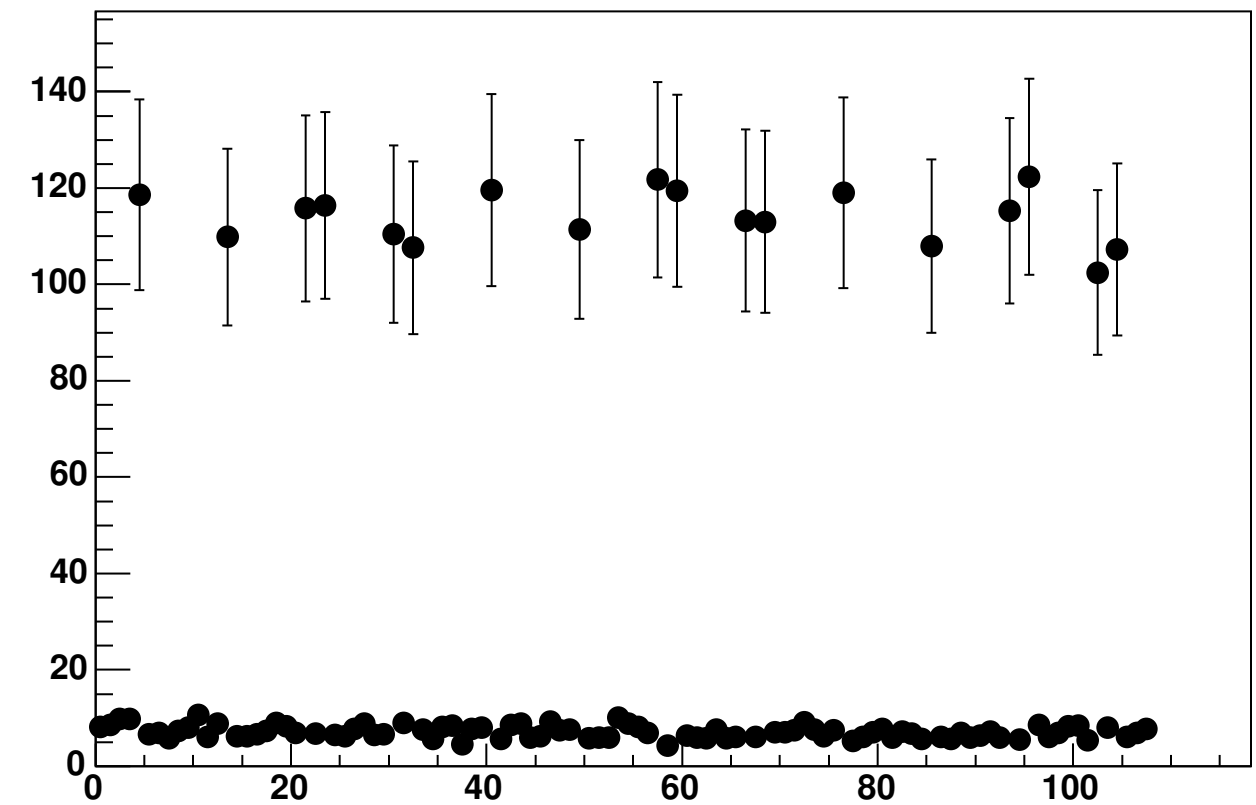
Enable 4, DAC=4000, Hold=0, ADC Noise vs 18*Chip+Chan



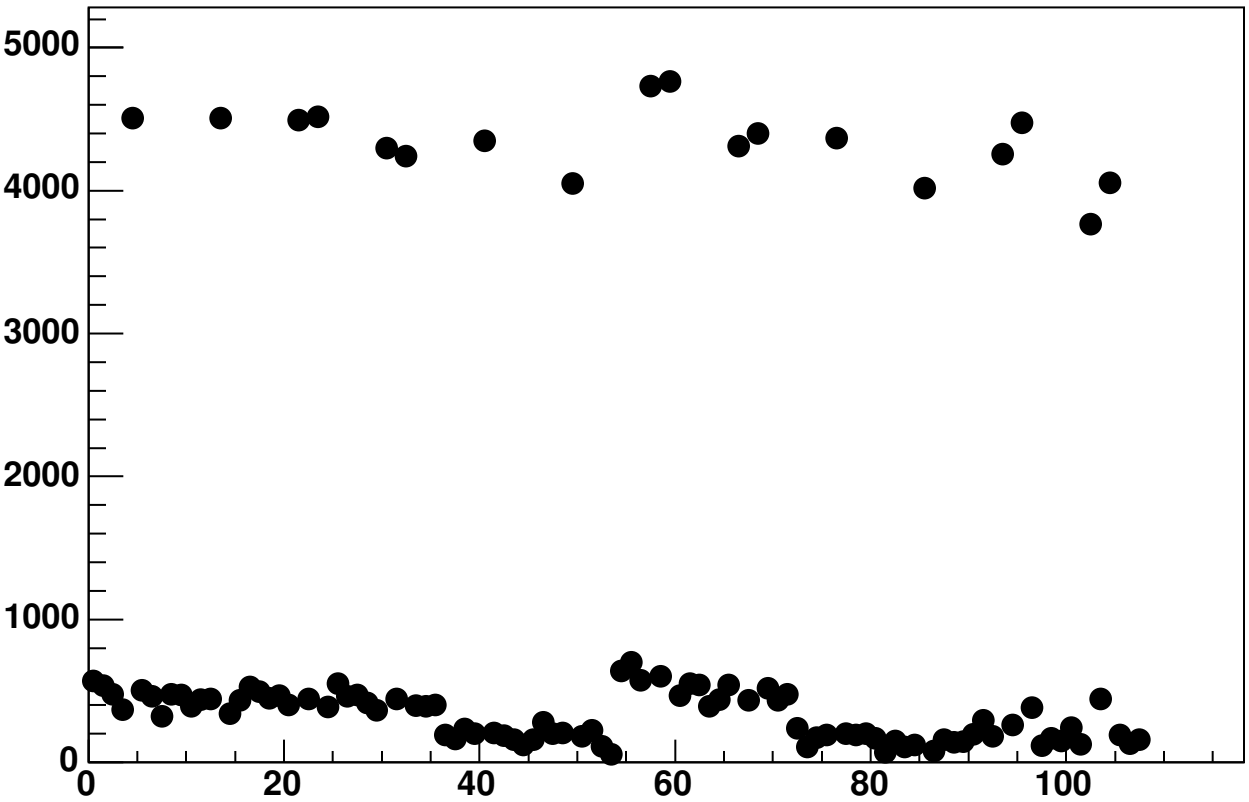
Enable 4, DAC=4000, Hold=5, ADC Mean vs 18*Chip+Chan



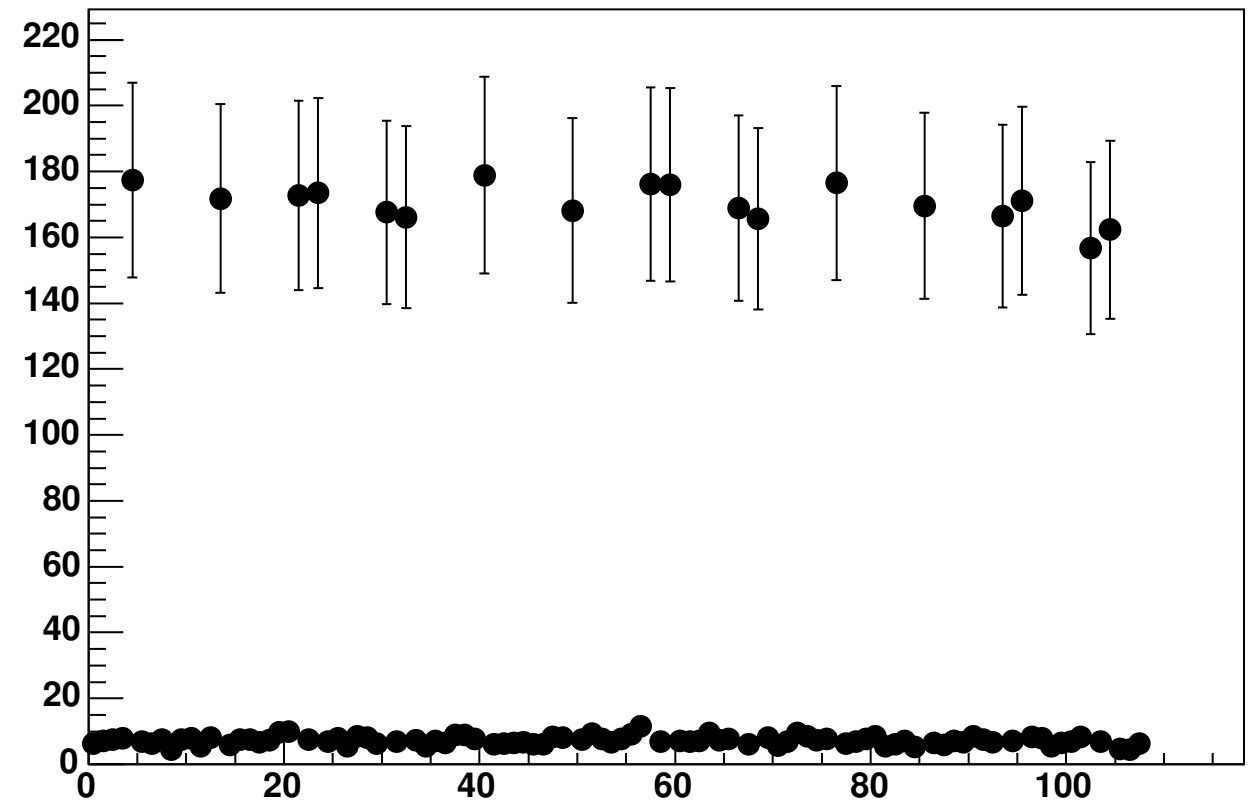
Enable 4, DAC=4000, Hold=5, ADC Noise vs 18*Chip+Chan



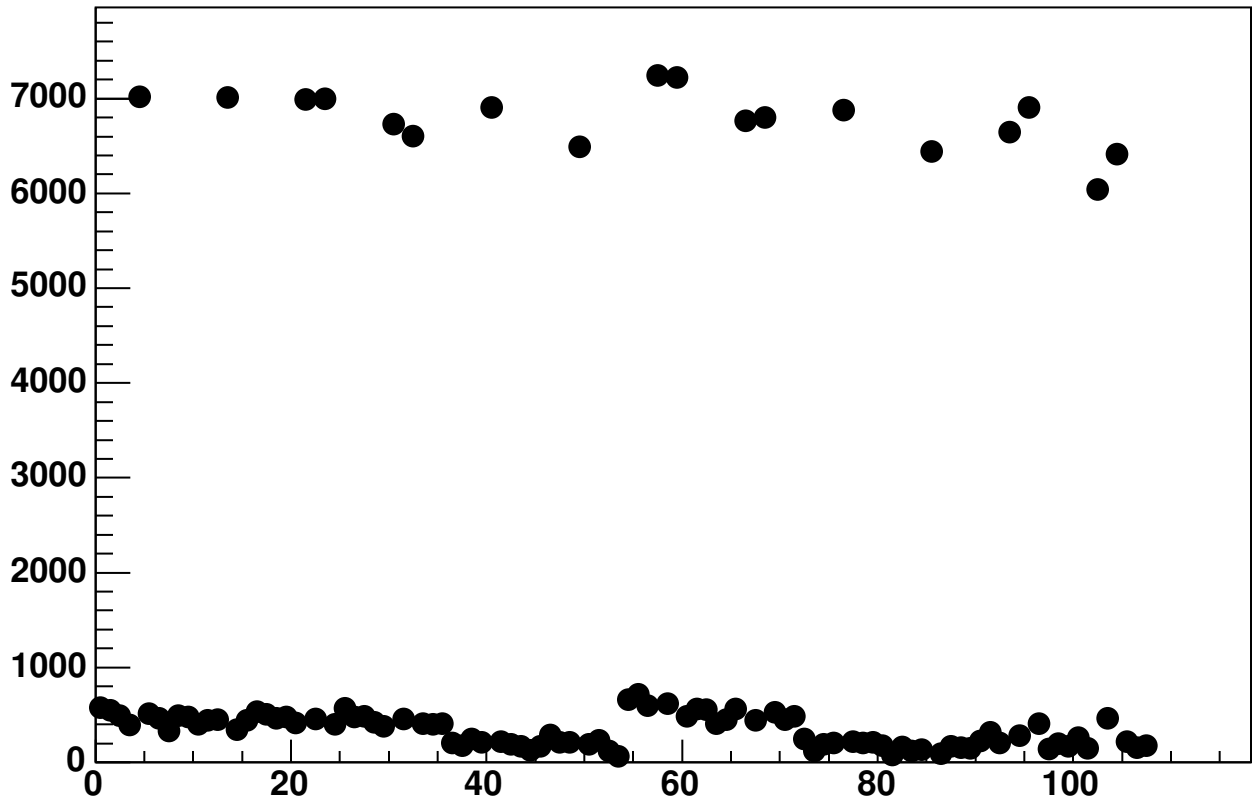
Enable 4, DAC=4000, Hold=10, ADC Mean vs 18*Chip+Chan



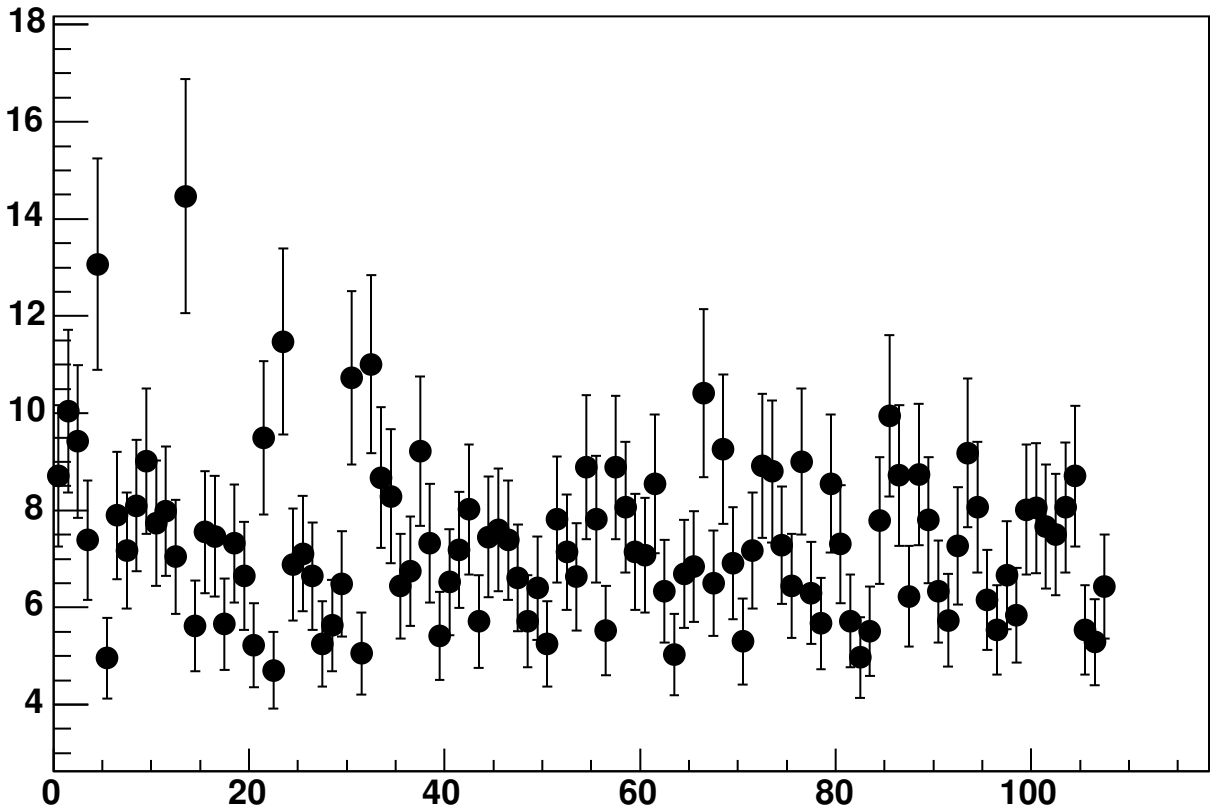
Enable 4, DAC=4000, Hold=10, ADC Noise vs 18*Chip+Chan



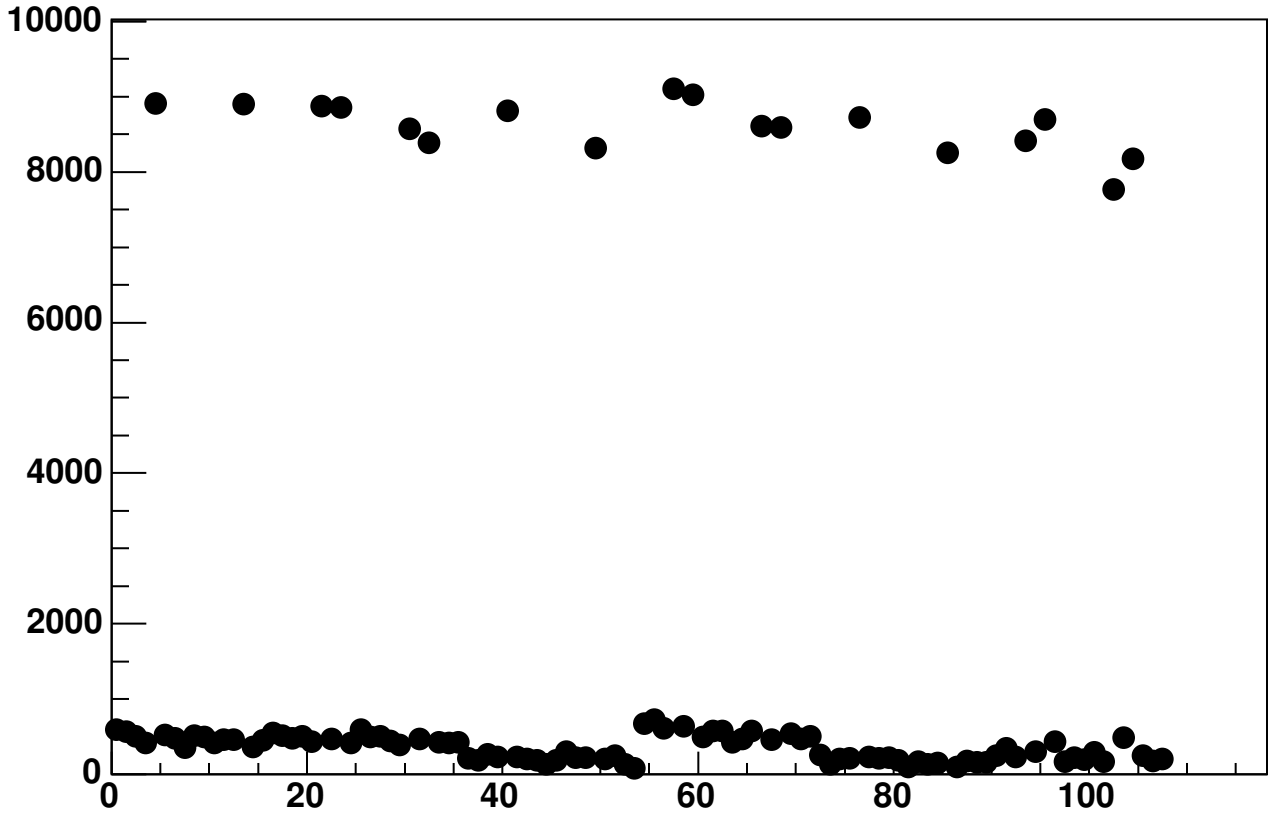
Enable 4, DAC=4000, Hold=15, ADC Mean vs 18*Chip+Chan



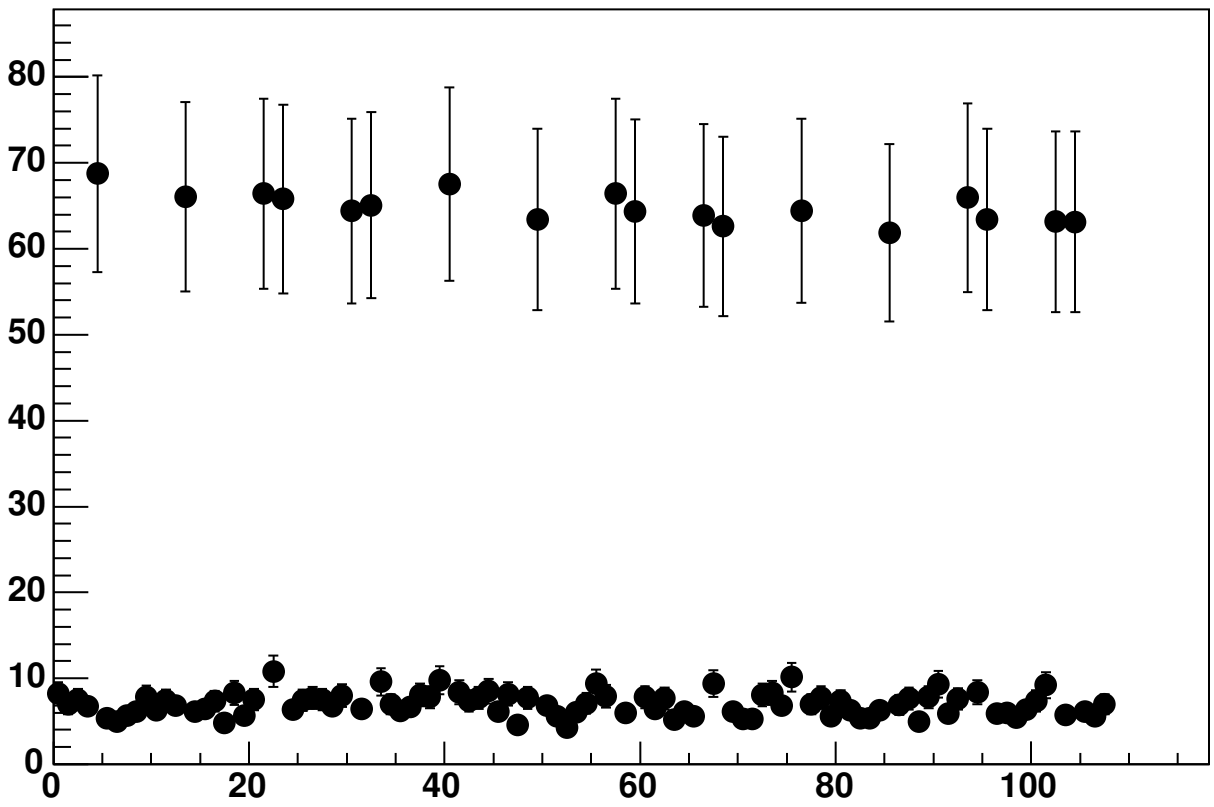
Enable 4, DAC=4000, Hold=15, ADC Noise vs 18*Chip+Chan



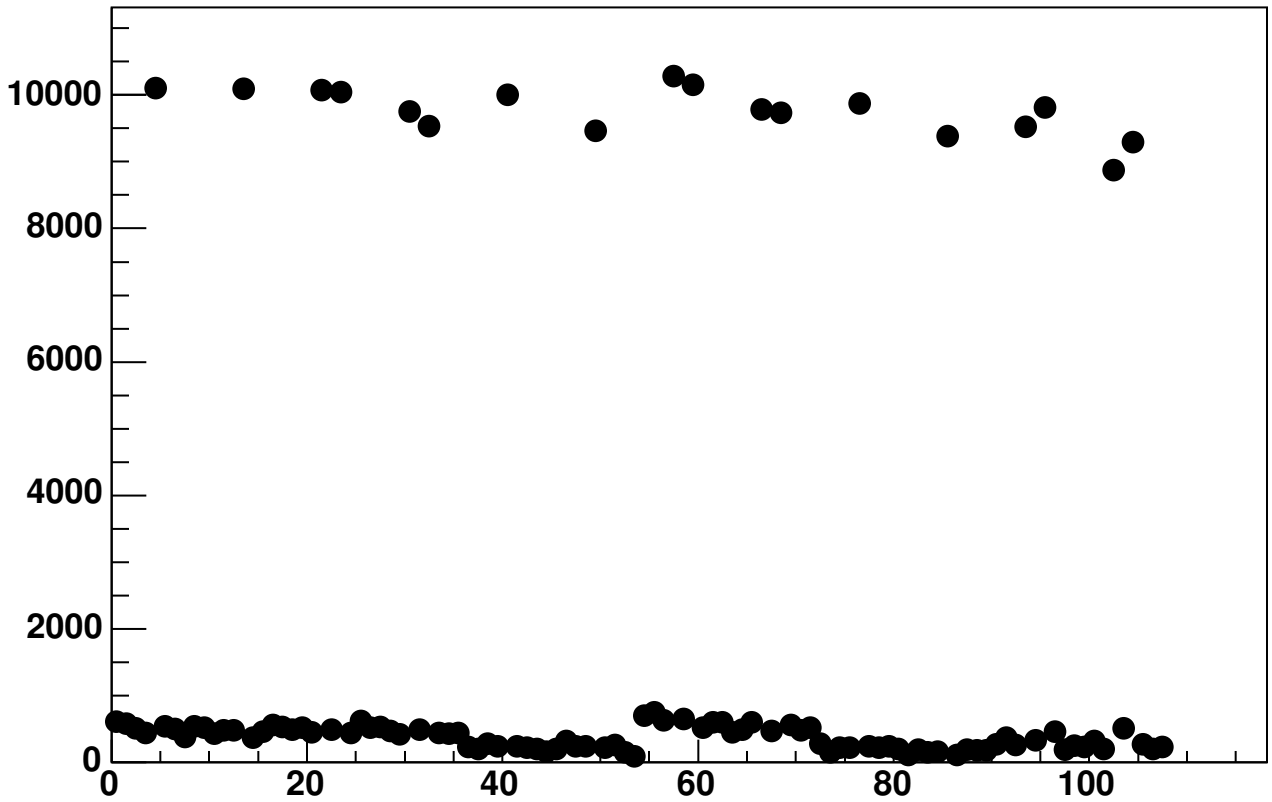
Enable 4, DAC=4000, Hold=20, ADC Mean vs 18*Chip+Chan



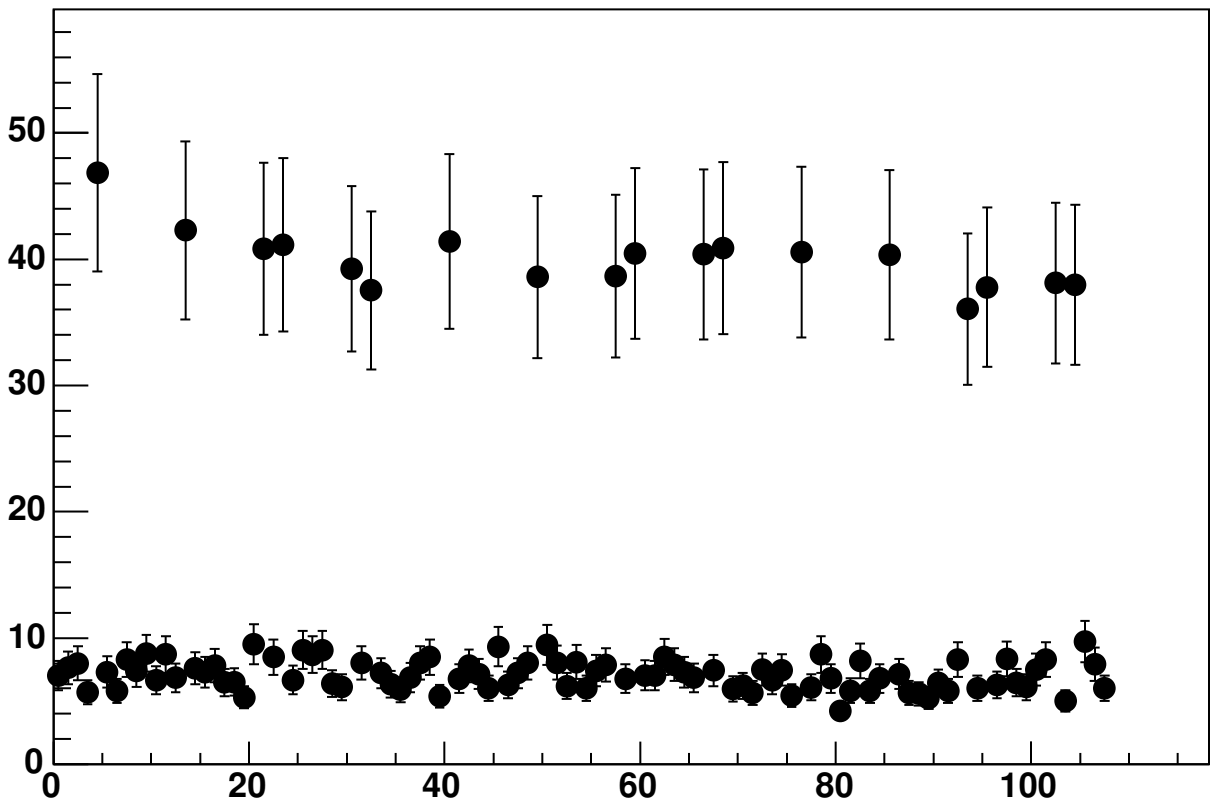
Enable 4, DAC=4000, Hold=20, ADC Noise vs 18*Chip+Chan



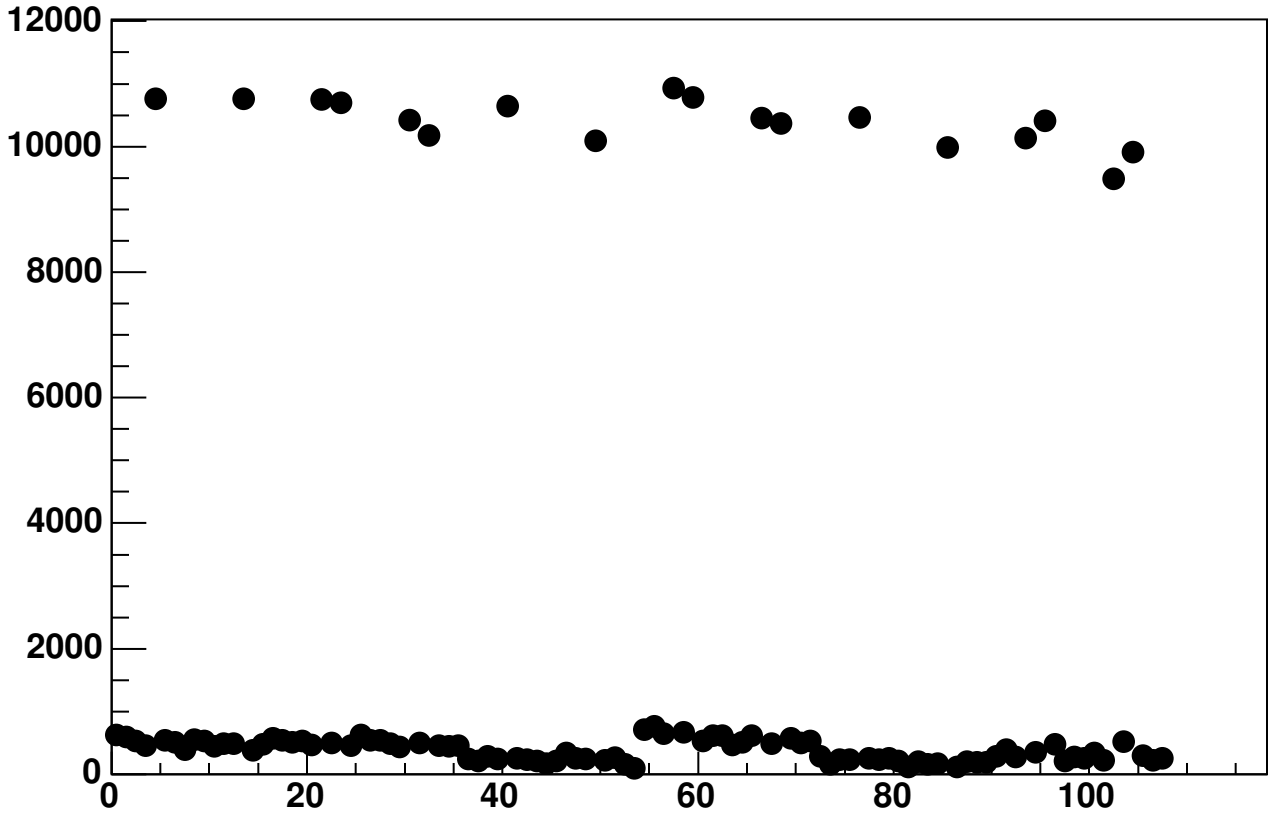
Enable 4, DAC=4000, Hold=25, ADC Mean vs 18*Chip+Chan



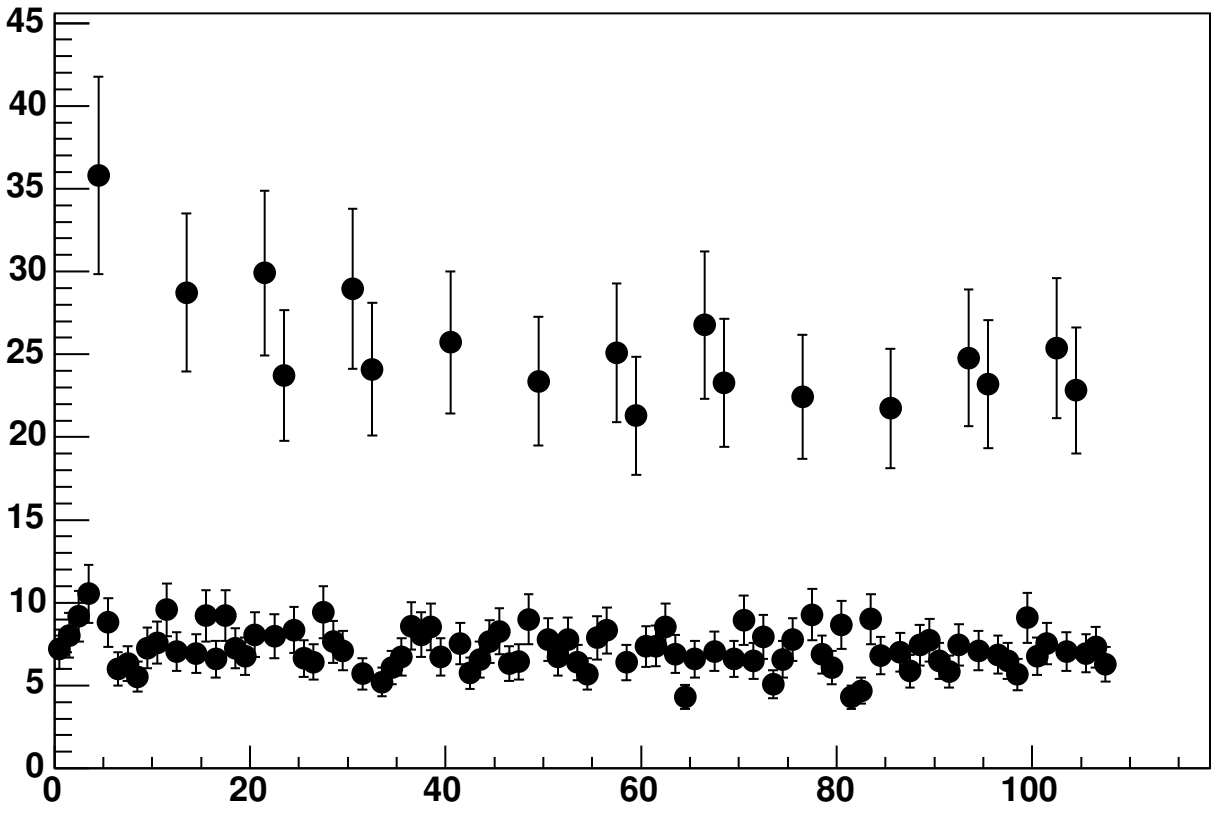
Enable 4, DAC=4000, Hold=25, ADC Noise vs 18*Chip+Chan



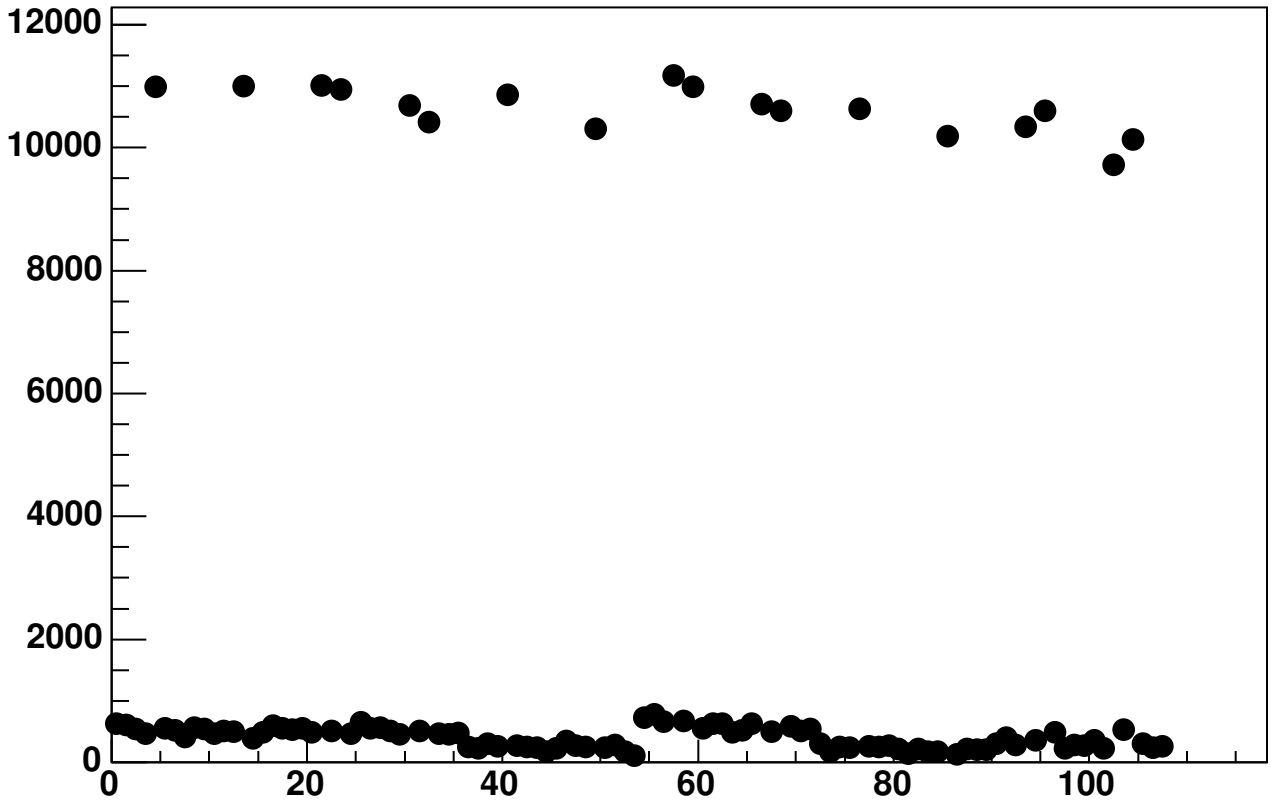
Enable 4, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



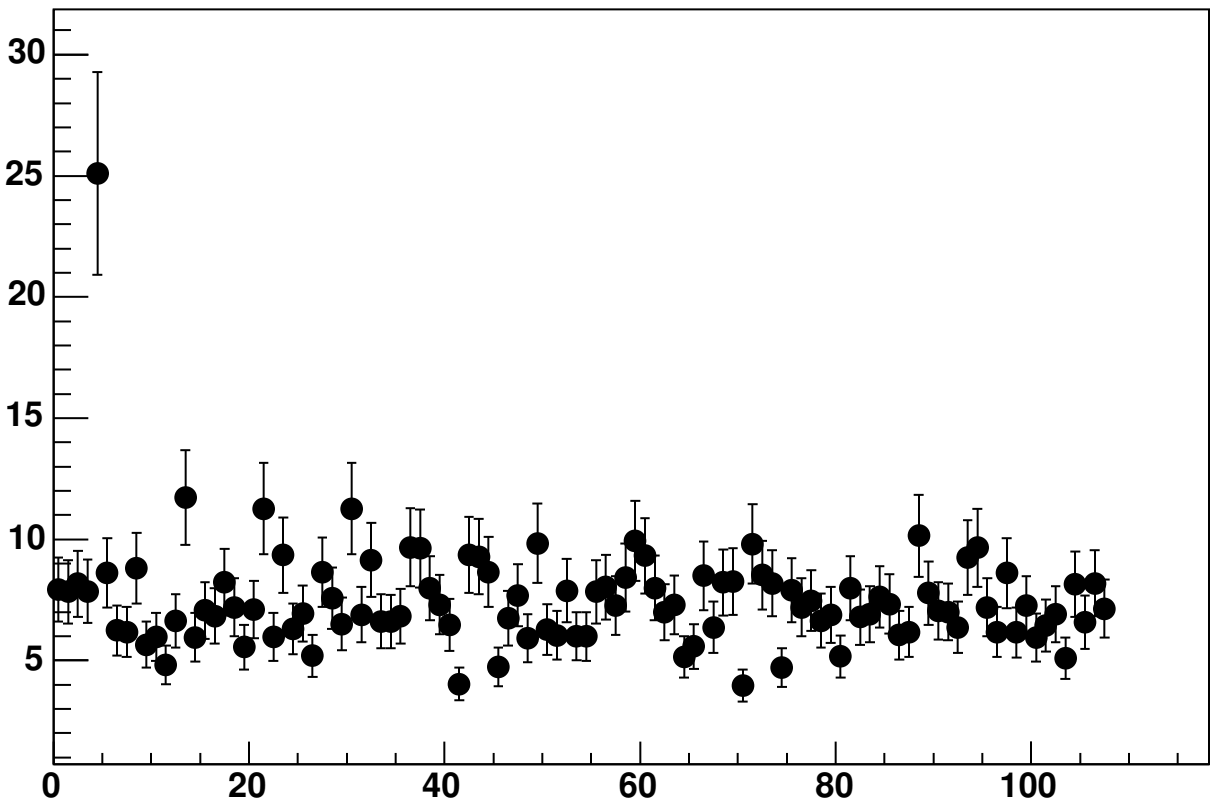
Enable 4, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



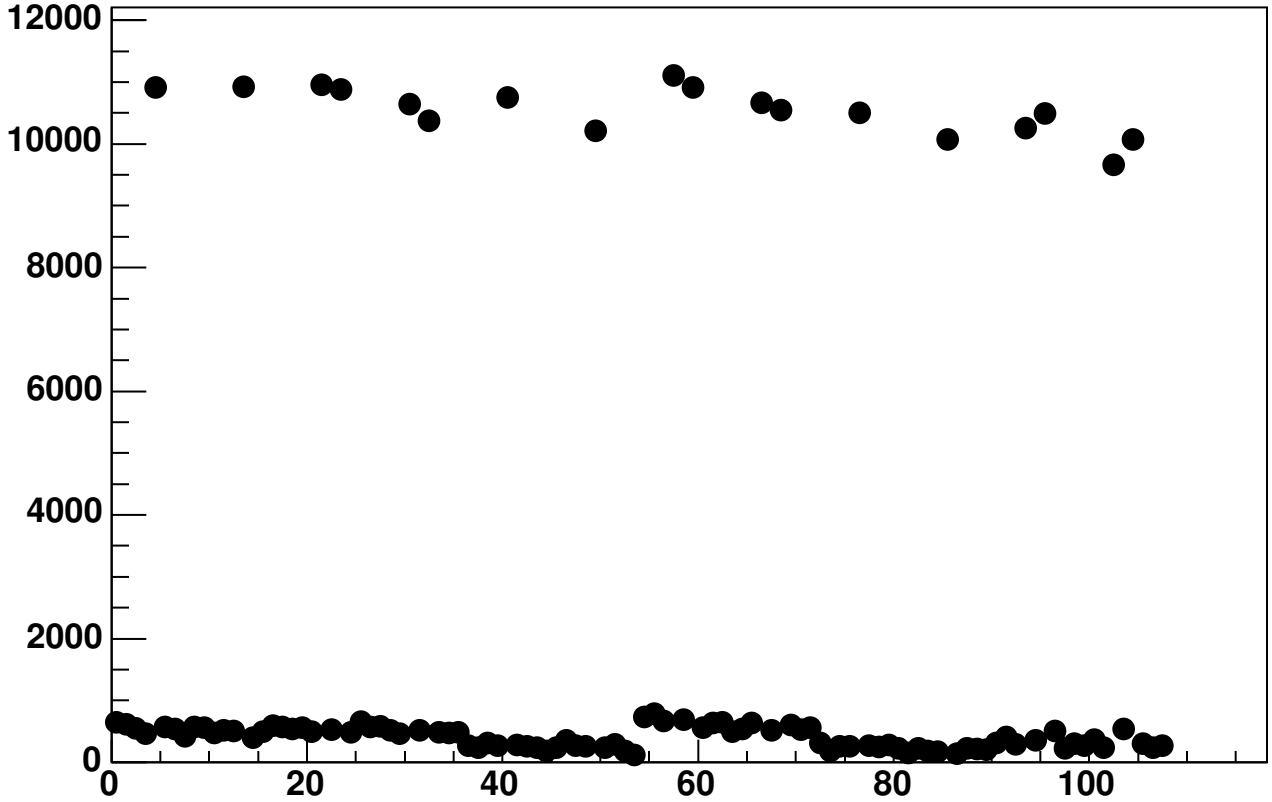
Enable 4, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



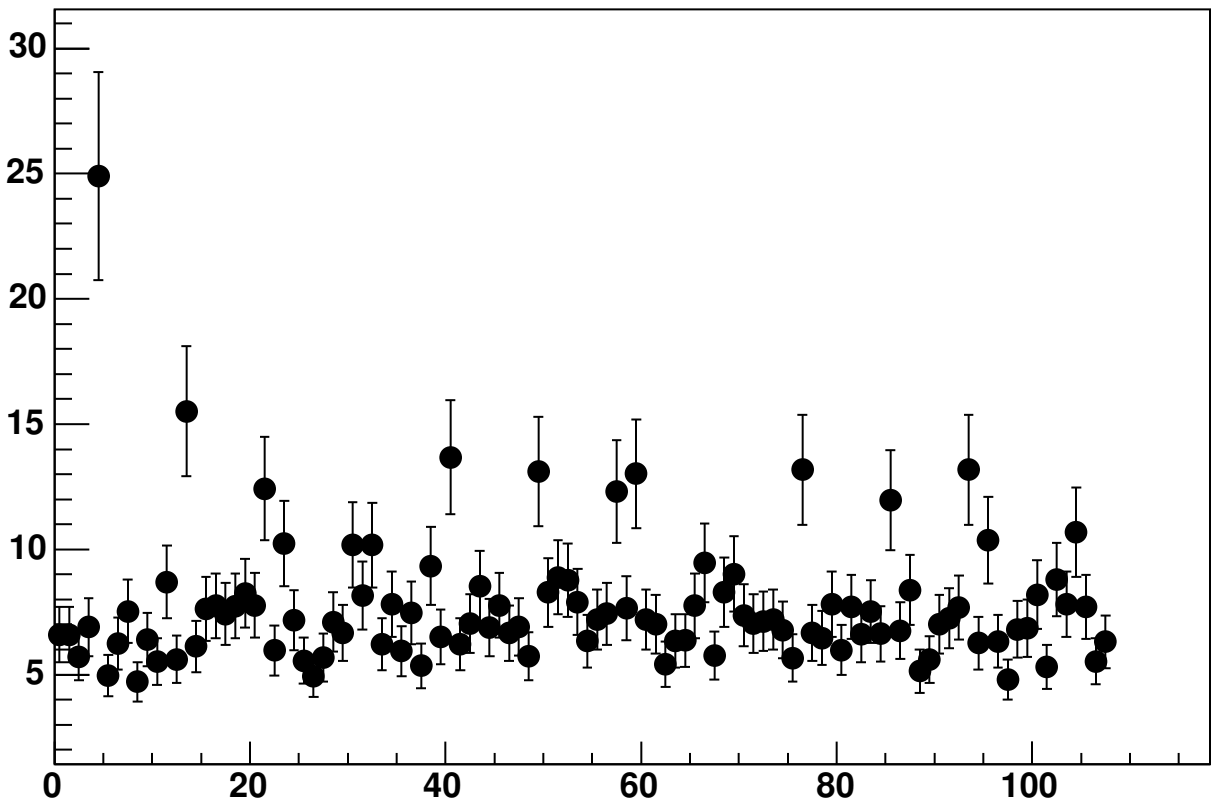
Enable 4, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



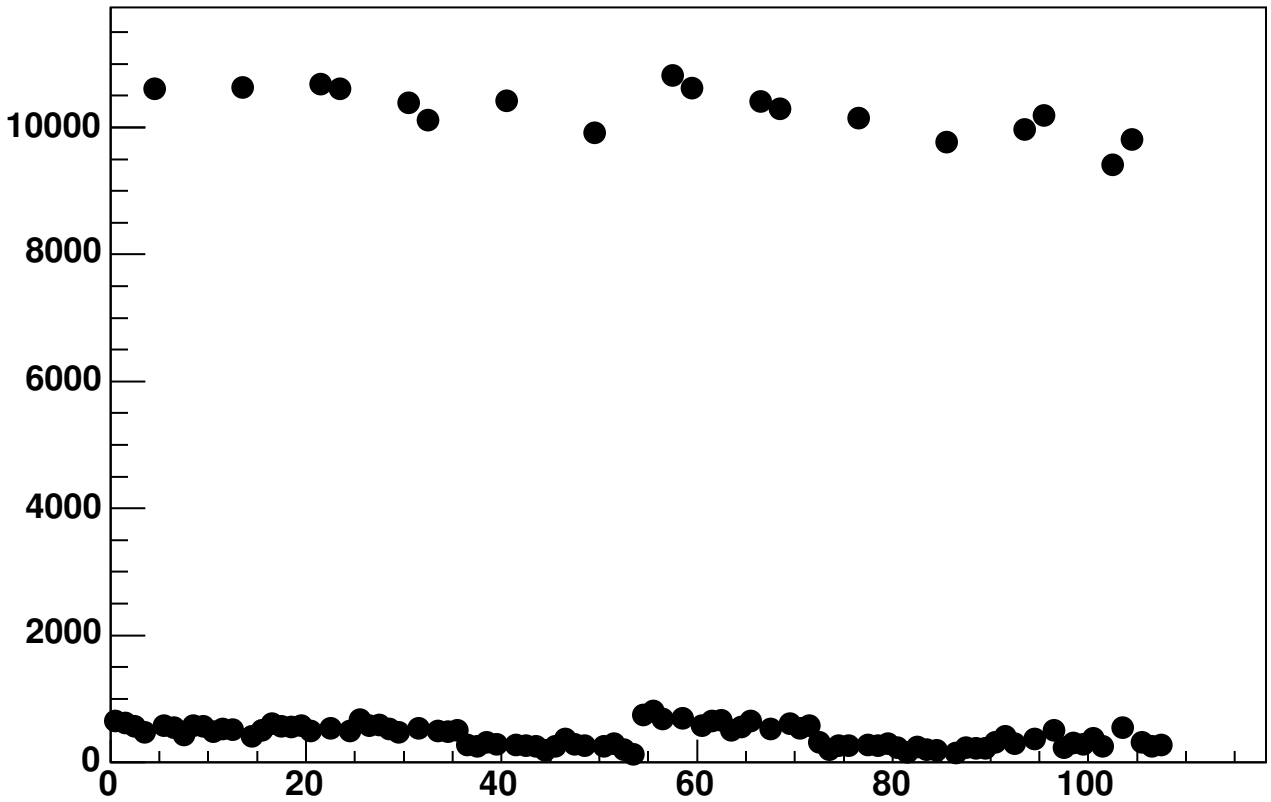
Enable 4, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



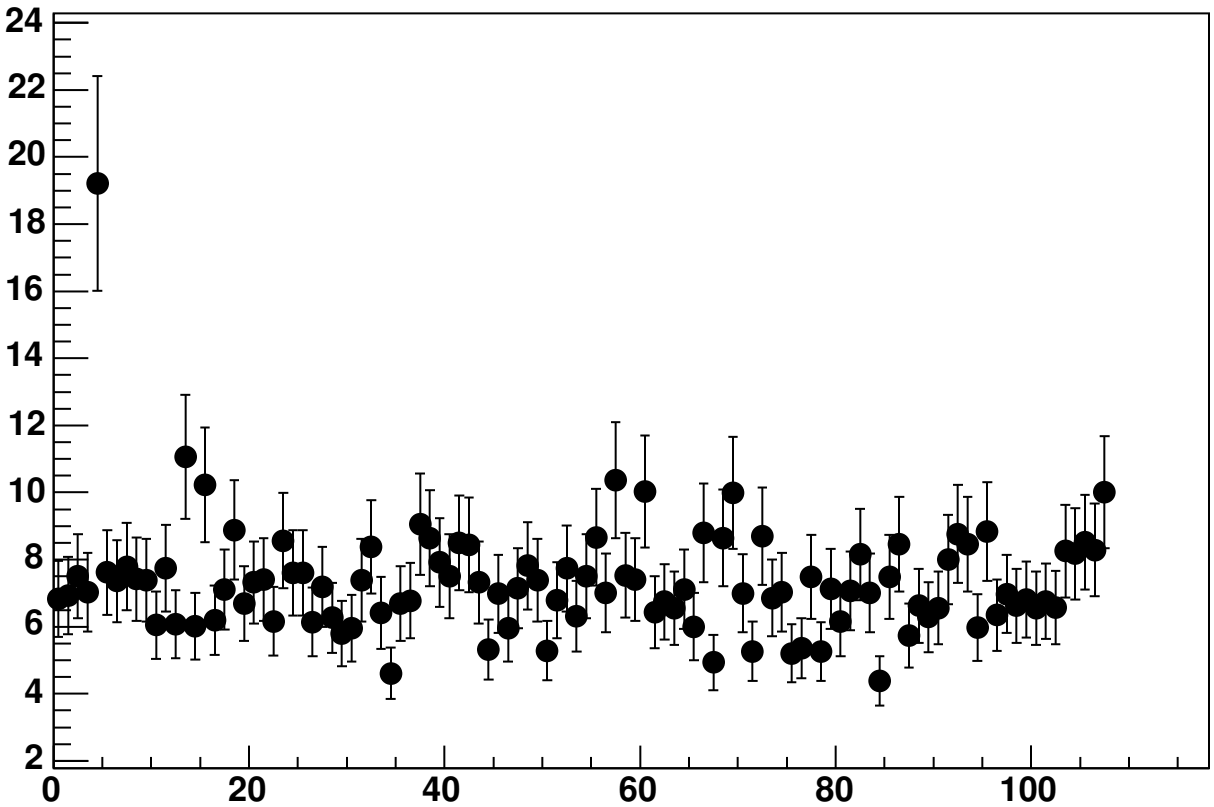
Enable 4, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



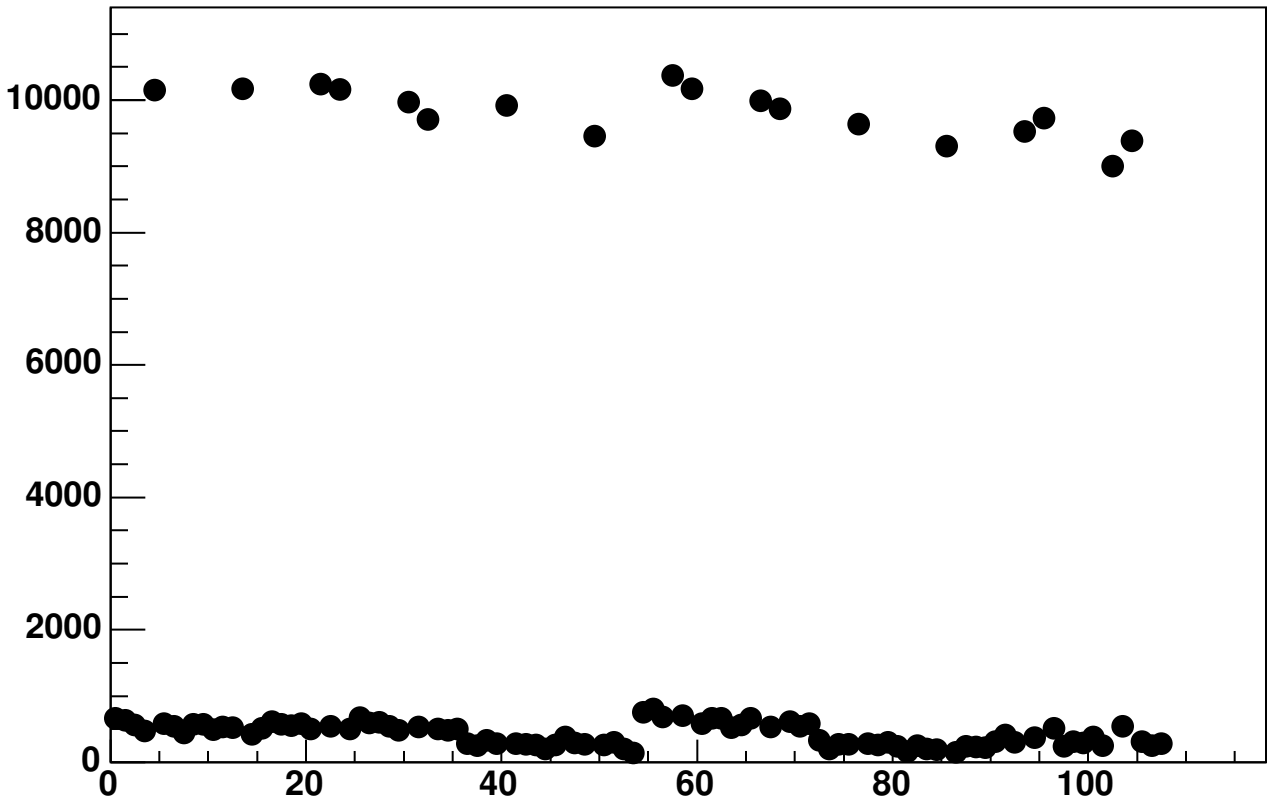
Enable 4, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



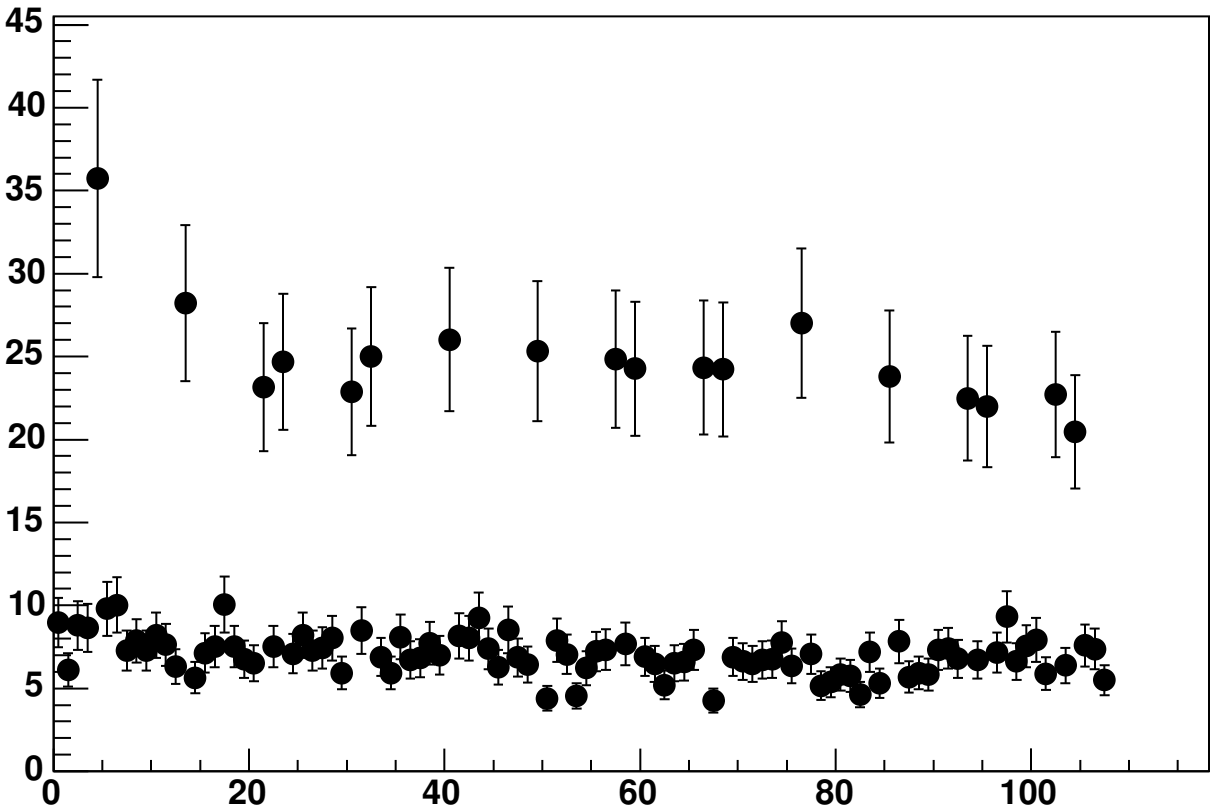
Enable 4, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



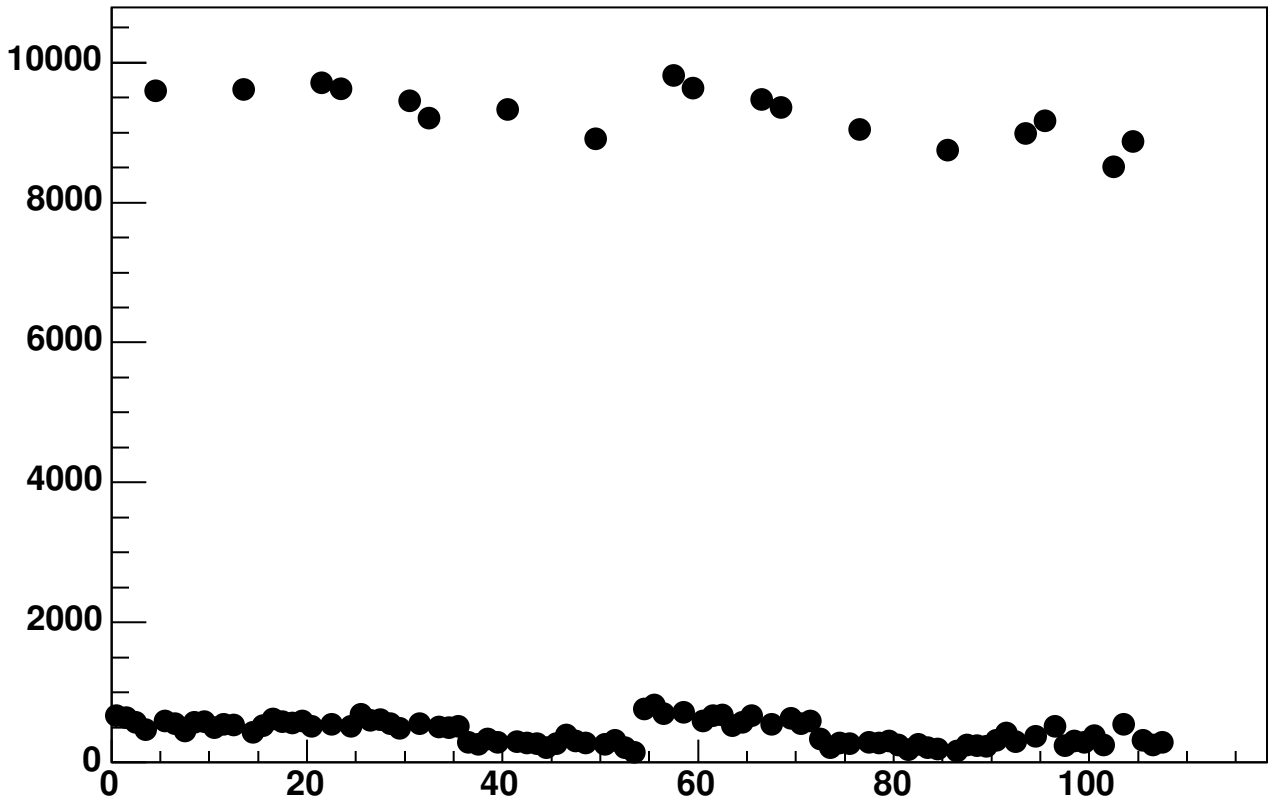
Enable 4, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



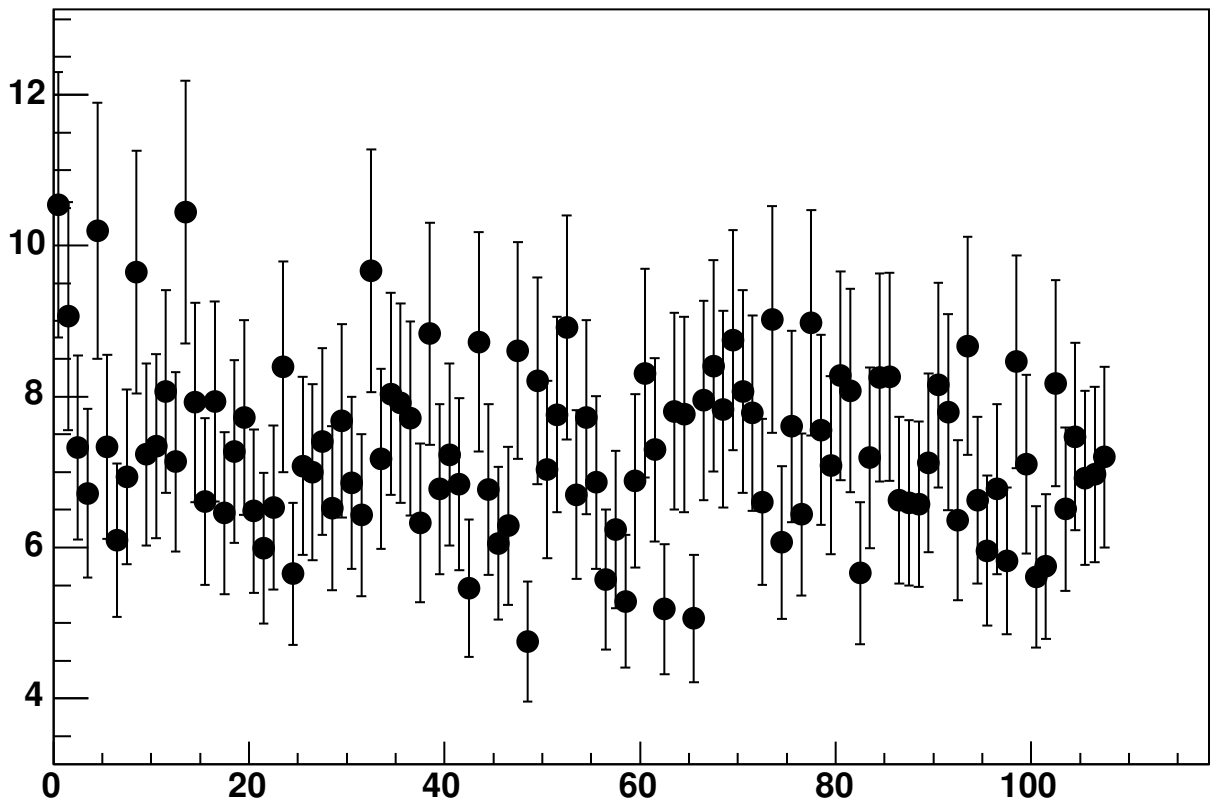
Enable 4, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



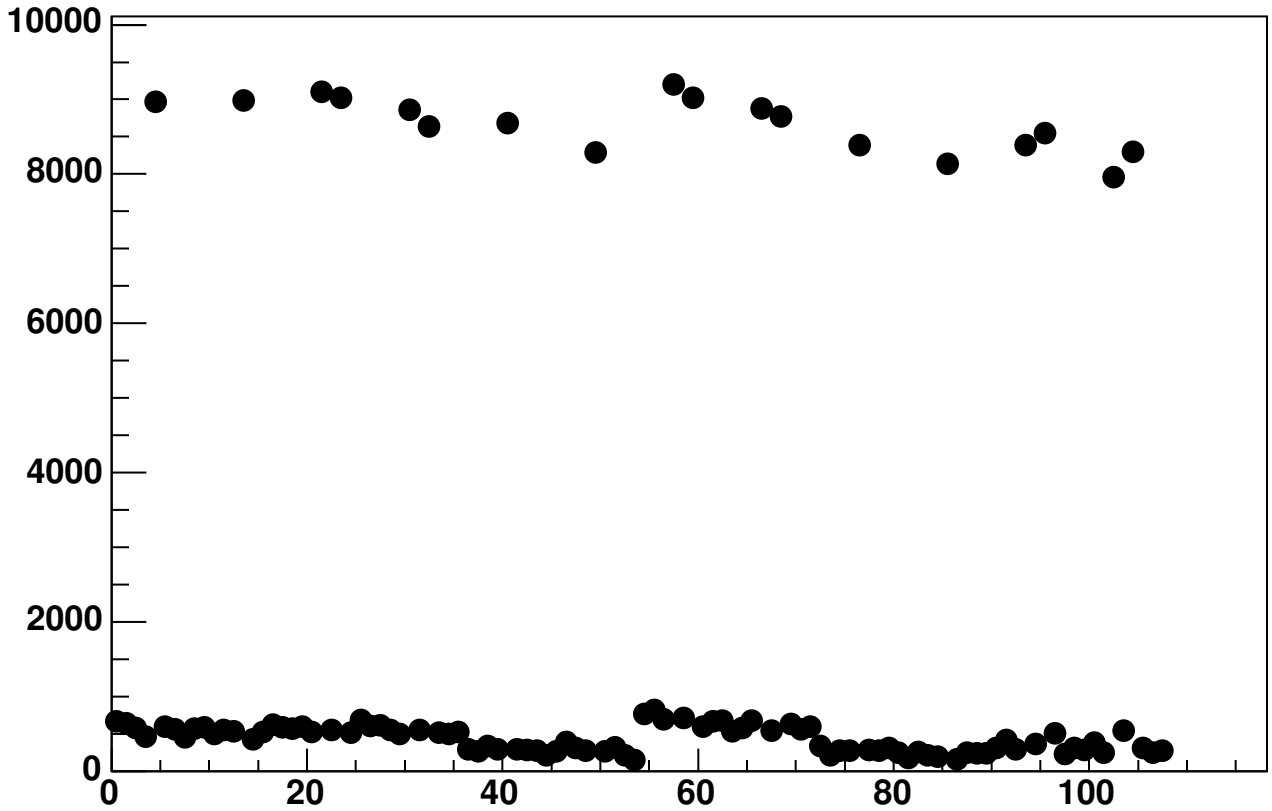
Enable 4, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



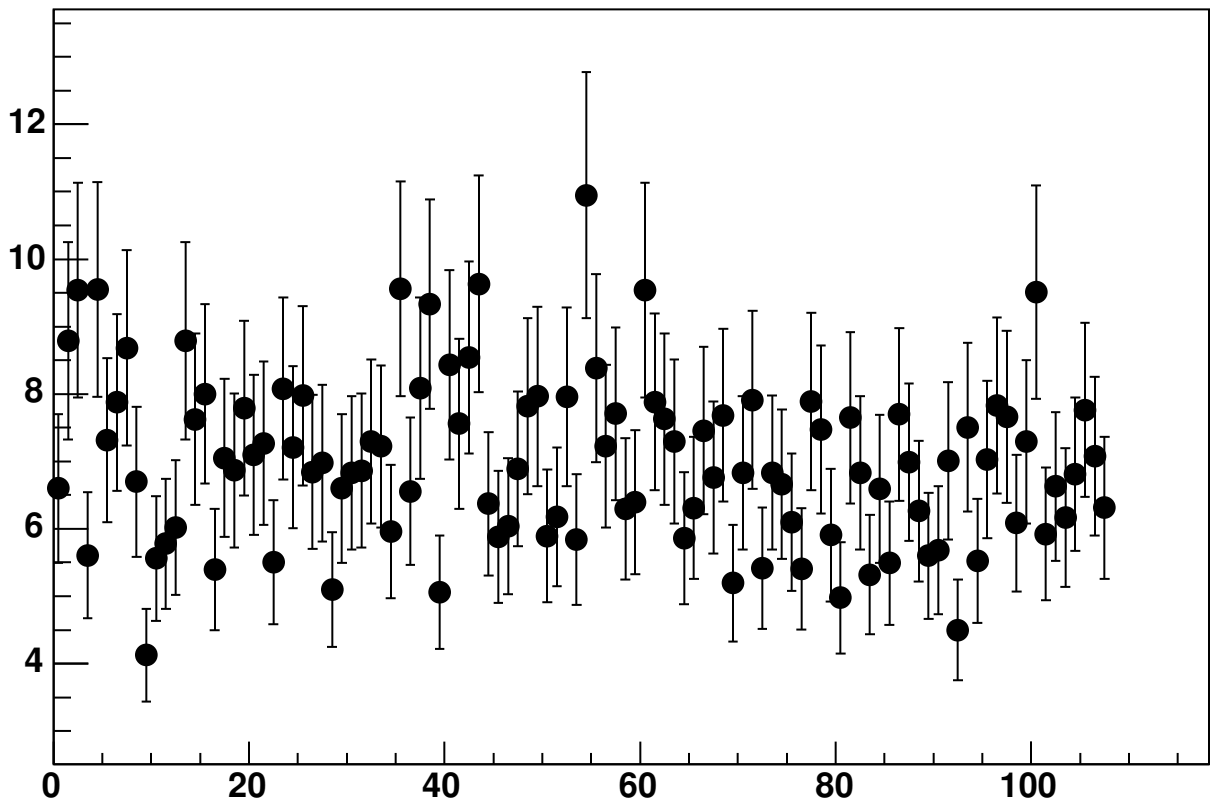
Enable 4, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



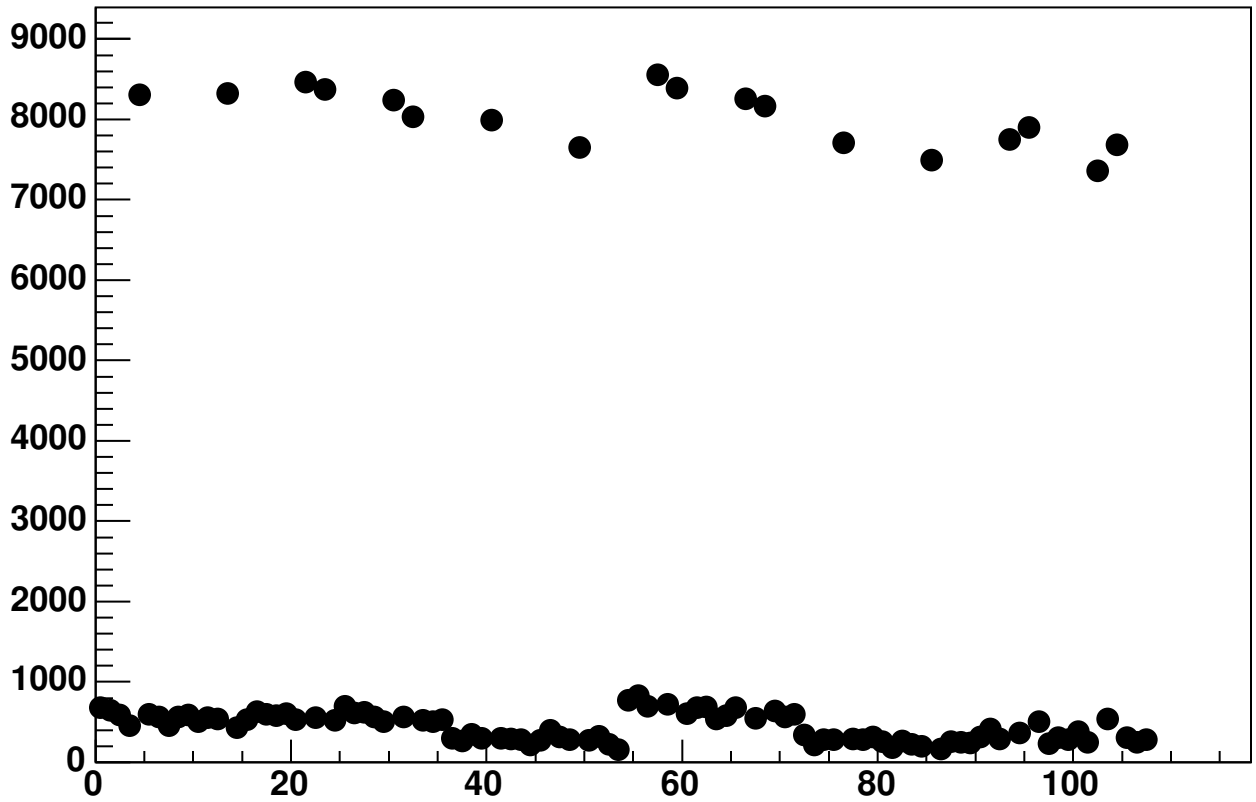
Enable 4, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



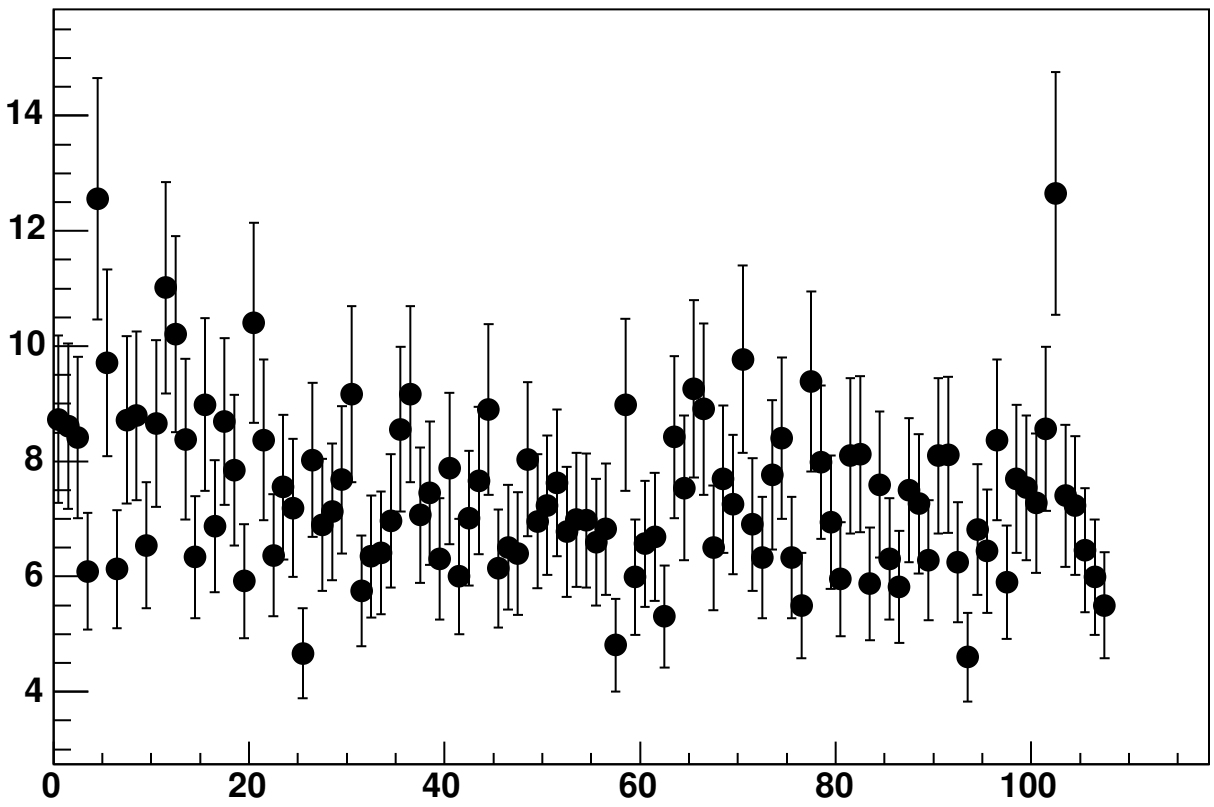
Enable 4, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



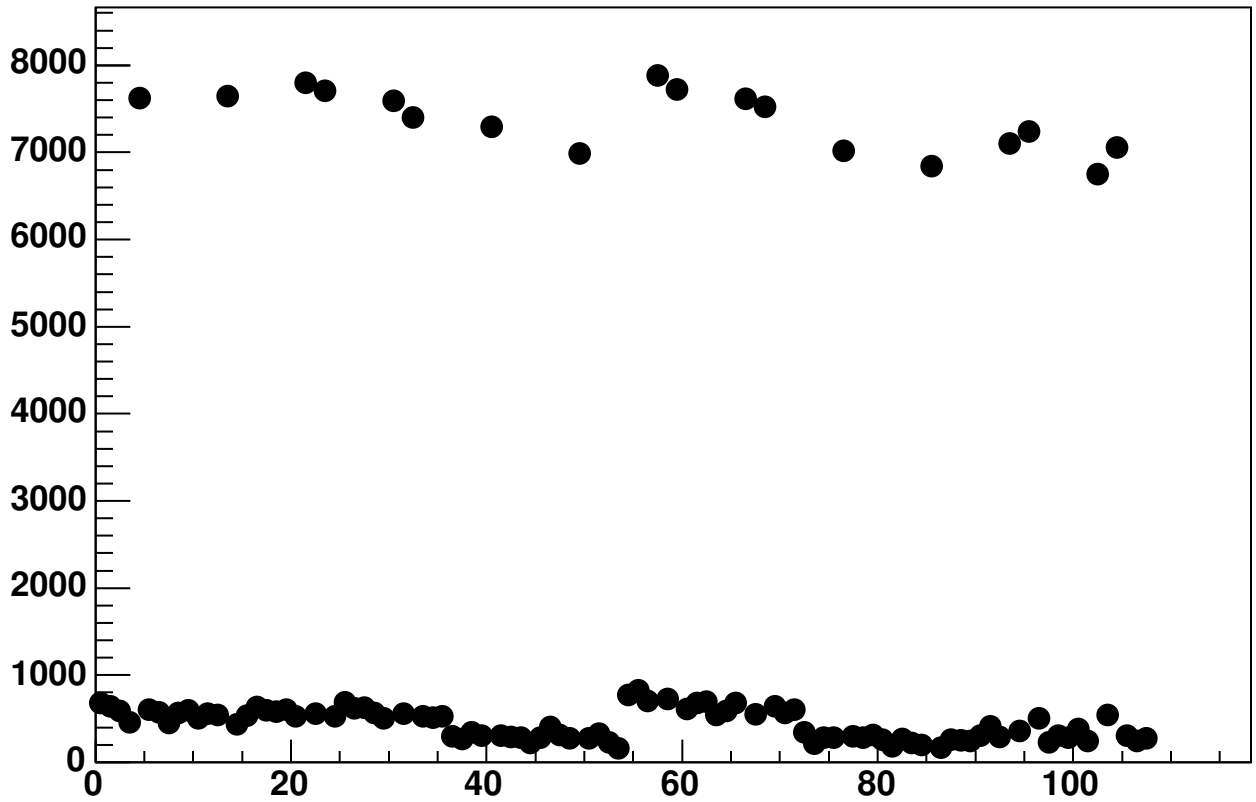
Enable 4, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



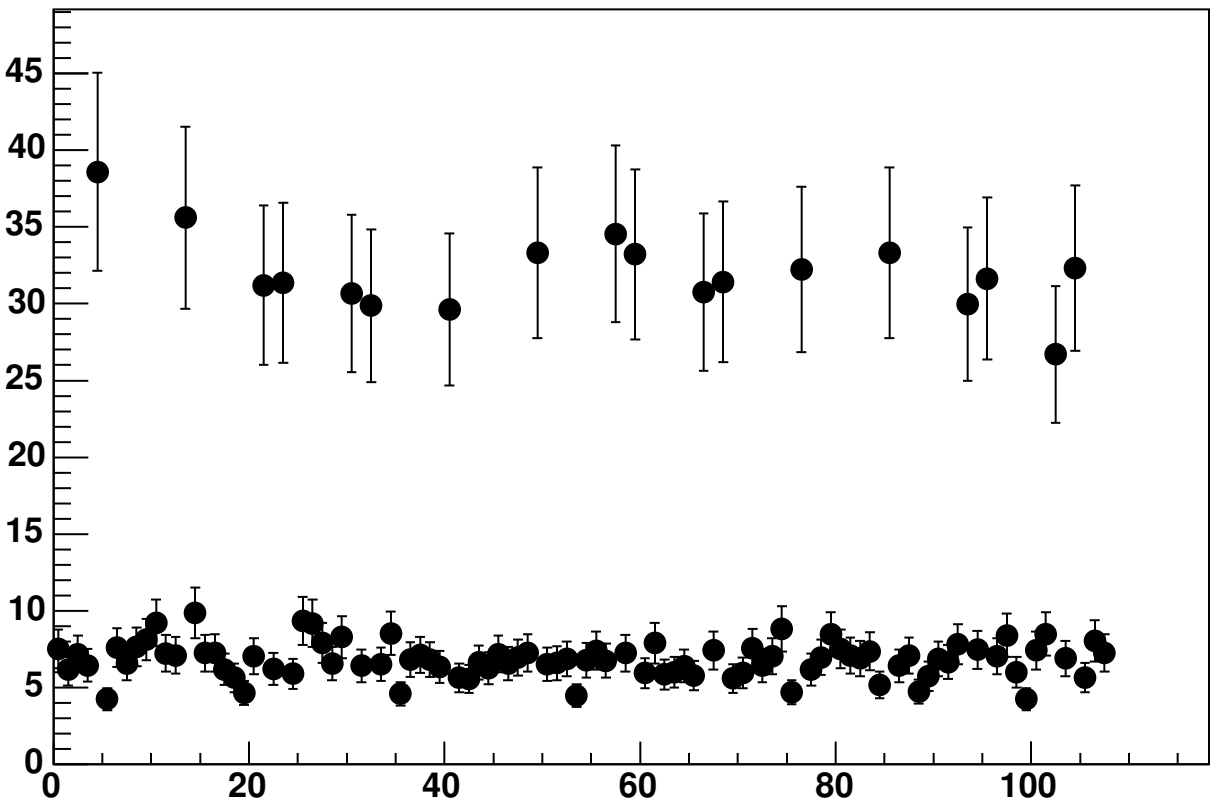
Enable 4, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



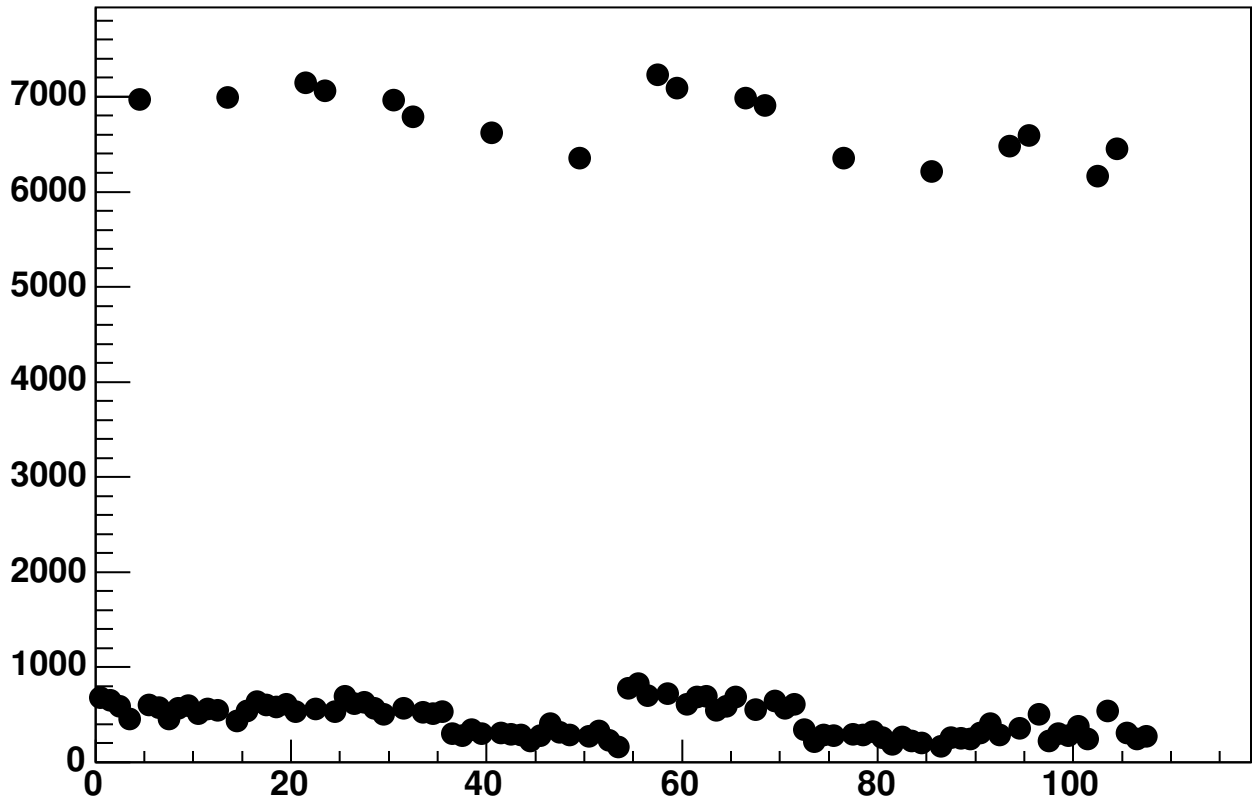
Enable 4, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



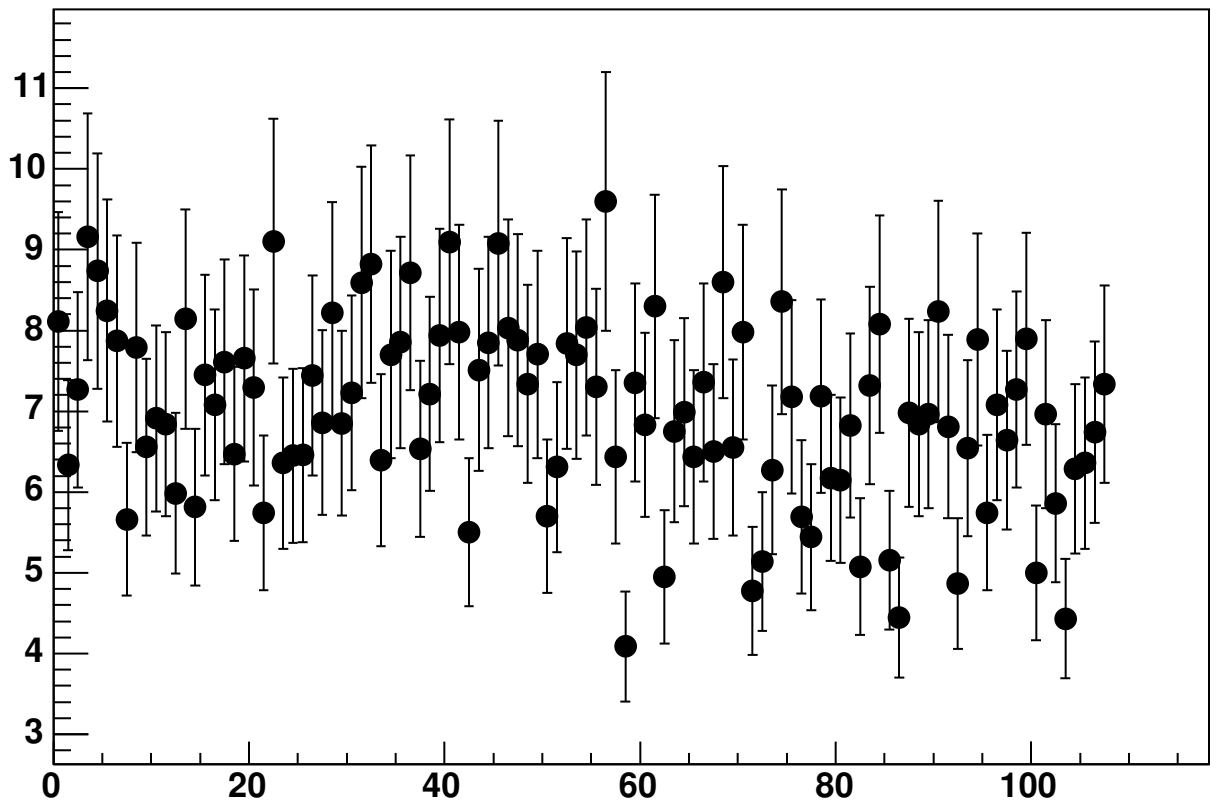
Enable 4, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



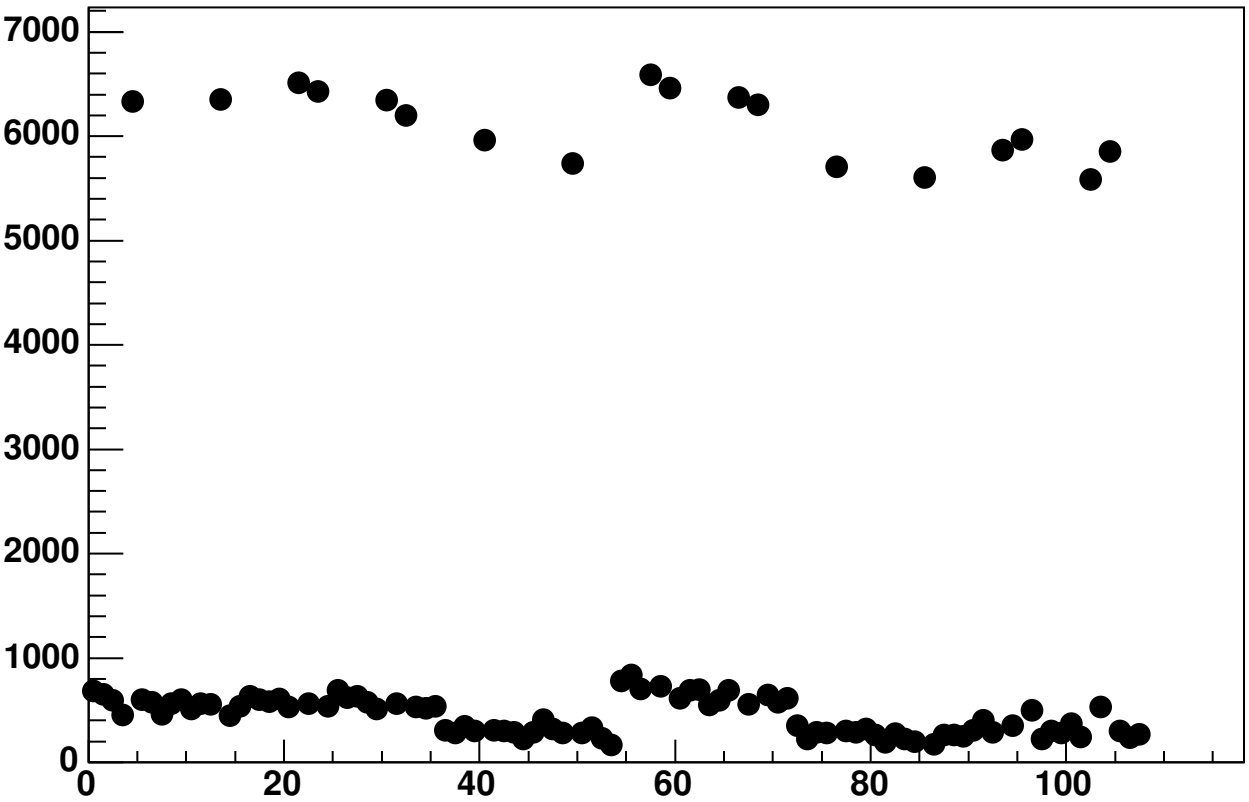
Enable 4, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



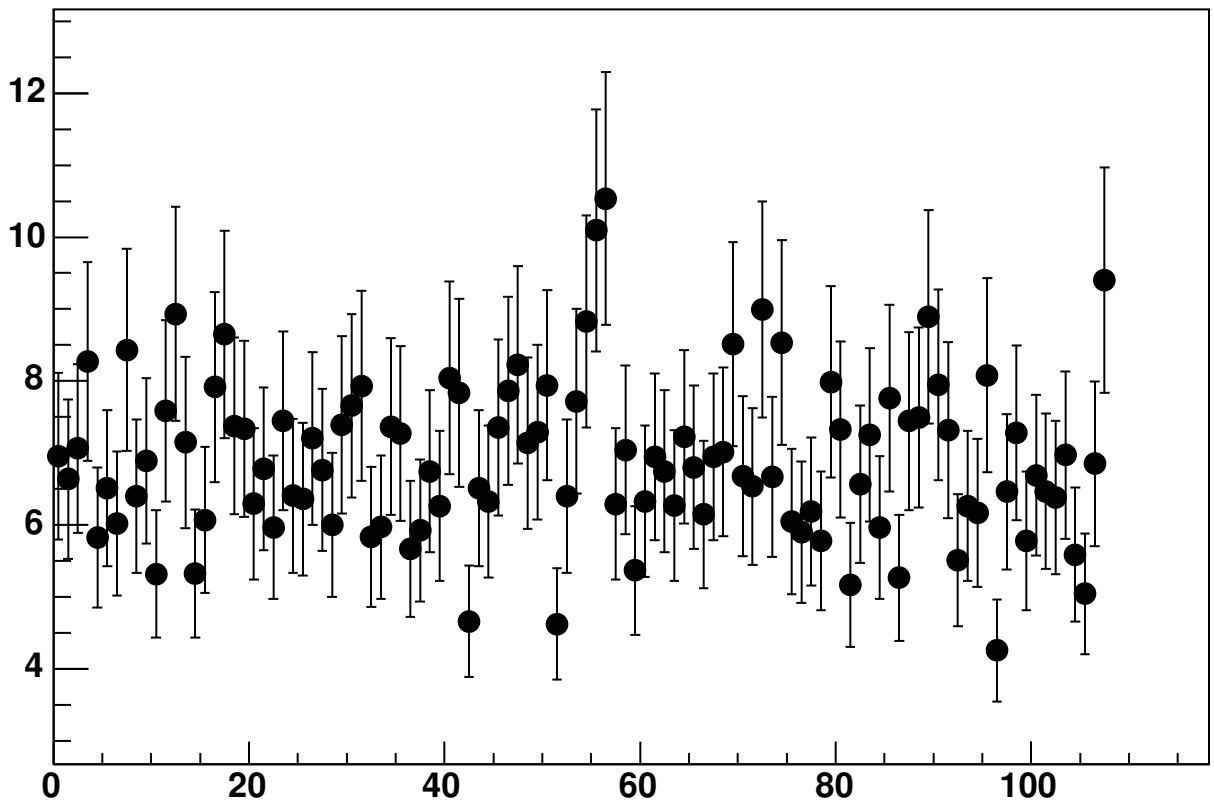
Enable 4, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



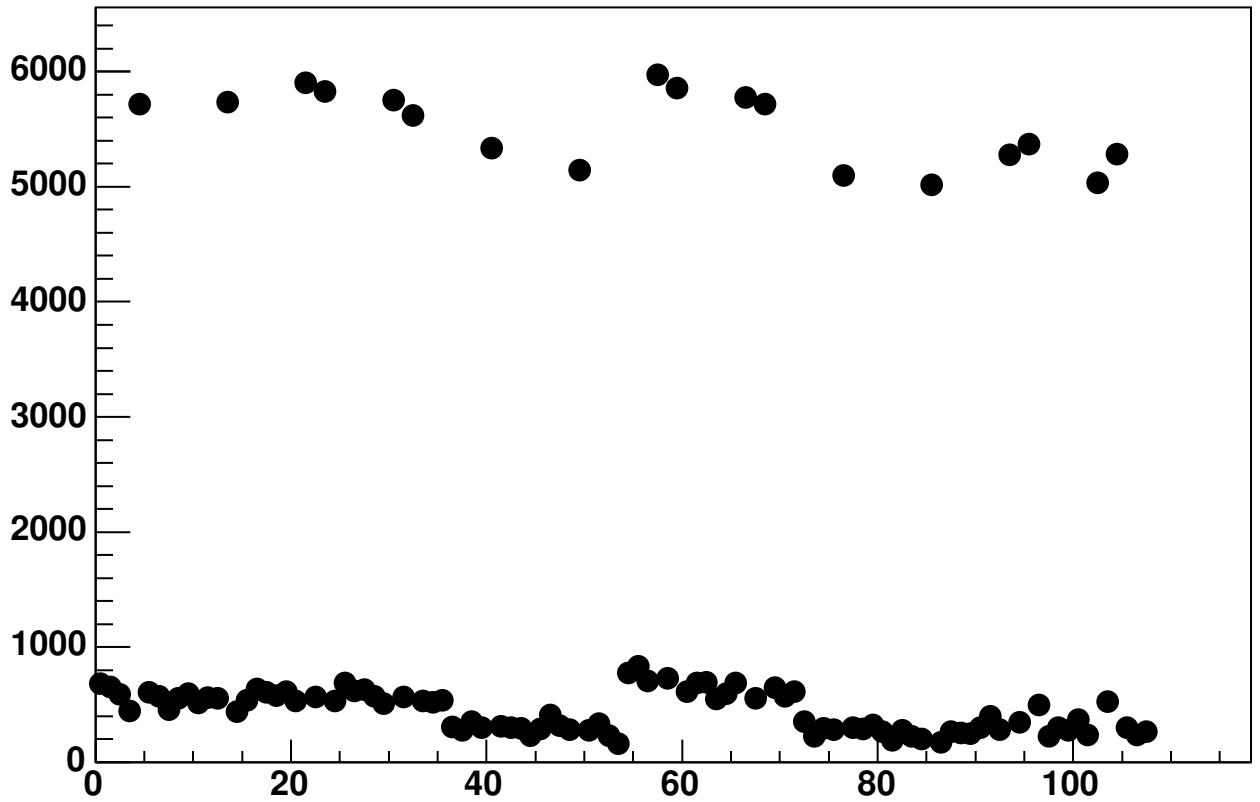
Enable 4, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



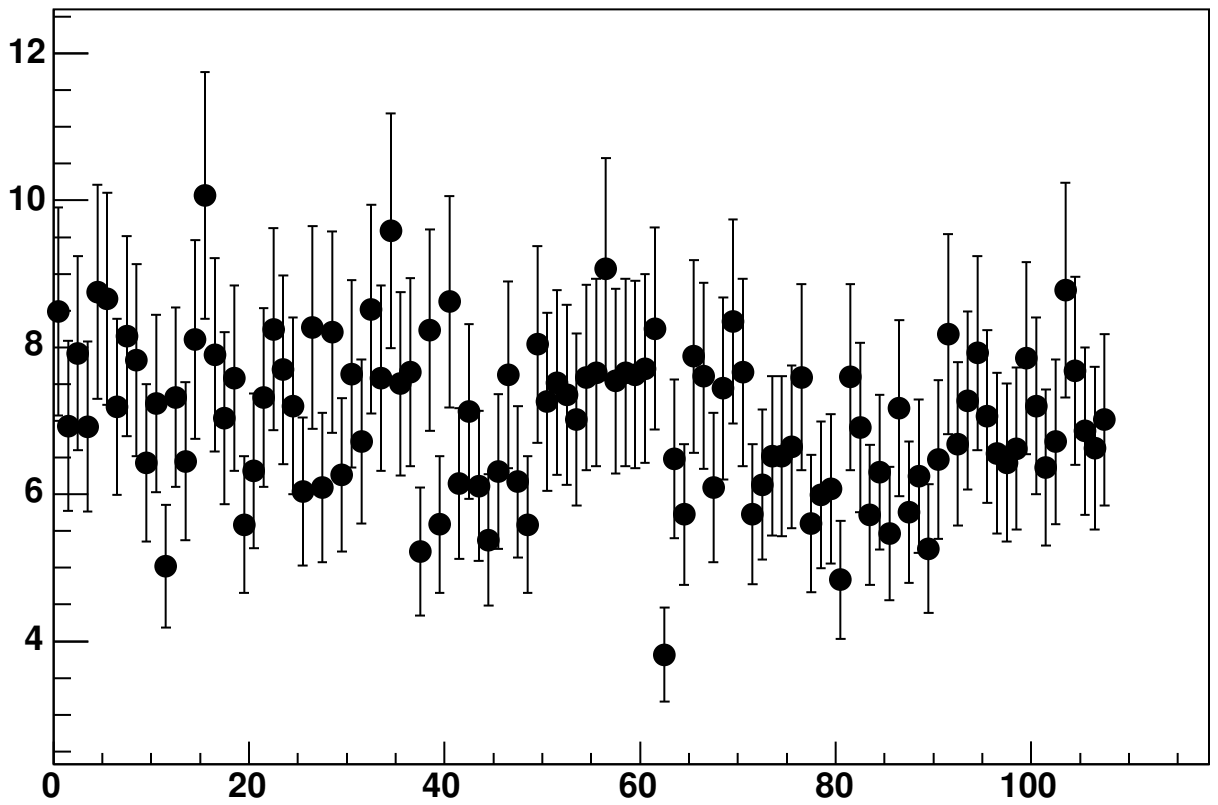
Enable 4, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



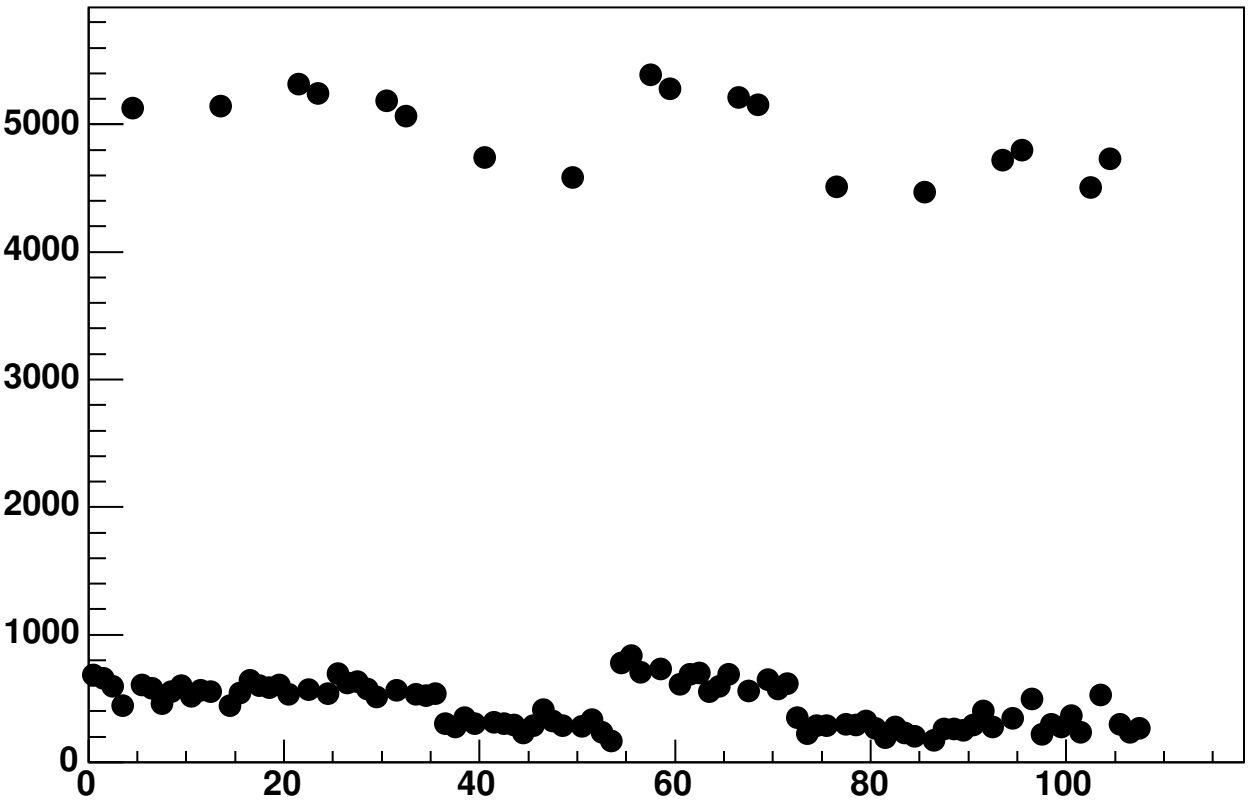
Enable 4, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



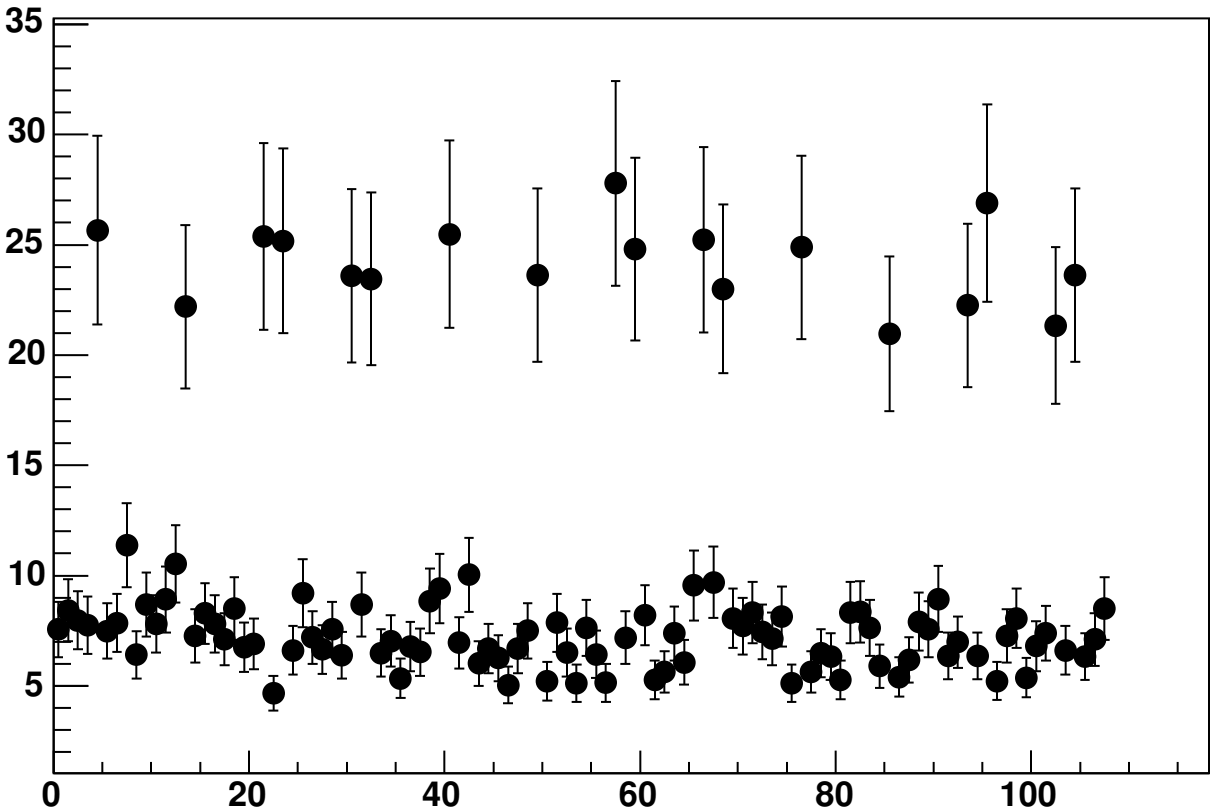
Enable 4, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



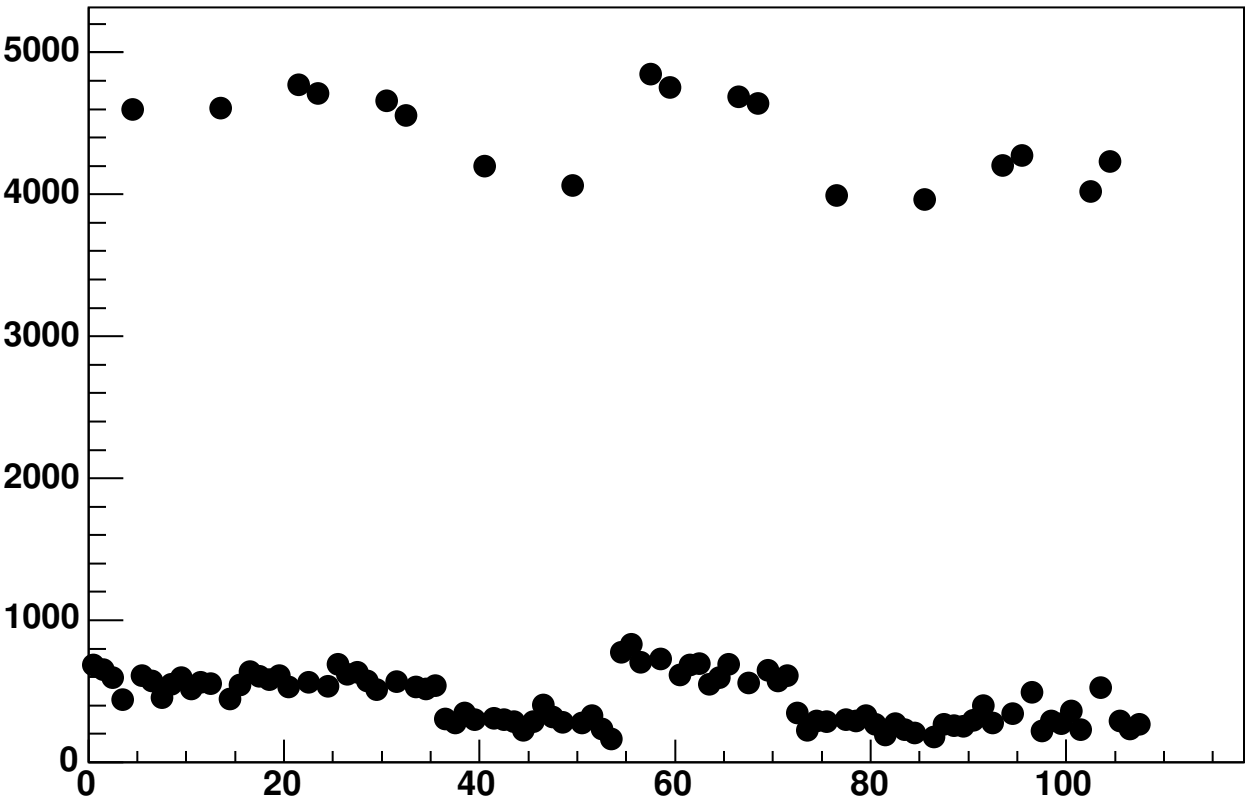
Enable 4, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



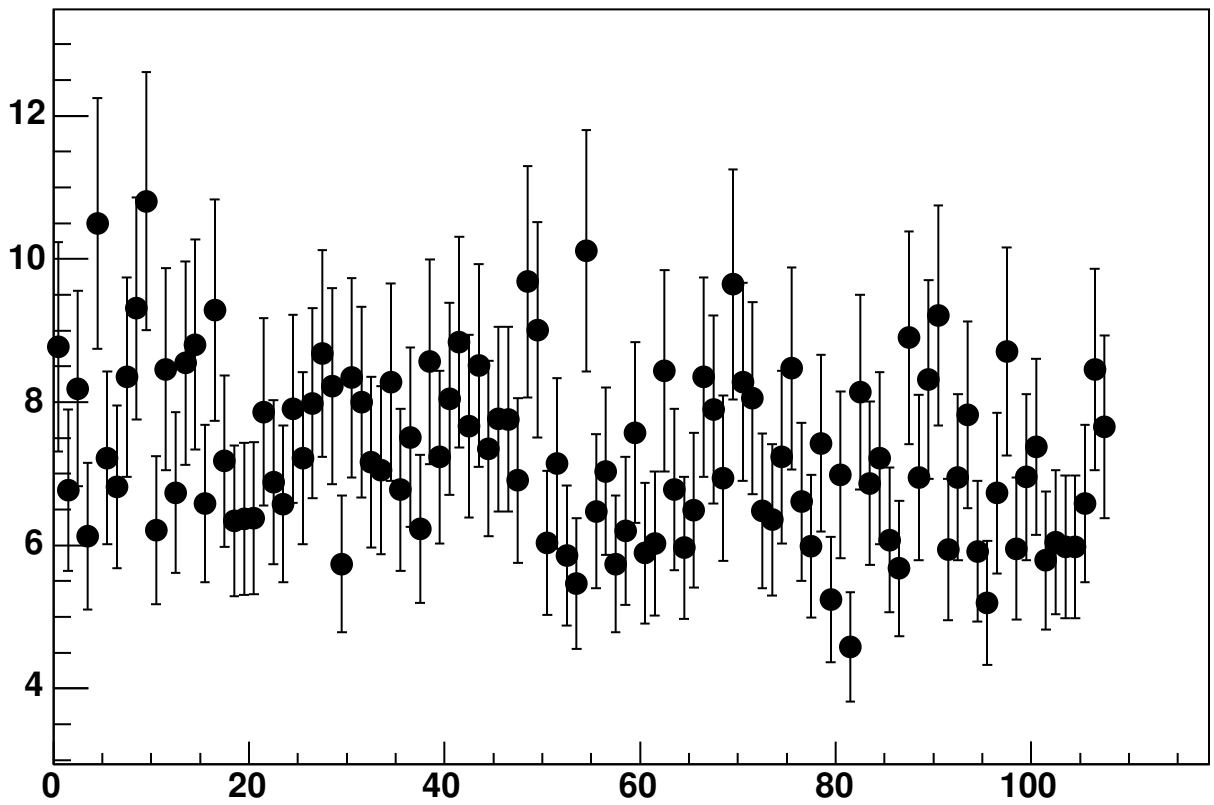
Enable 4, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



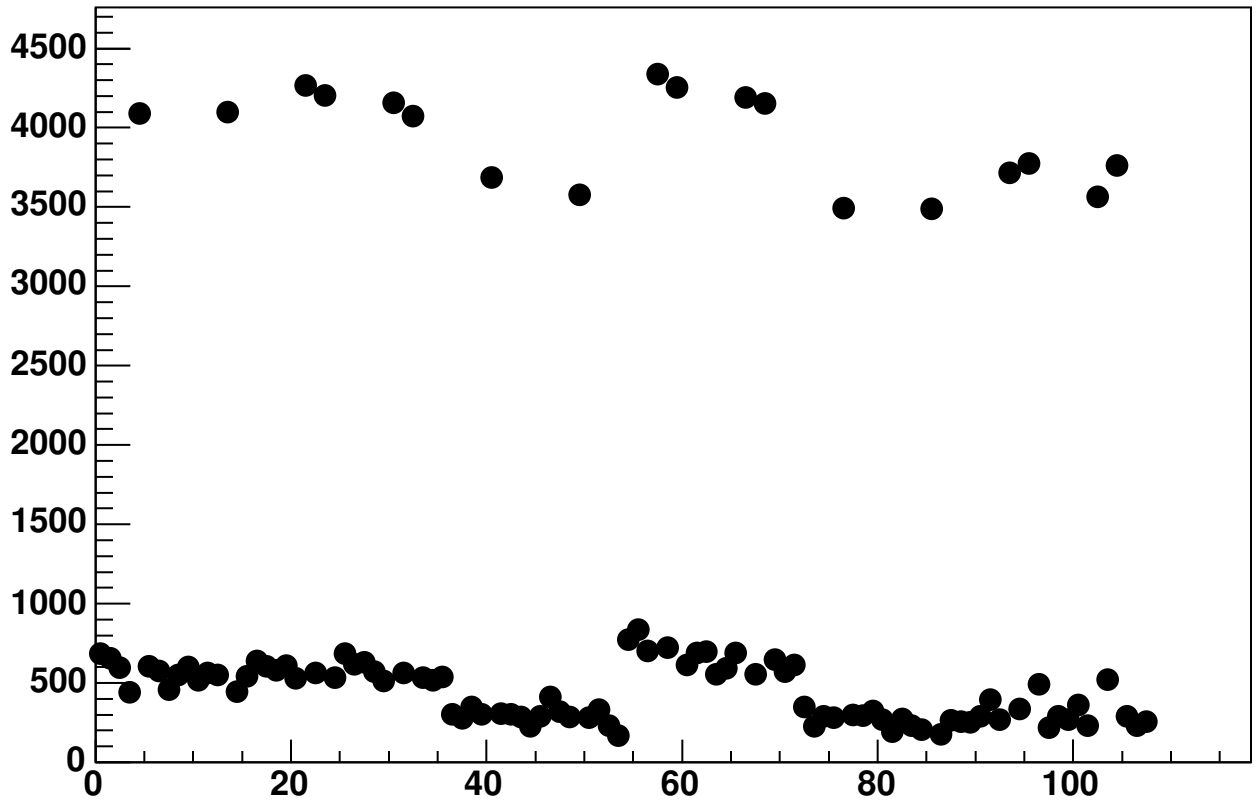
Enable 4, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



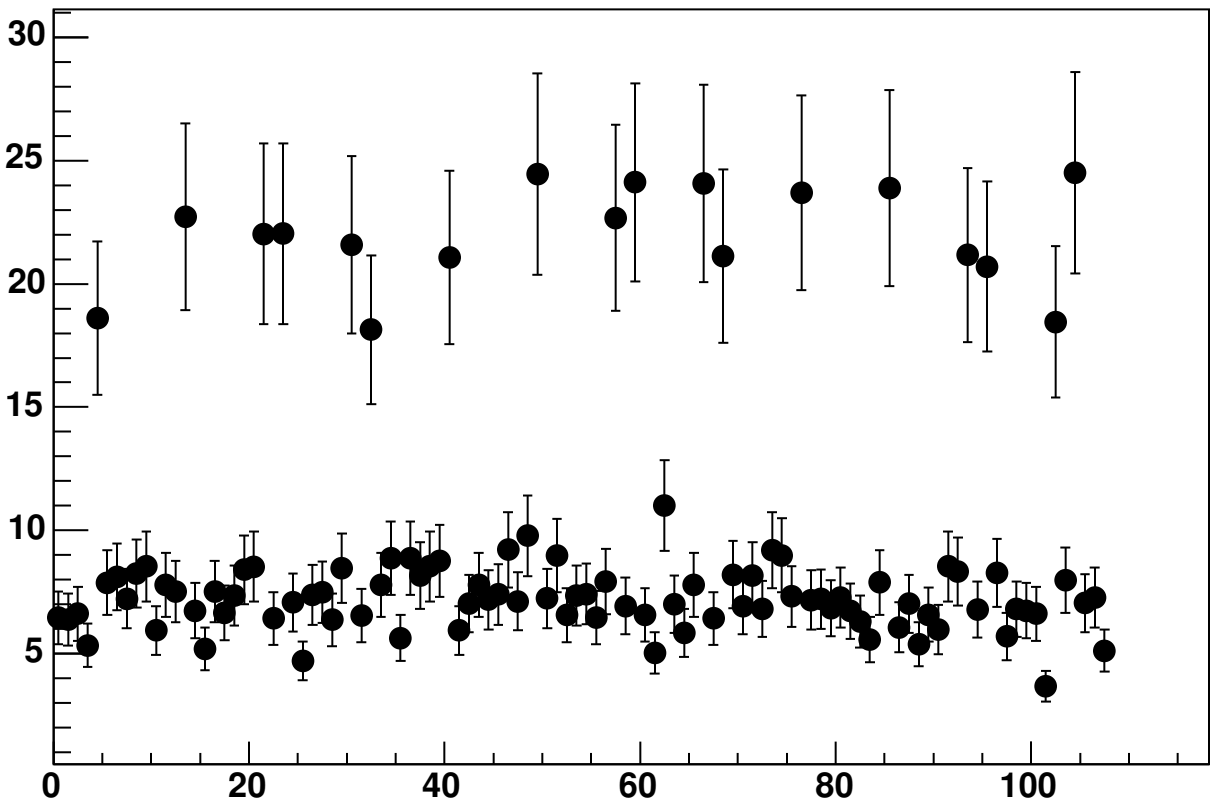
Enable 4, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



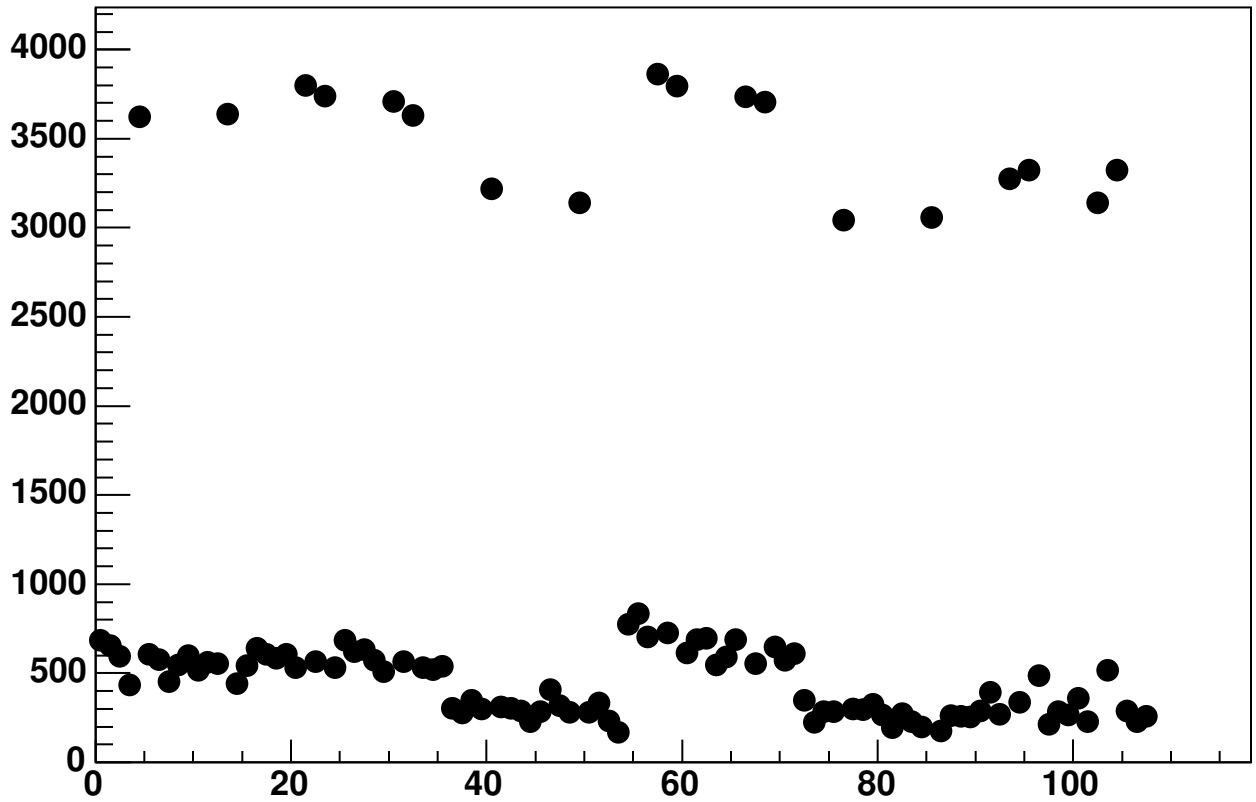
Enable 4, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



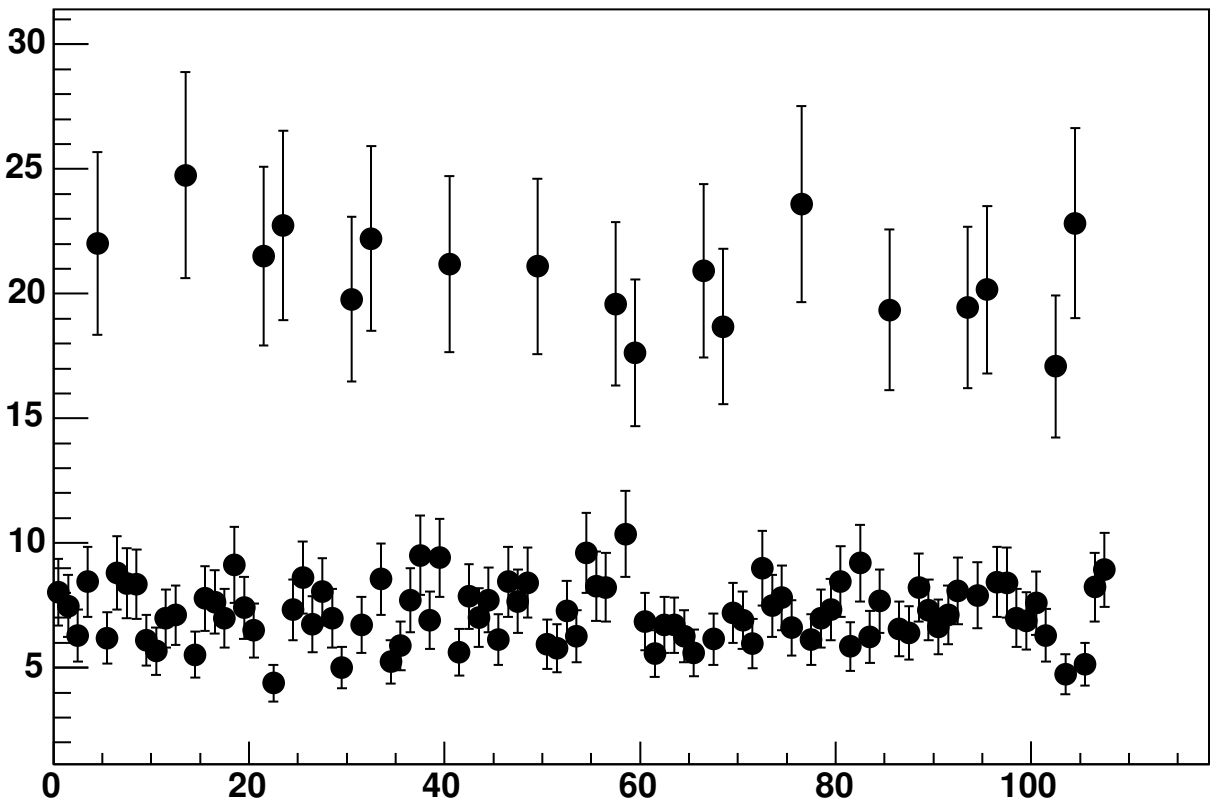
Enable 4, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



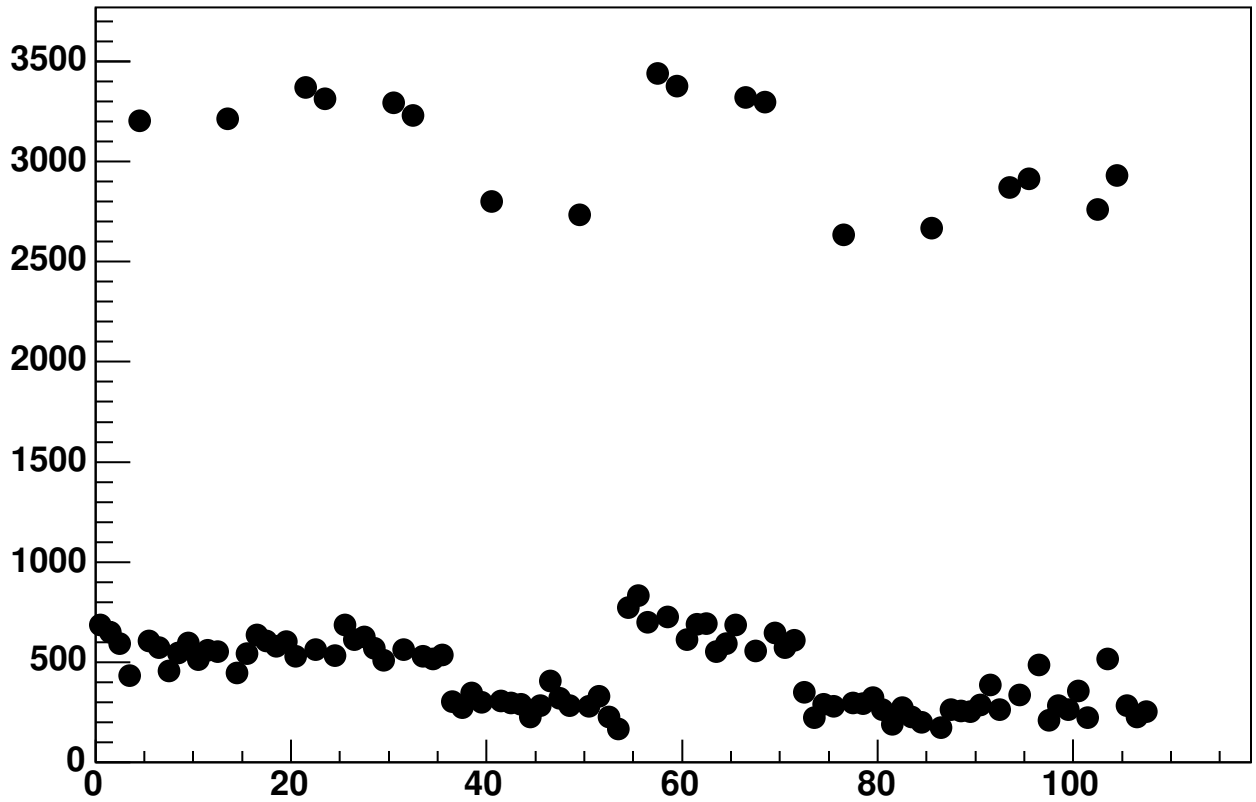
Enable 4, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



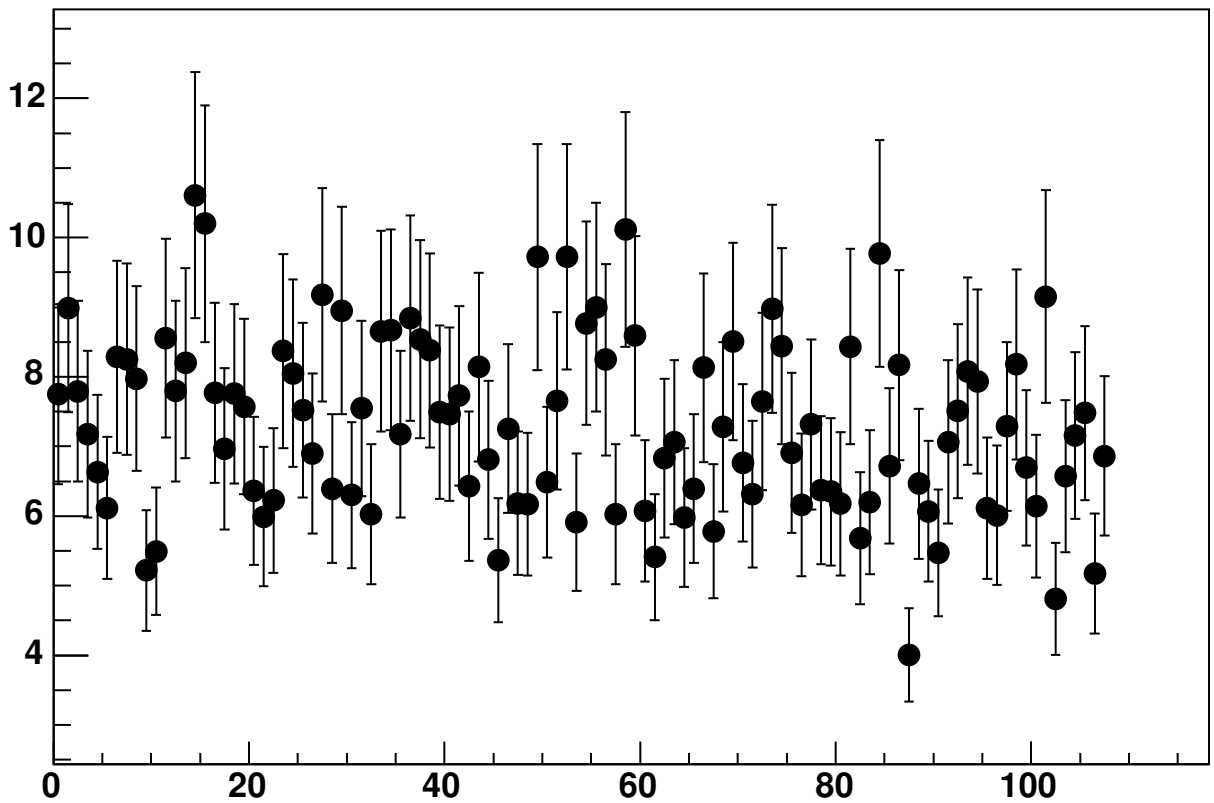
Enable 4, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



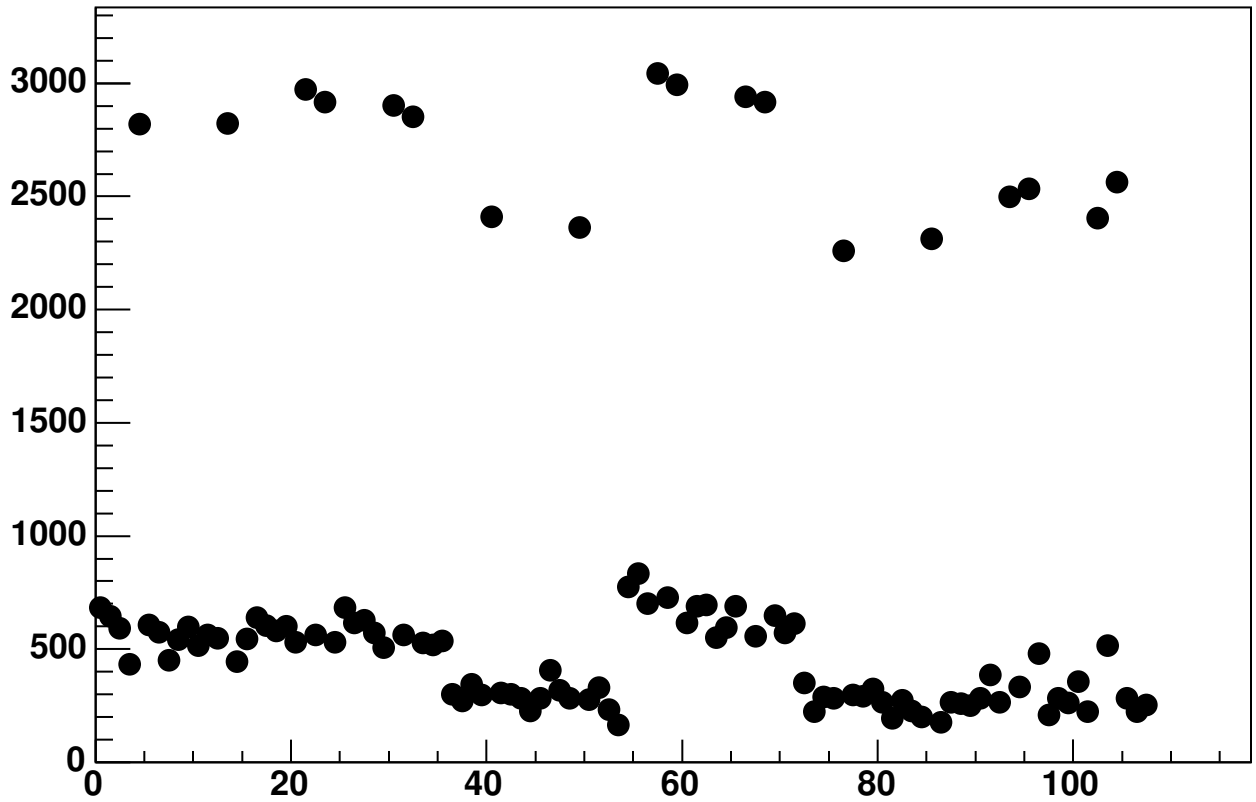
Enable 4, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



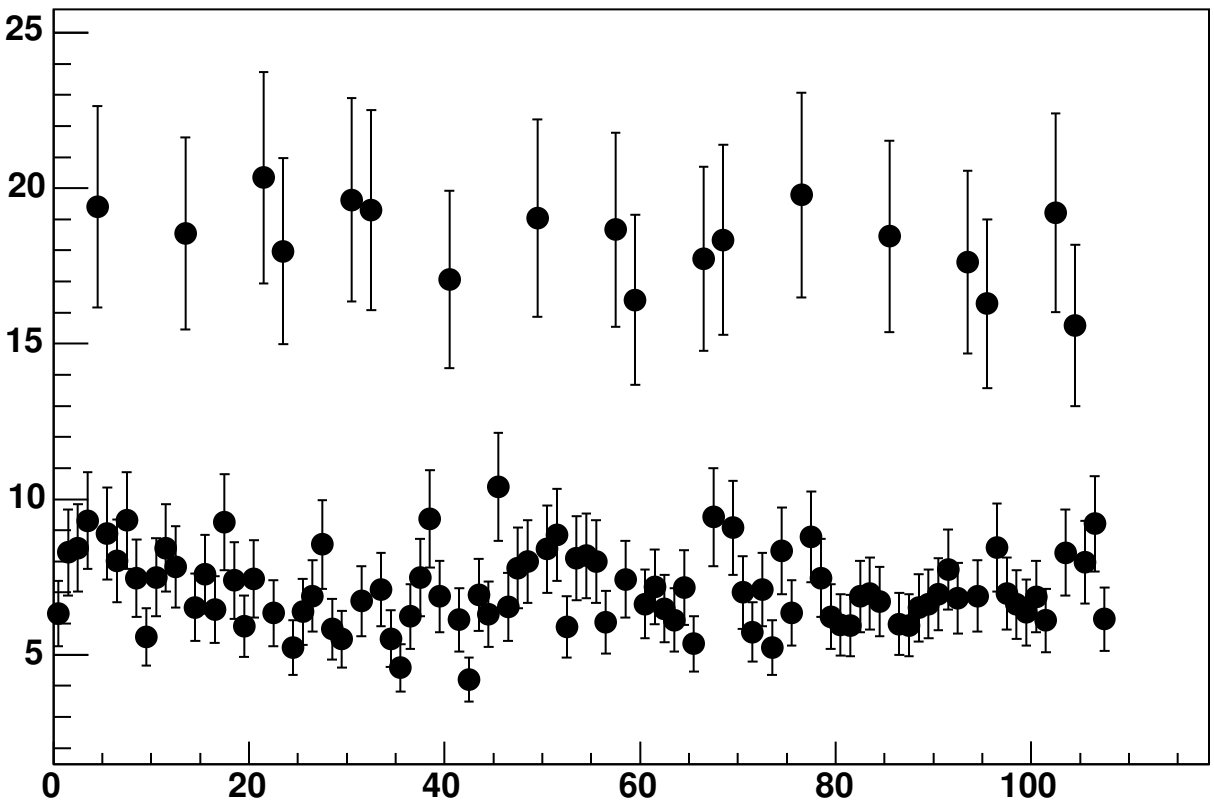
Enable 4, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



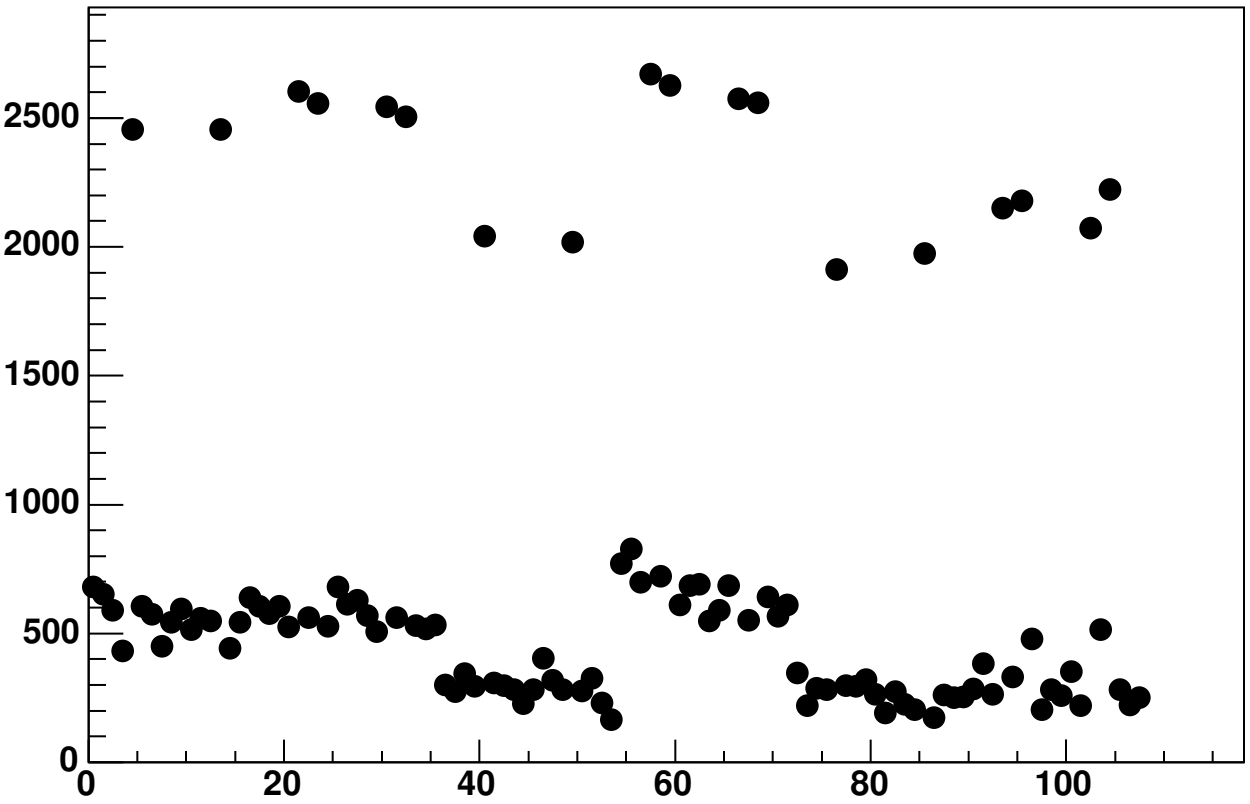
Enable 4, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



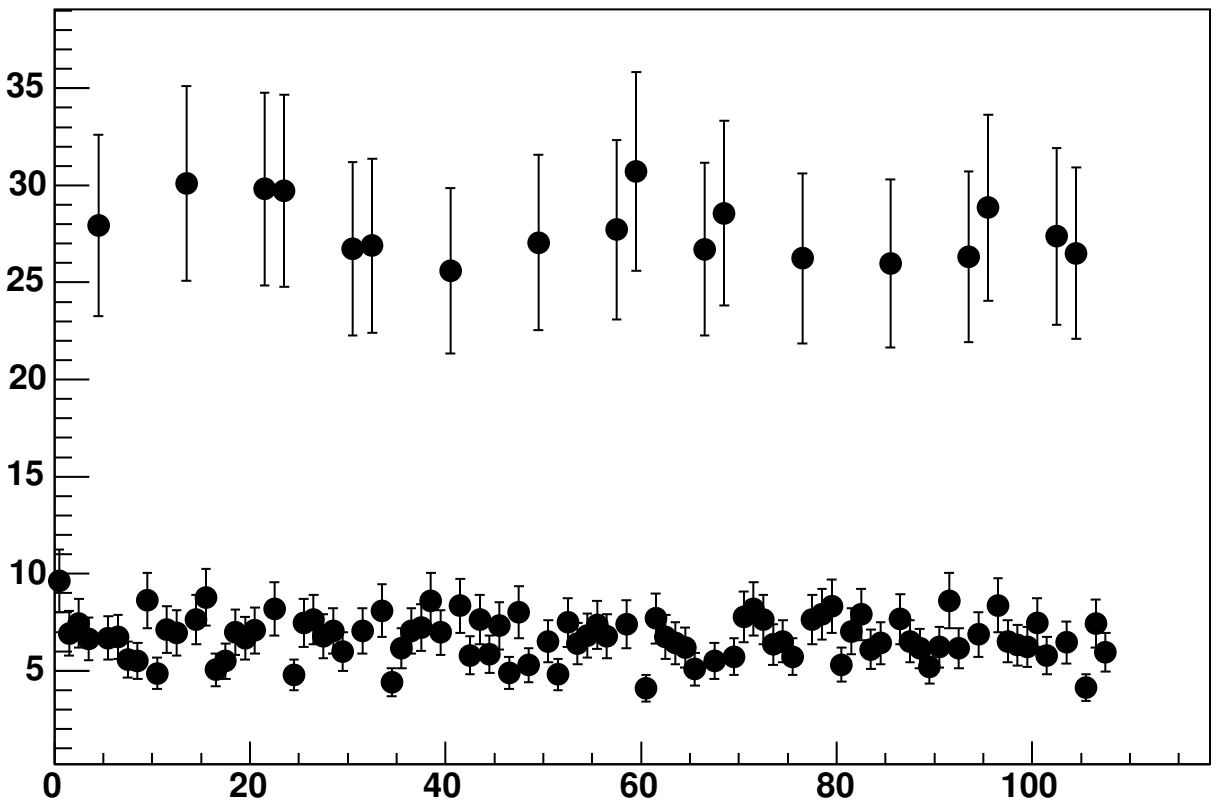
Enable 4, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



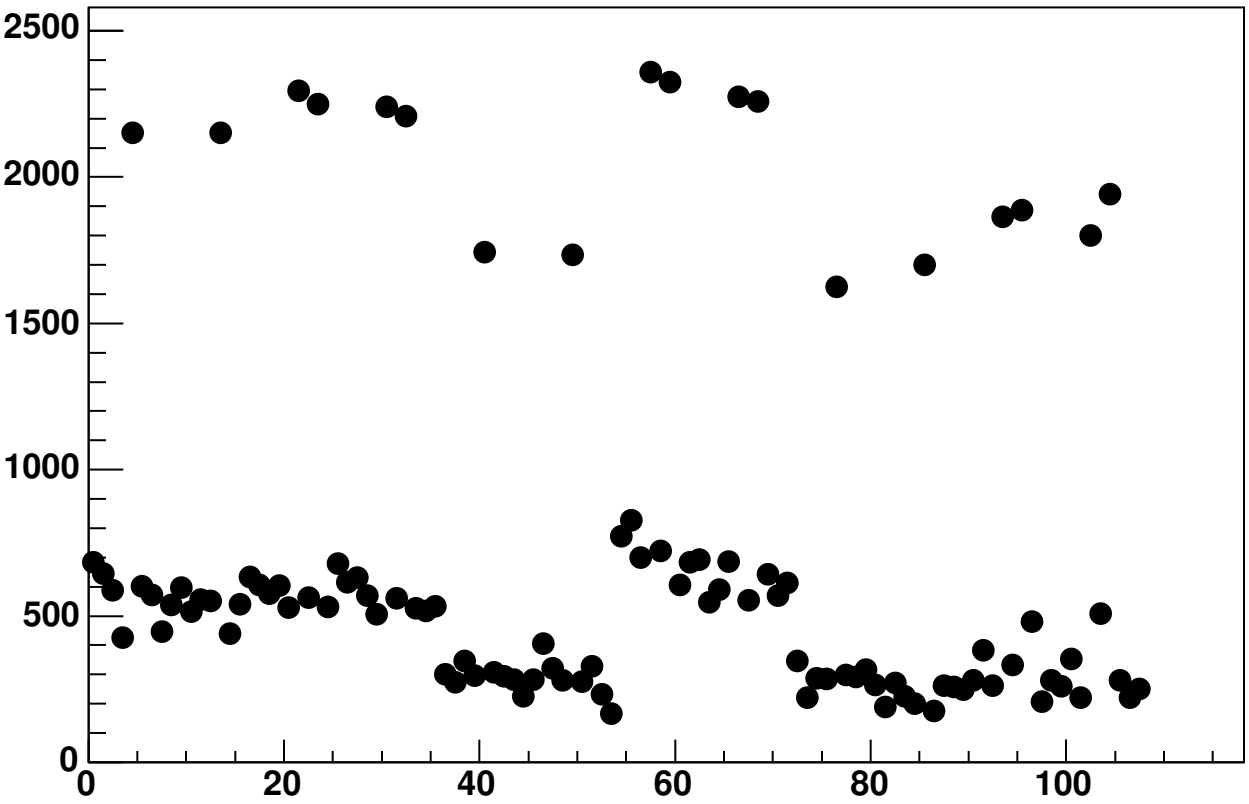
Enable 4, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



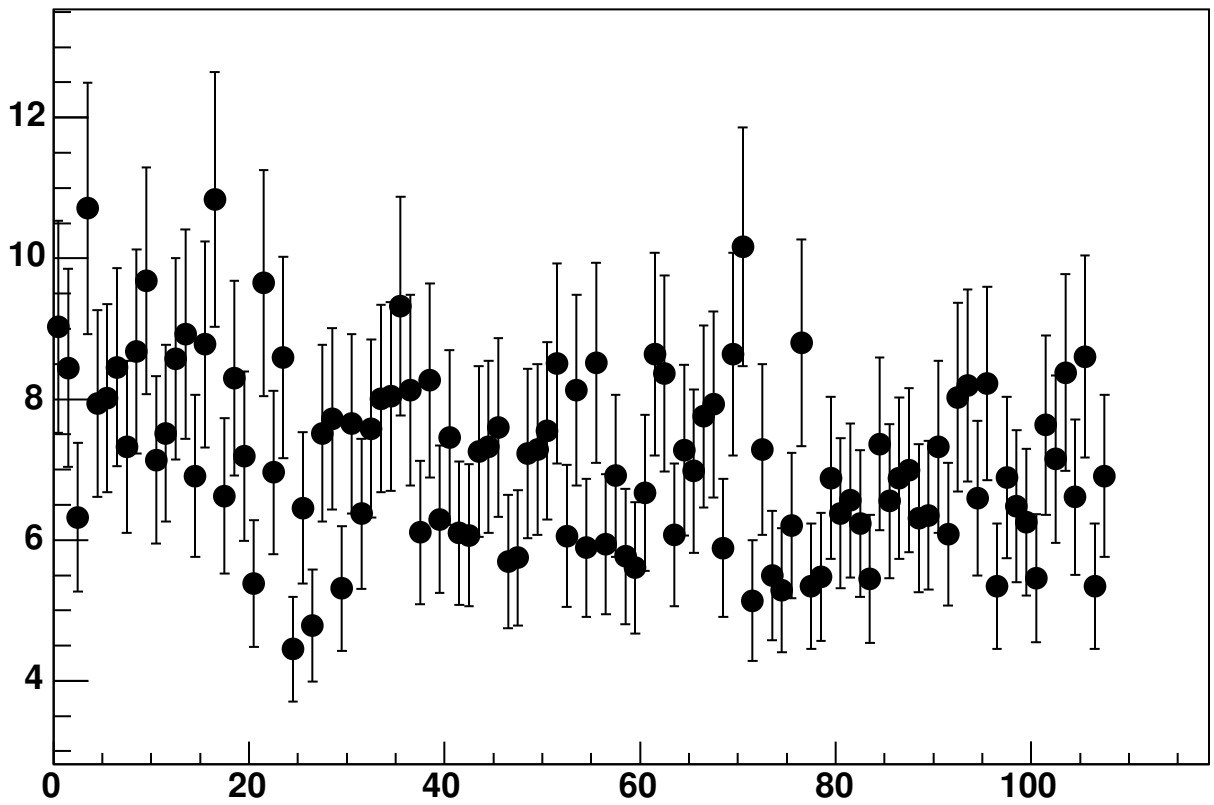
Enable 4, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



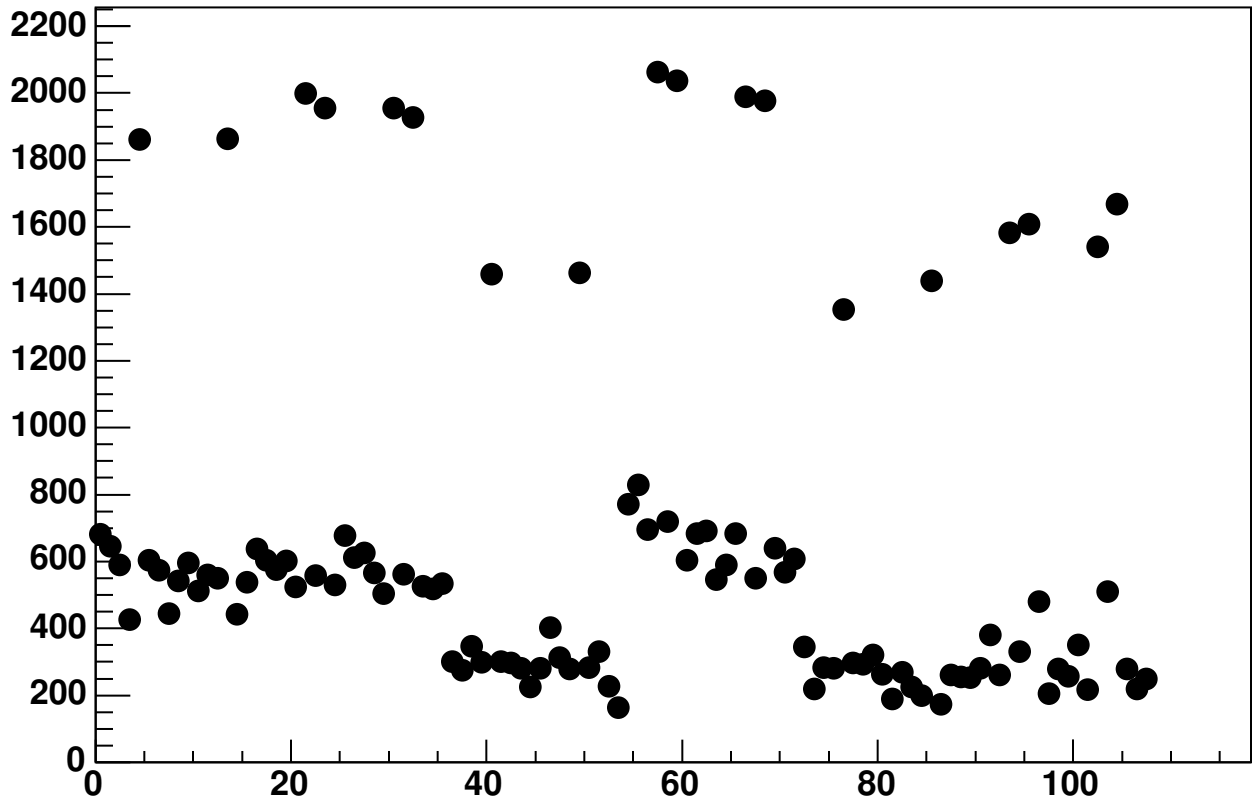
Enable 4, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



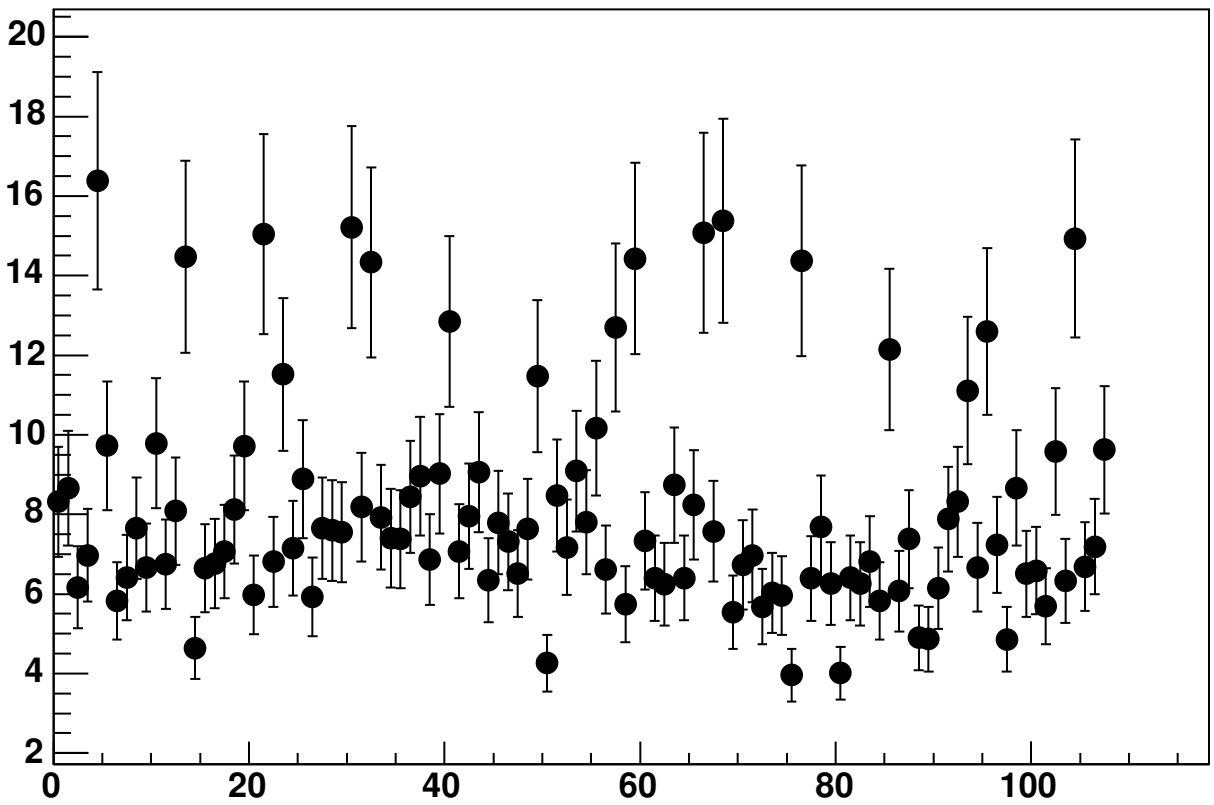
Enable 4, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



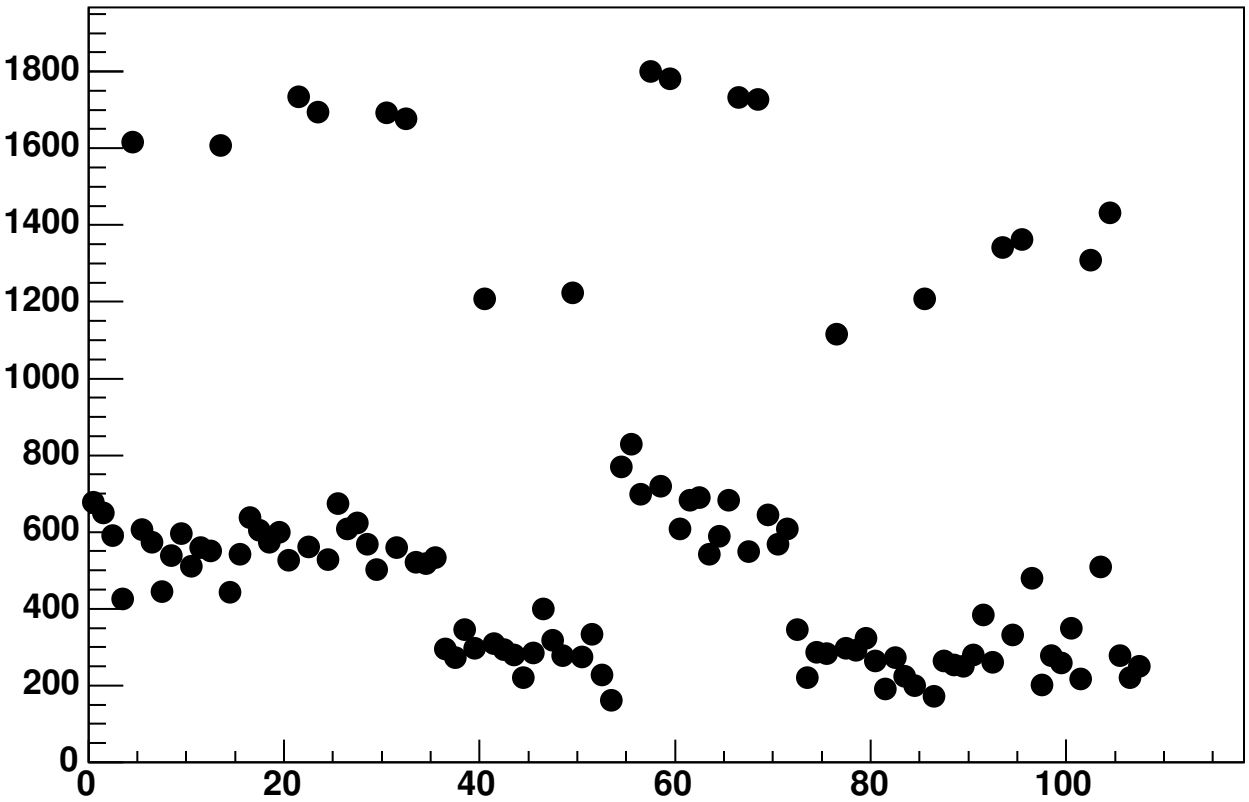
Enable 4, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



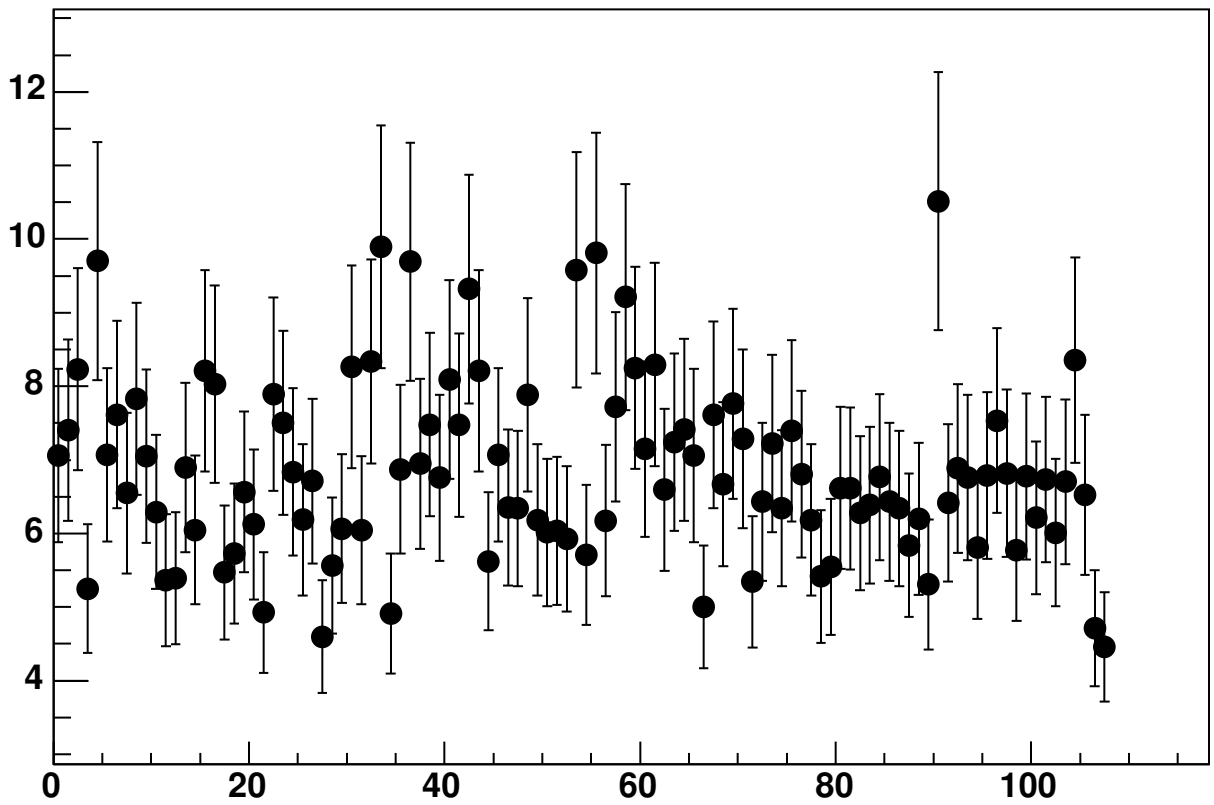
Enable 4, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



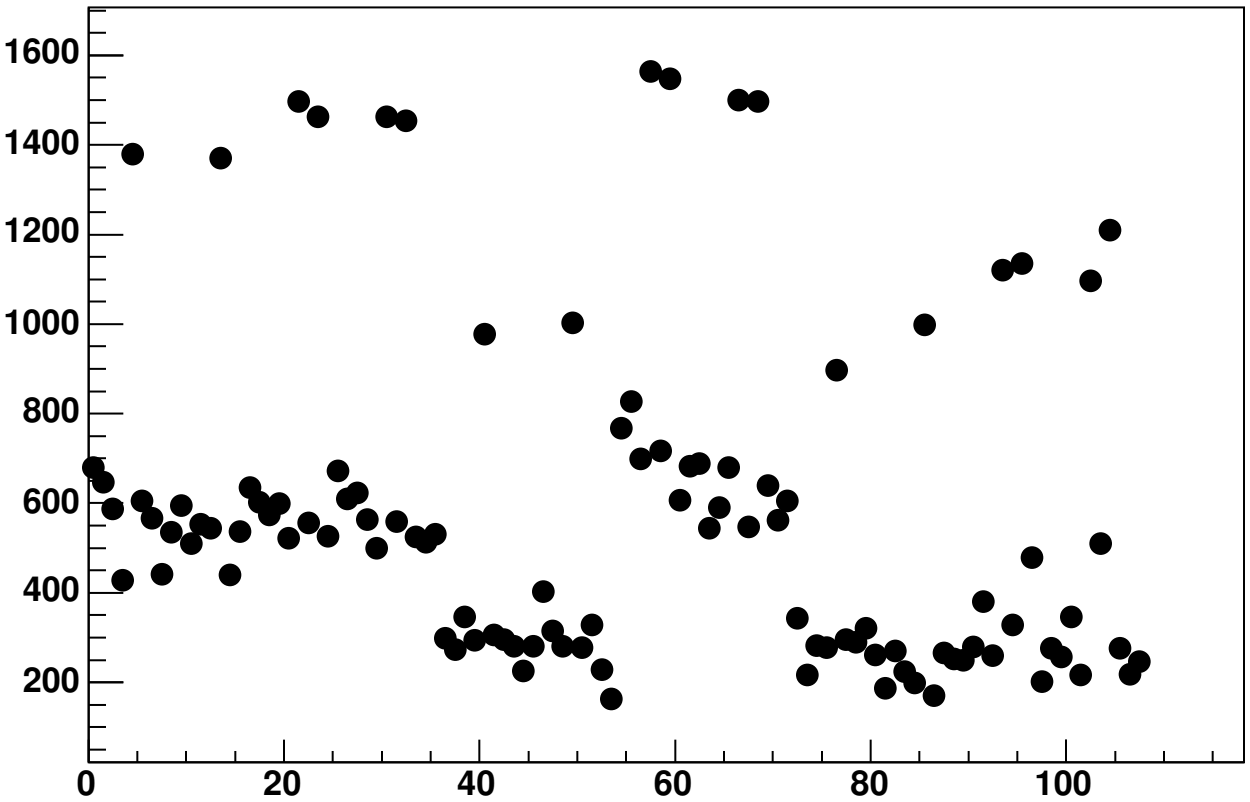
Enable 4, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



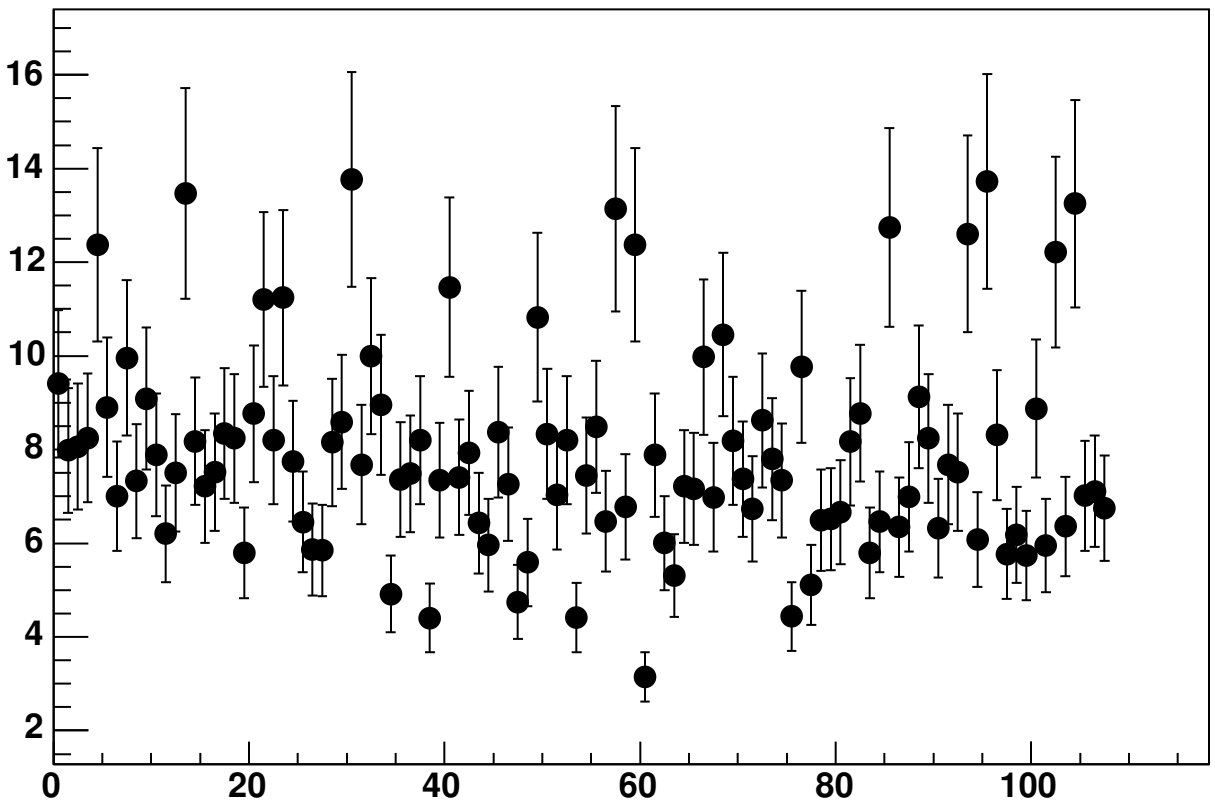
Enable 4, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



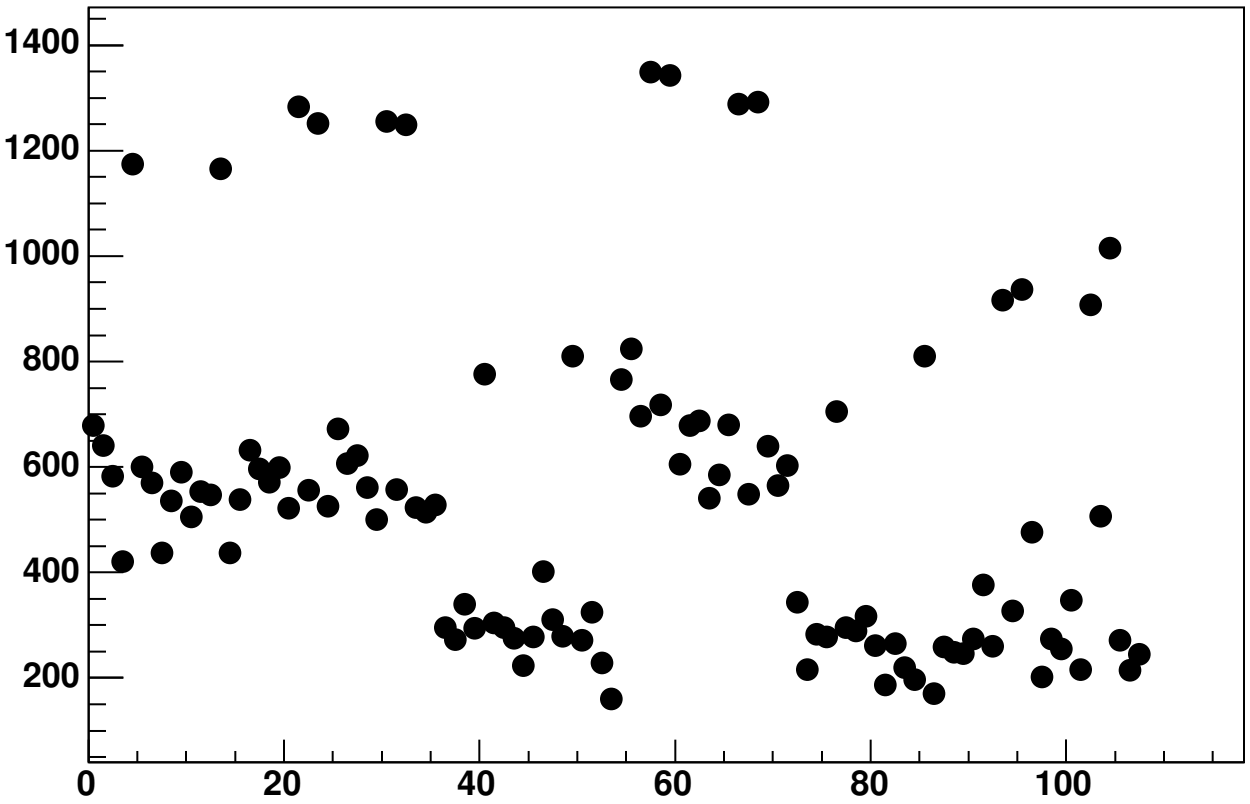
Enable 4, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



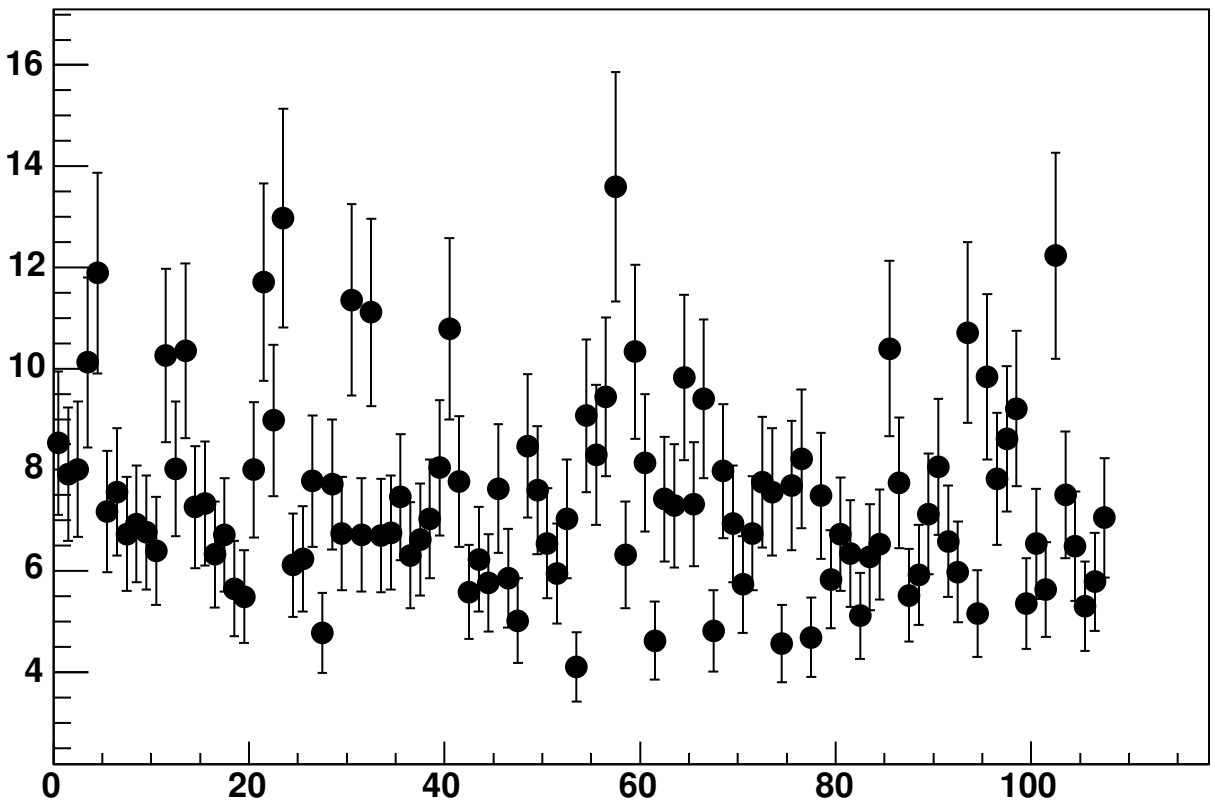
Enable 4, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



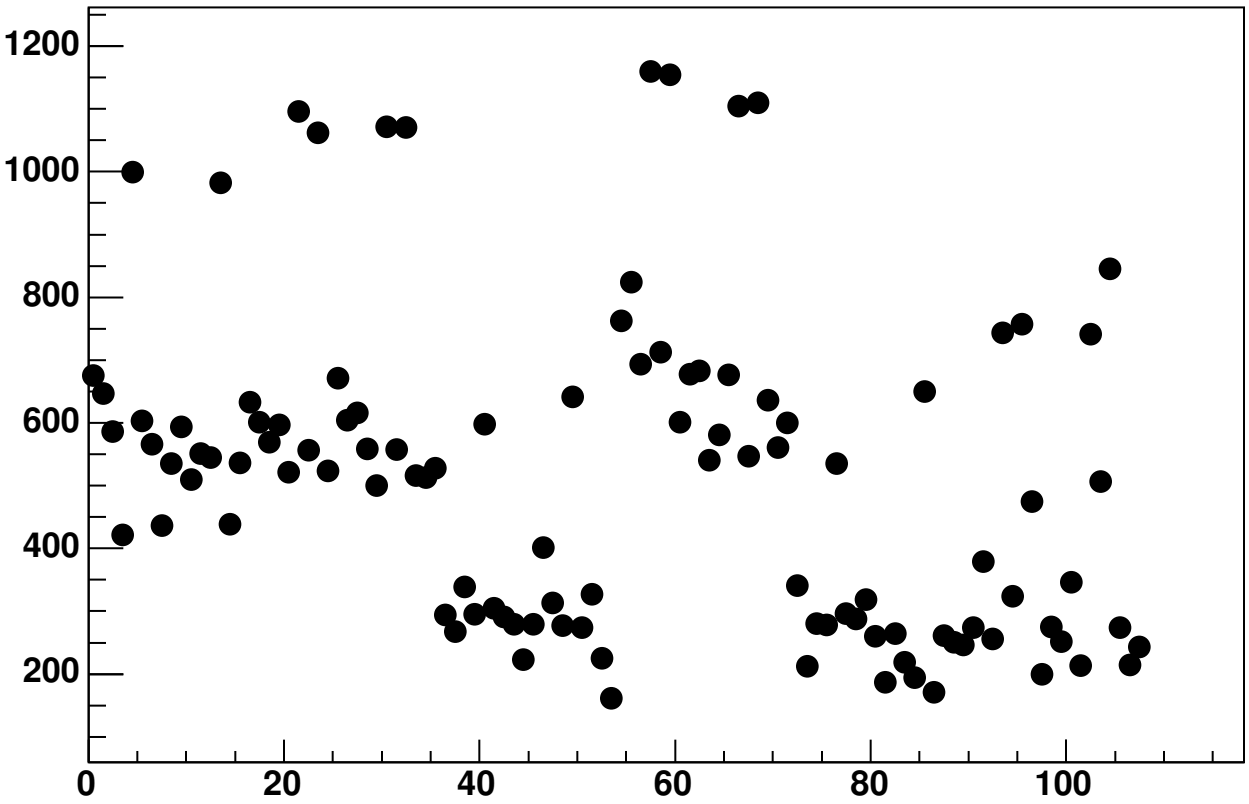
Enable 4, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



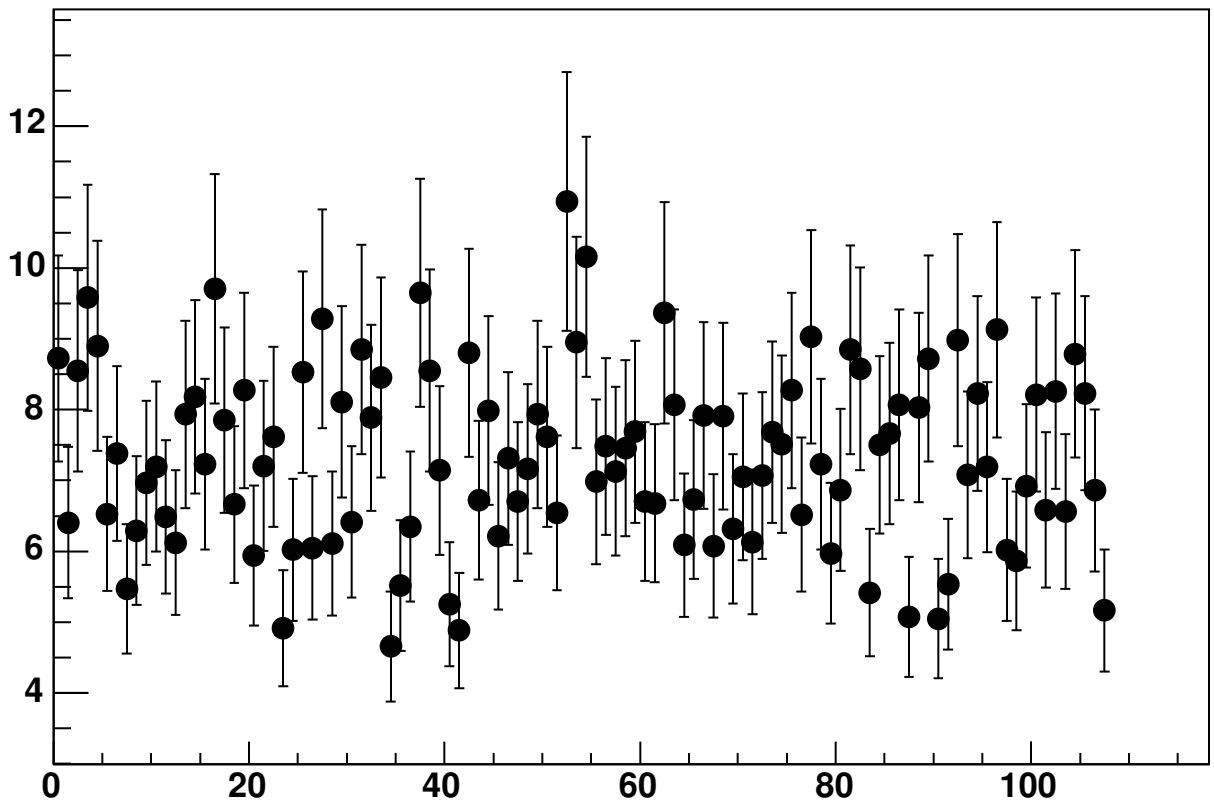
Enable 4, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



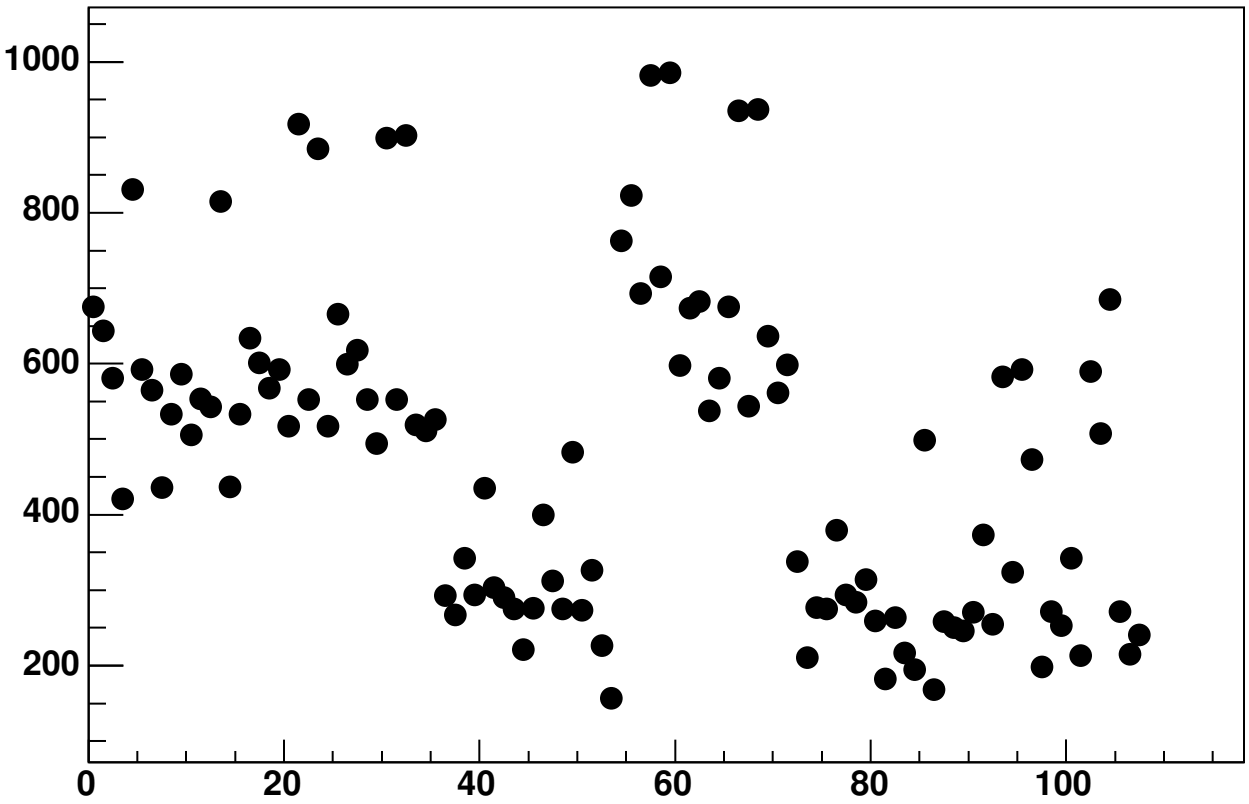
Enable 4, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



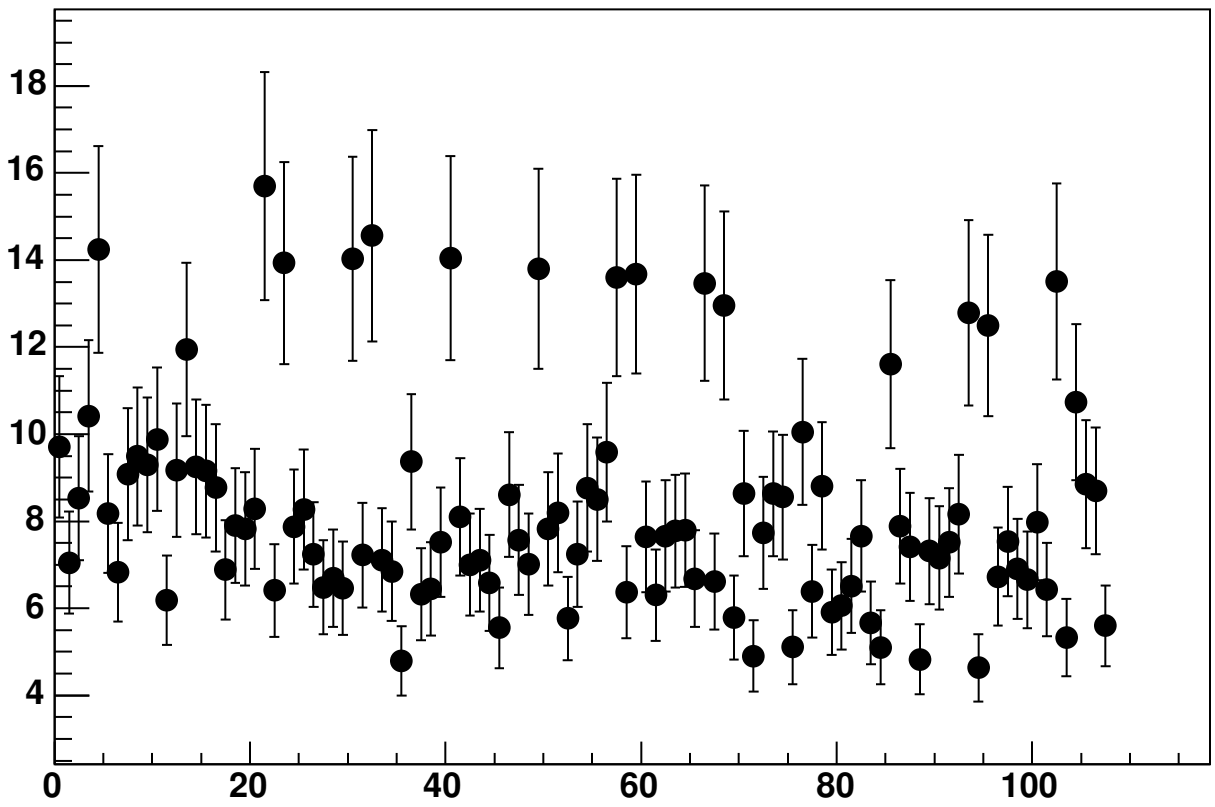
Enable 4, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



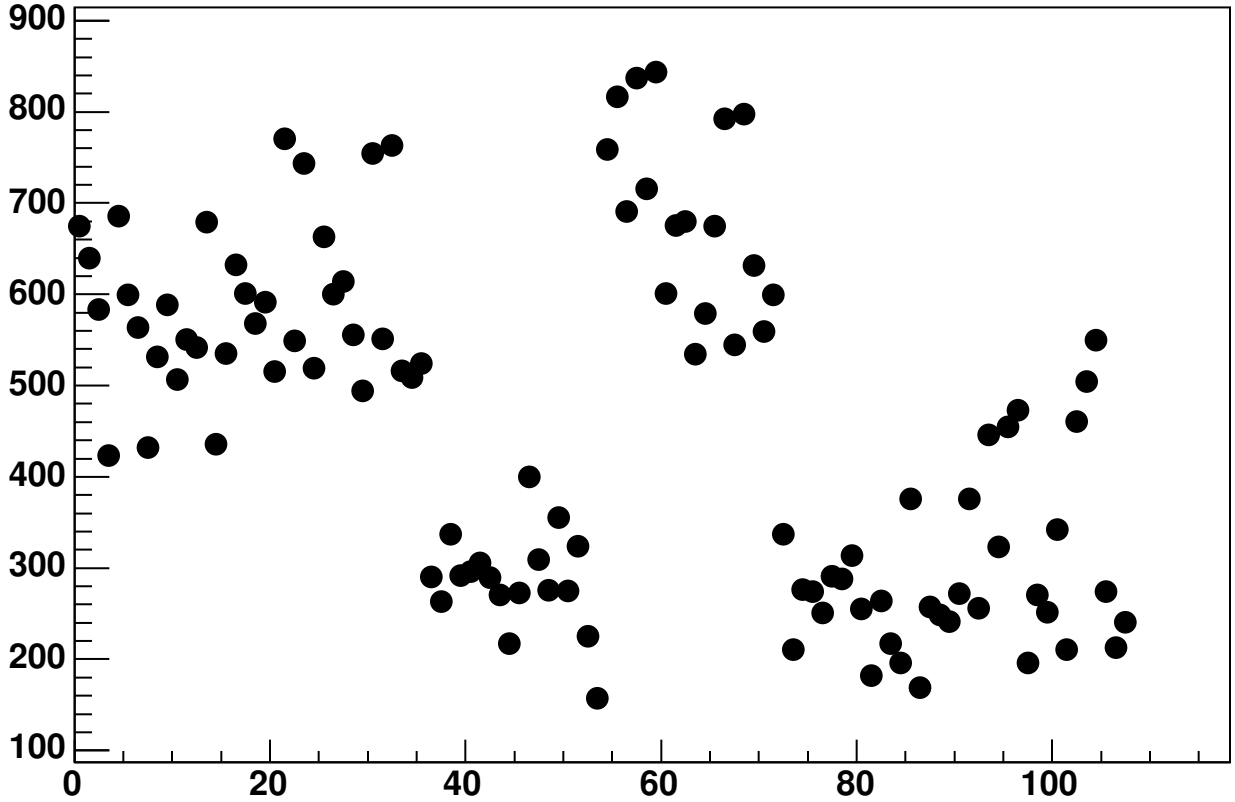
Enable 4, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



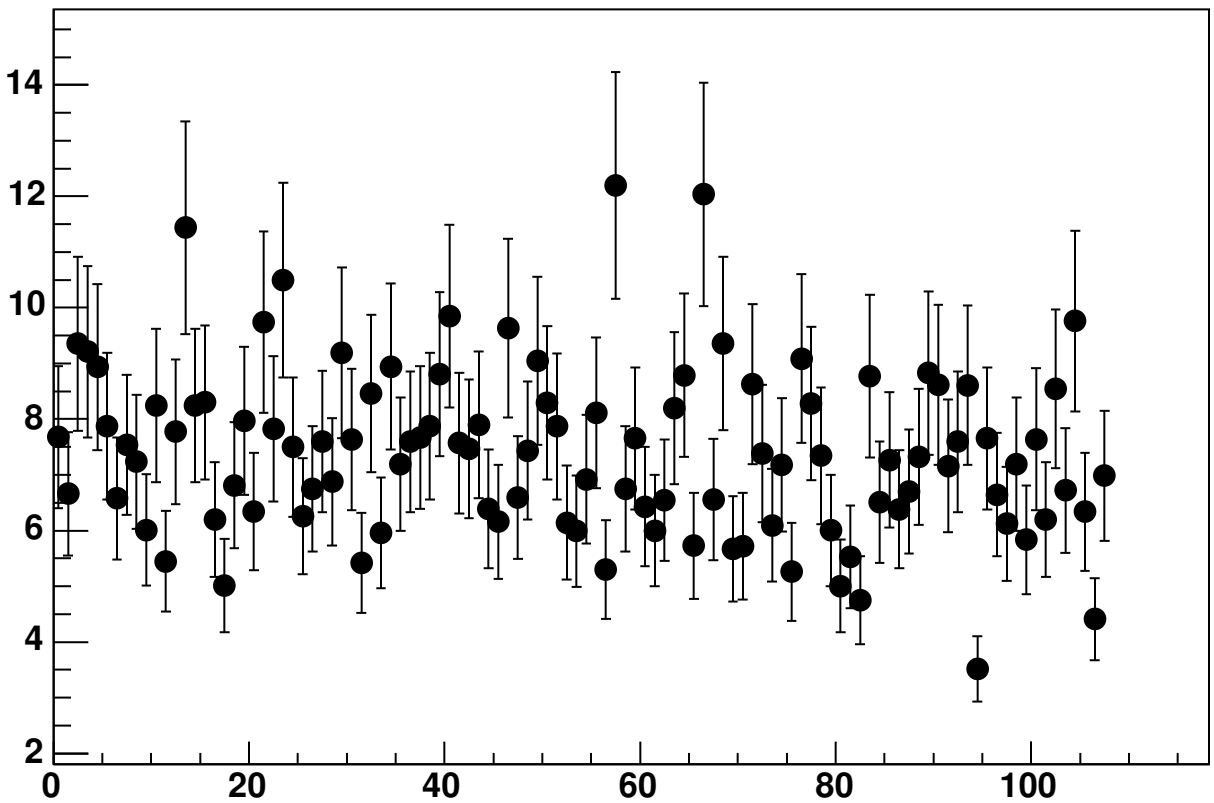
Enable 4, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



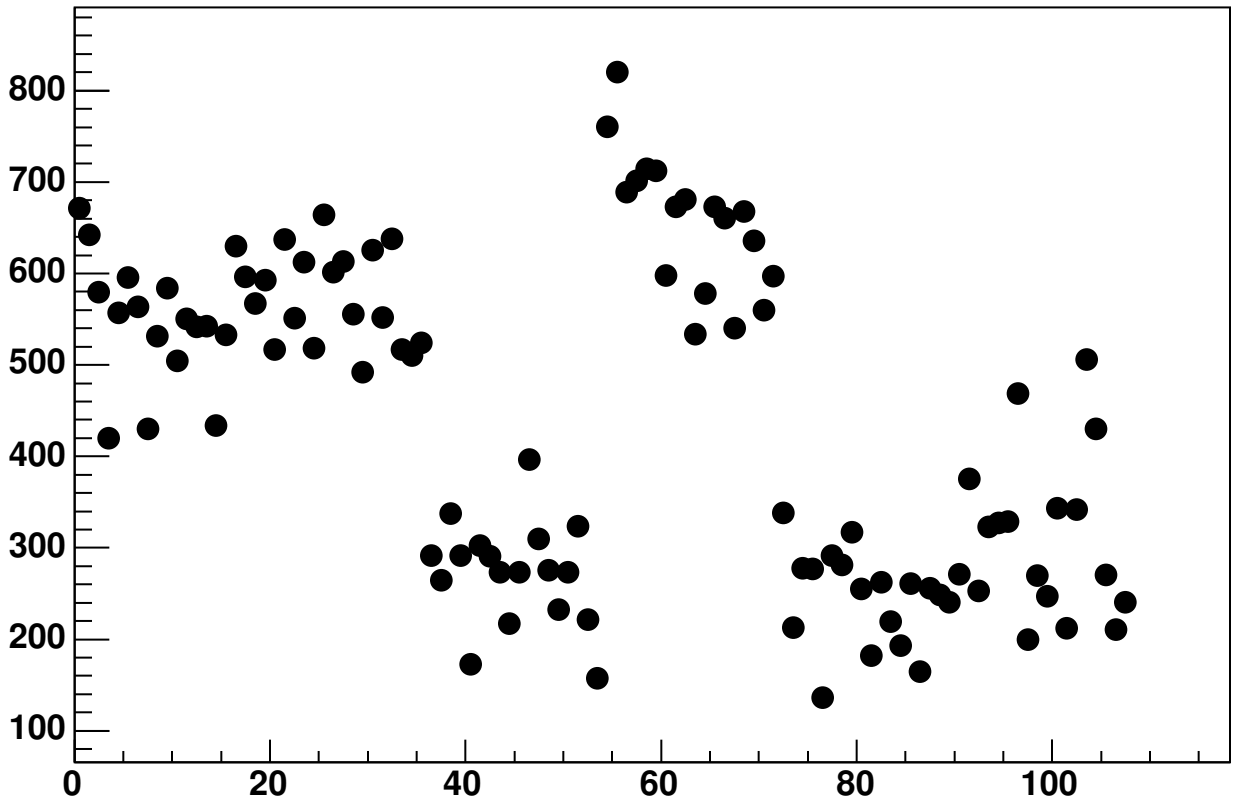
Enable 4, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



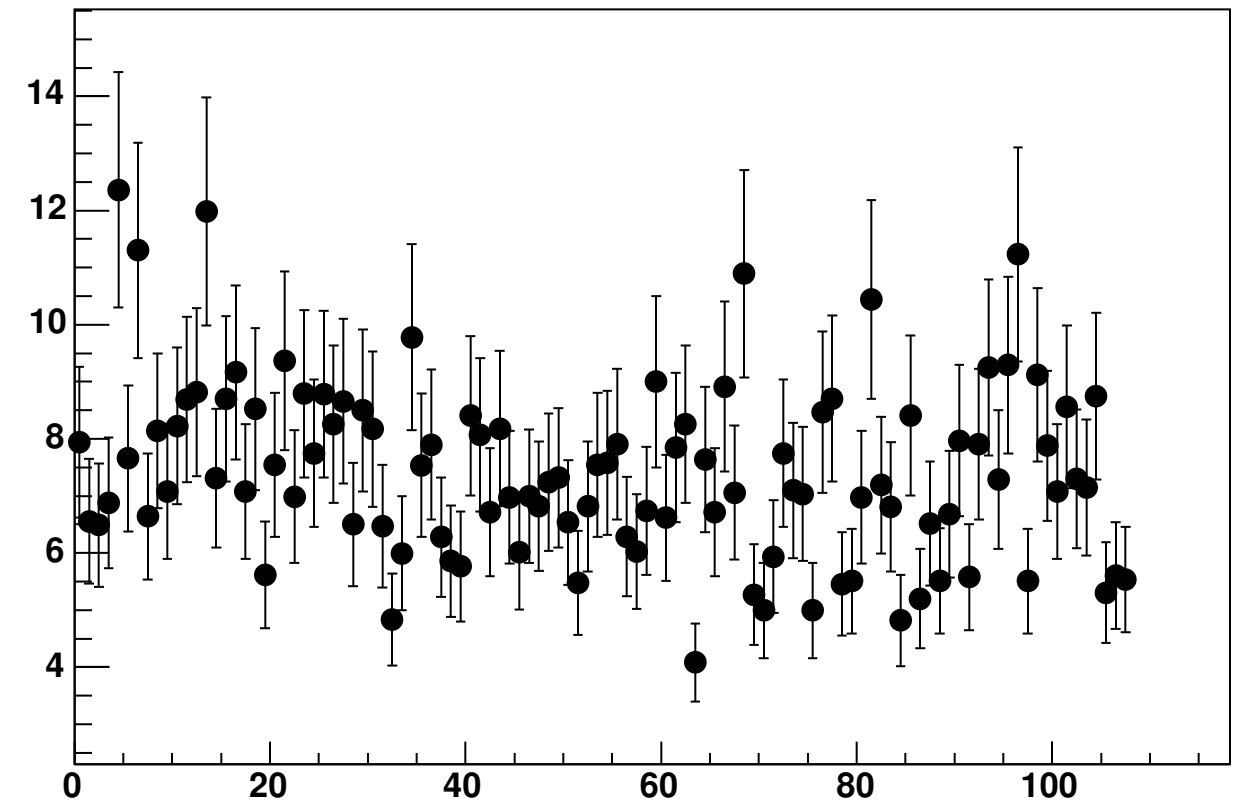
Enable 4, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



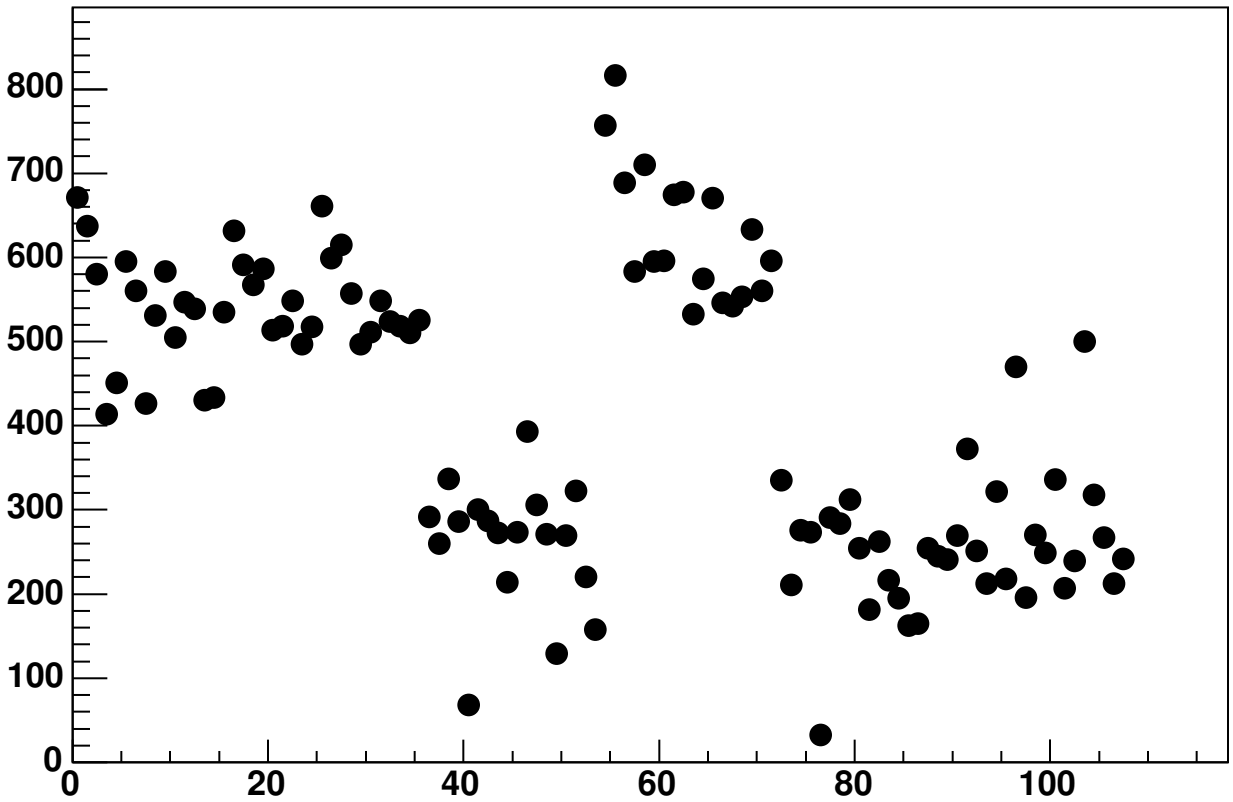
Enable 4, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



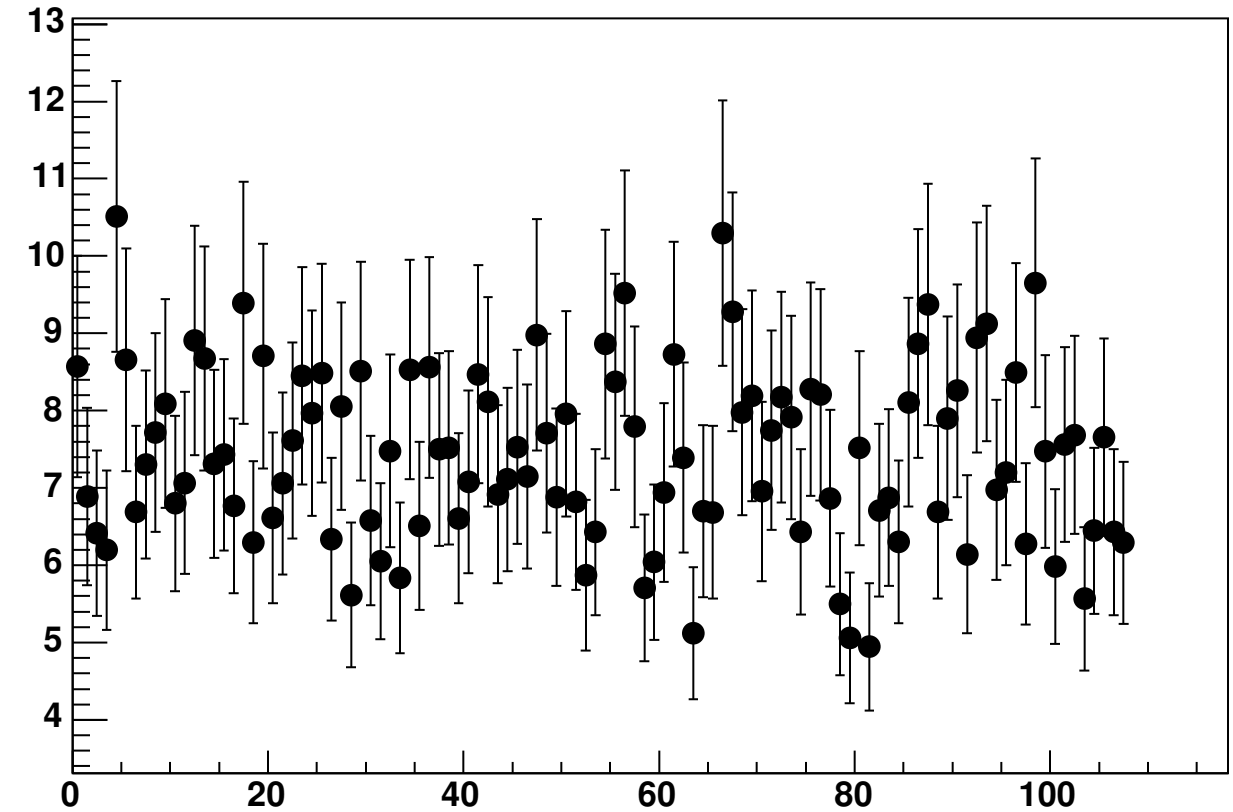
Enable 4, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



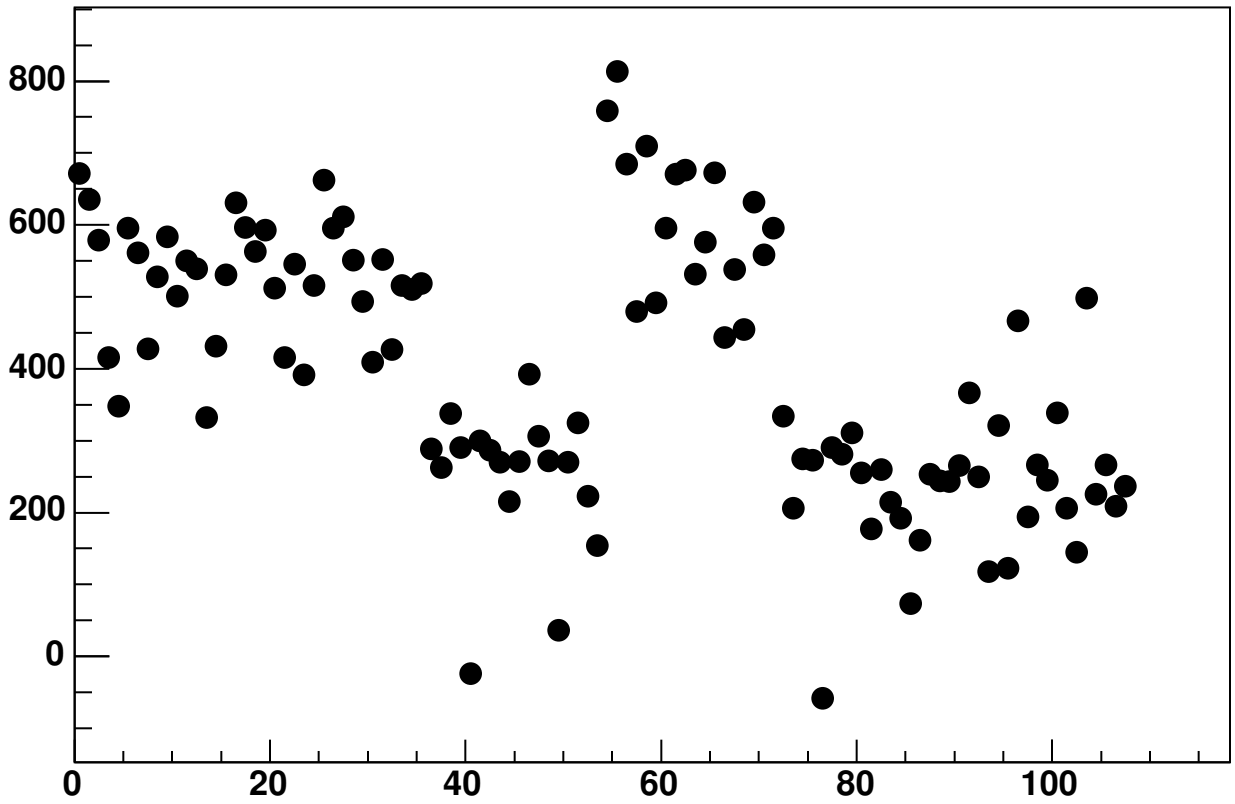
Enable 4, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



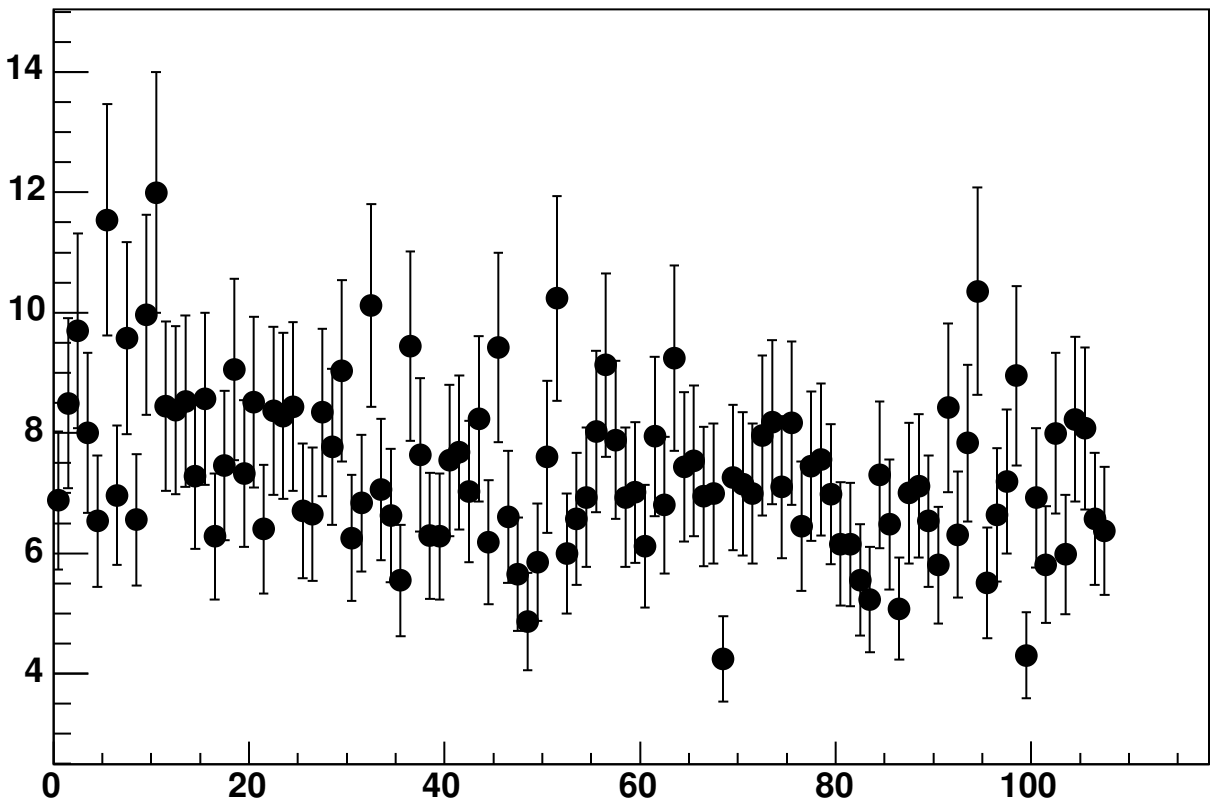
Enable 4, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



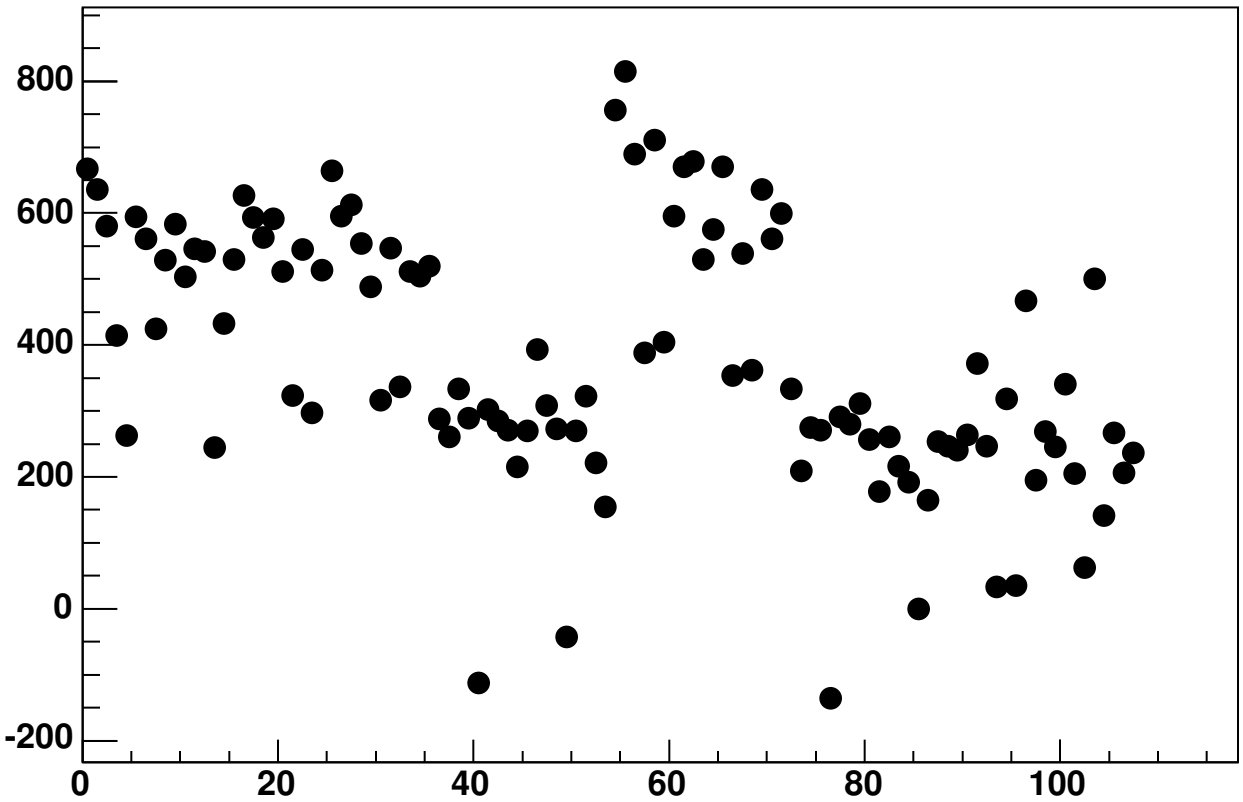
Enable 4, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



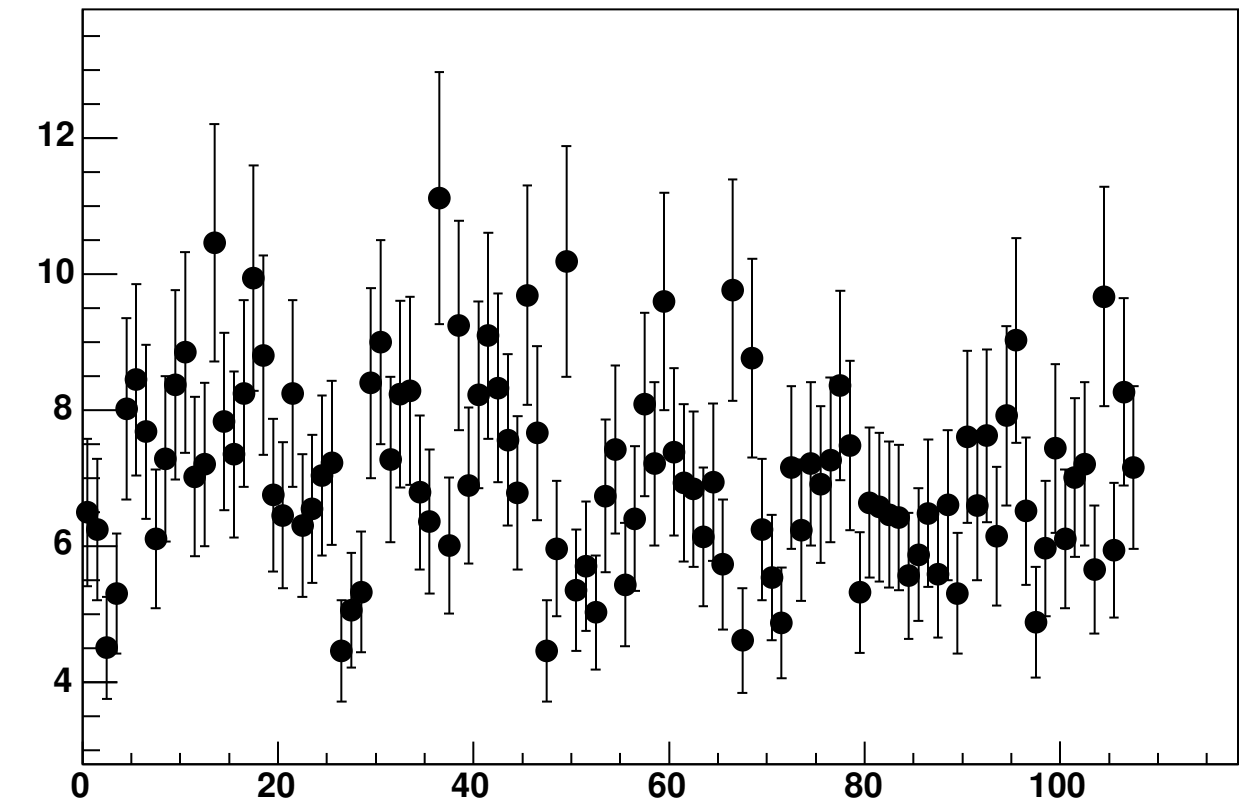
Enable 4, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



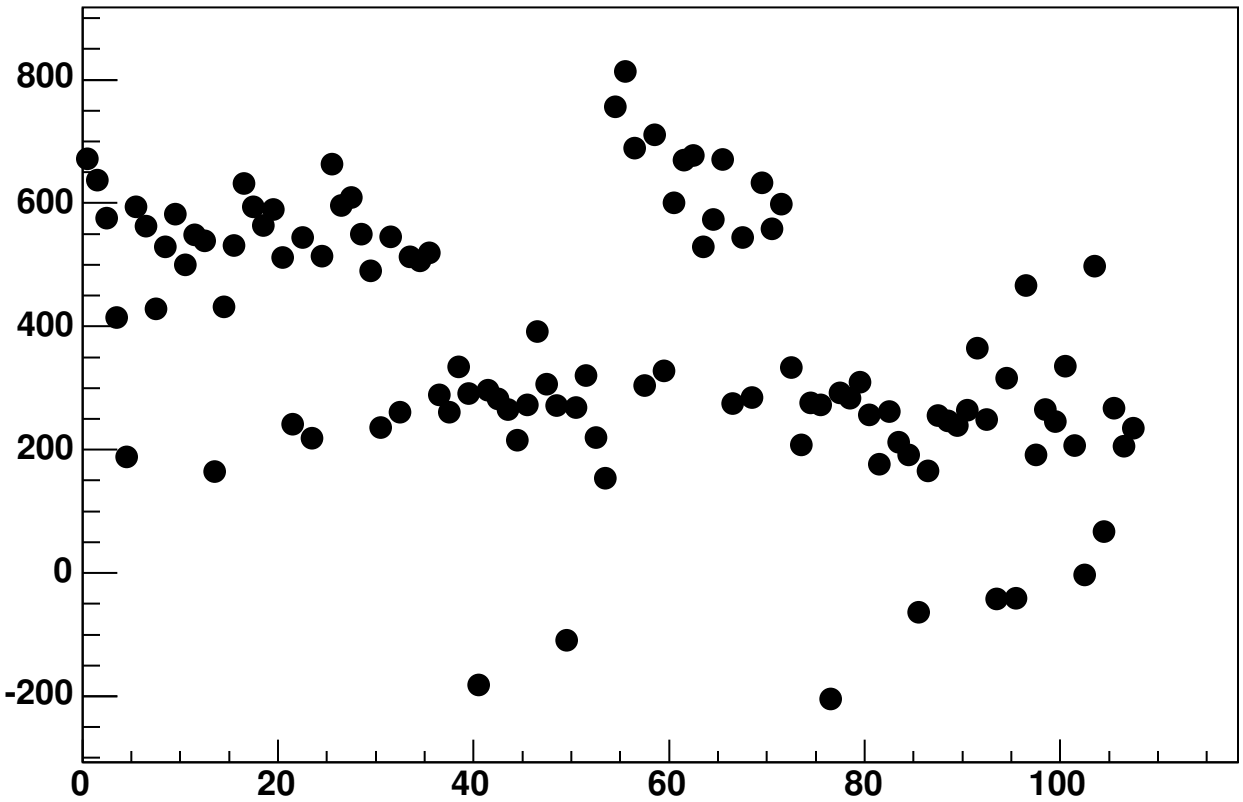
Enable 4, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



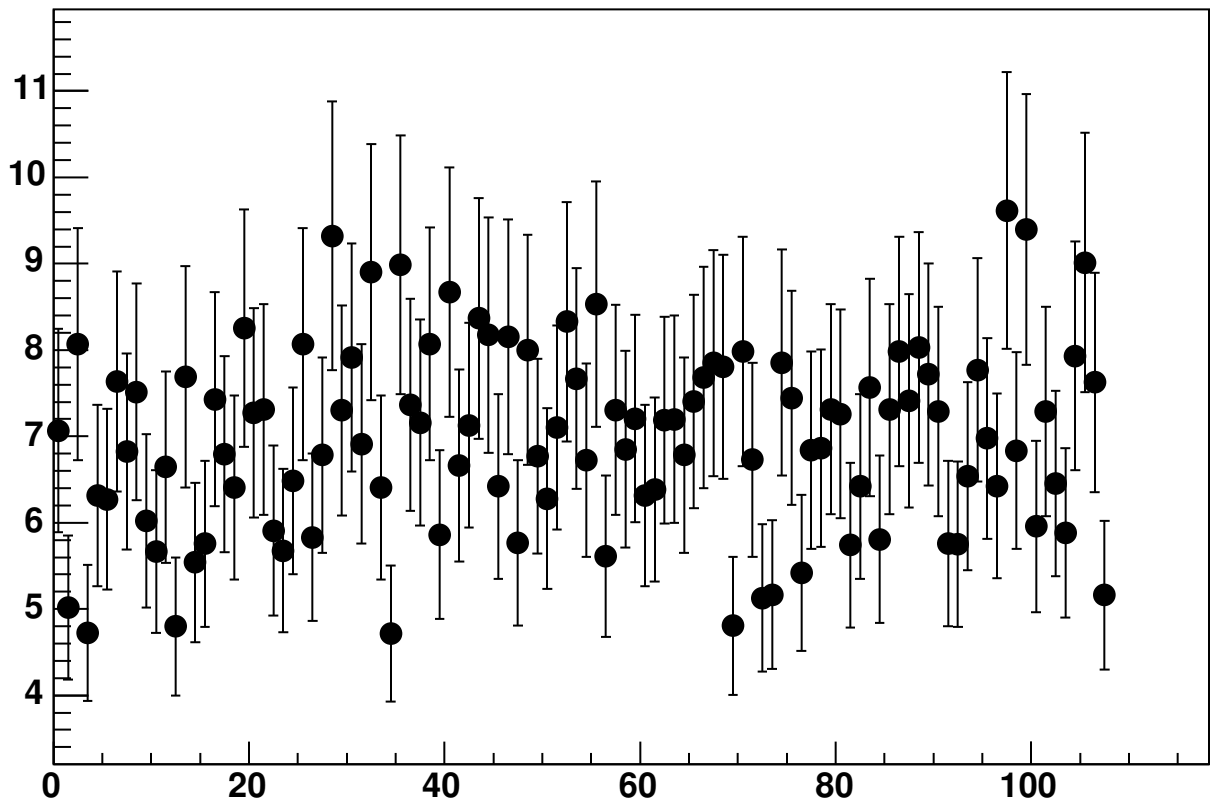
Enable 4, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



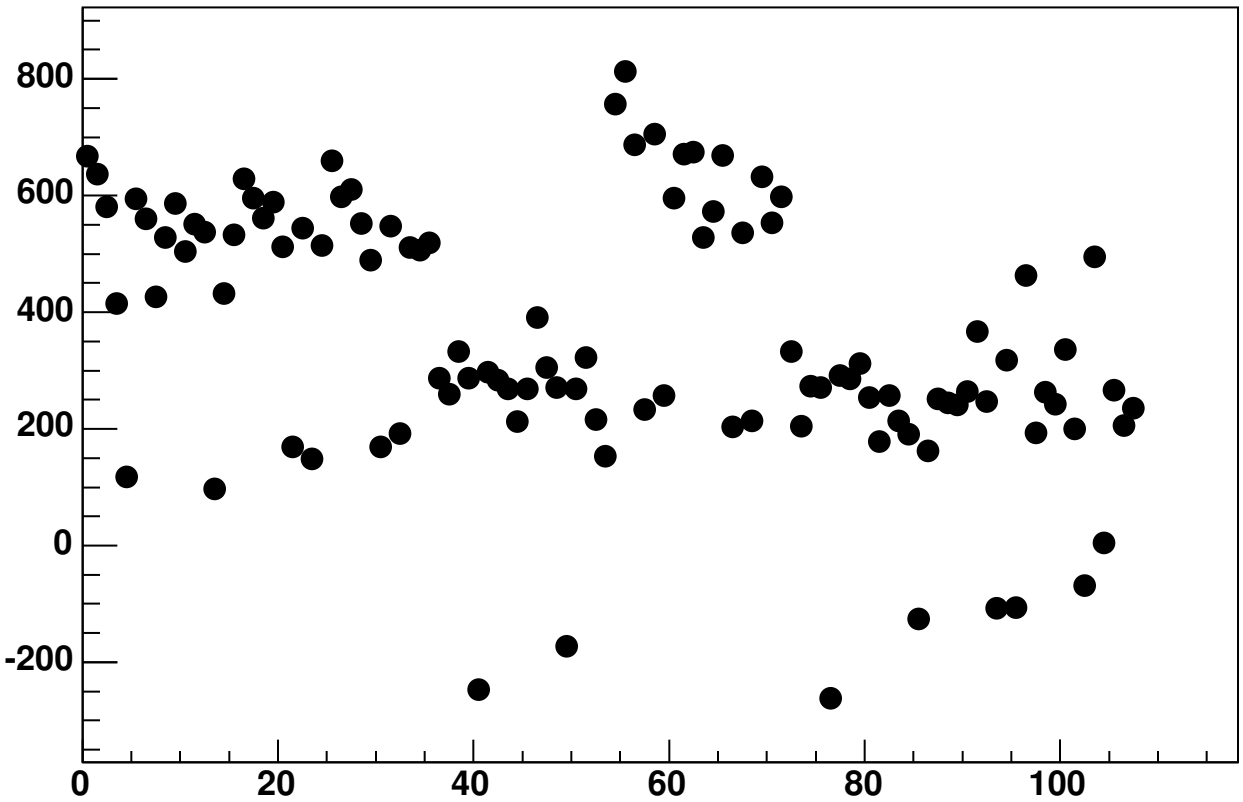
Enable 4, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



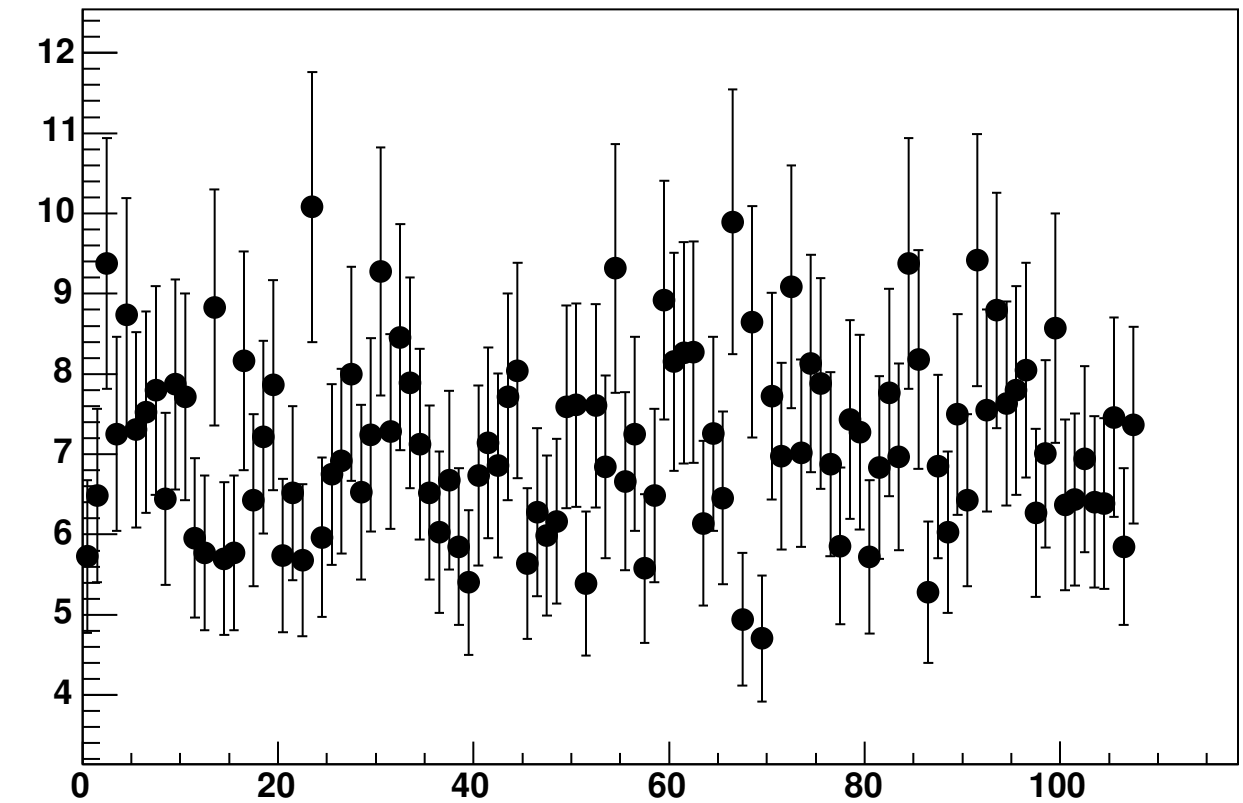
Enable 4, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



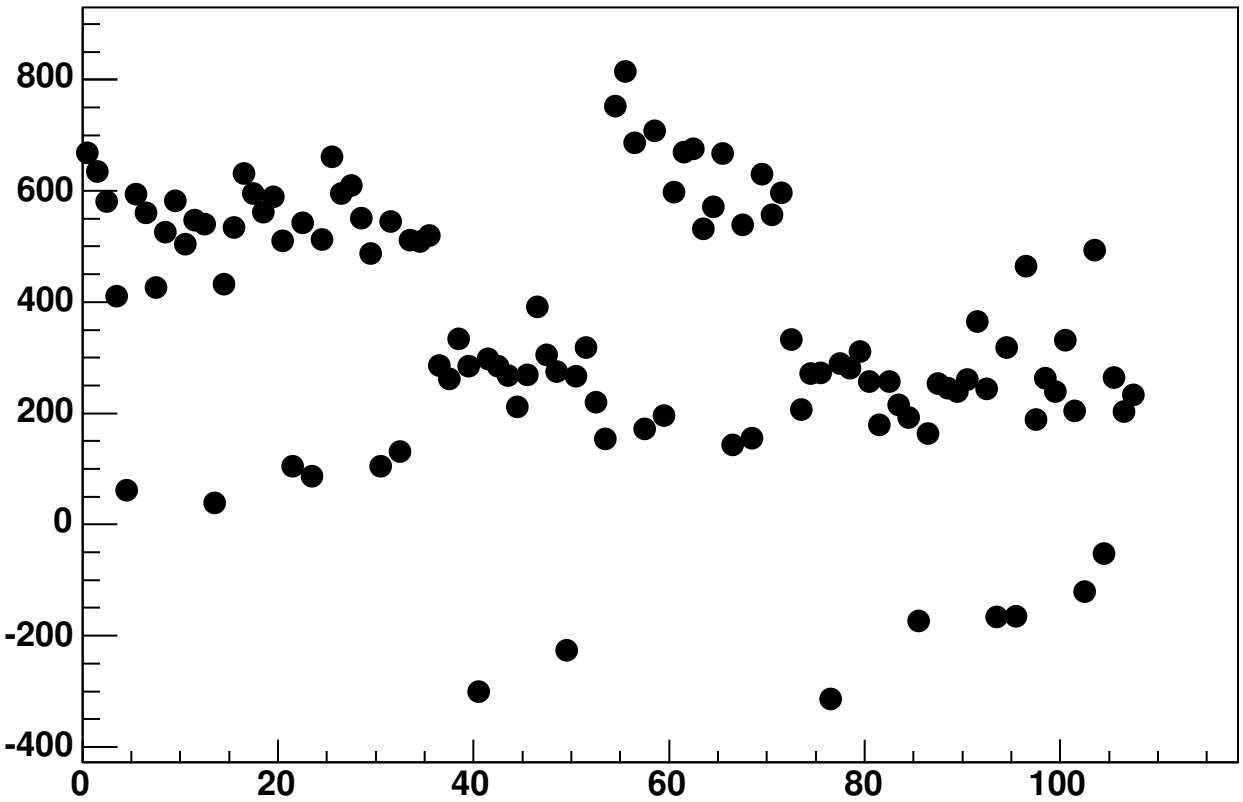
Enable 4, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



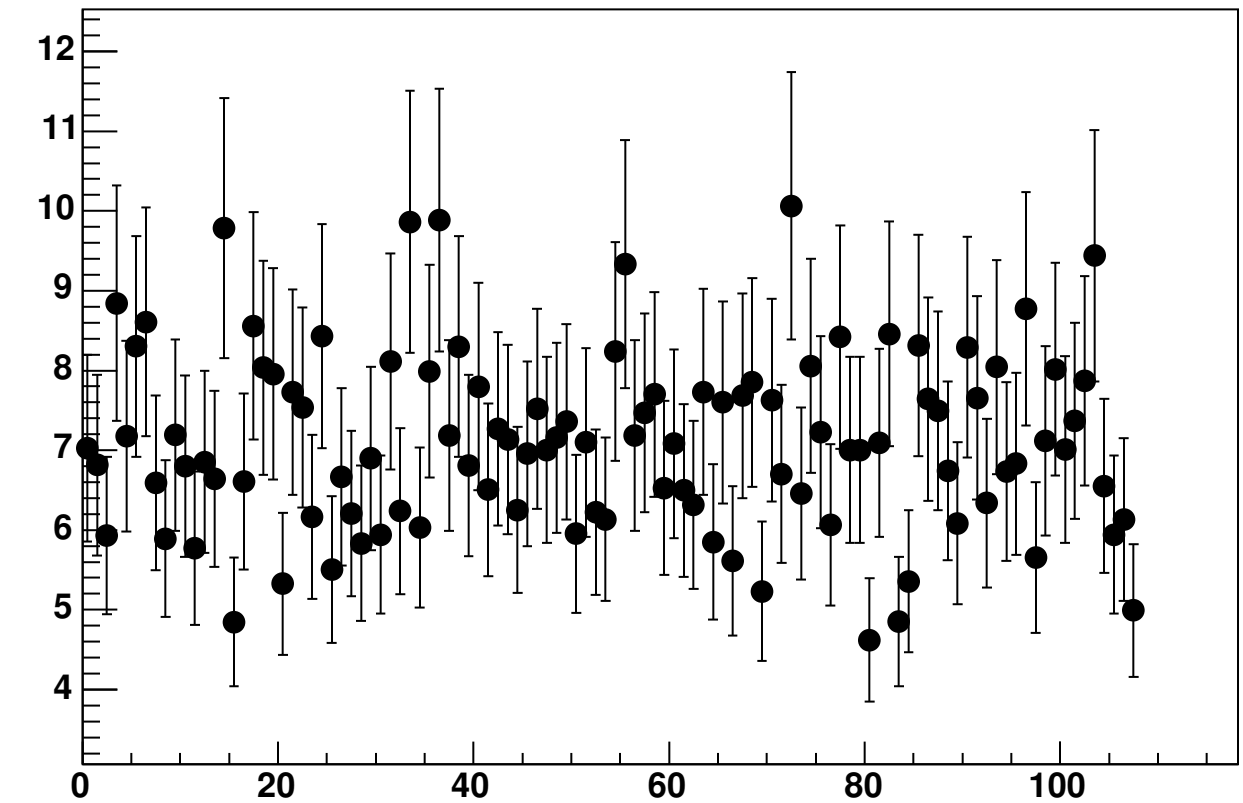
Enable 4, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



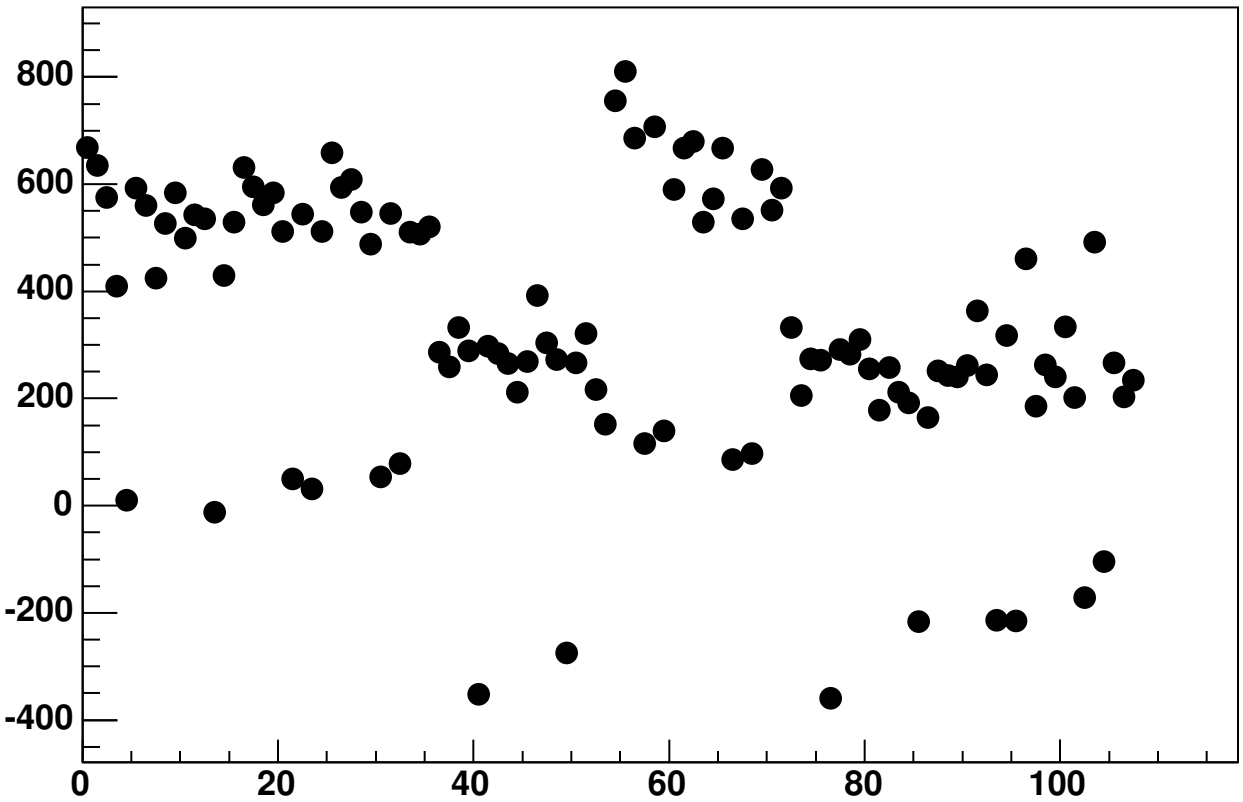
Enable 4, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



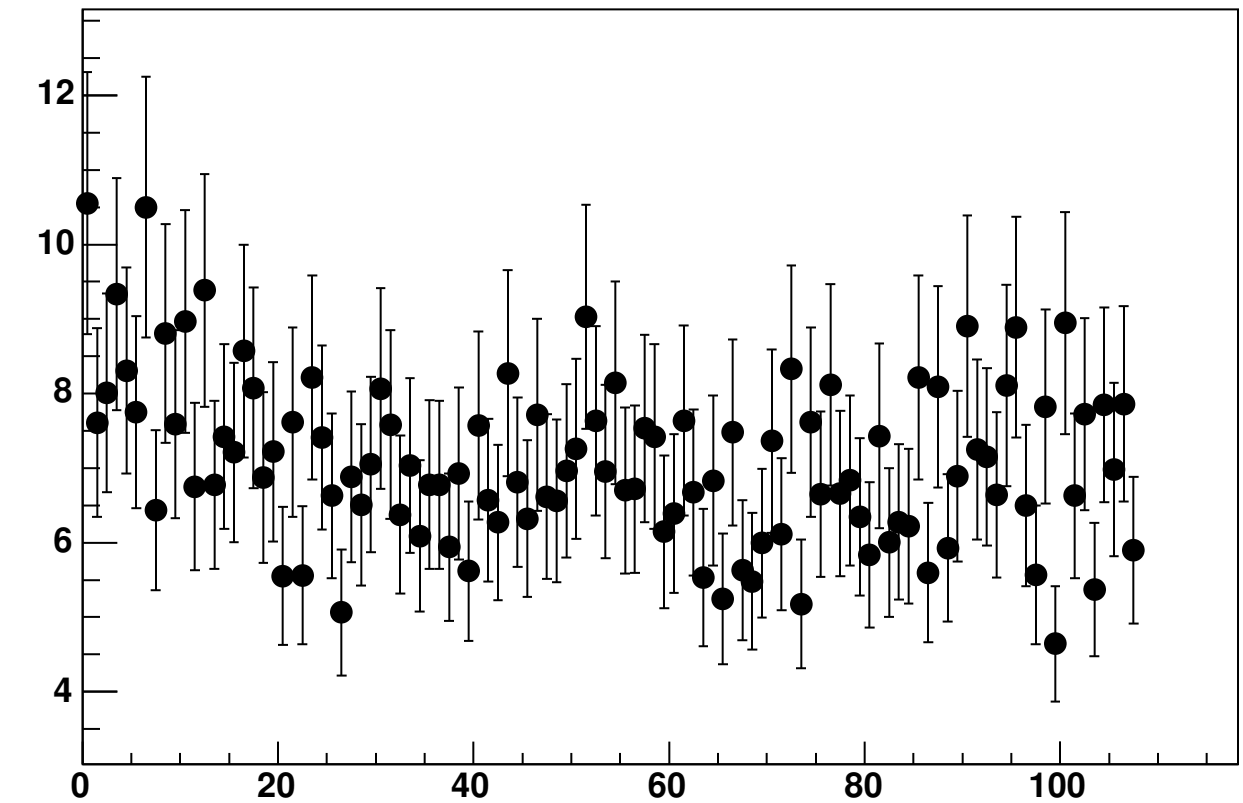
Enable 4, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



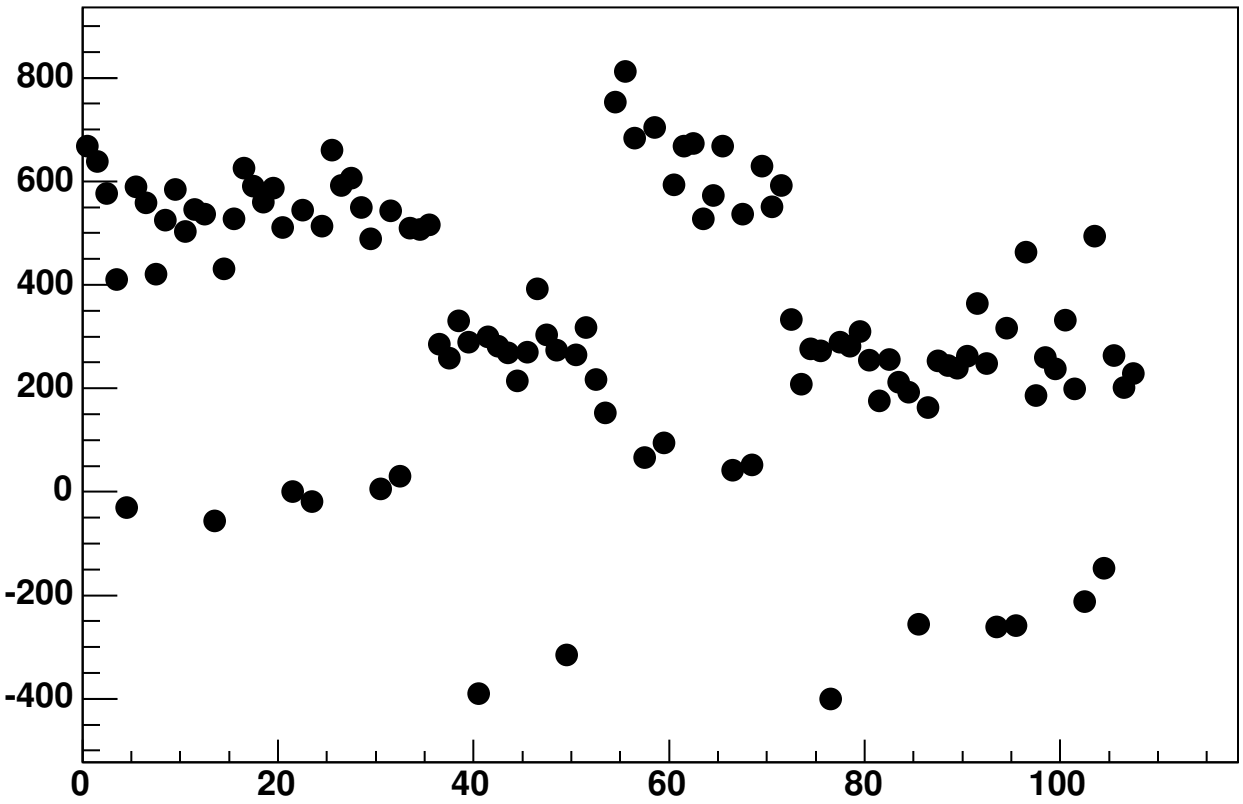
Enable 4, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



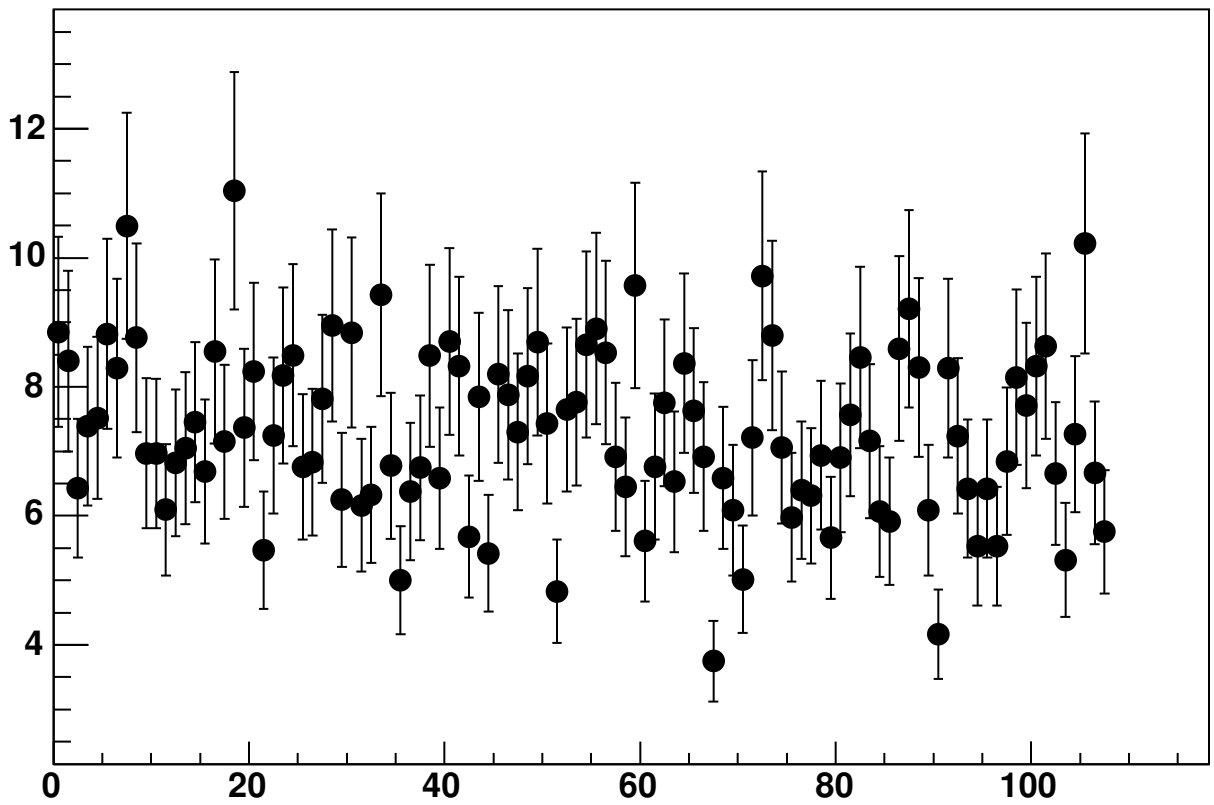
Enable 4, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



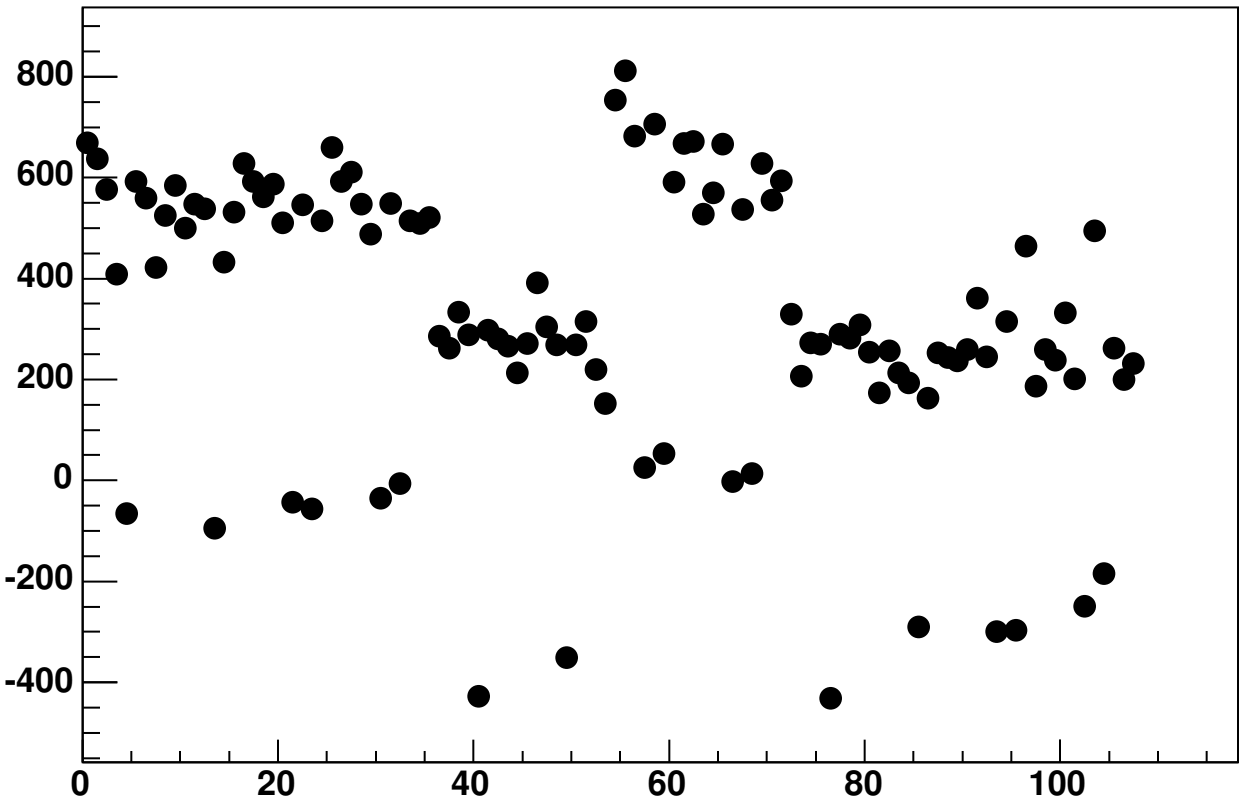
Enable 4, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



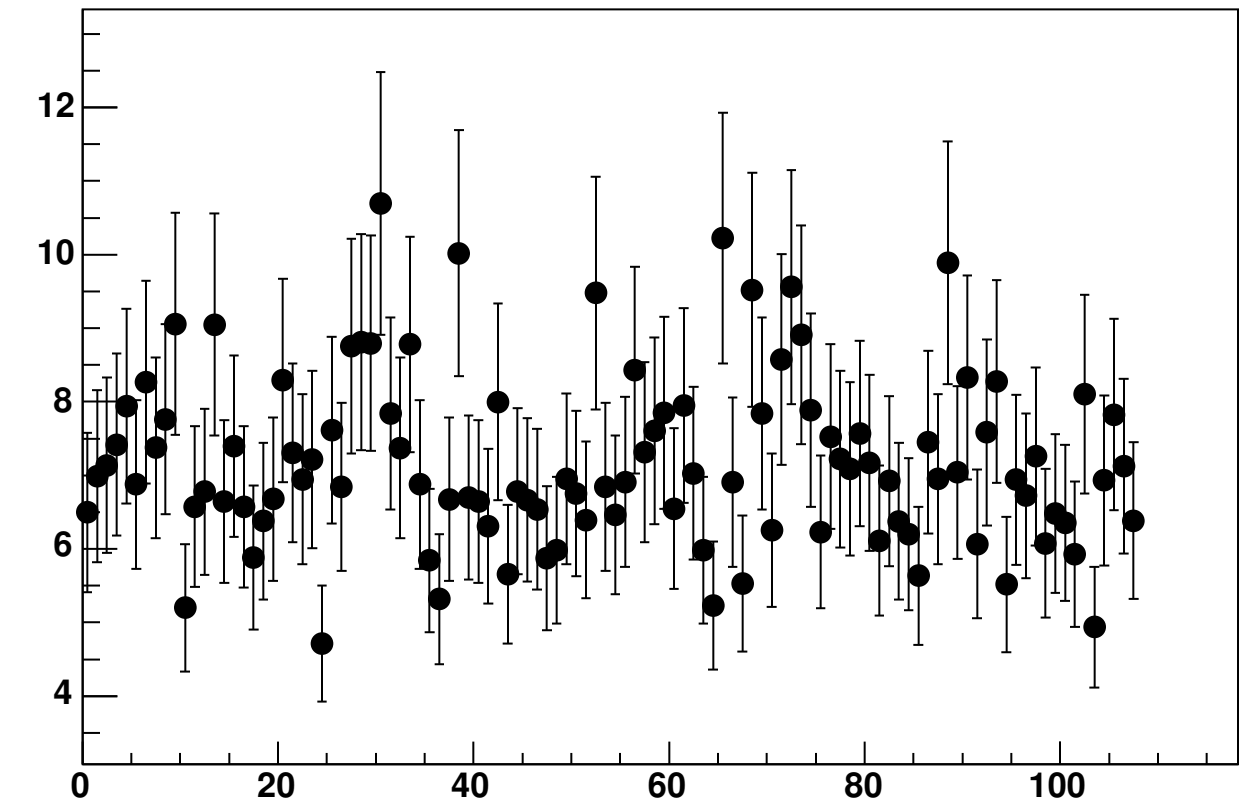
Enable 4, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



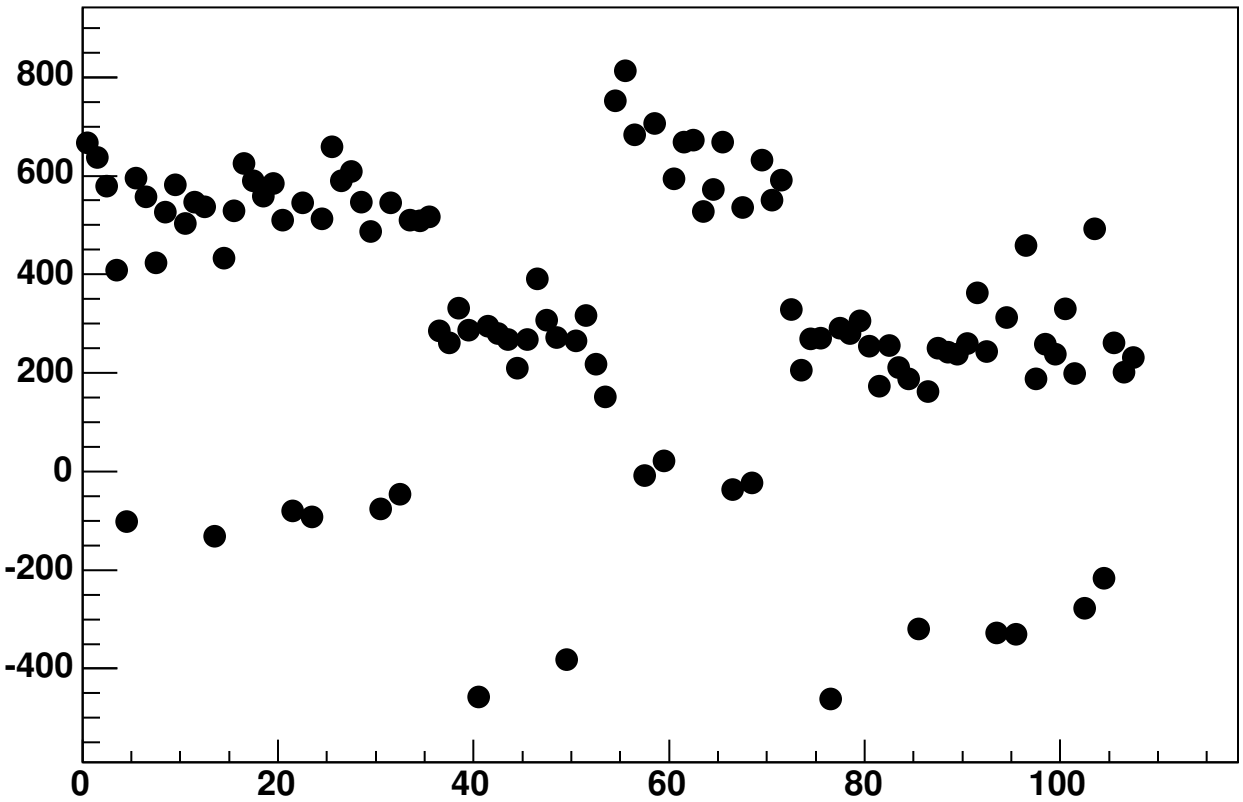
Enable 4, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



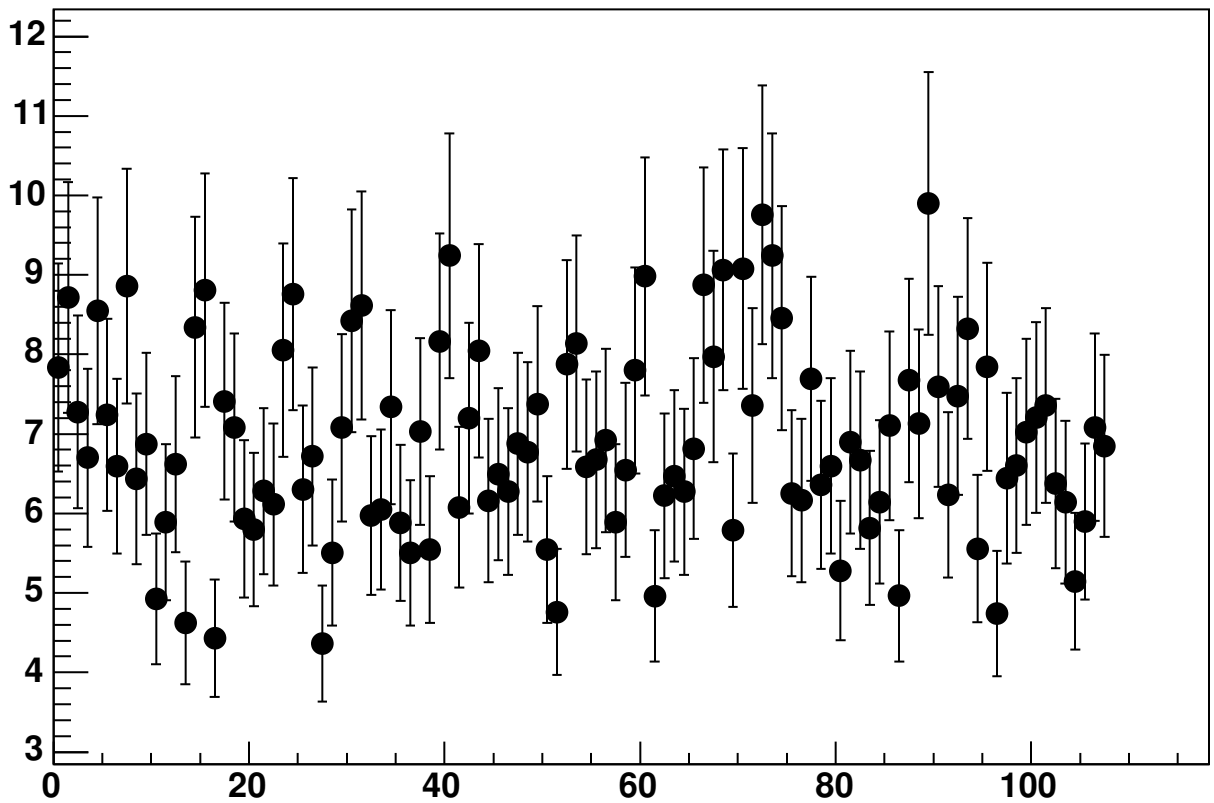
Enable 4, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



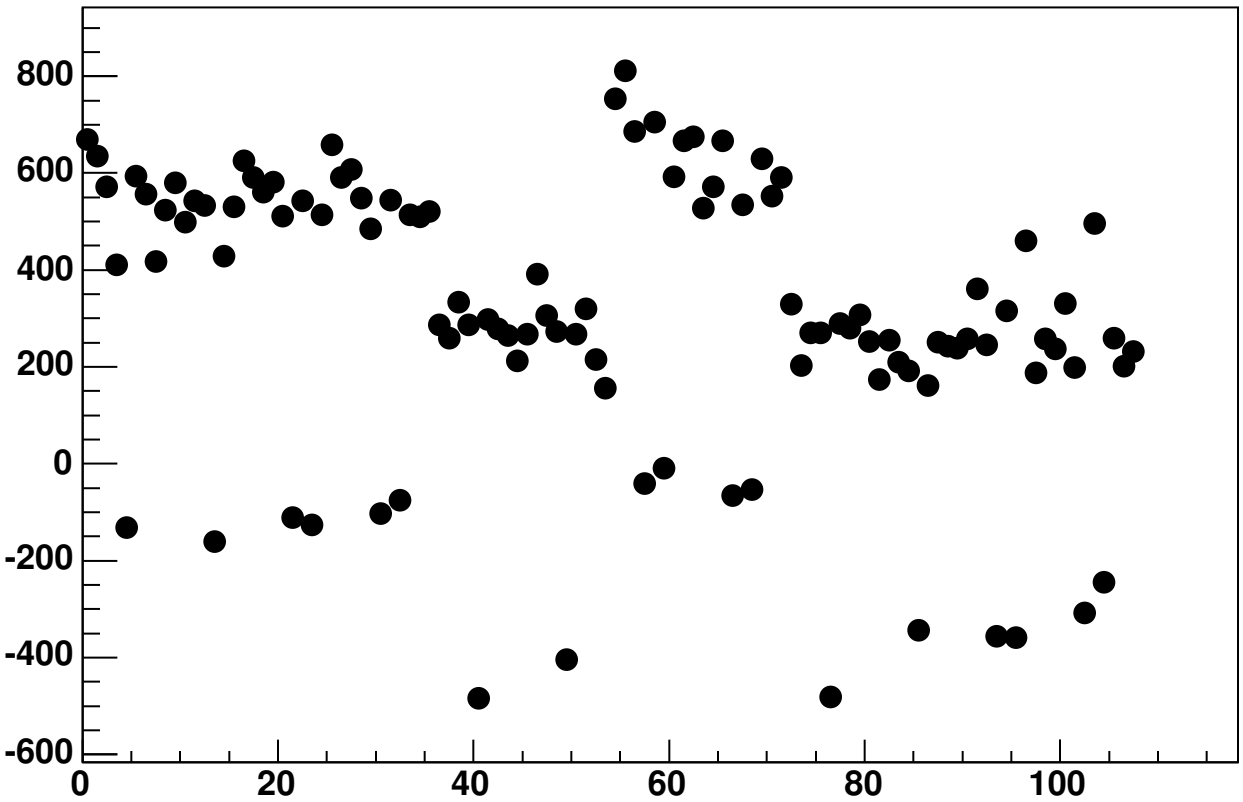
Enable 4, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



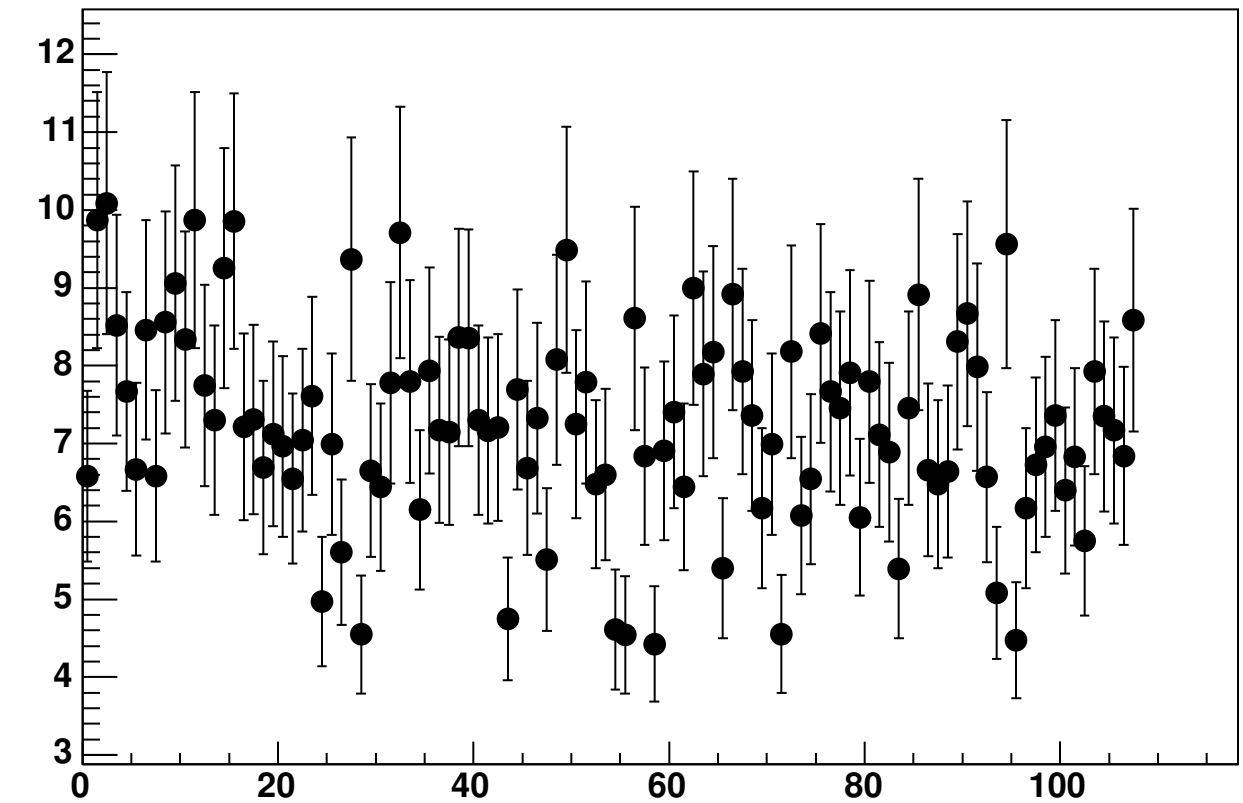
Enable 4, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



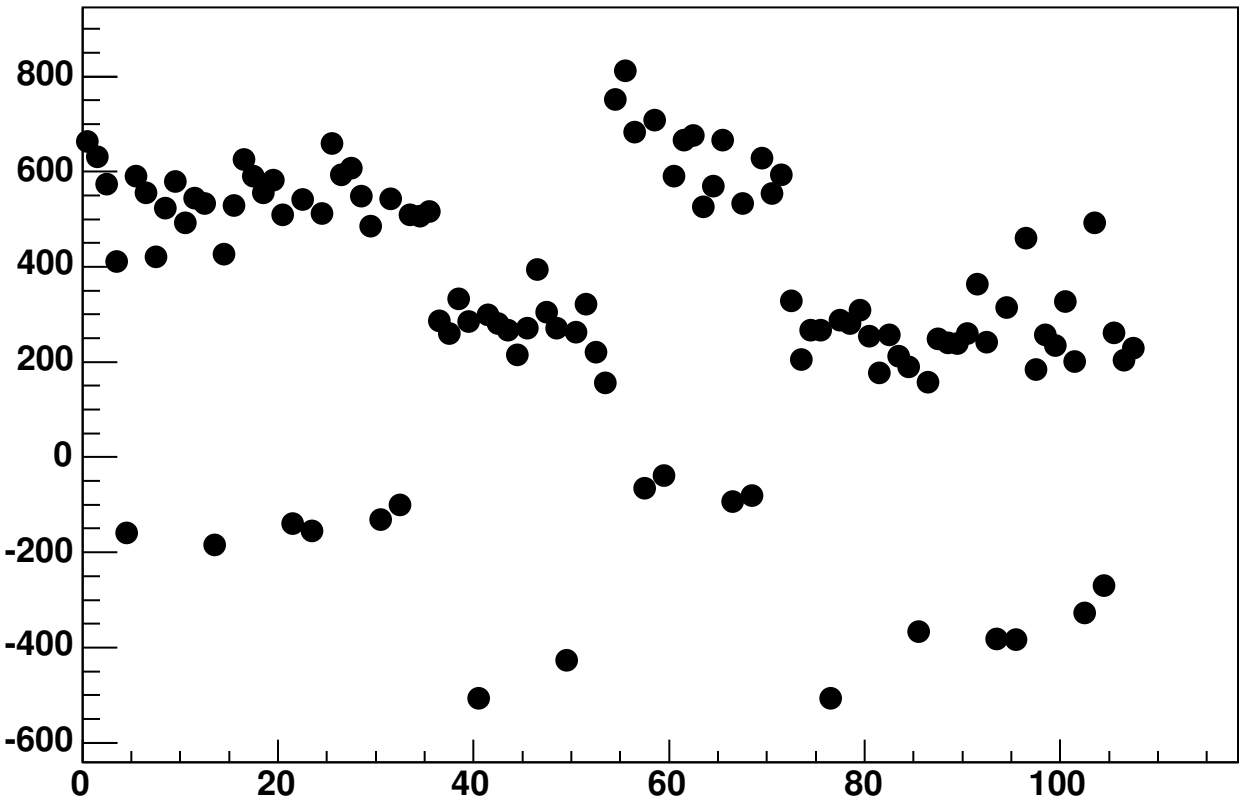
Enable 4, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



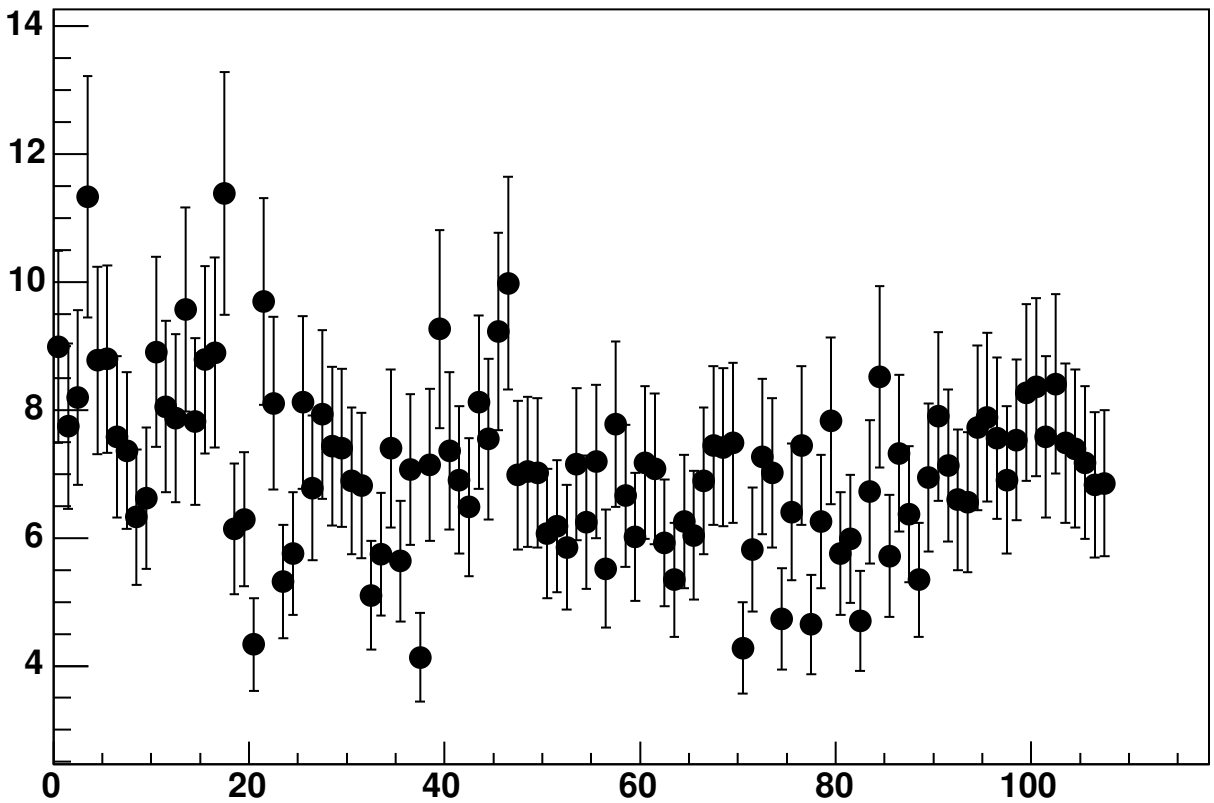
Enable 4, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



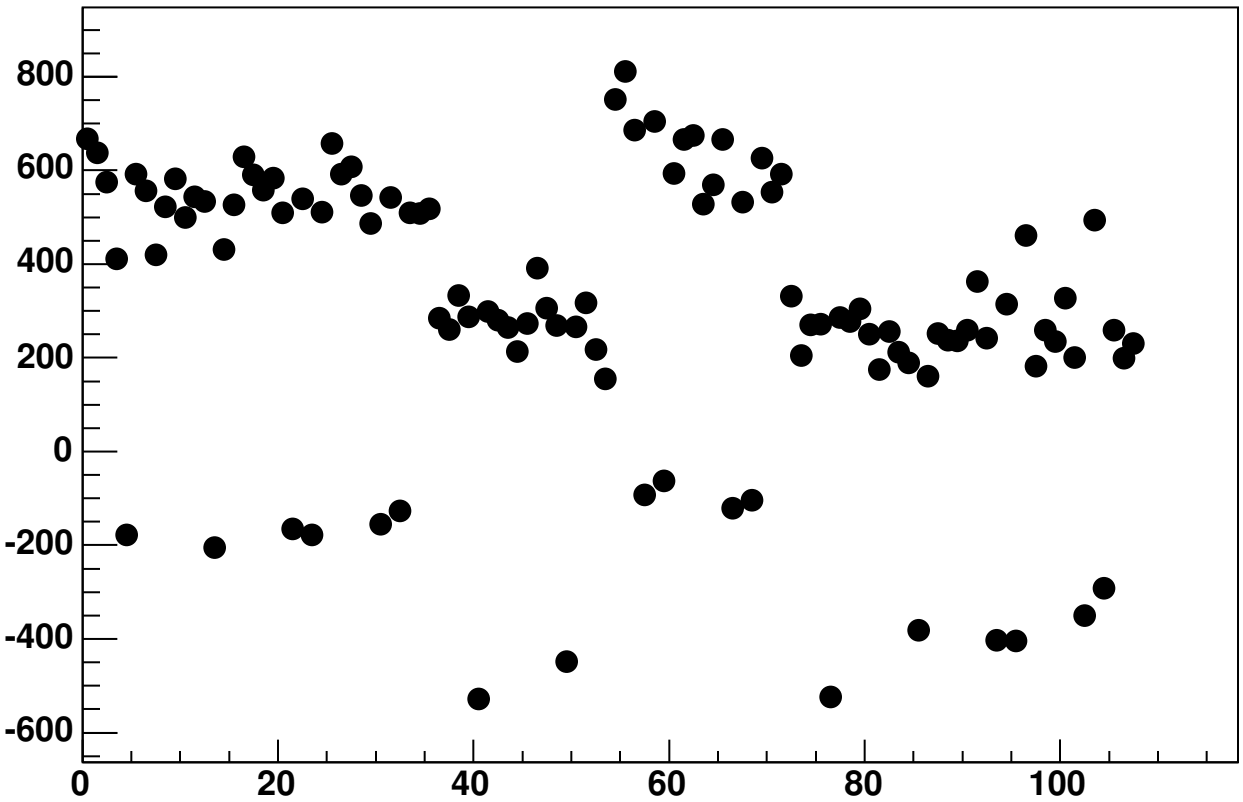
Enable 4, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



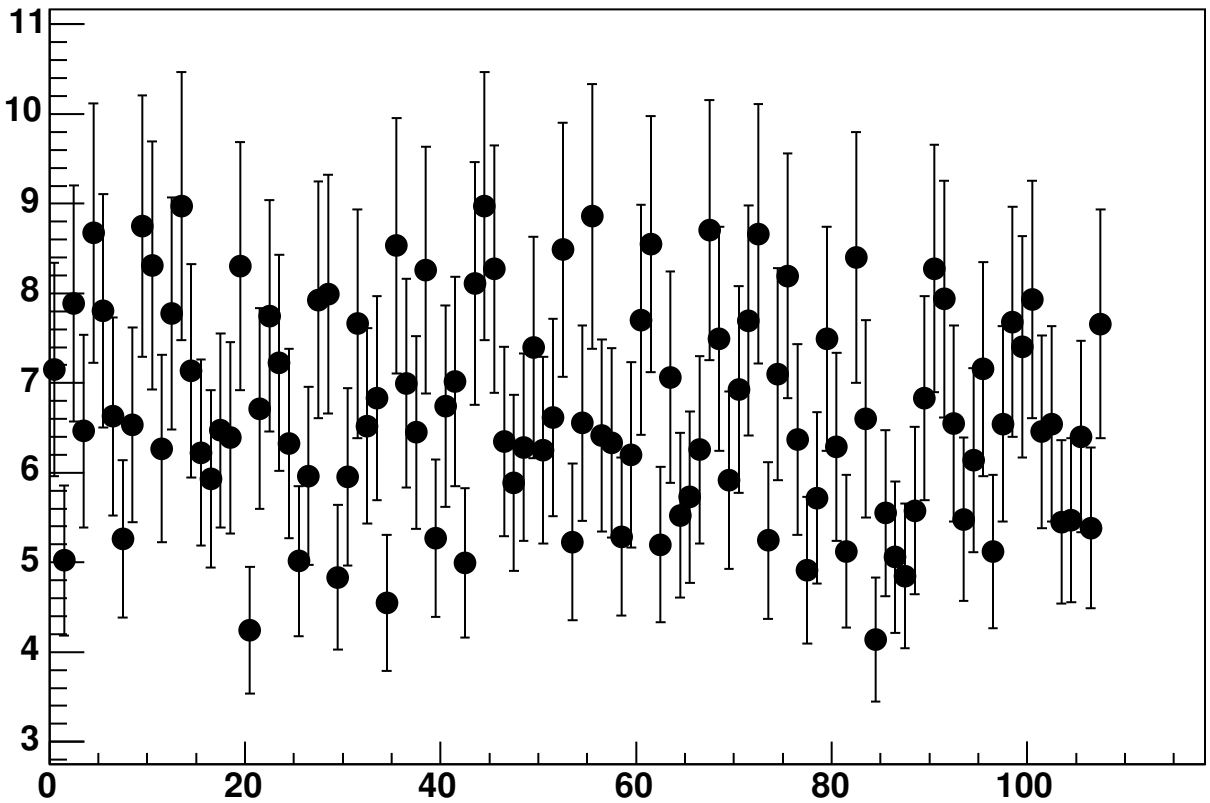
Enable 4, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



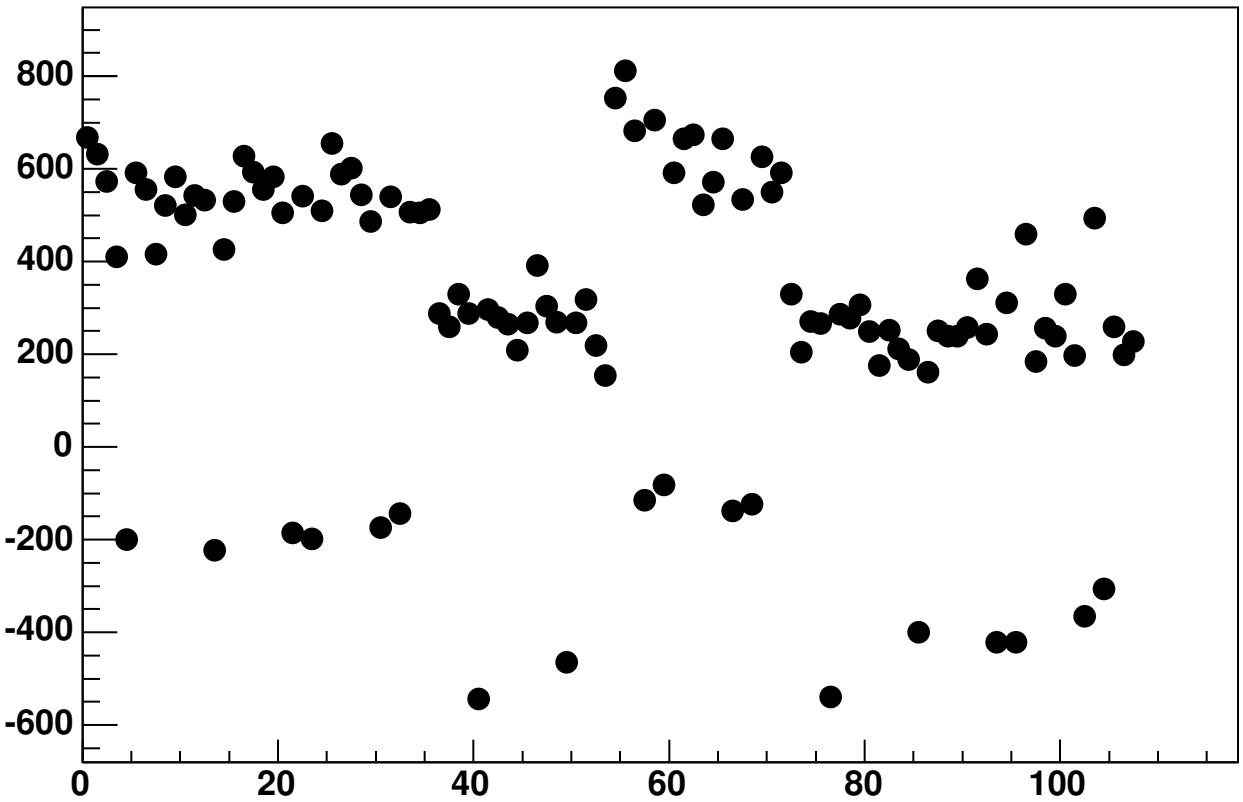
Enable 4, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



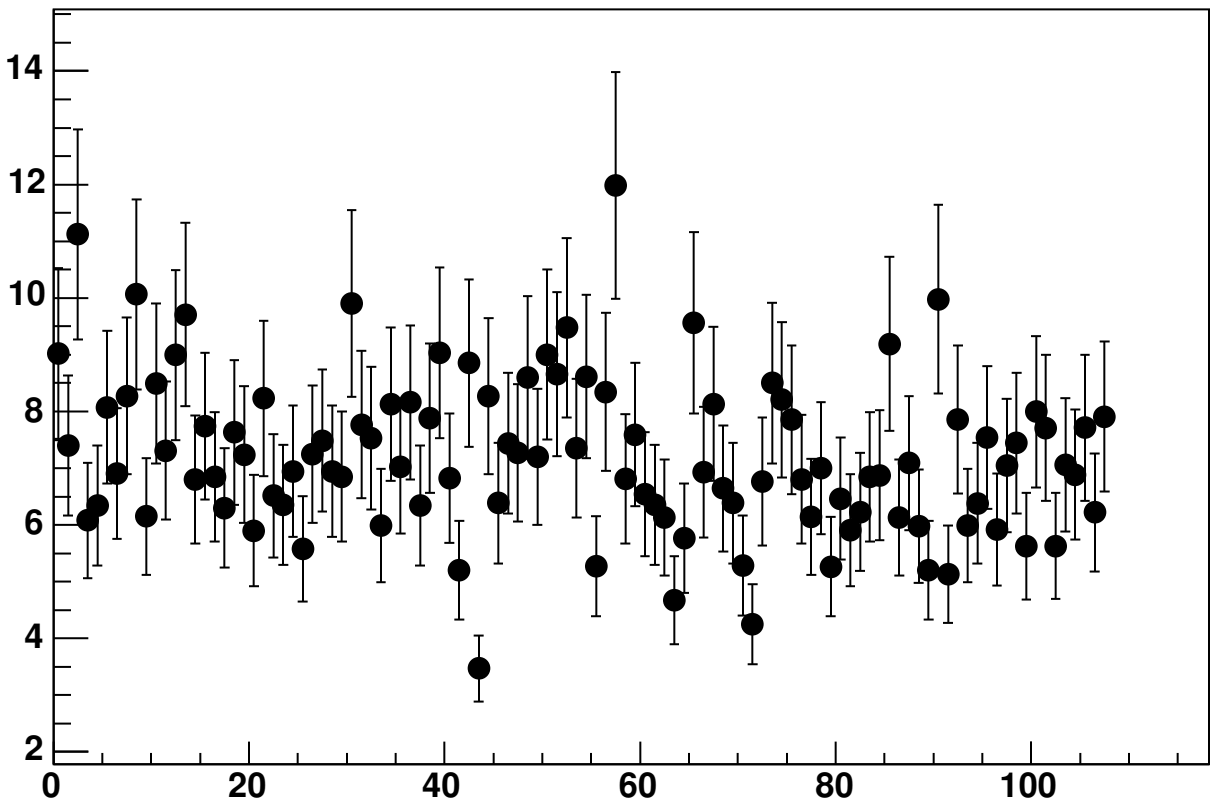
Enable 4, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



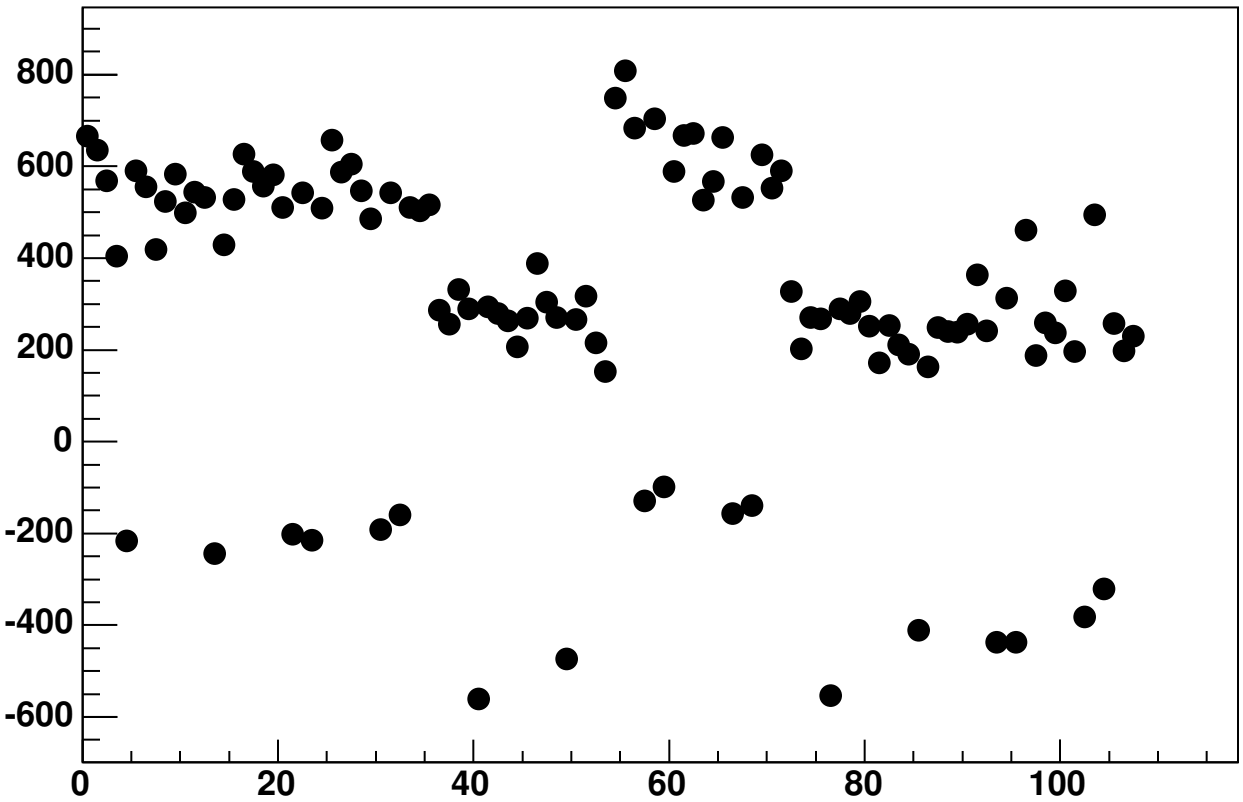
Enable 4, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



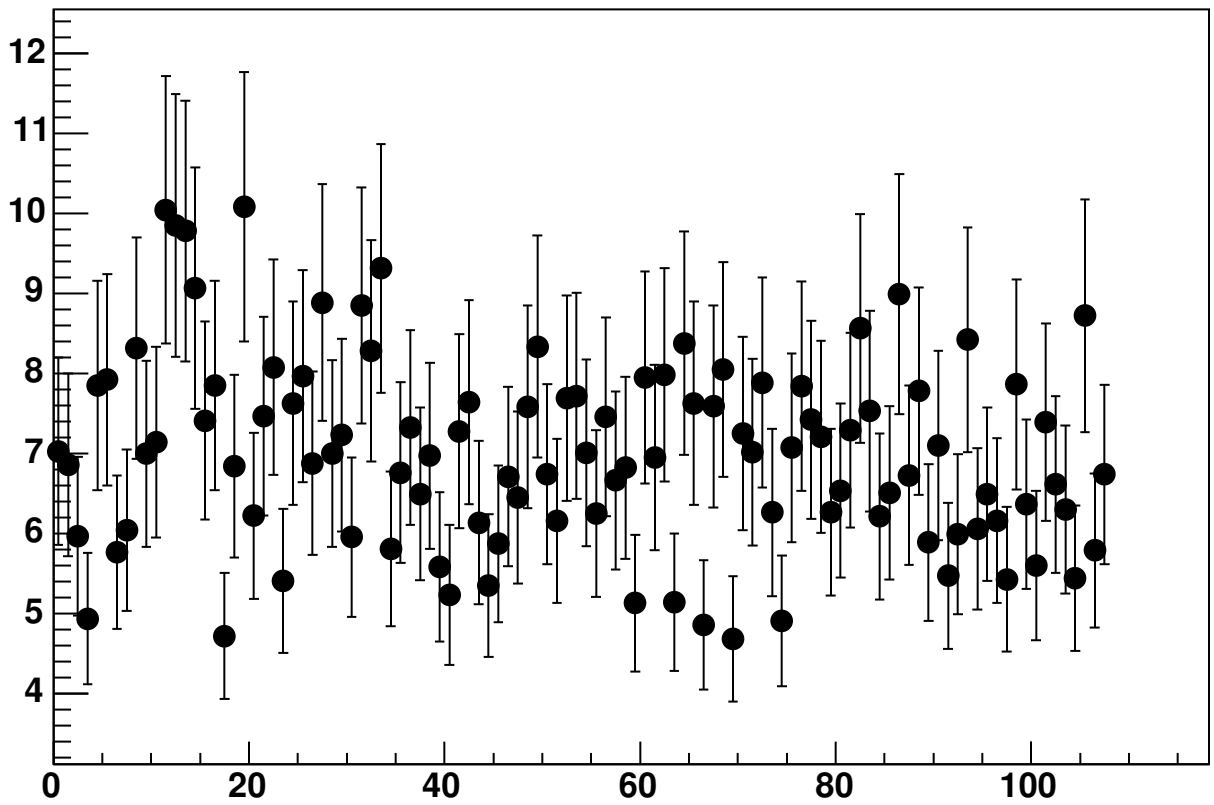
Enable 4, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan



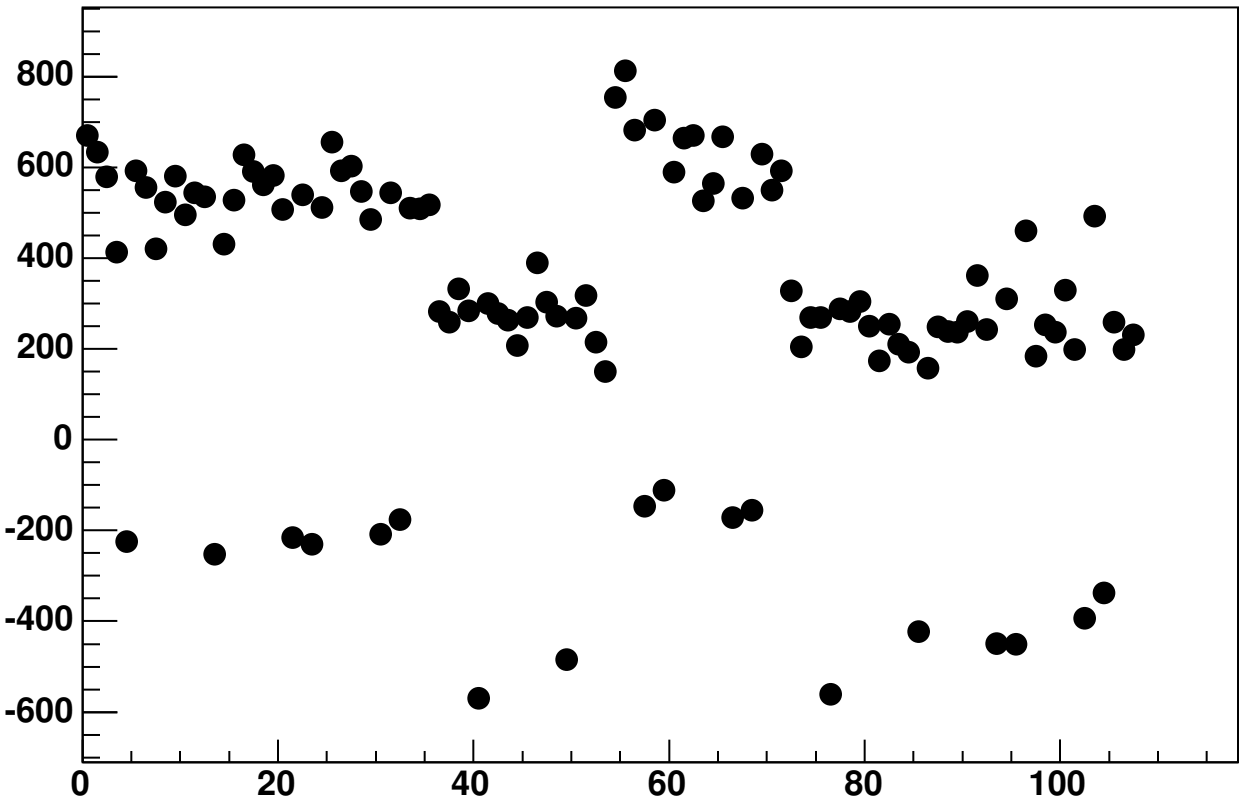
Enable 4, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan



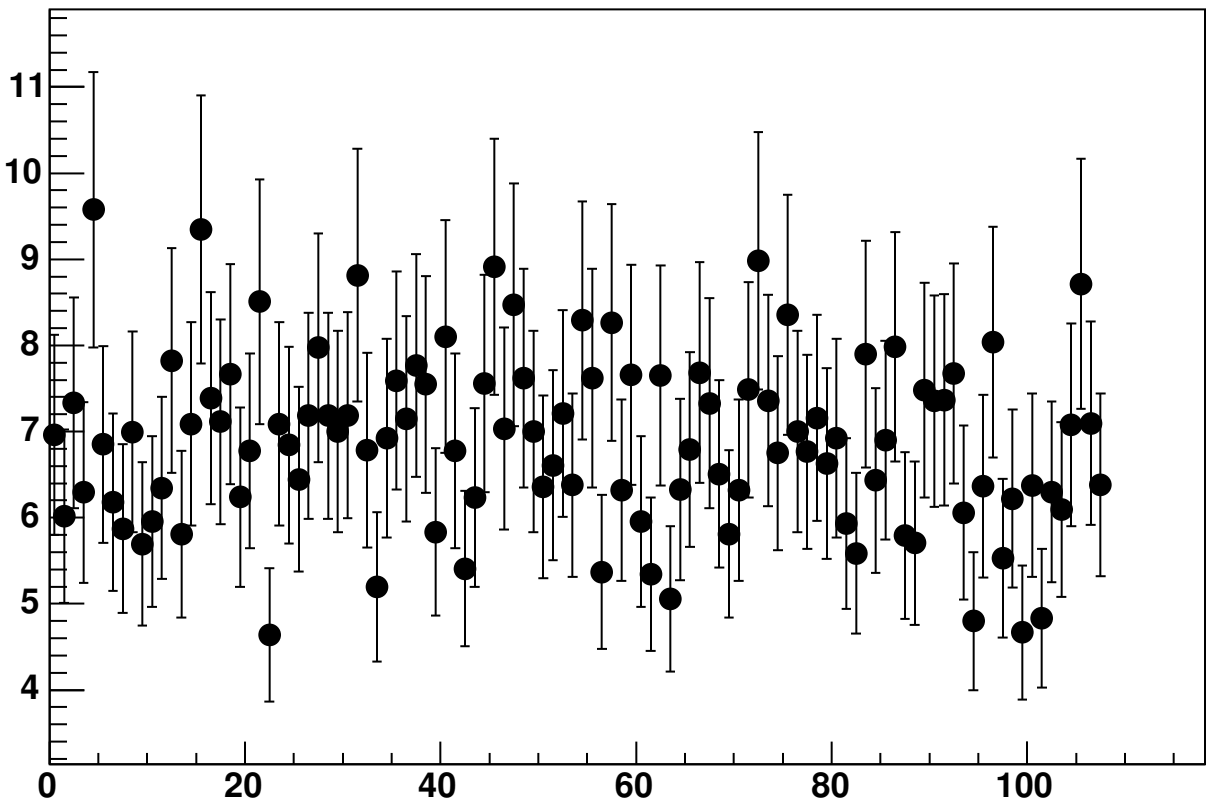
Enable 4, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan



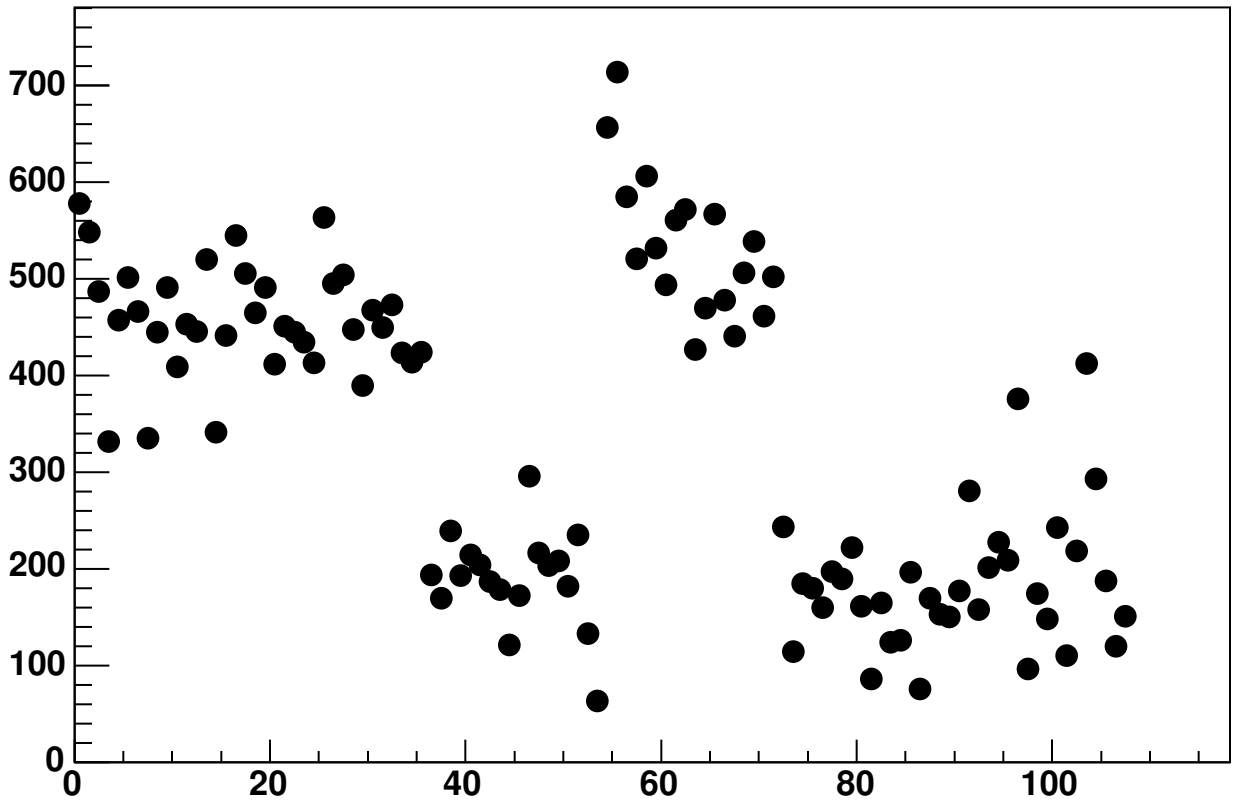
Enable 4, DAC=4000, Hold=245, ADC Mean vs 18*Chip+Chan



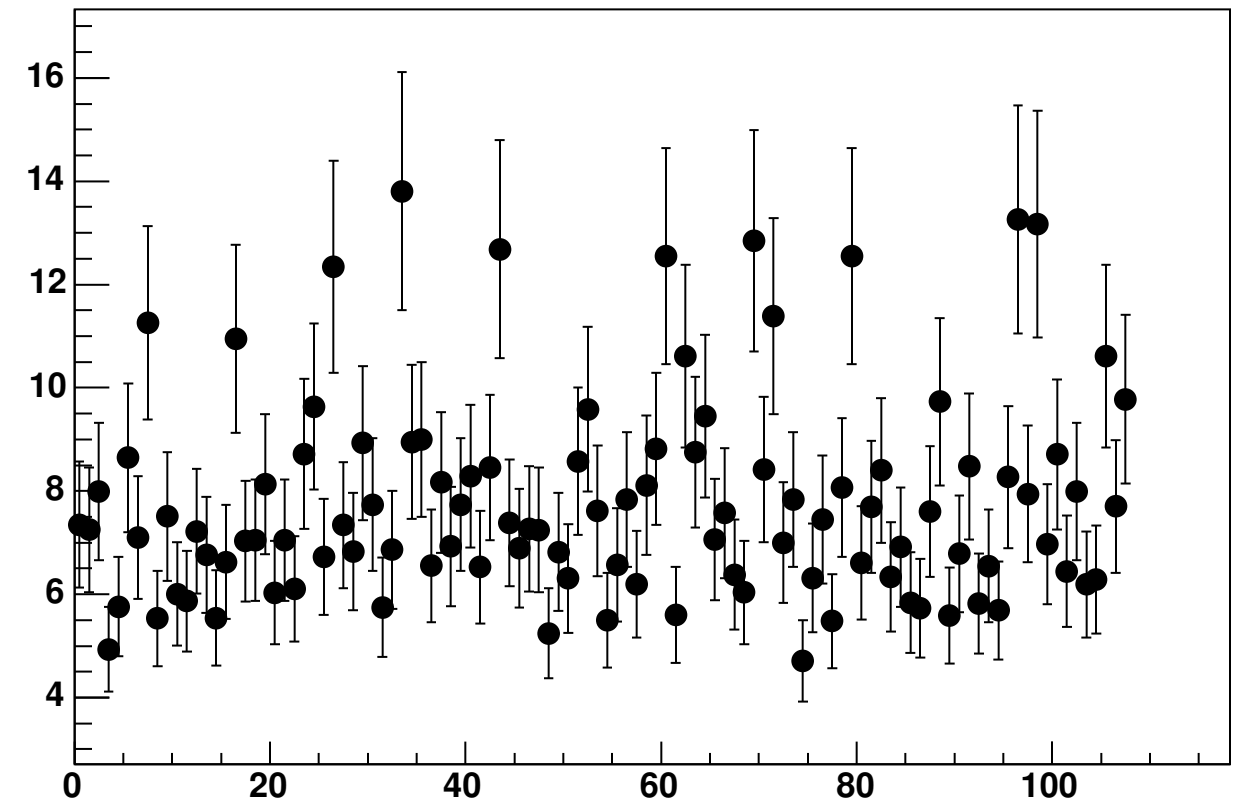
Enable 4, DAC=4000, Hold=245, ADC Noise vs 18*Chip+Chan



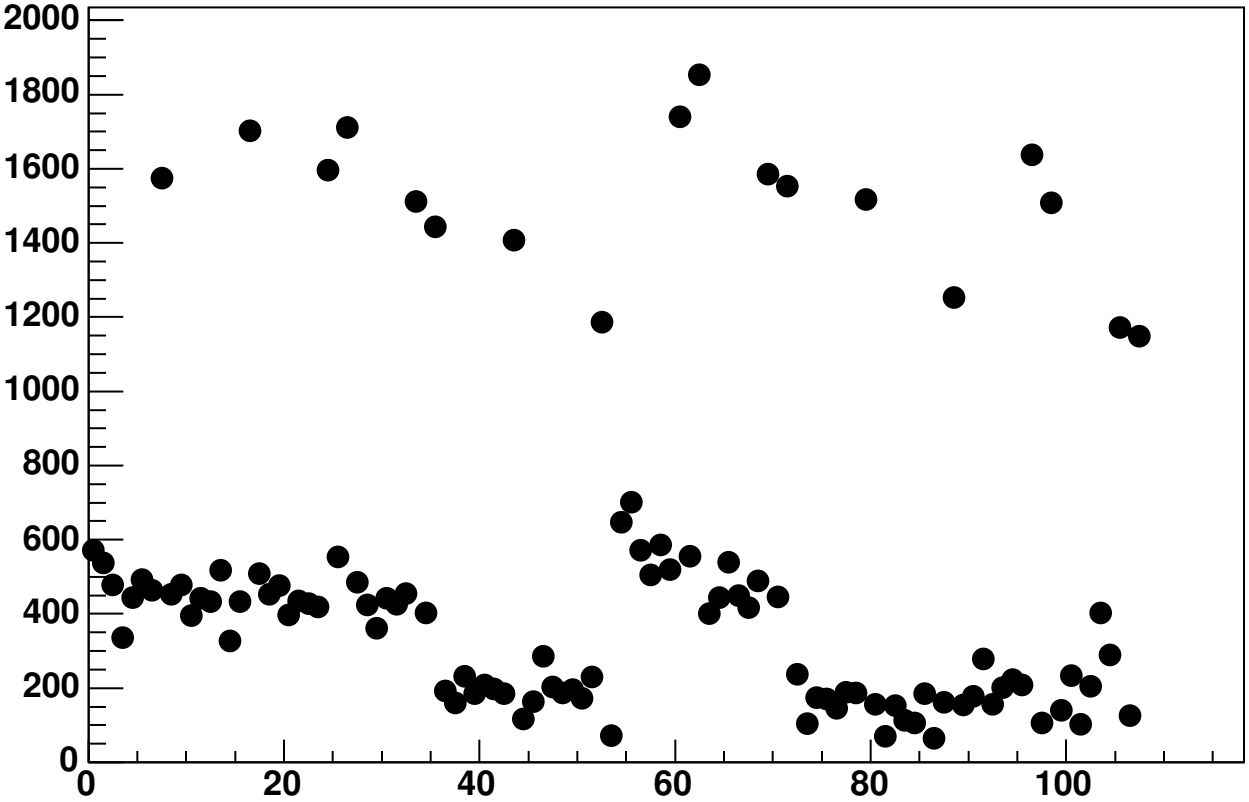
Enable 5, DAC=4000, Hold=0, ADC Mean vs 18*Chip+Chan



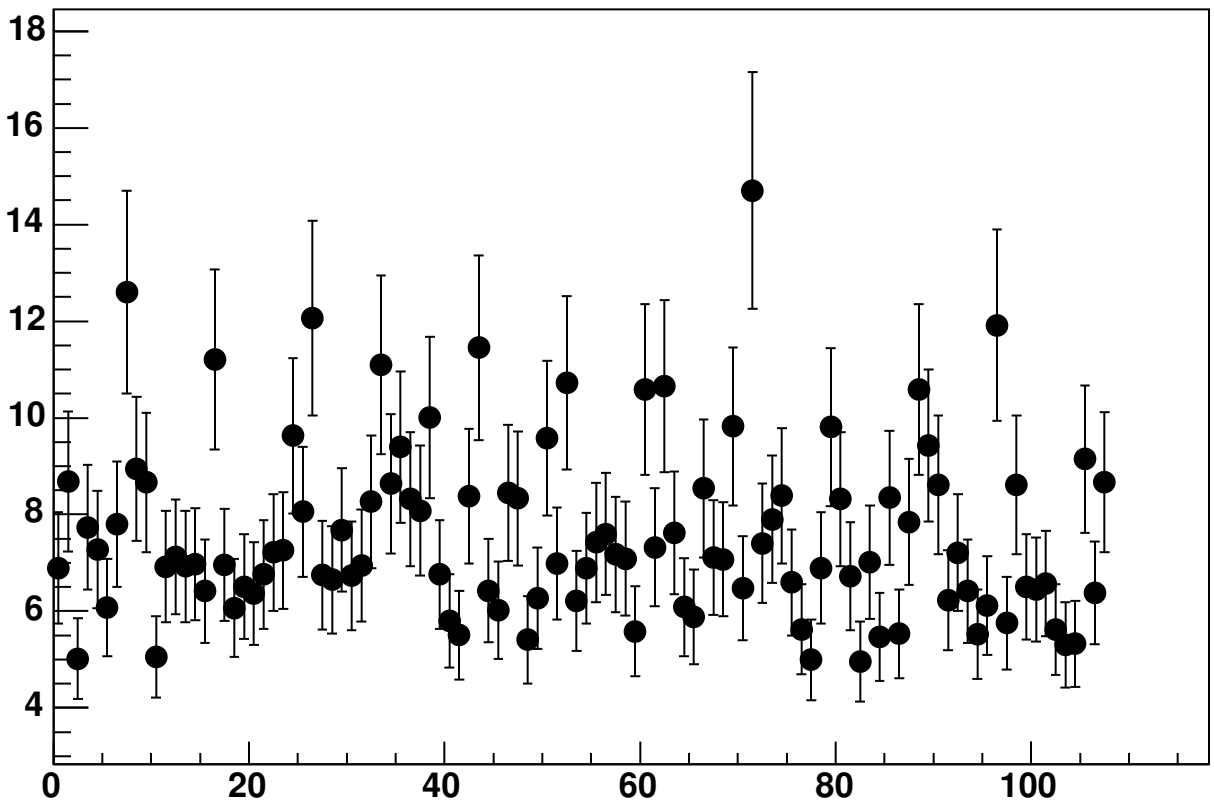
Enable 5, DAC=4000, Hold=0, ADC Noise vs 18*Chip+Chan



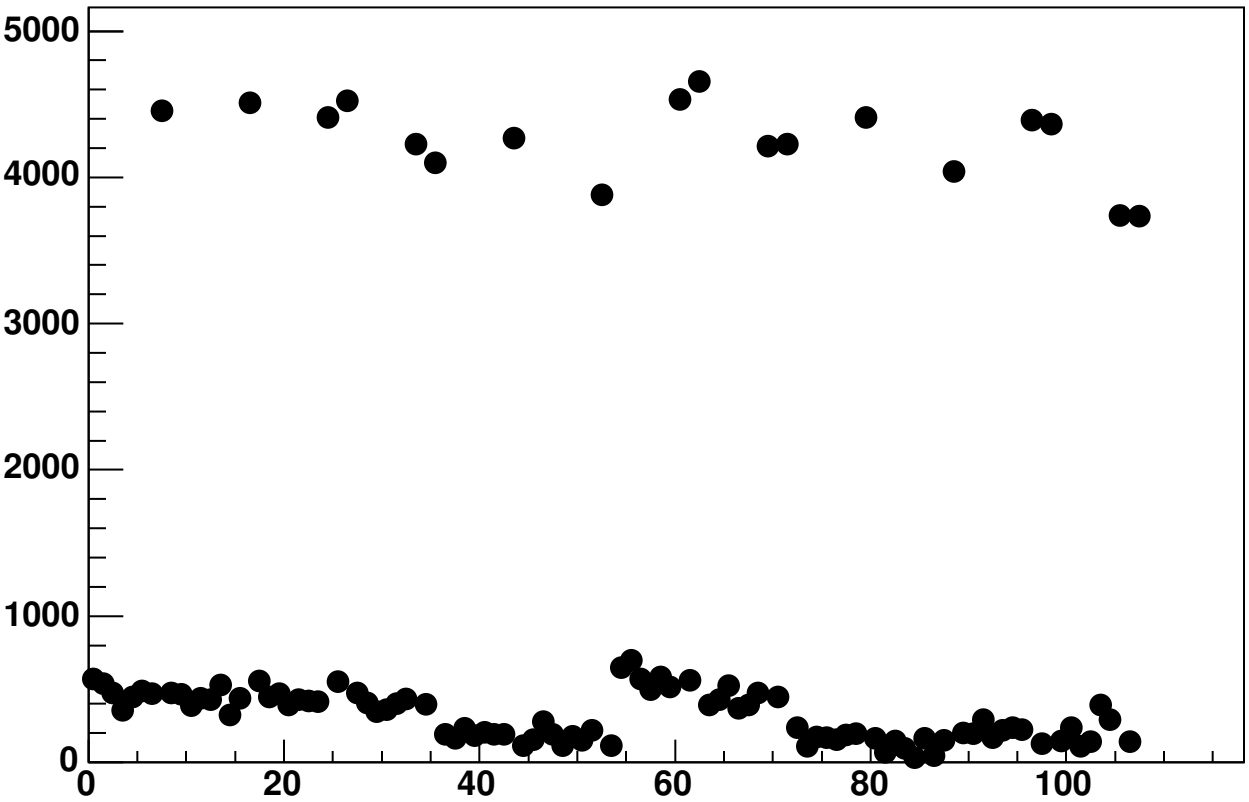
Enable 5, DAC=4000, Hold=5, ADC Mean vs 18*Chip+Chan



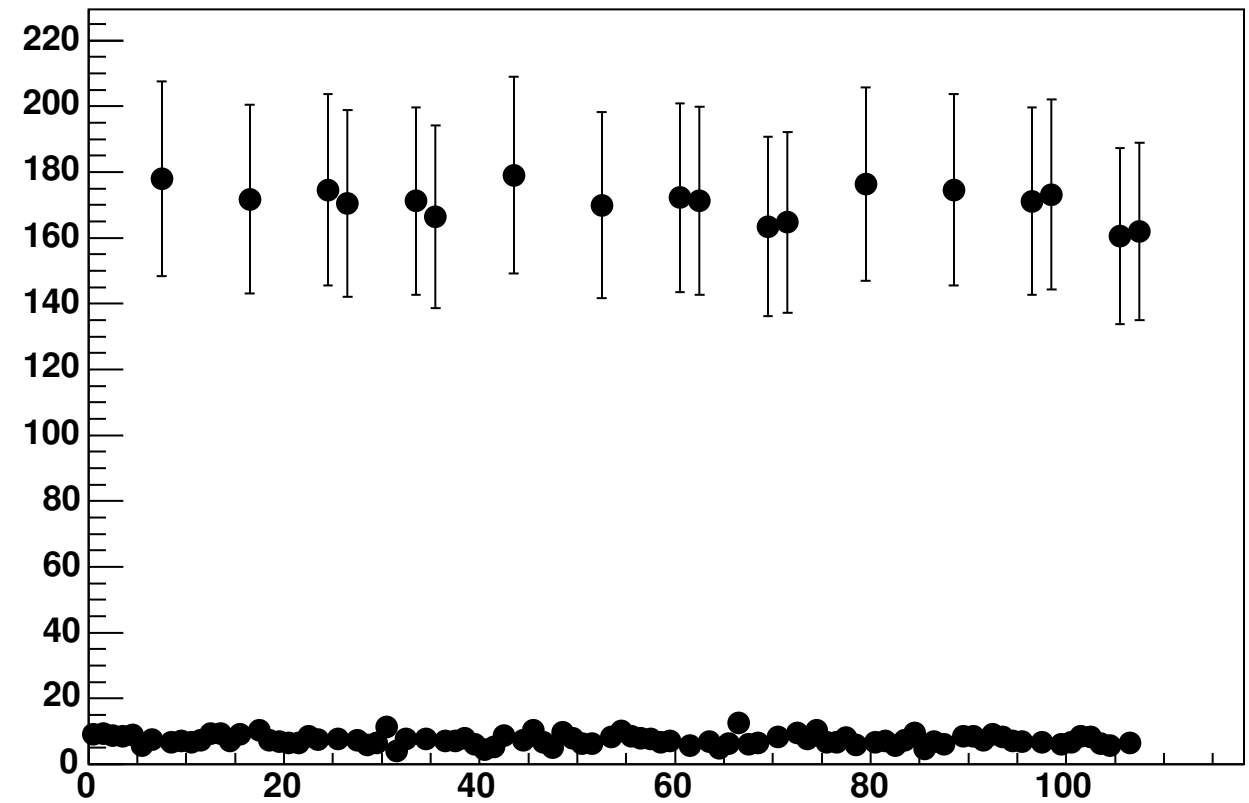
Enable 5, DAC=4000, Hold=5, ADC Noise vs 18*Chip+Chan



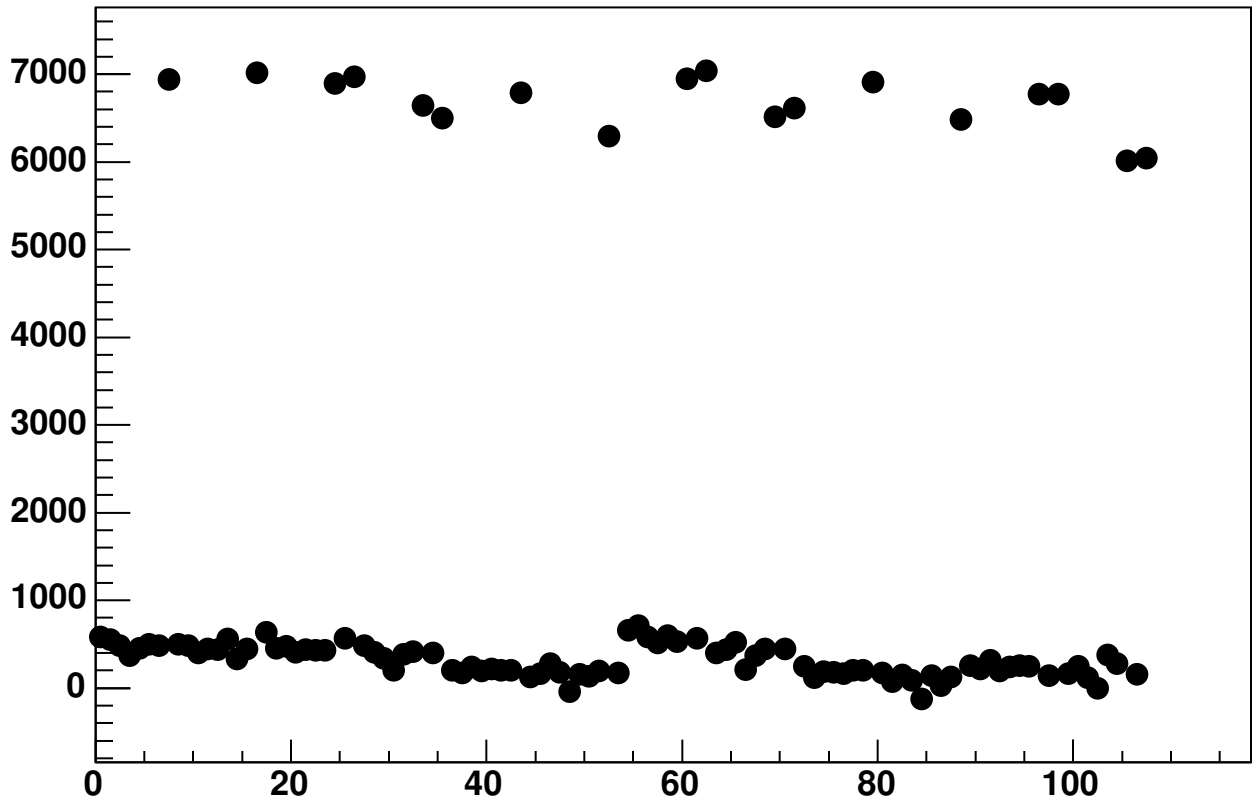
Enable 5, DAC=4000, Hold=10, ADC Mean vs 18*Chip+Chan



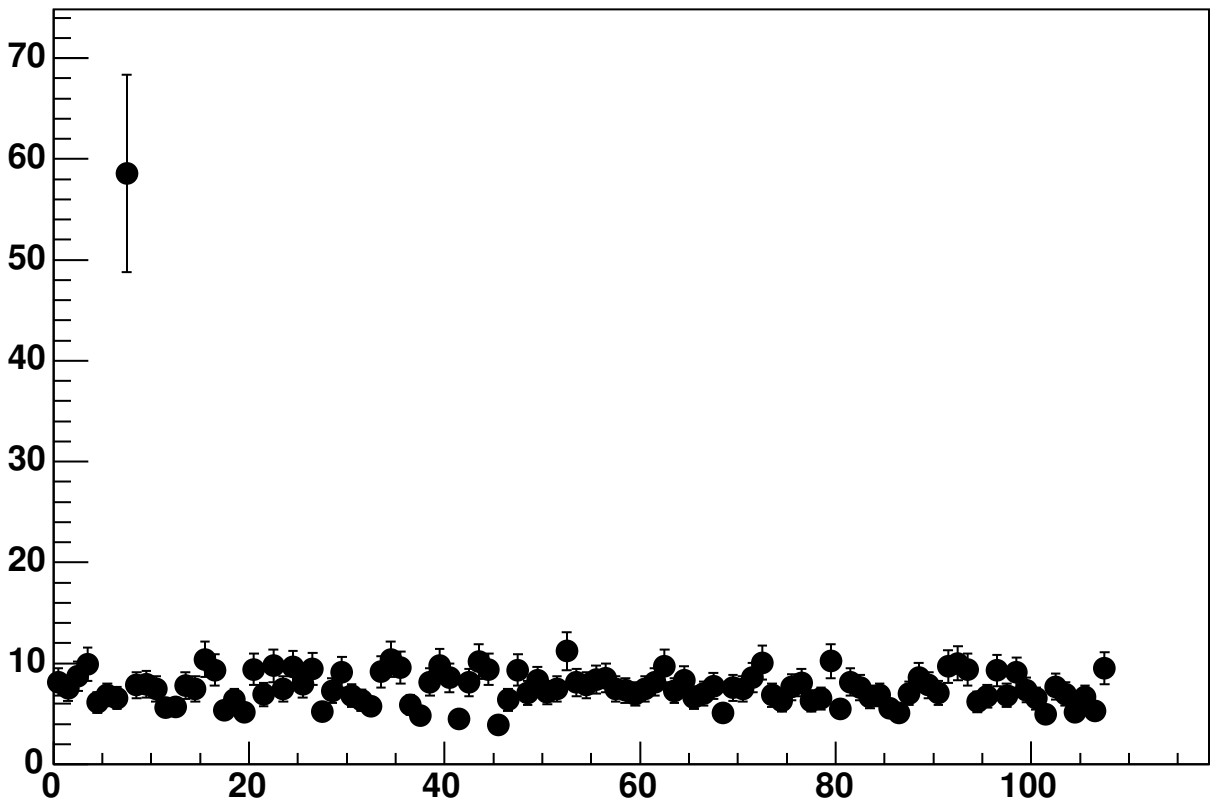
Enable 5, DAC=4000, Hold=10, ADC Noise vs 18*Chip+Chan



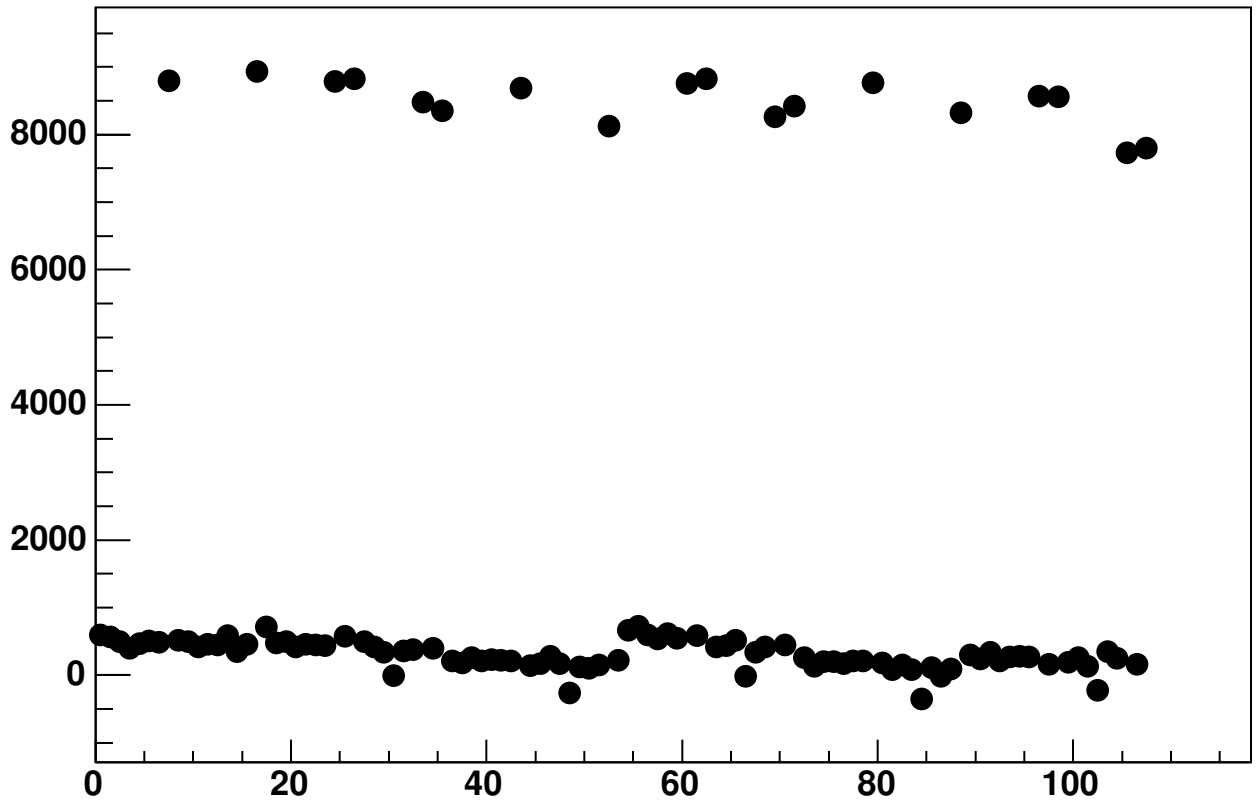
Enable 5, DAC=4000, Hold=15, ADC Mean vs 18*Chip+Chan



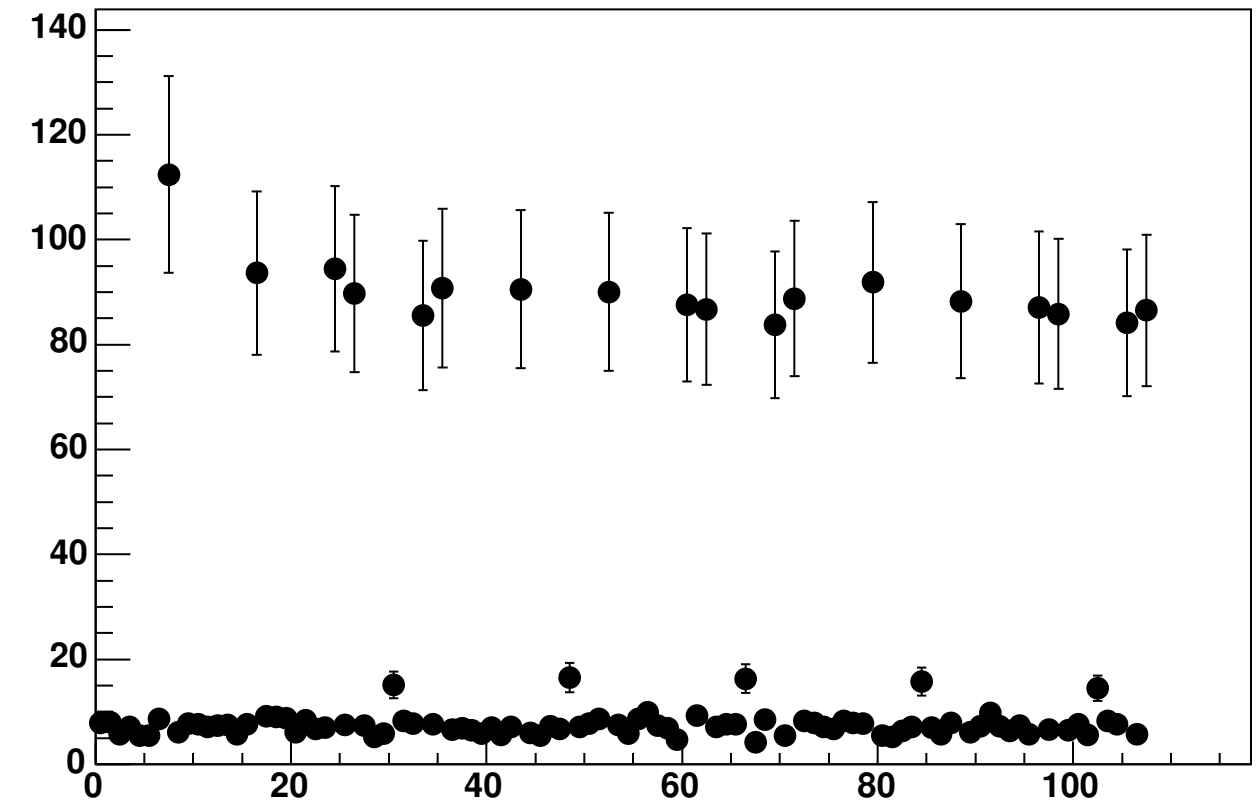
Enable 5, DAC=4000, Hold=15, ADC Noise vs 18*Chip+Chan



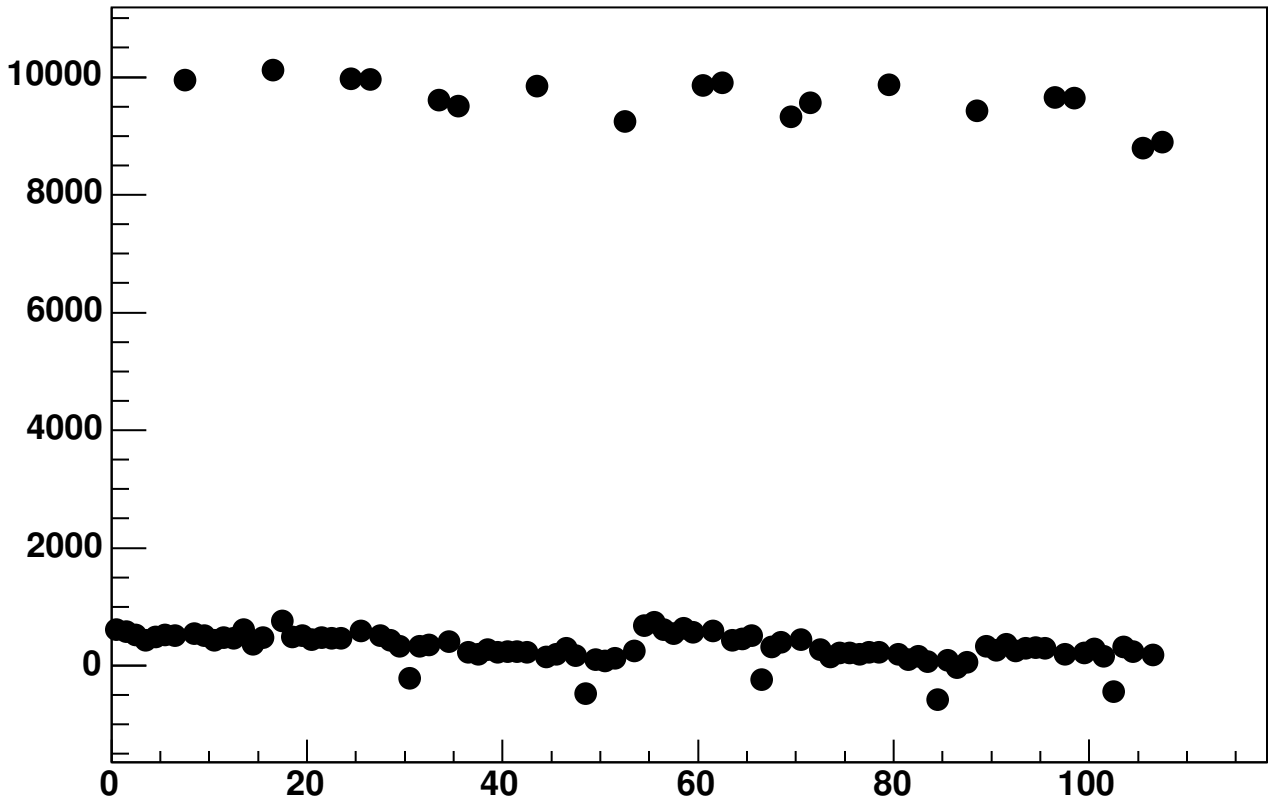
Enable 5, DAC=4000, Hold=20, ADC Mean vs 18*Chip+Chan



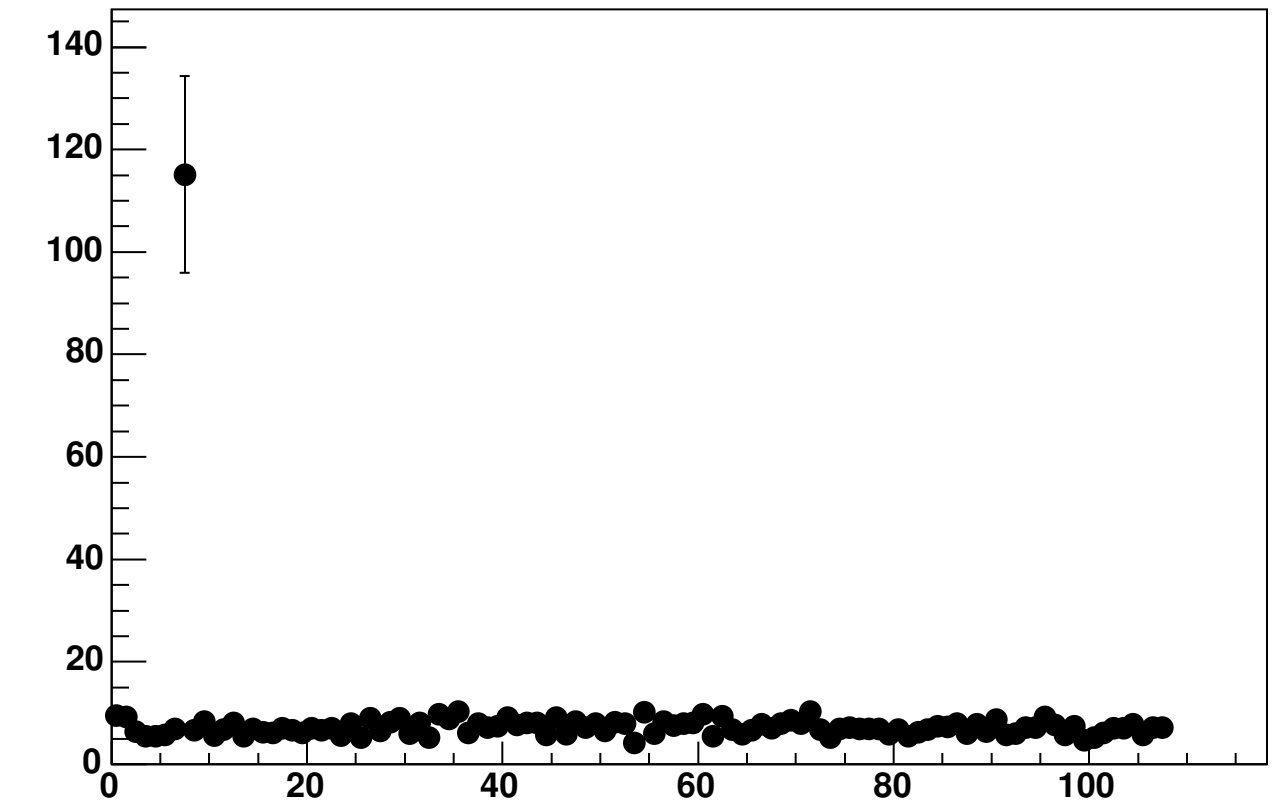
Enable 5, DAC=4000, Hold=20, ADC Noise vs 18*Chip+Chan



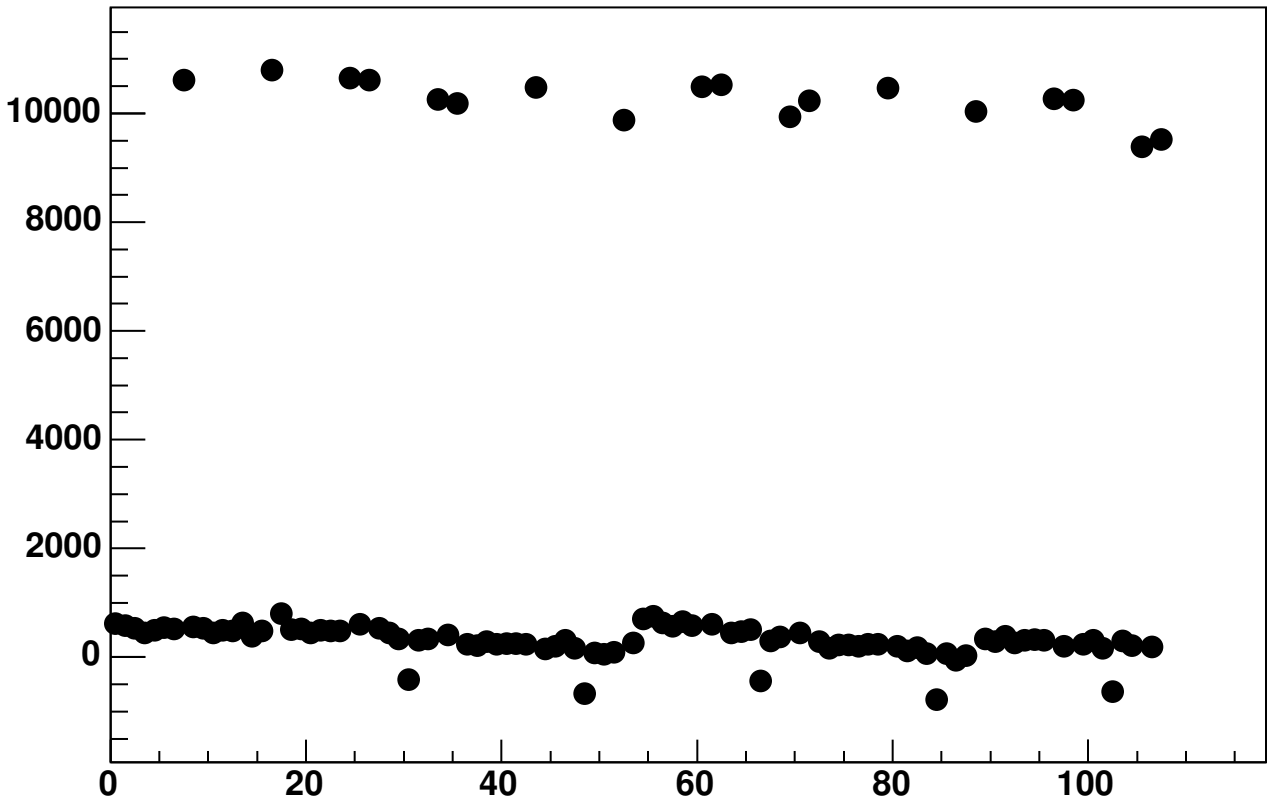
Enable 5, DAC=4000, Hold=25, ADC Mean vs 18*Chip+Chan



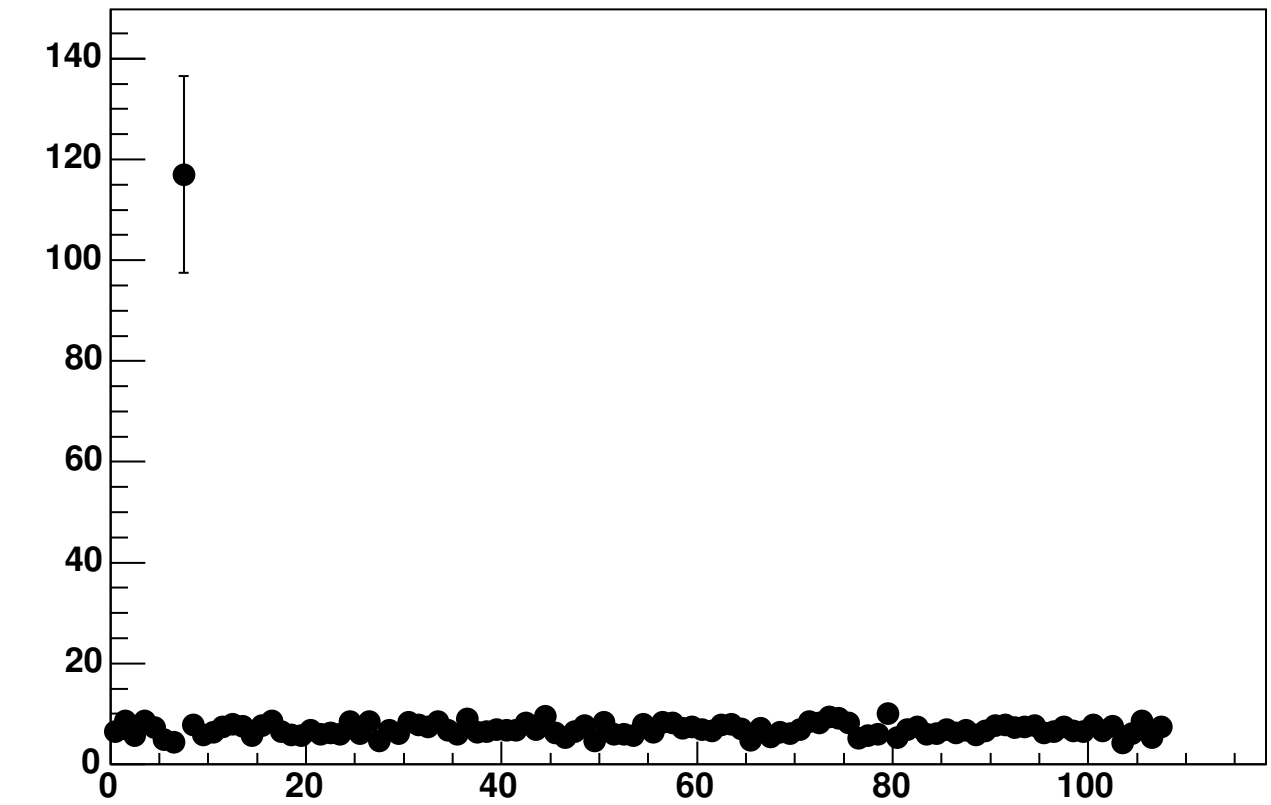
Enable 5, DAC=4000, Hold=25, ADC Noise vs 18*Chip+Chan



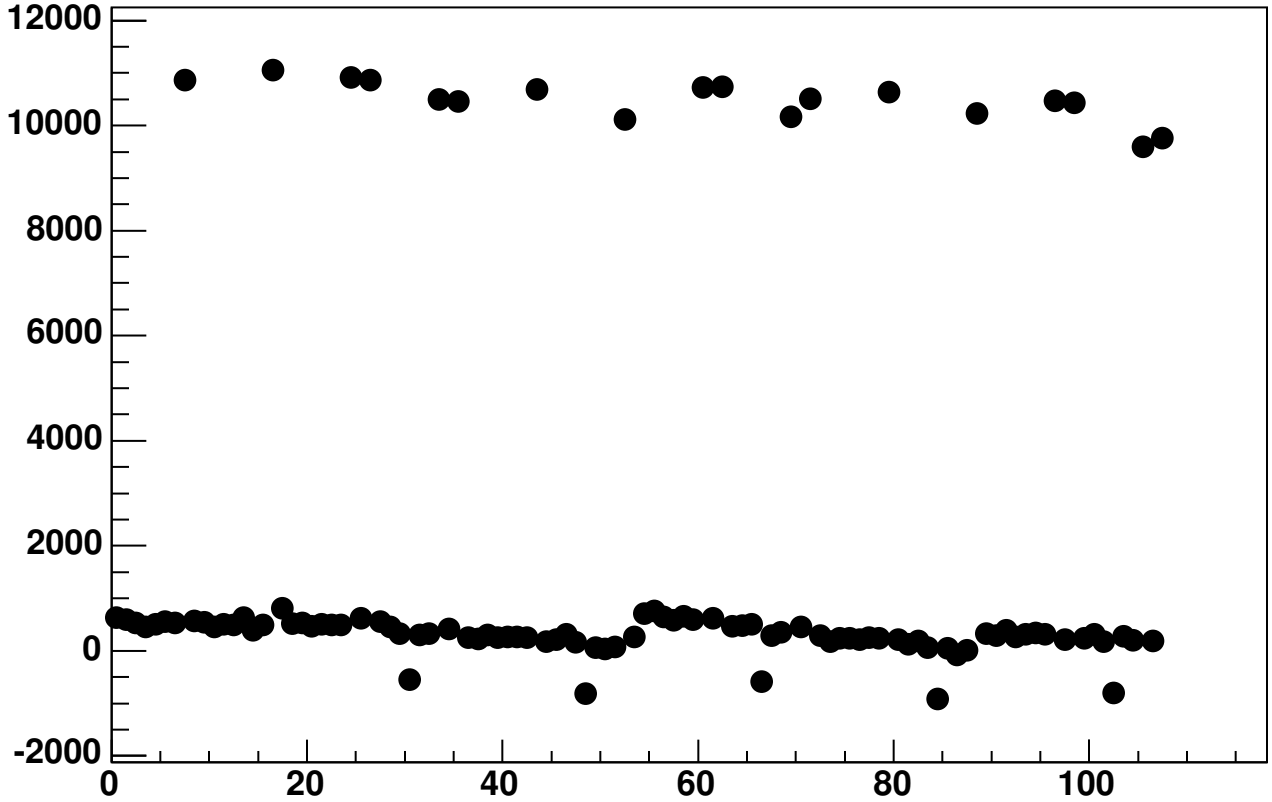
Enable 5, DAC=4000, Hold=30, ADC Mean vs 18*Chip+Chan



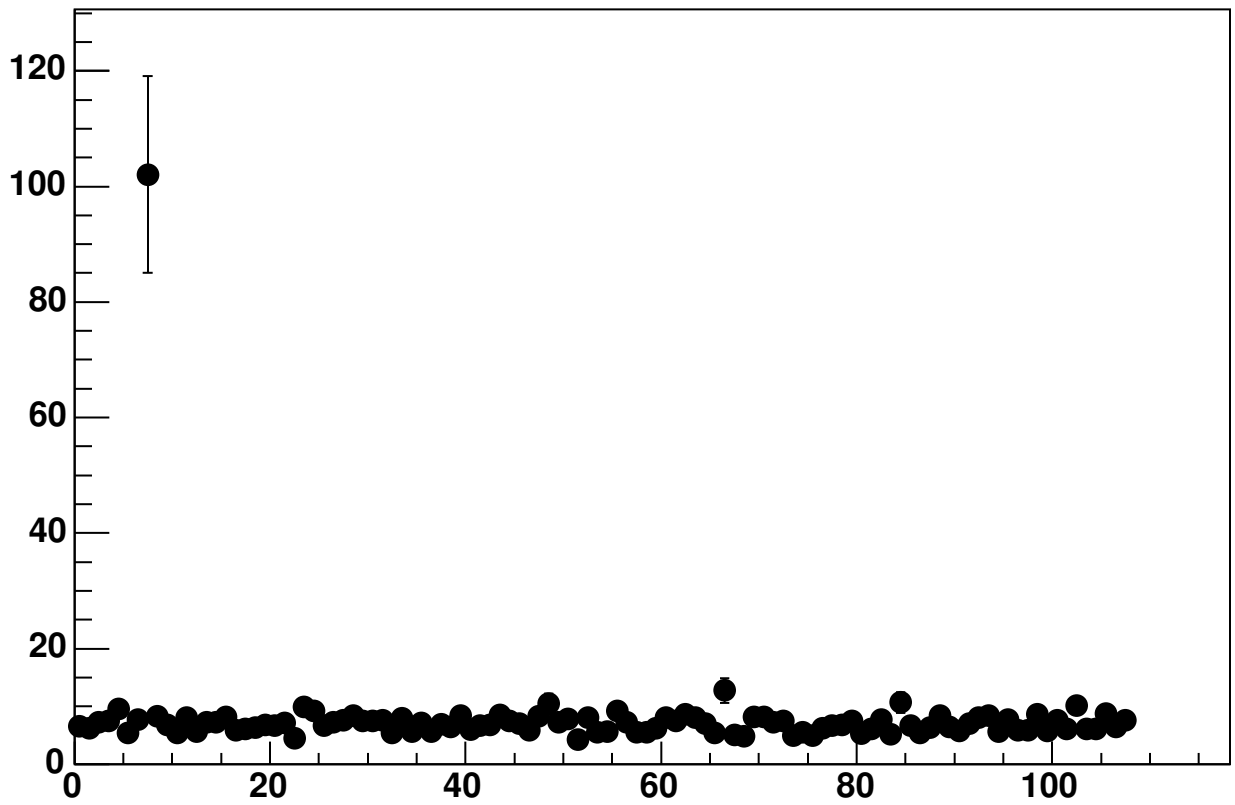
Enable 5, DAC=4000, Hold=30, ADC Noise vs 18*Chip+Chan



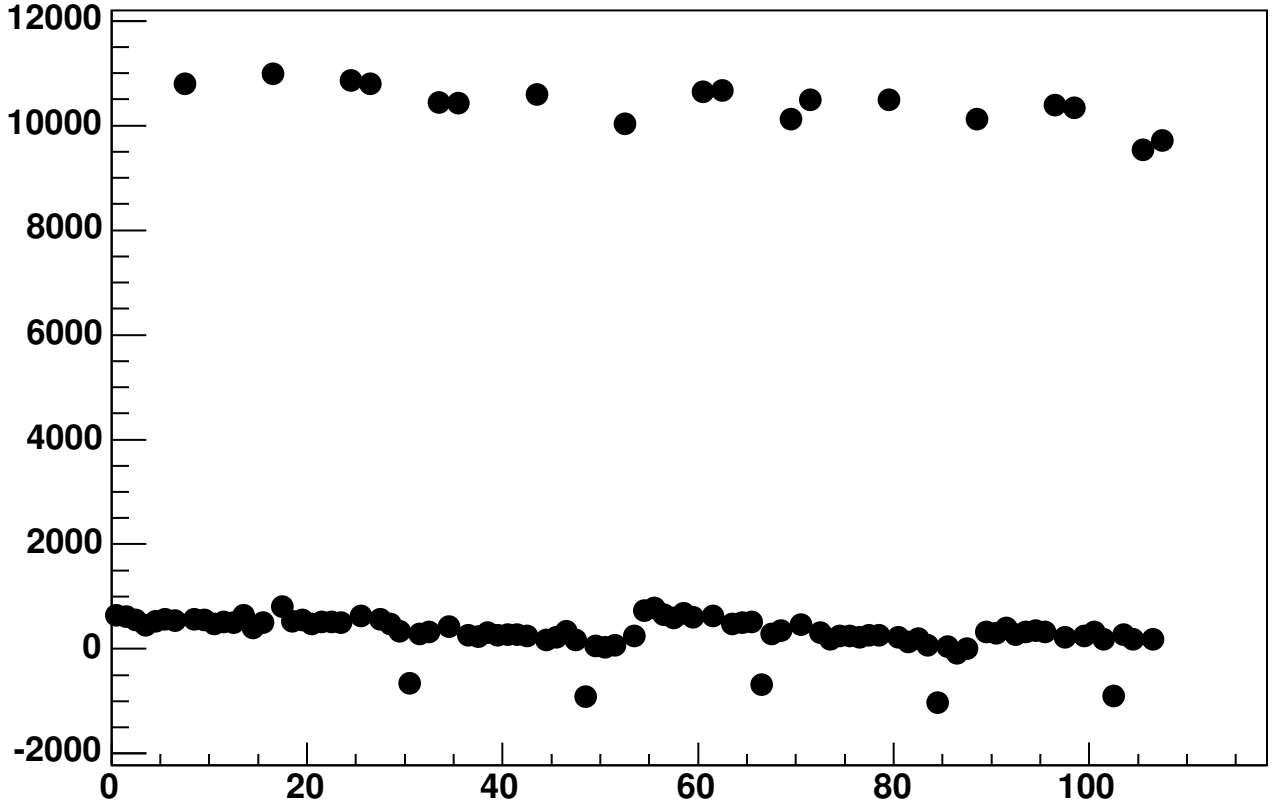
Enable 5, DAC=4000, Hold=35, ADC Mean vs 18*Chip+Chan



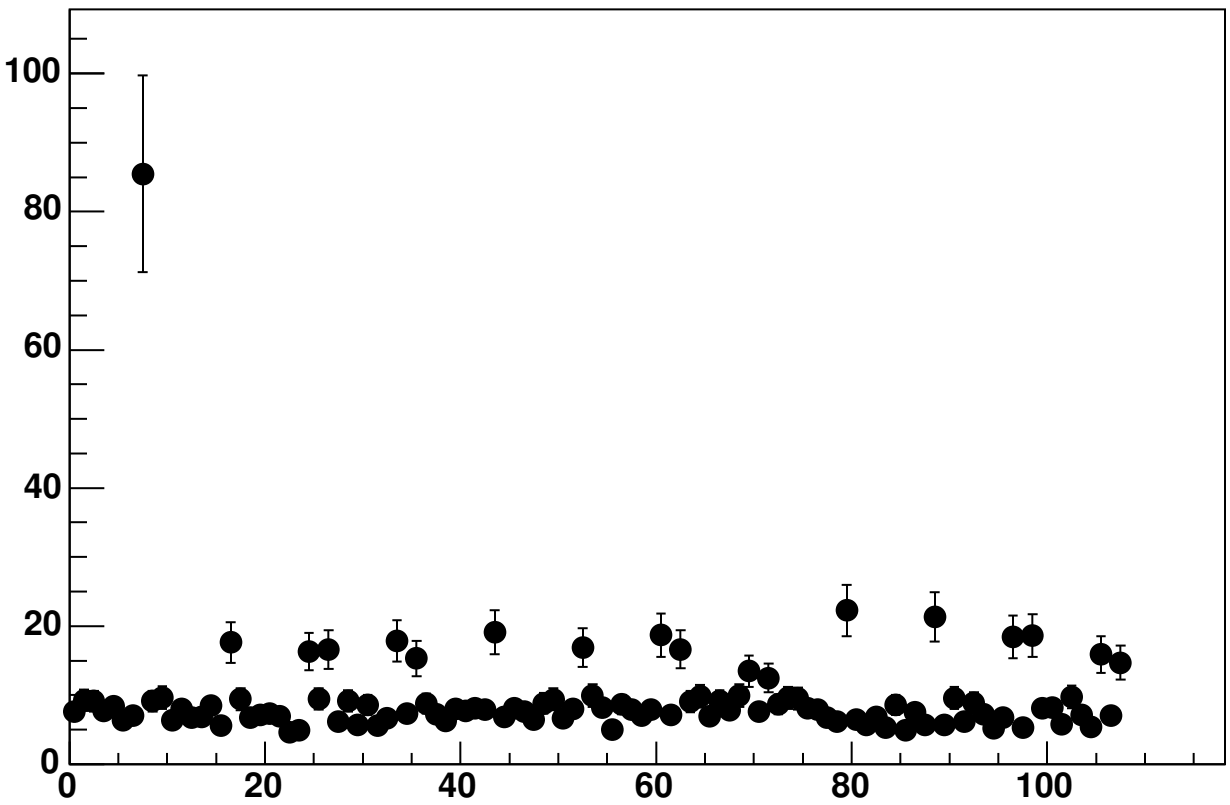
Enable 5, DAC=4000, Hold=35, ADC Noise vs 18*Chip+Chan



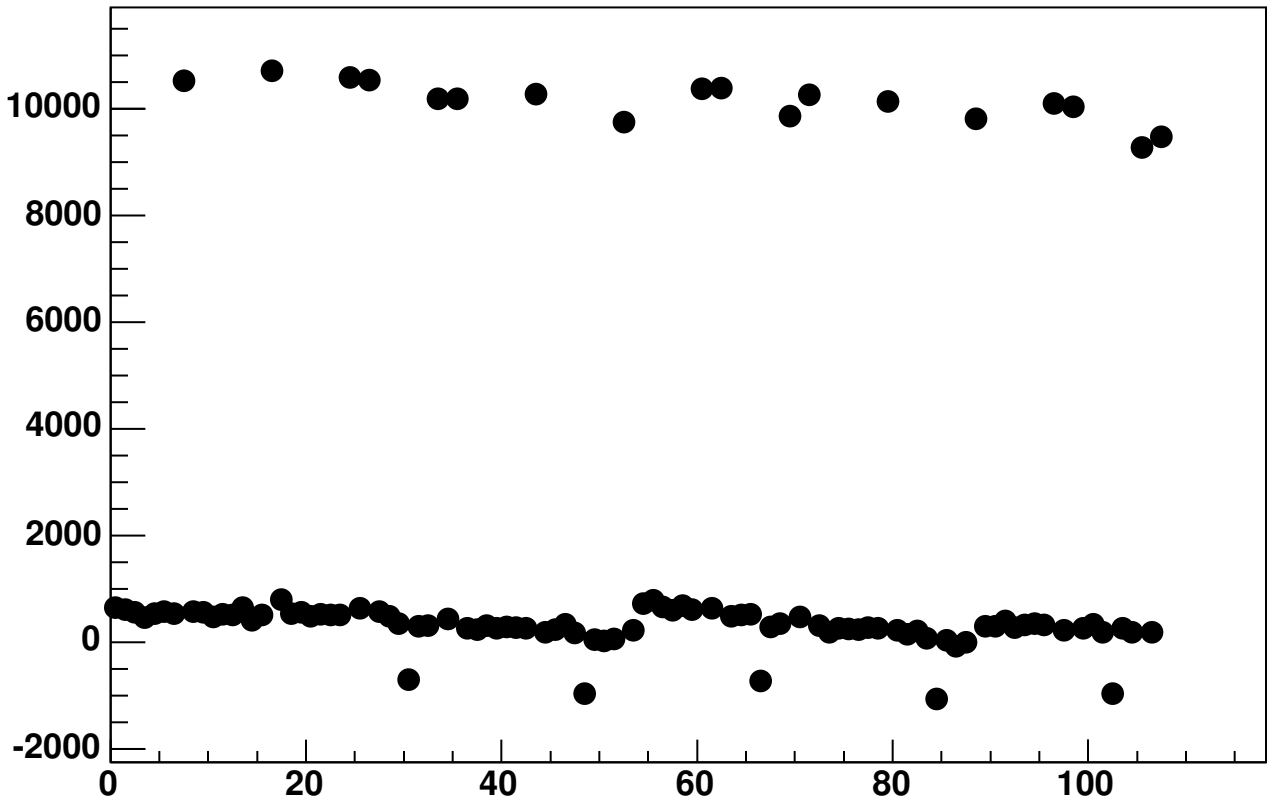
Enable 5, DAC=4000, Hold=40, ADC Mean vs 18*Chip+Chan



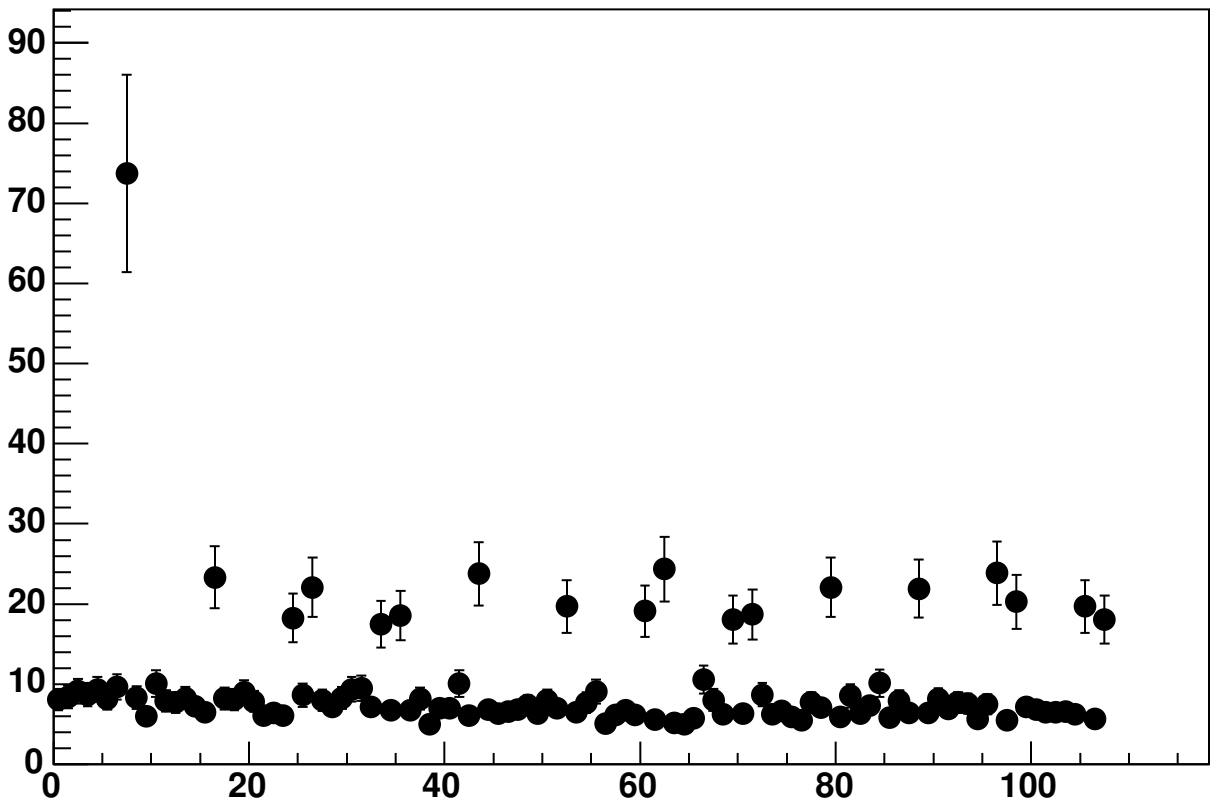
Enable 5, DAC=4000, Hold=40, ADC Noise vs 18*Chip+Chan



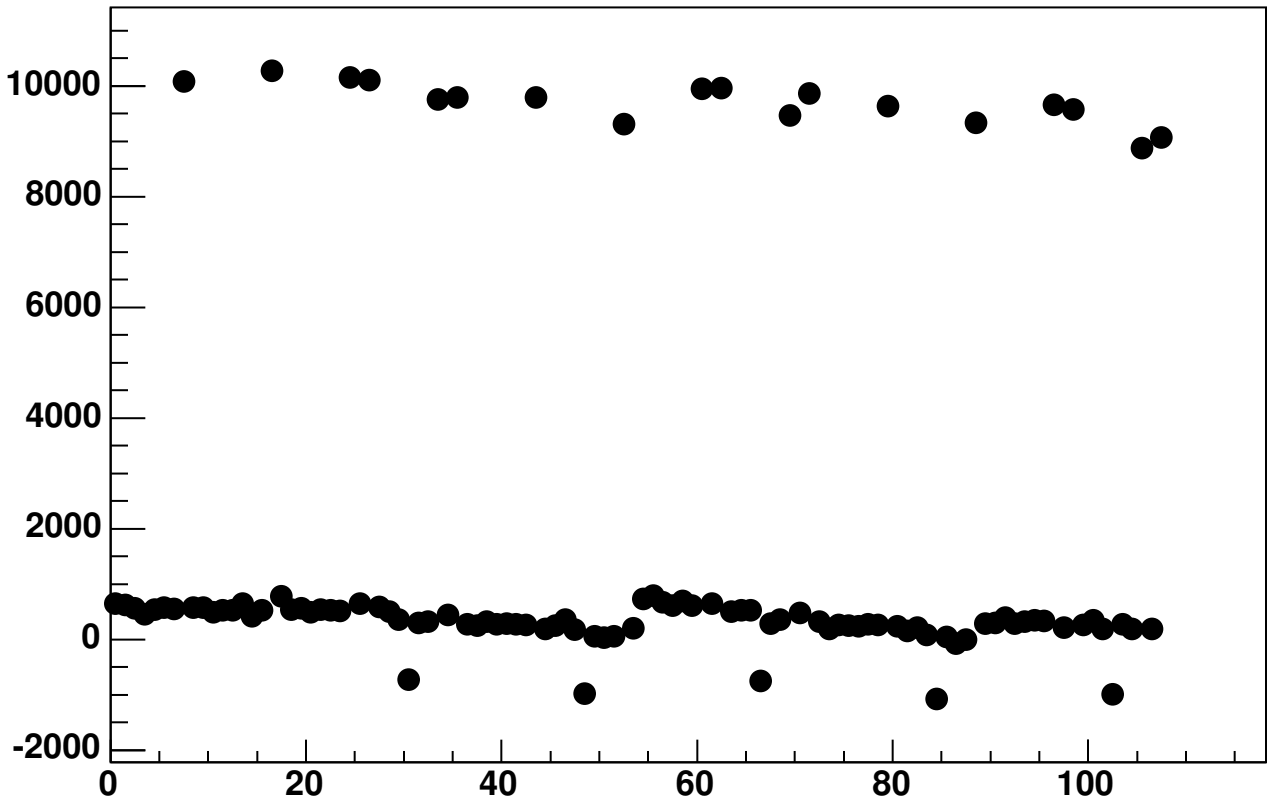
Enable 5, DAC=4000, Hold=45, ADC Mean vs 18*Chip+Chan



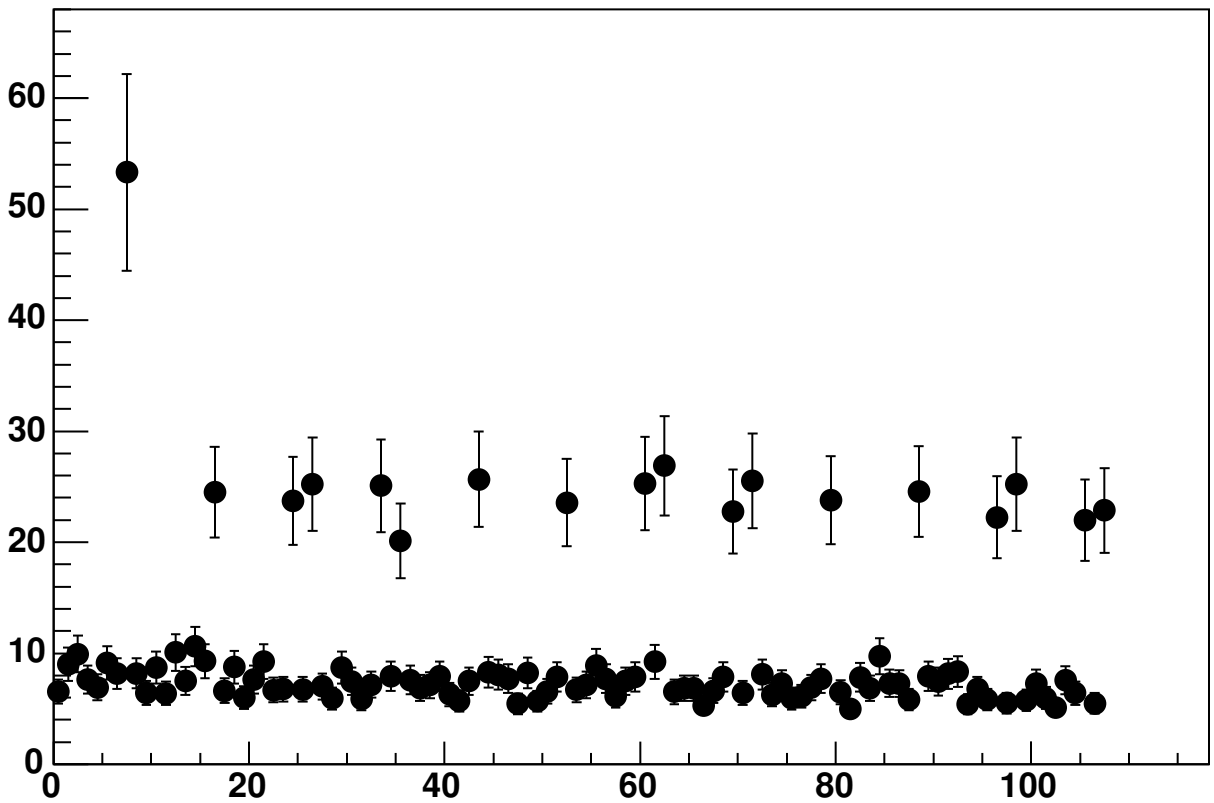
Enable 5, DAC=4000, Hold=45, ADC Noise vs 18*Chip+Chan



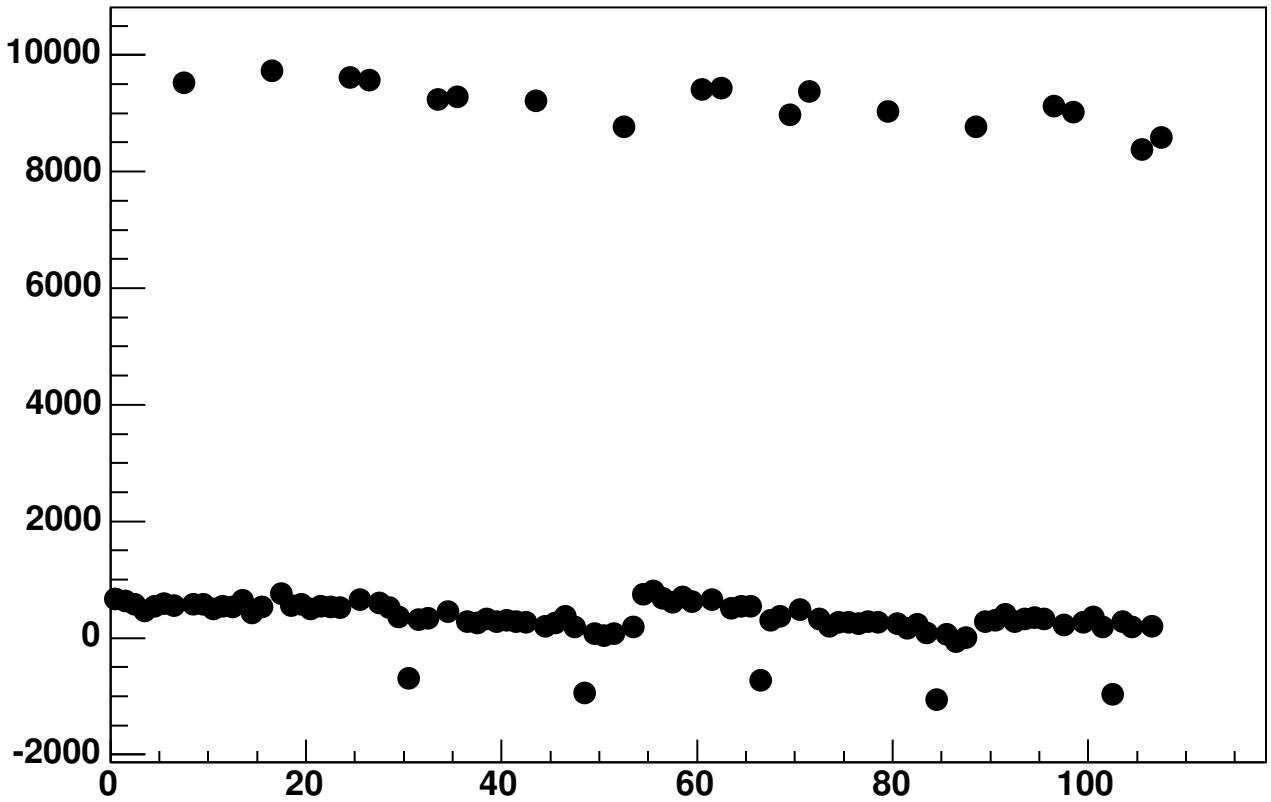
Enable 5, DAC=4000, Hold=50, ADC Mean vs 18*Chip+Chan



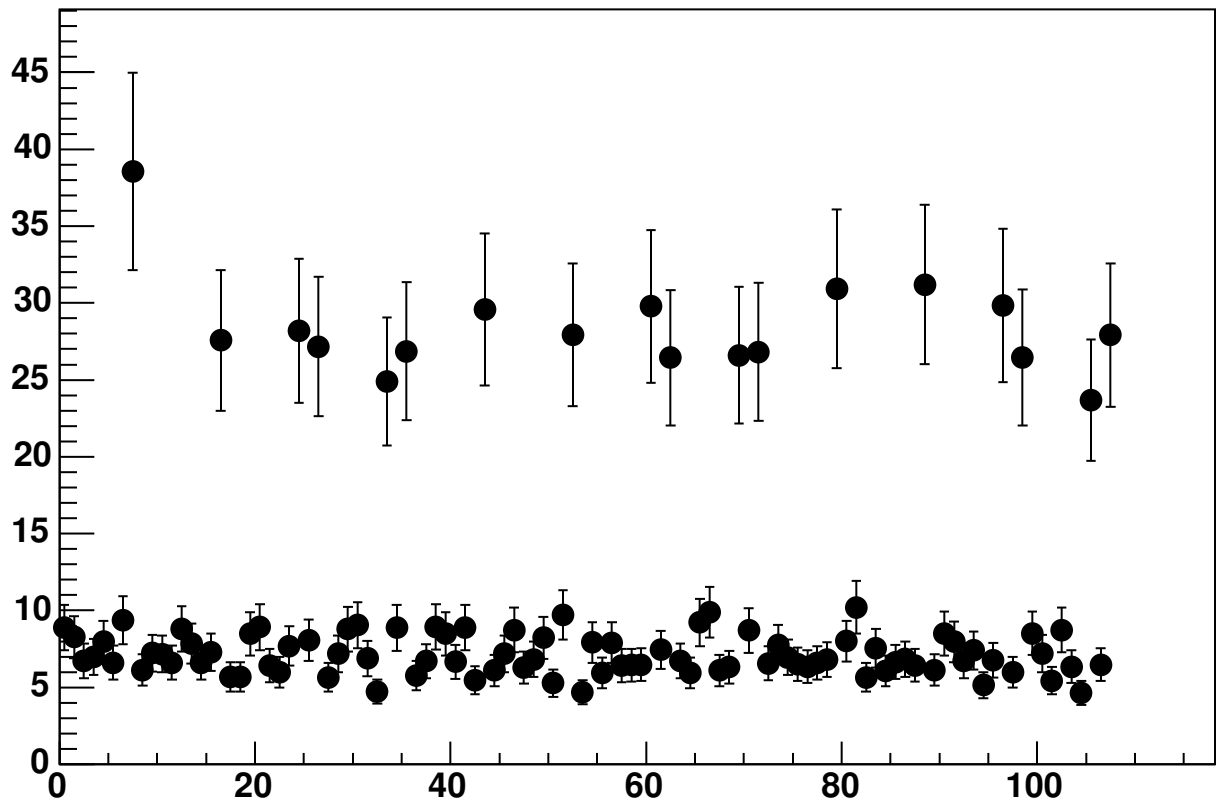
Enable 5, DAC=4000, Hold=50, ADC Noise vs 18*Chip+Chan



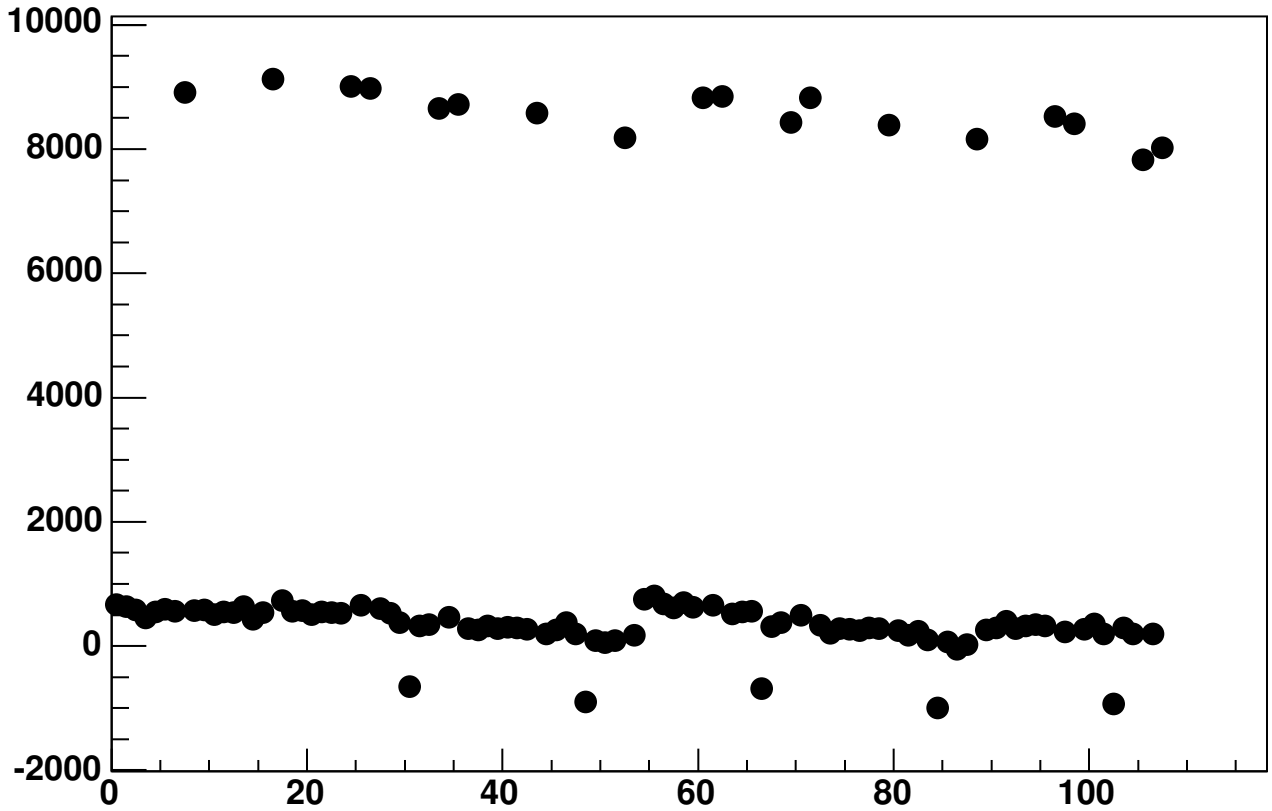
Enable 5, DAC=4000, Hold=55, ADC Mean vs 18*Chip+Chan



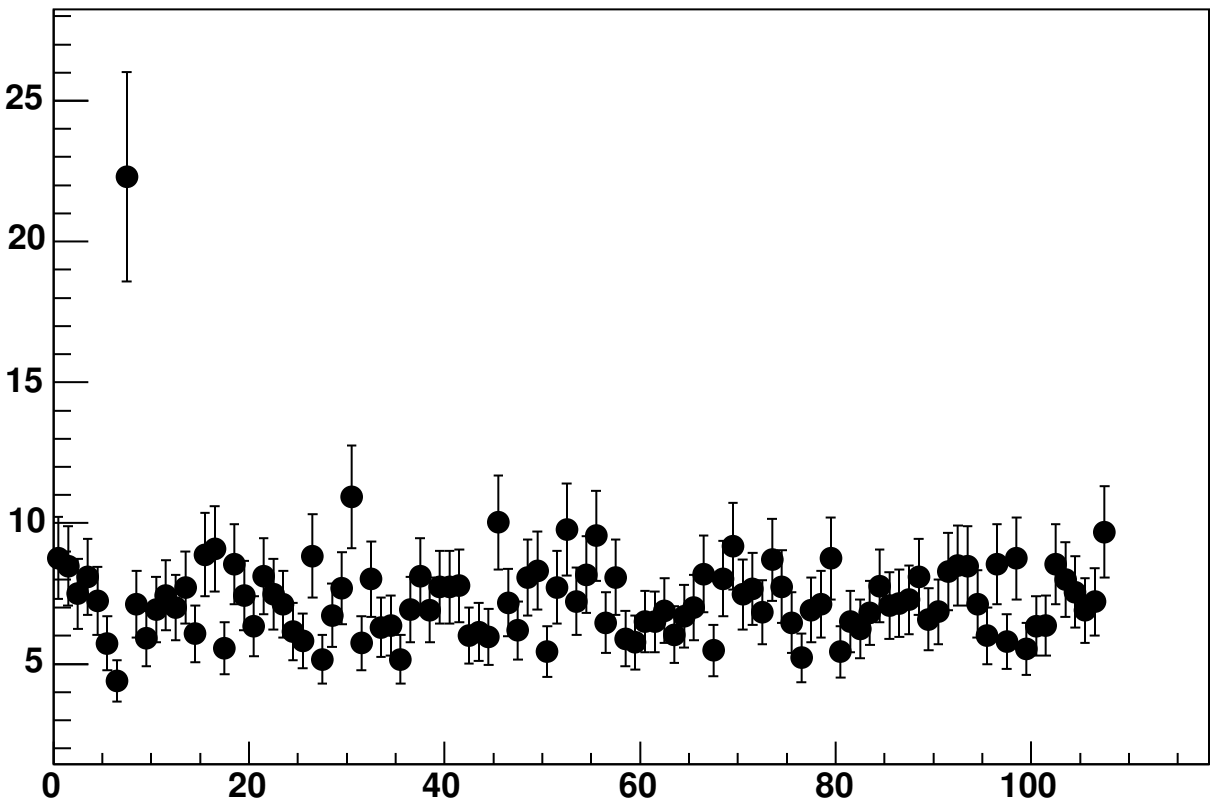
Enable 5, DAC=4000, Hold=55, ADC Noise vs 18*Chip+Chan



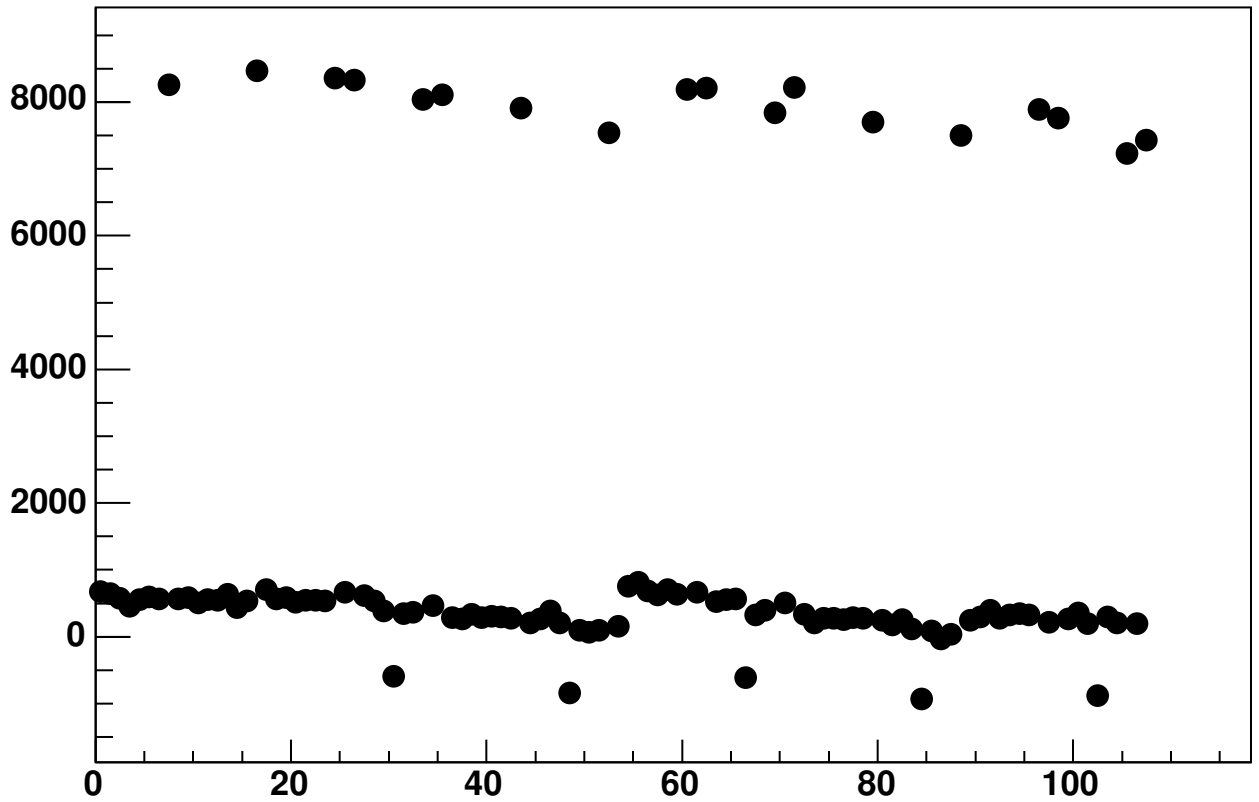
Enable 5, DAC=4000, Hold=60, ADC Mean vs 18*Chip+Chan



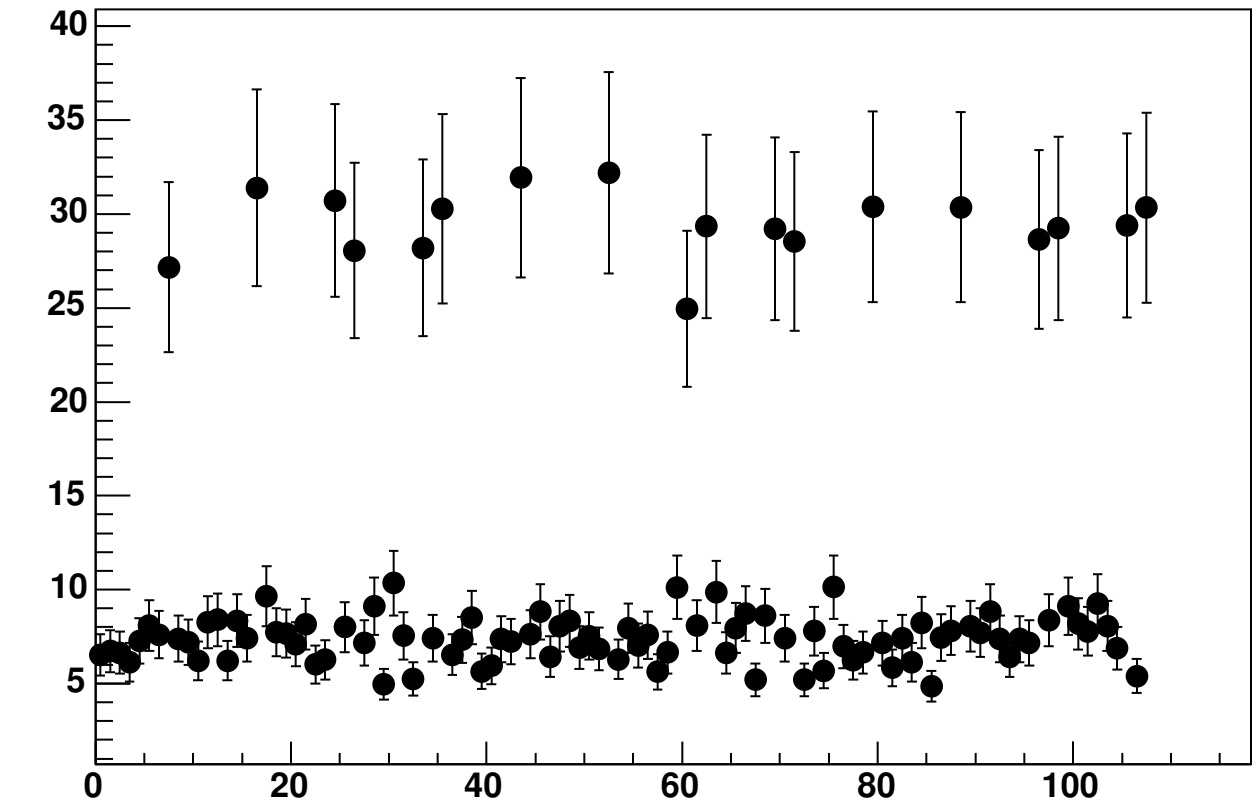
Enable 5, DAC=4000, Hold=60, ADC Noise vs 18*Chip+Chan



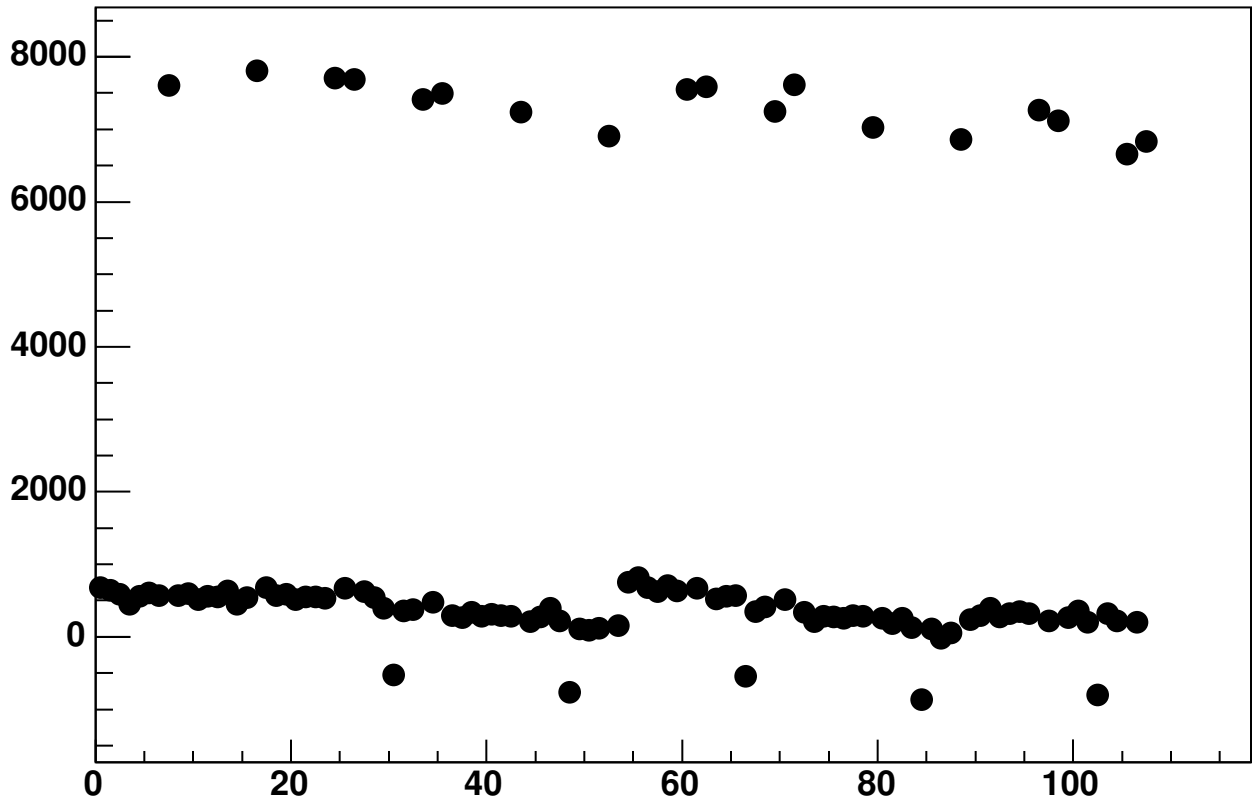
Enable 5, DAC=4000, Hold=65, ADC Mean vs 18*Chip+Chan



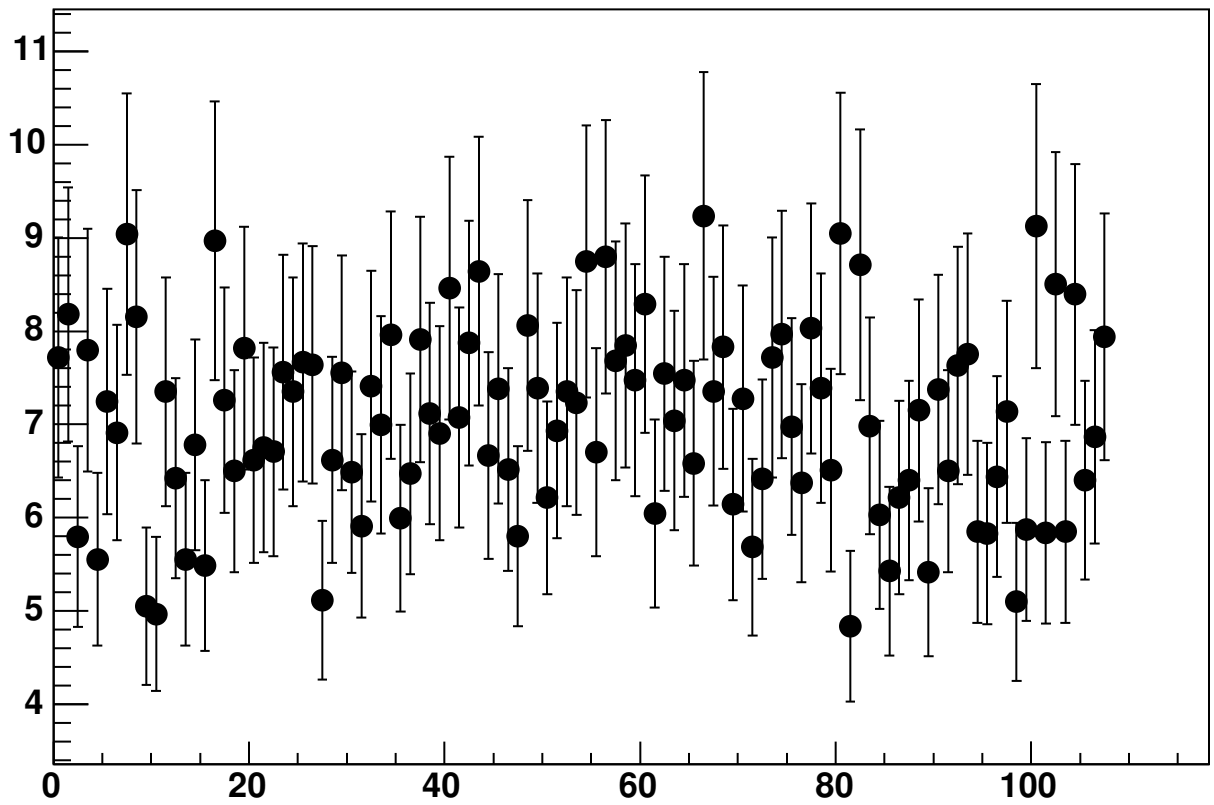
Enable 5, DAC=4000, Hold=65, ADC Noise vs 18*Chip+Chan



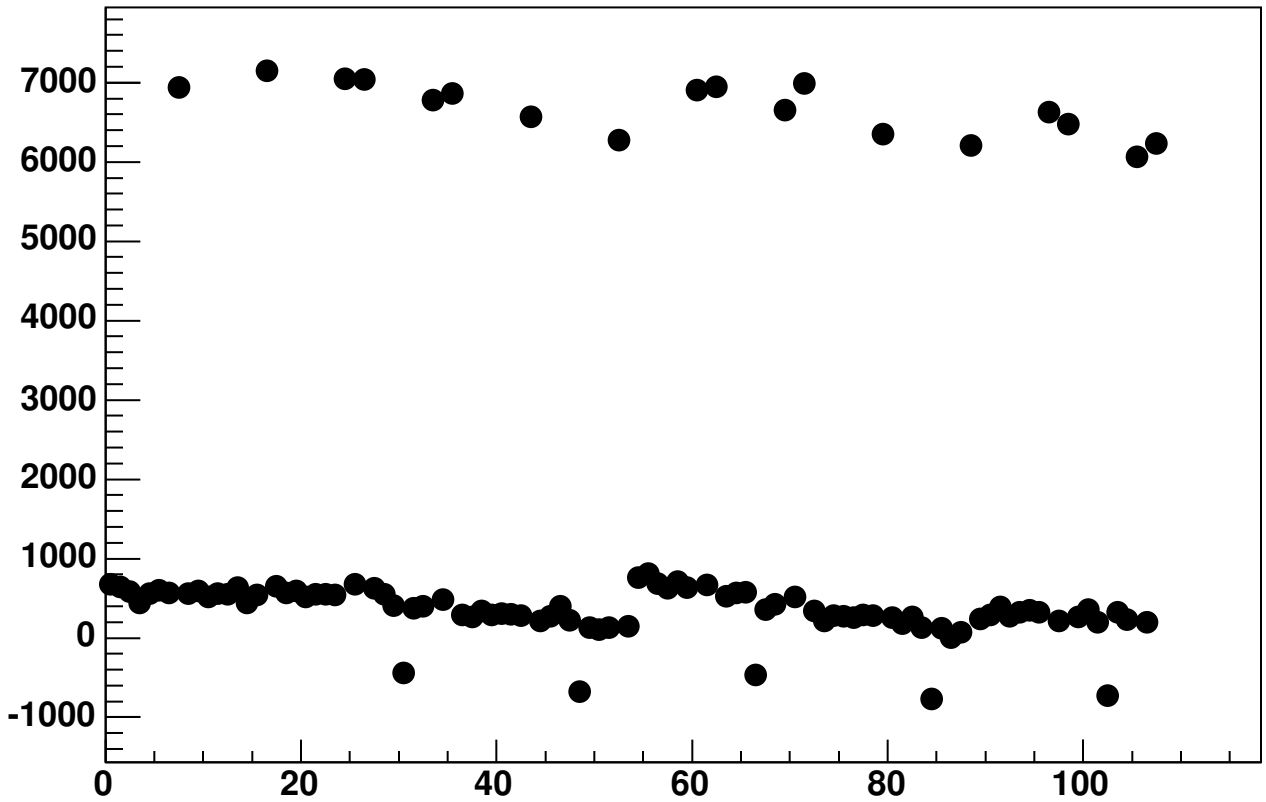
Enable 5, DAC=4000, Hold=70, ADC Mean vs 18*Chip+Chan



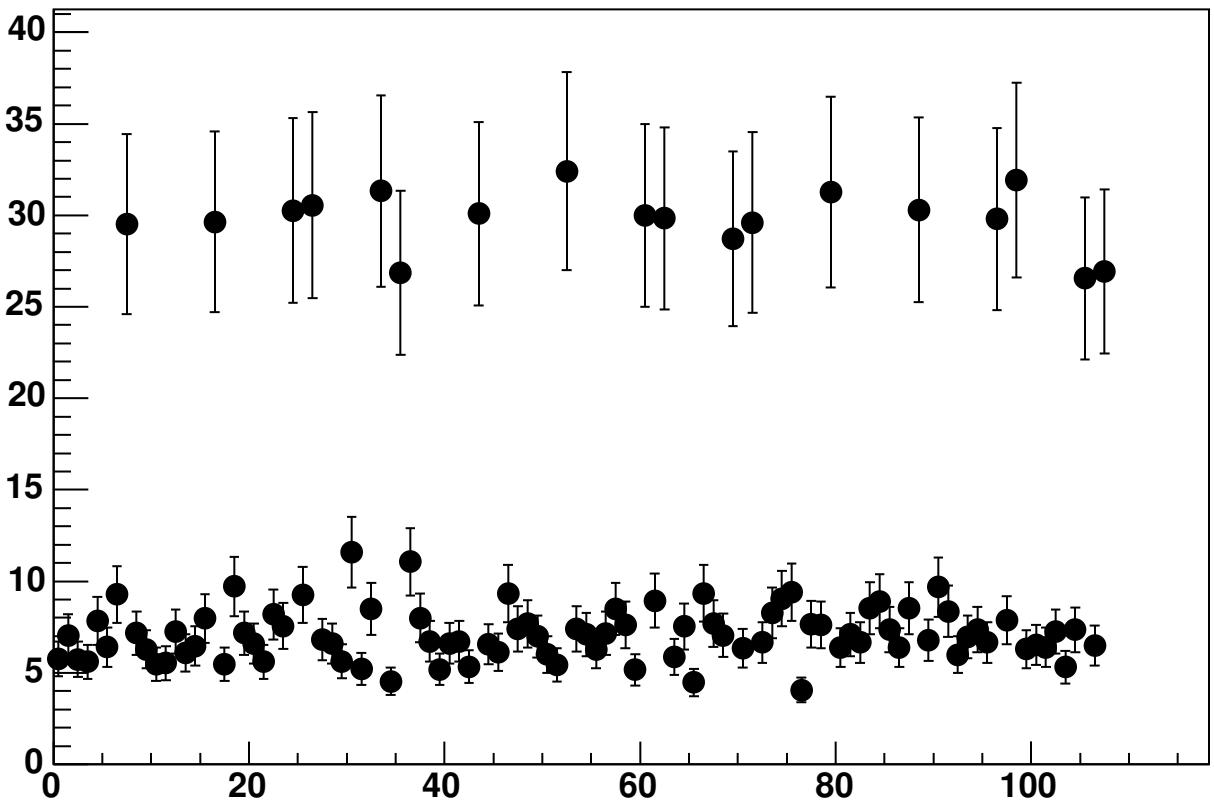
Enable 5, DAC=4000, Hold=70, ADC Noise vs 18*Chip+Chan



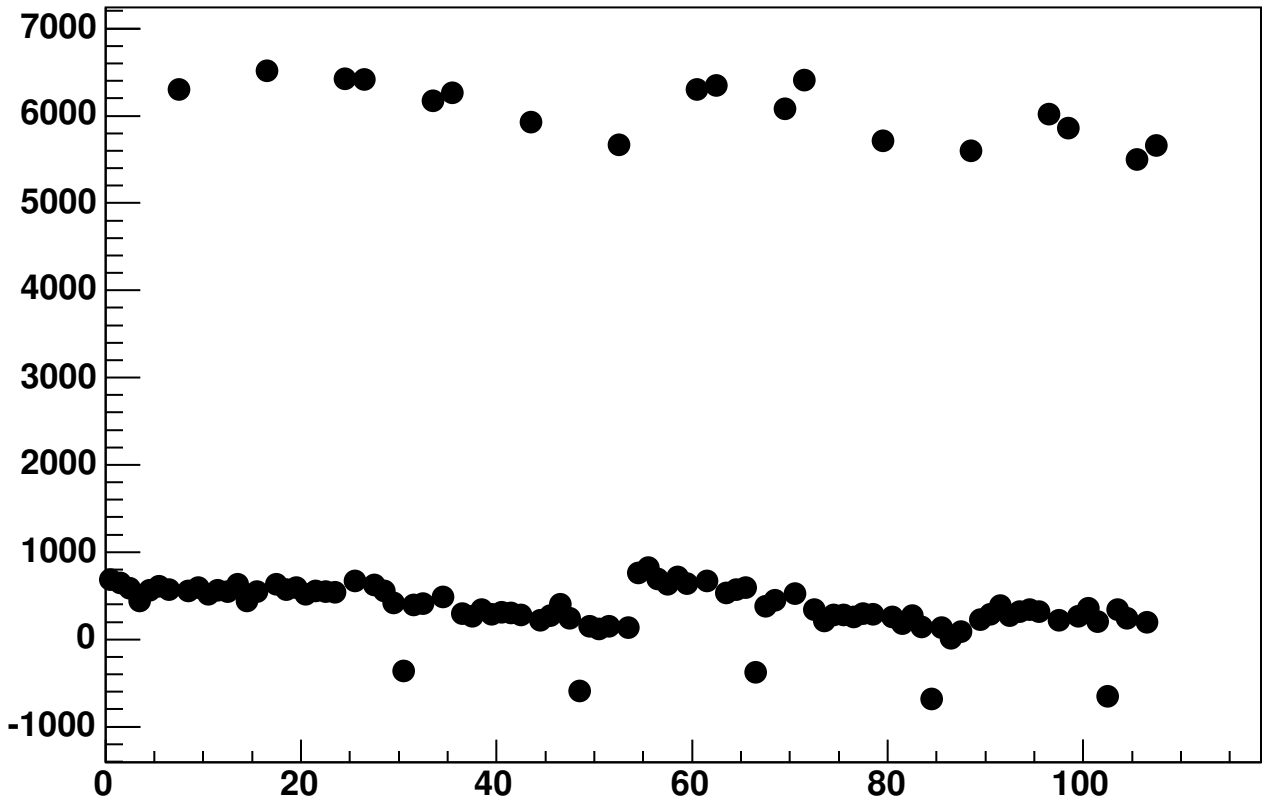
Enable 5, DAC=4000, Hold=75, ADC Mean vs 18*Chip+Chan



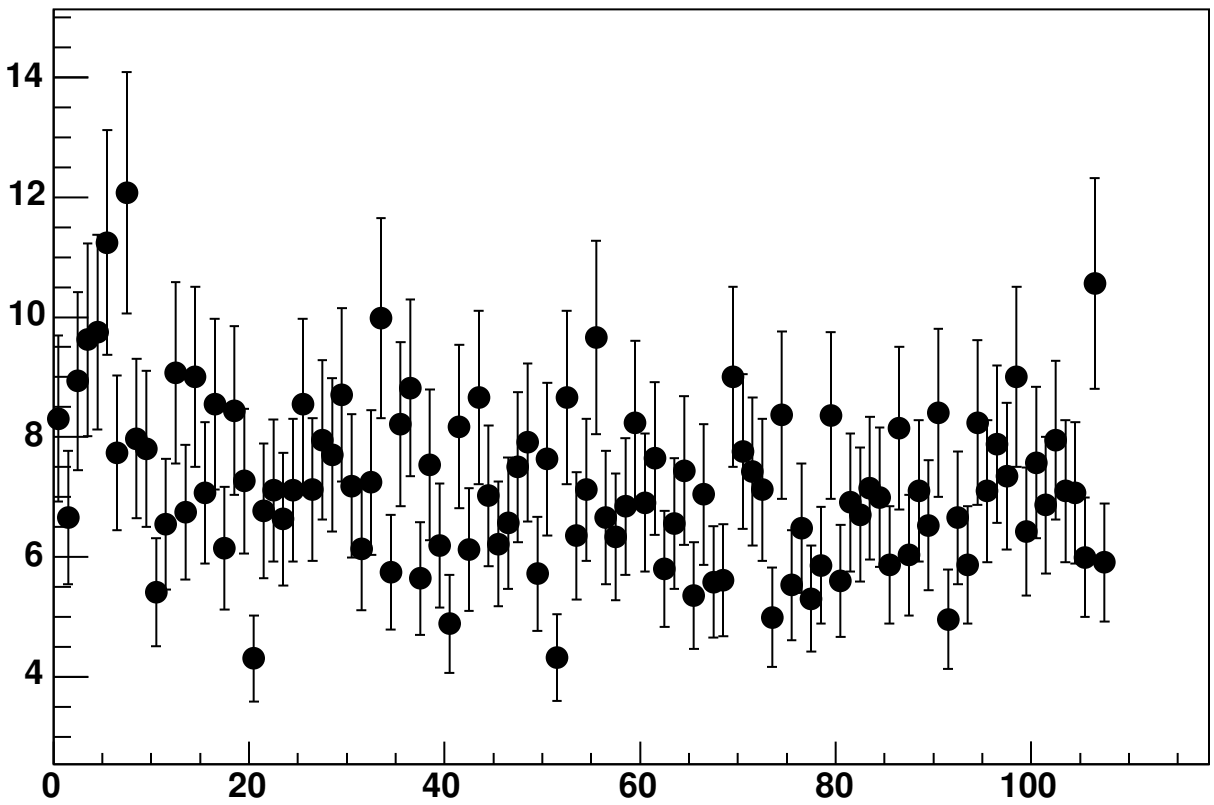
Enable 5, DAC=4000, Hold=75, ADC Noise vs 18*Chip+Chan



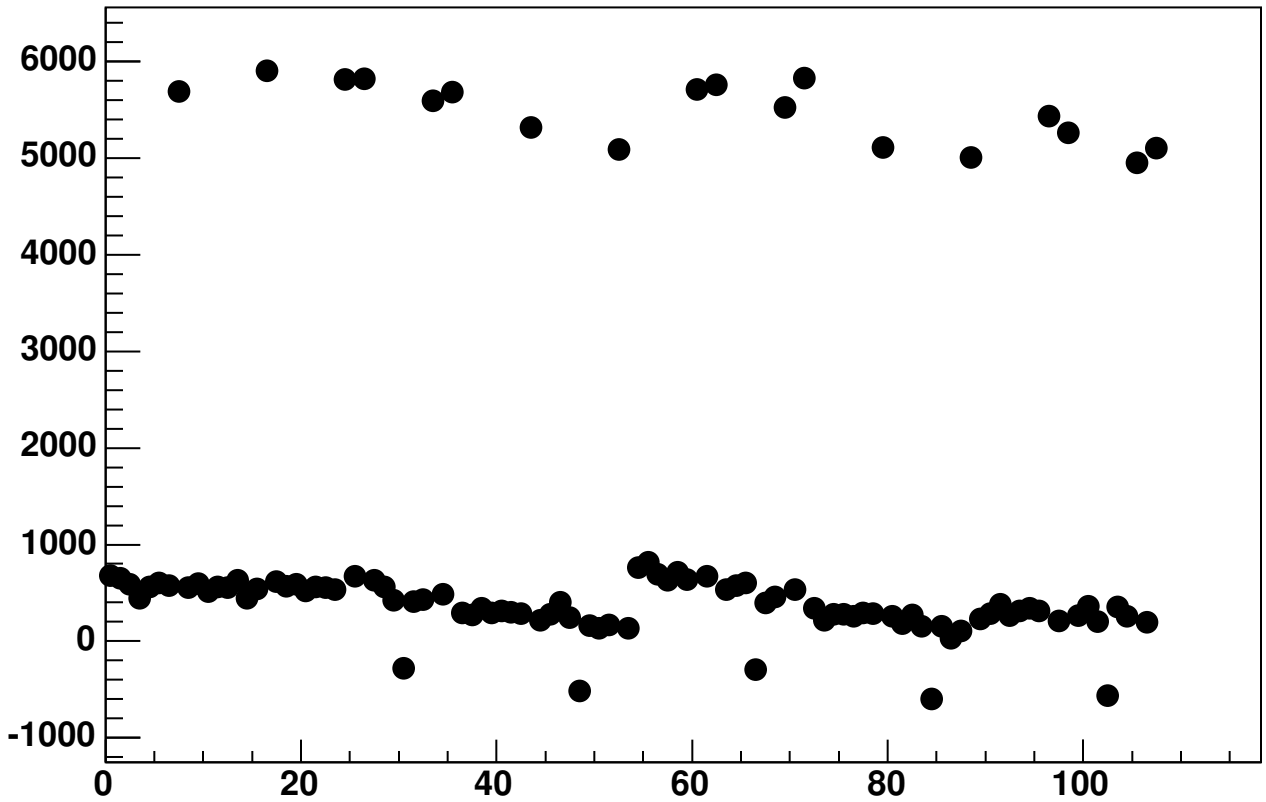
Enable 5, DAC=4000, Hold=80, ADC Mean vs 18*Chip+Chan



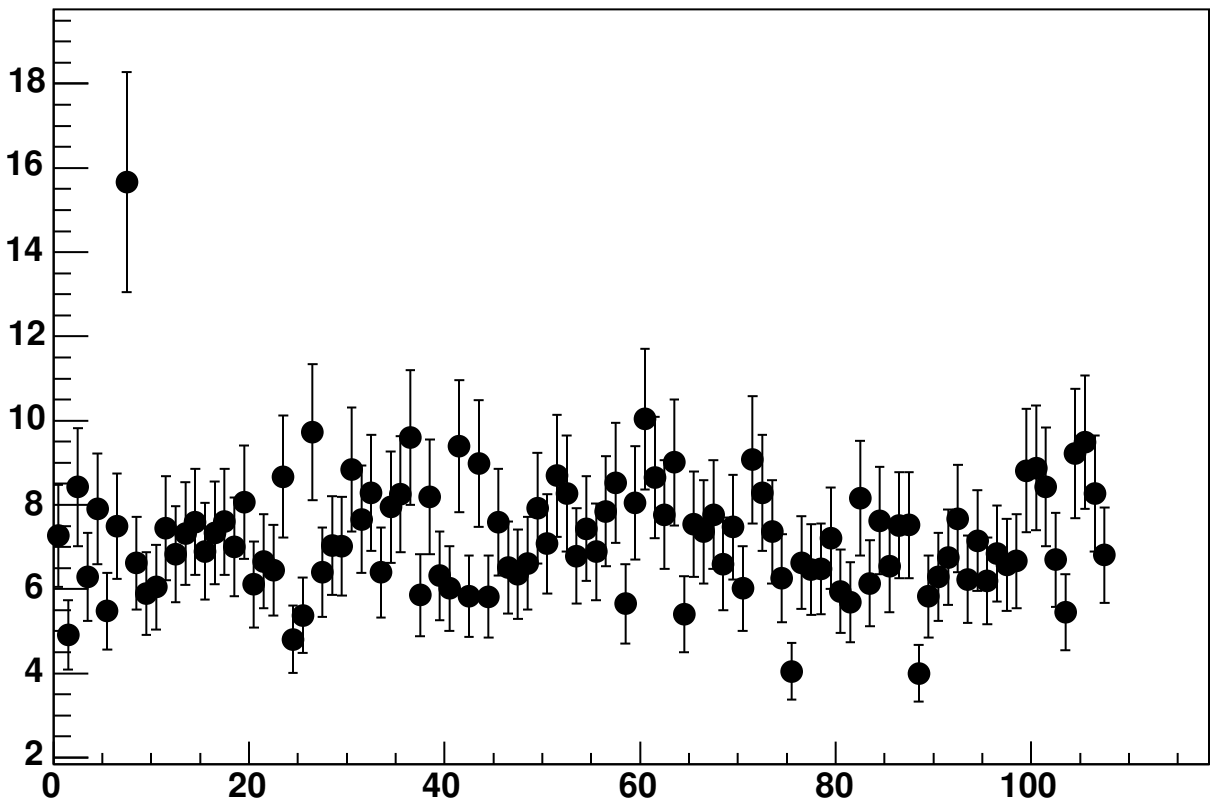
Enable 5, DAC=4000, Hold=80, ADC Noise vs 18*Chip+Chan



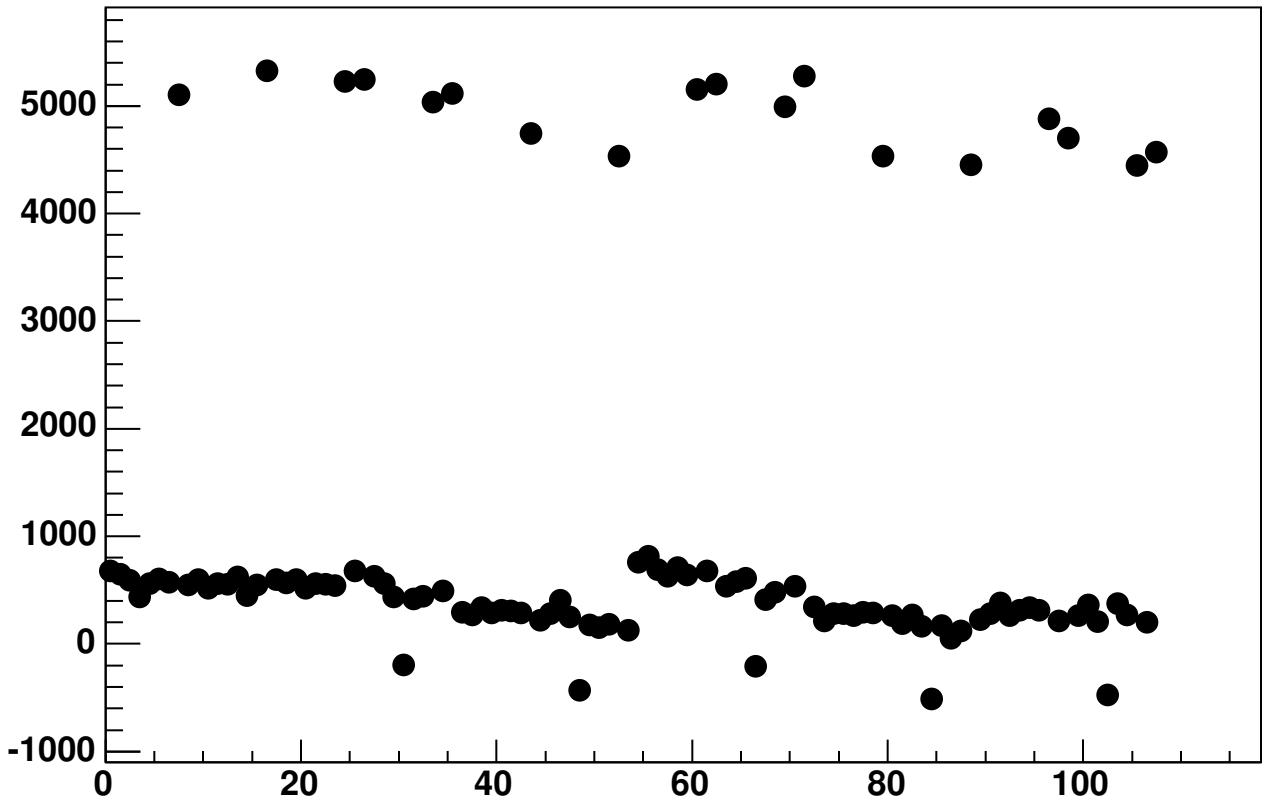
Enable 5, DAC=4000, Hold=85, ADC Mean vs 18*Chip+Chan



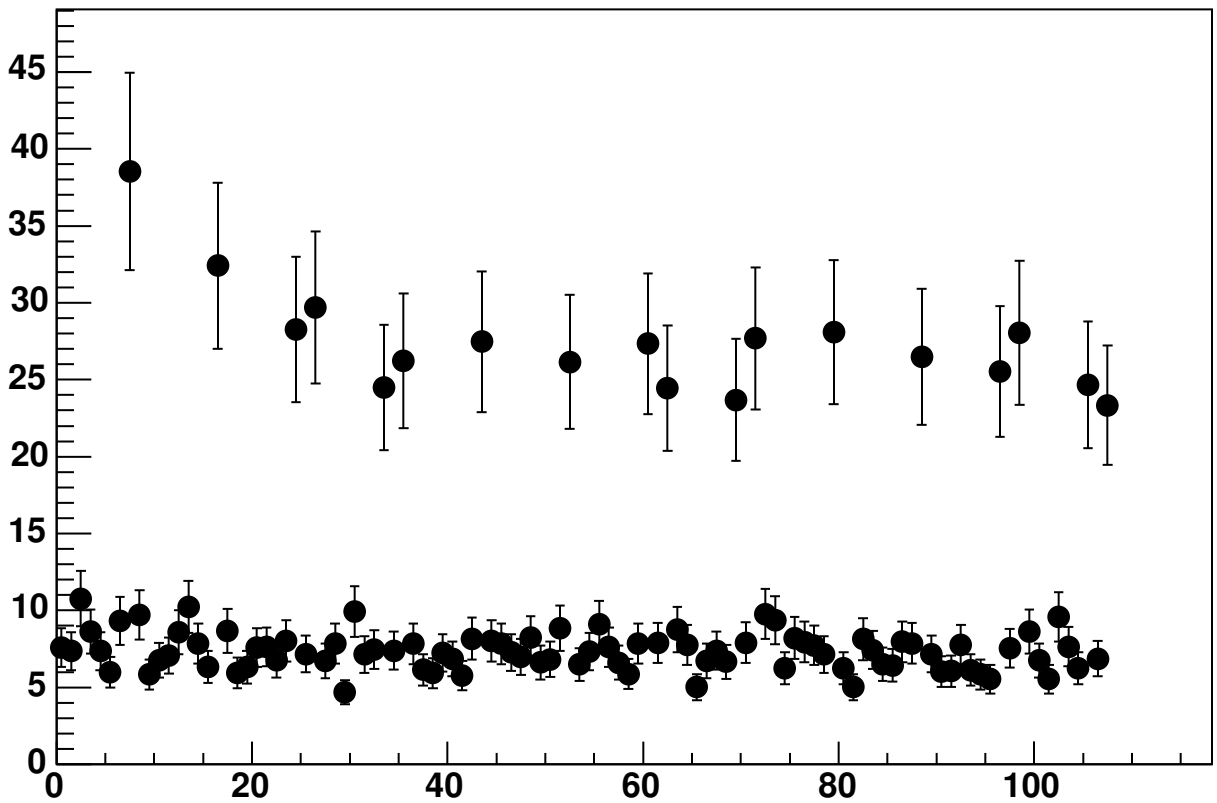
Enable 5, DAC=4000, Hold=85, ADC Noise vs 18*Chip+Chan



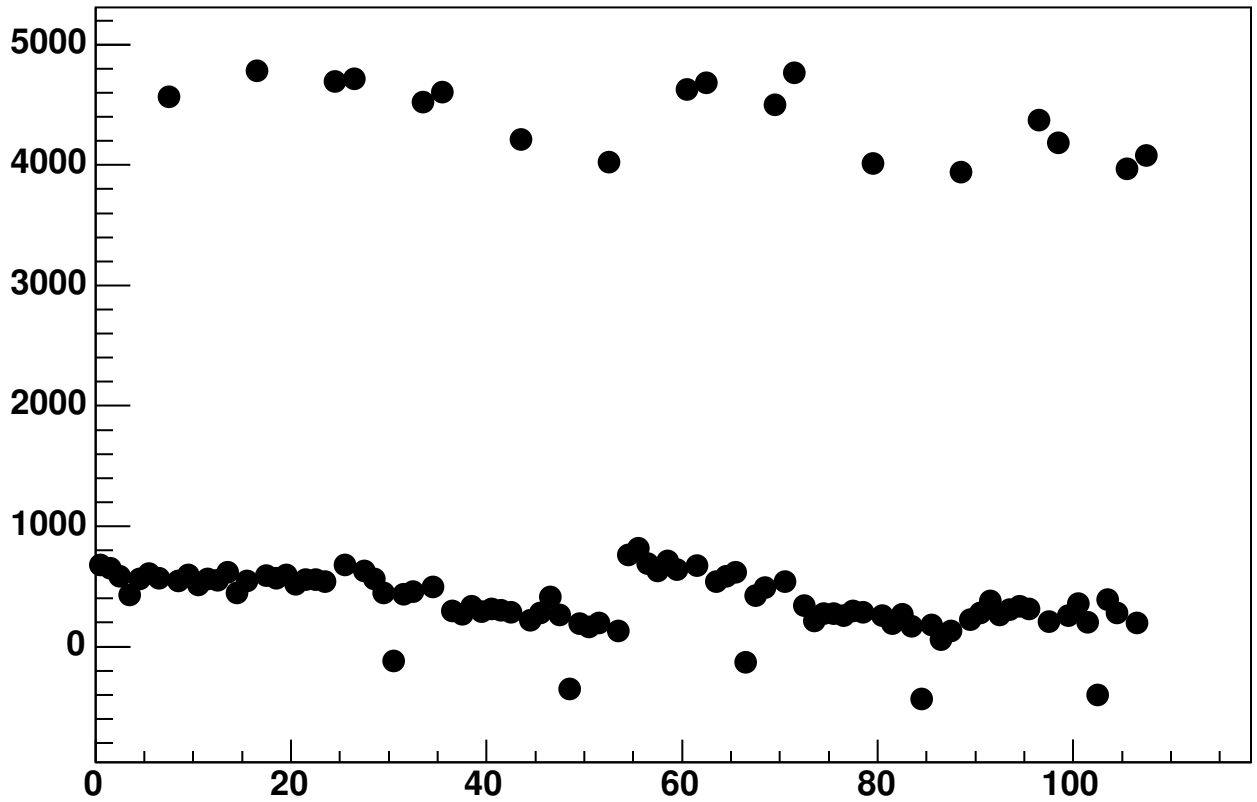
Enable 5, DAC=4000, Hold=90, ADC Mean vs 18*Chip+Chan



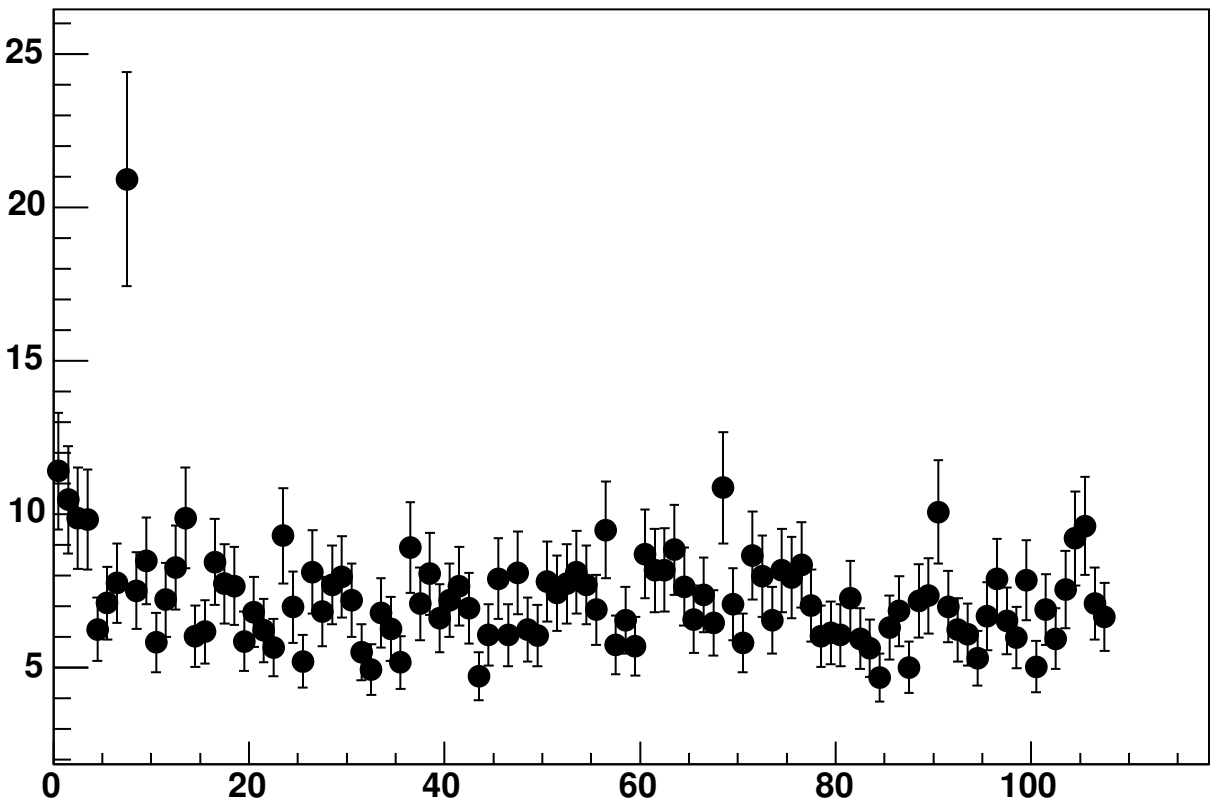
Enable 5, DAC=4000, Hold=90, ADC Noise vs 18*Chip+Chan



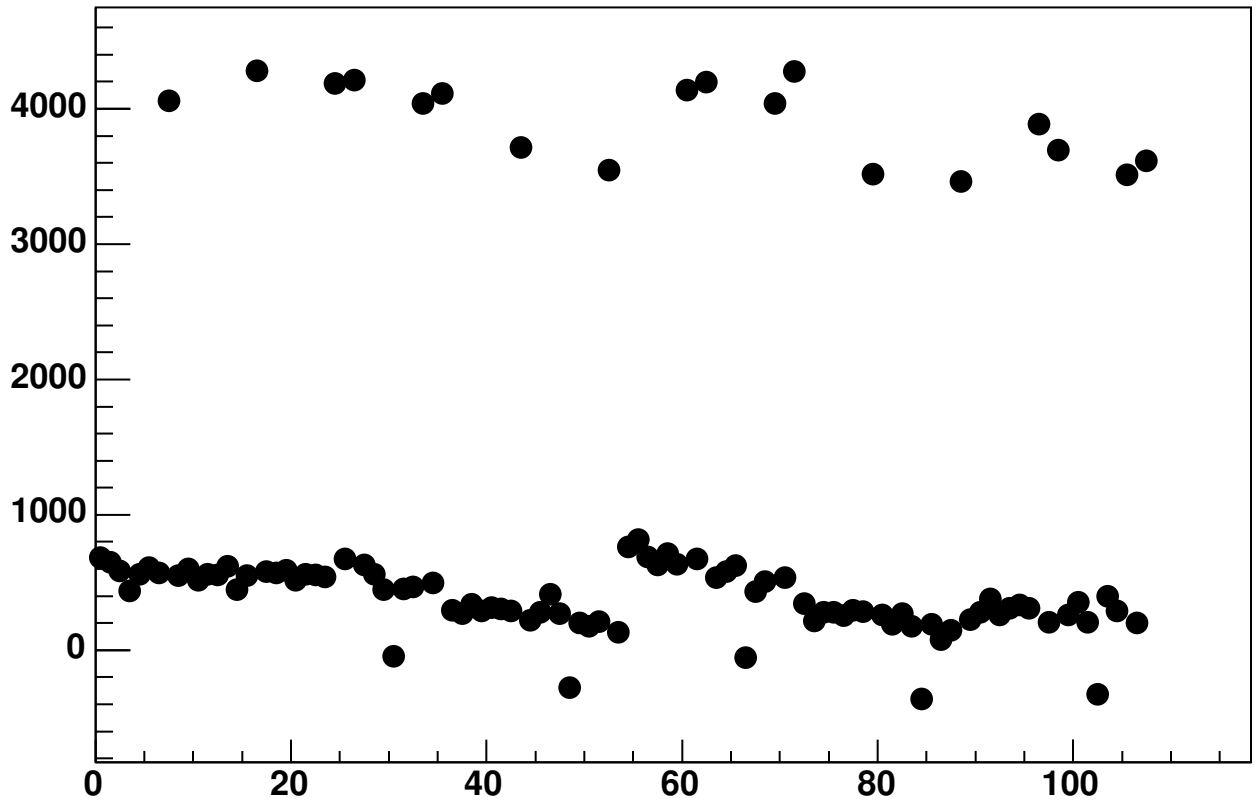
Enable 5, DAC=4000, Hold=95, ADC Mean vs 18*Chip+Chan



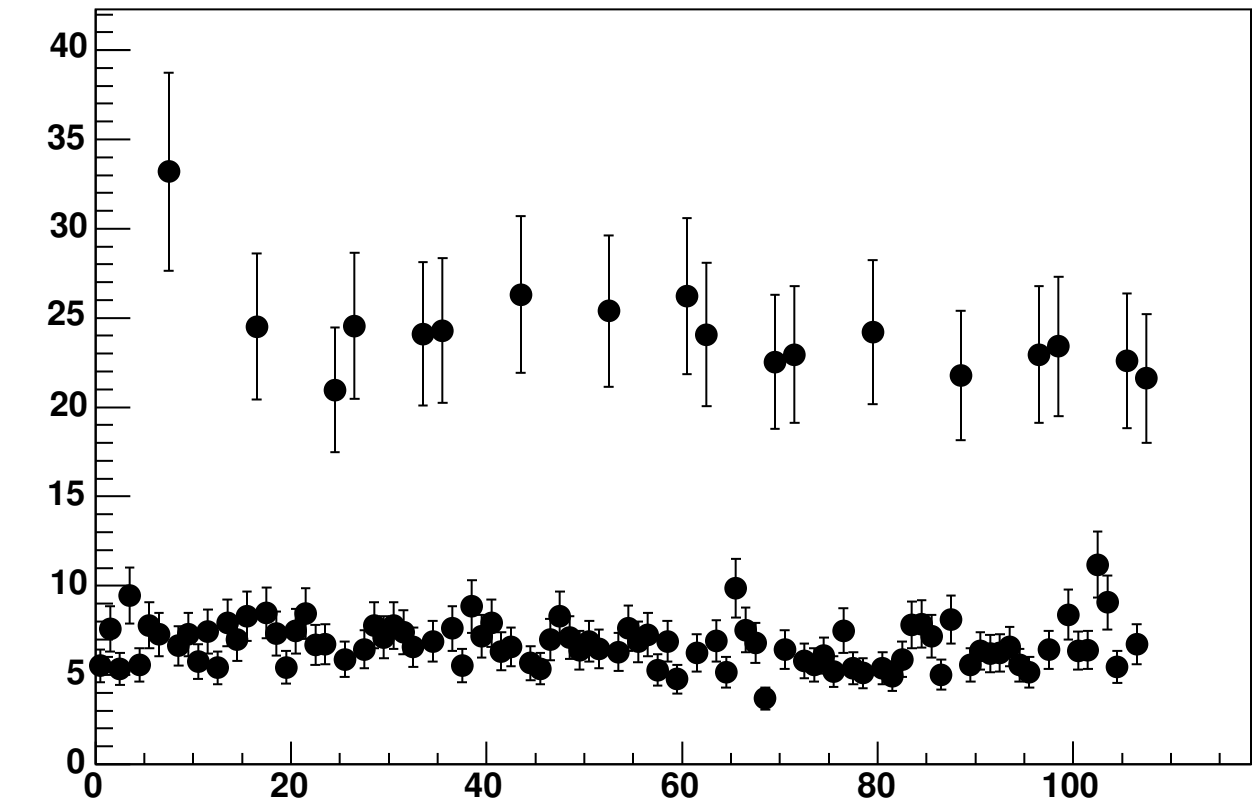
Enable 5, DAC=4000, Hold=95, ADC Noise vs 18*Chip+Chan



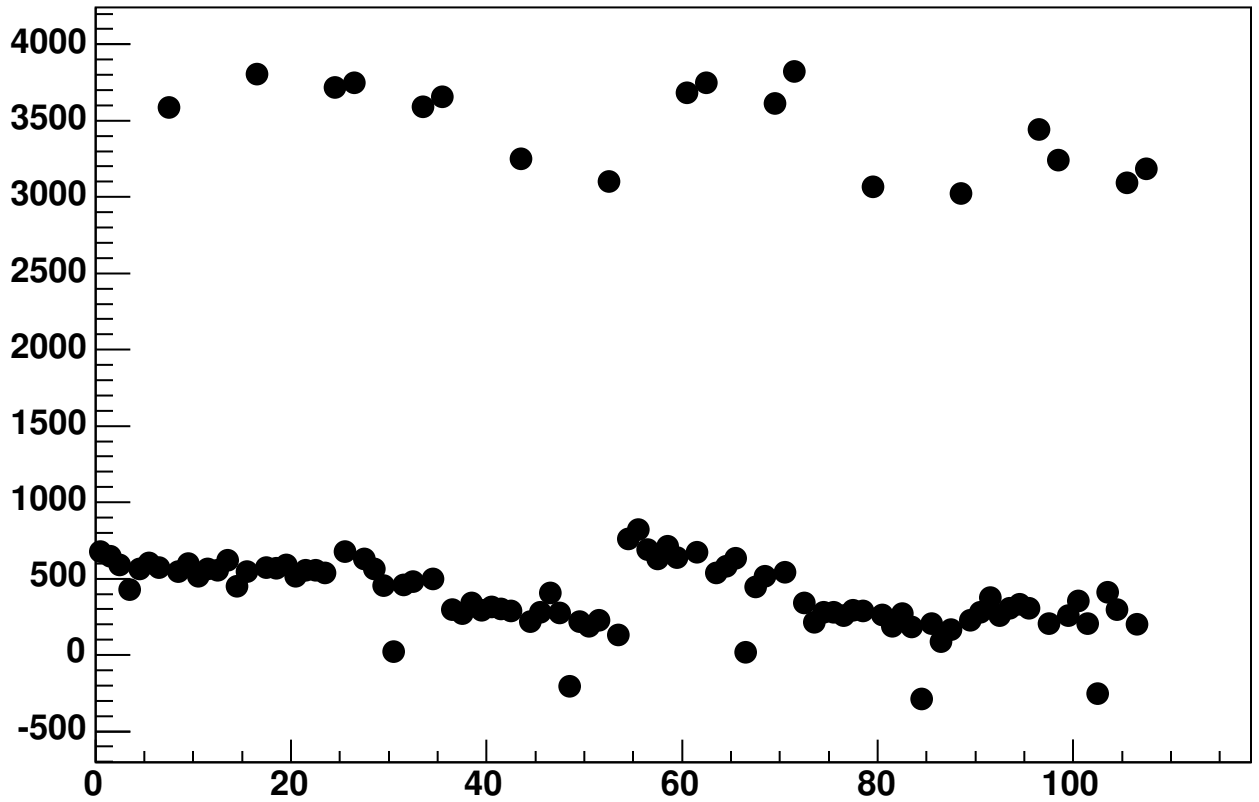
Enable 5, DAC=4000, Hold=100, ADC Mean vs 18*Chip+Chan



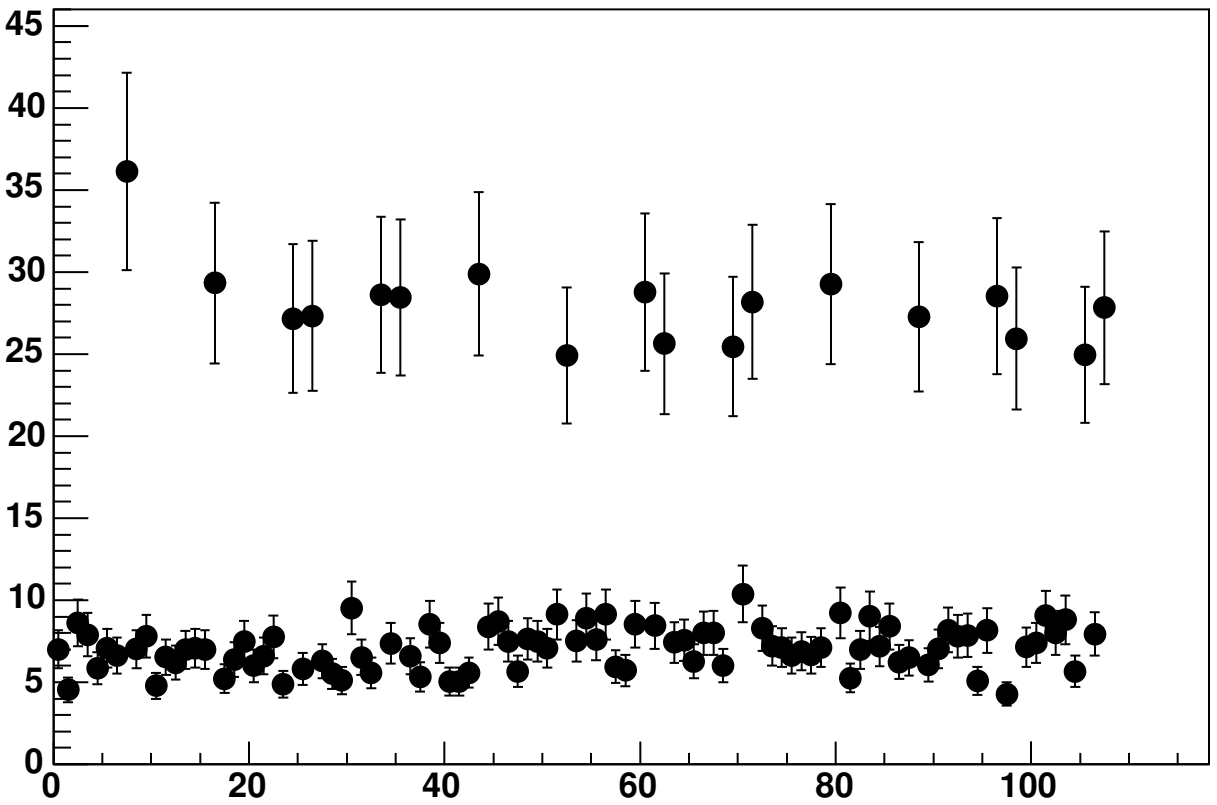
Enable 5, DAC=4000, Hold=100, ADC Noise vs 18*Chip+Chan



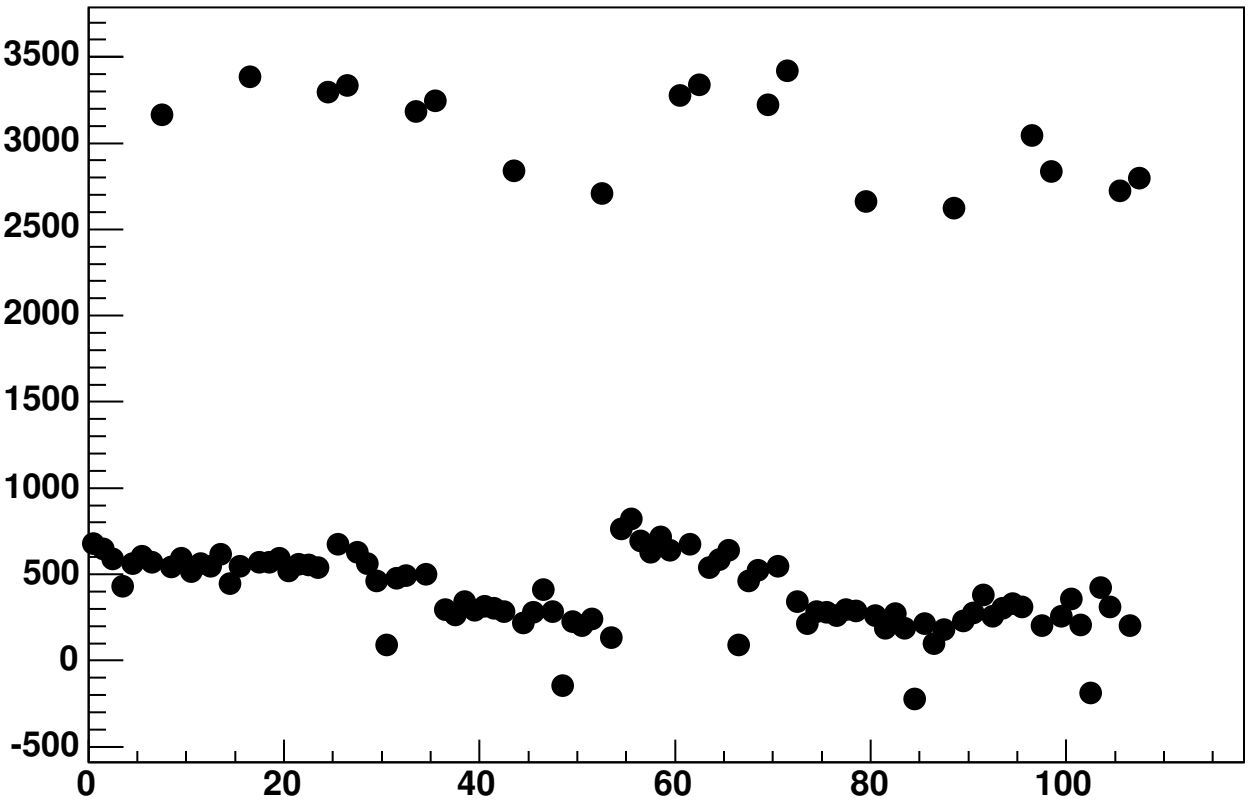
Enable 5, DAC=4000, Hold=105, ADC Mean vs 18*Chip+Chan



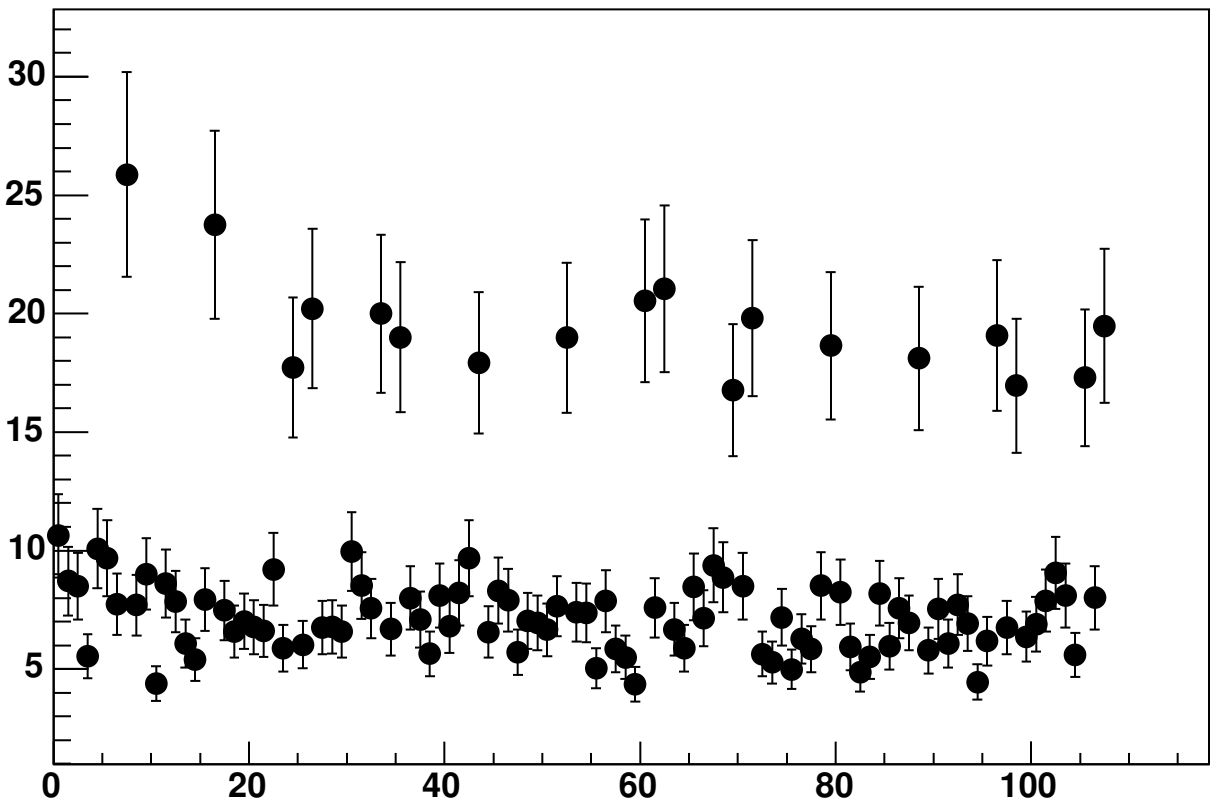
Enable 5, DAC=4000, Hold=105, ADC Noise vs 18*Chip+Chan



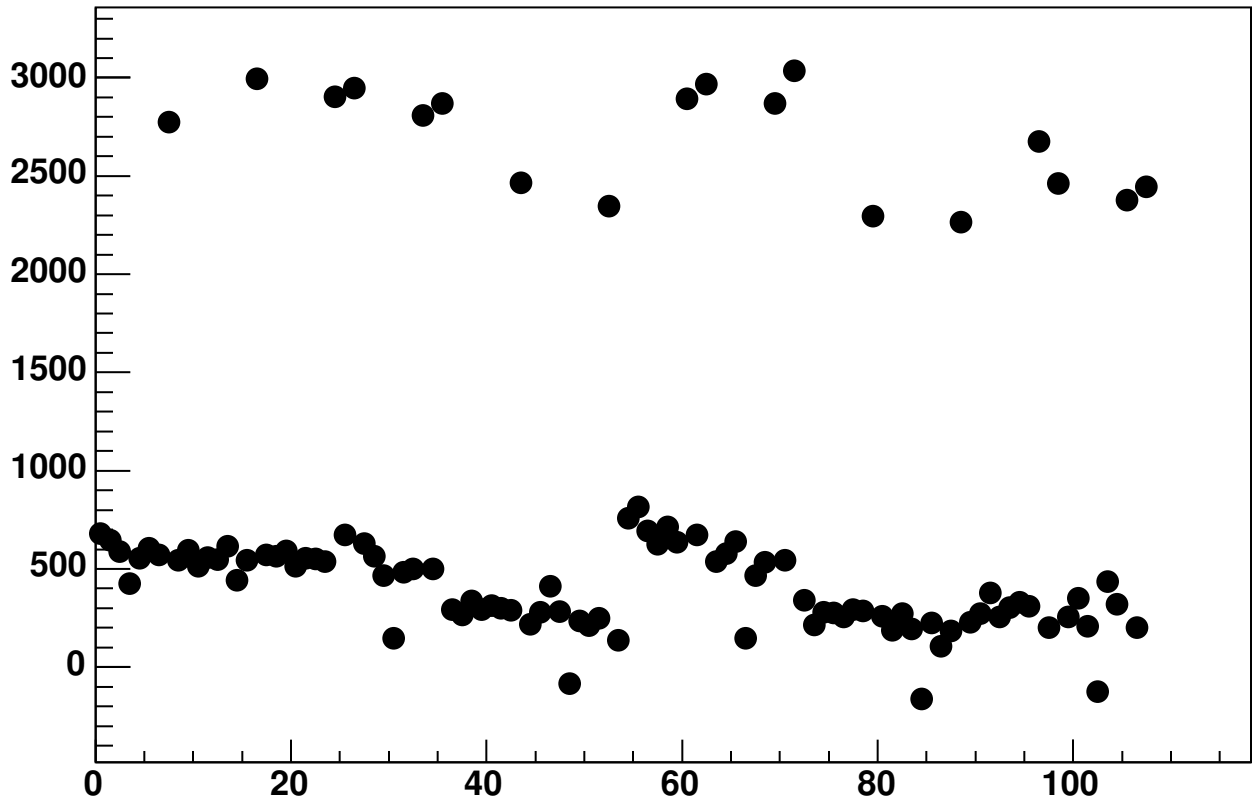
Enable 5, DAC=4000, Hold=110, ADC Mean vs 18*Chip+Chan



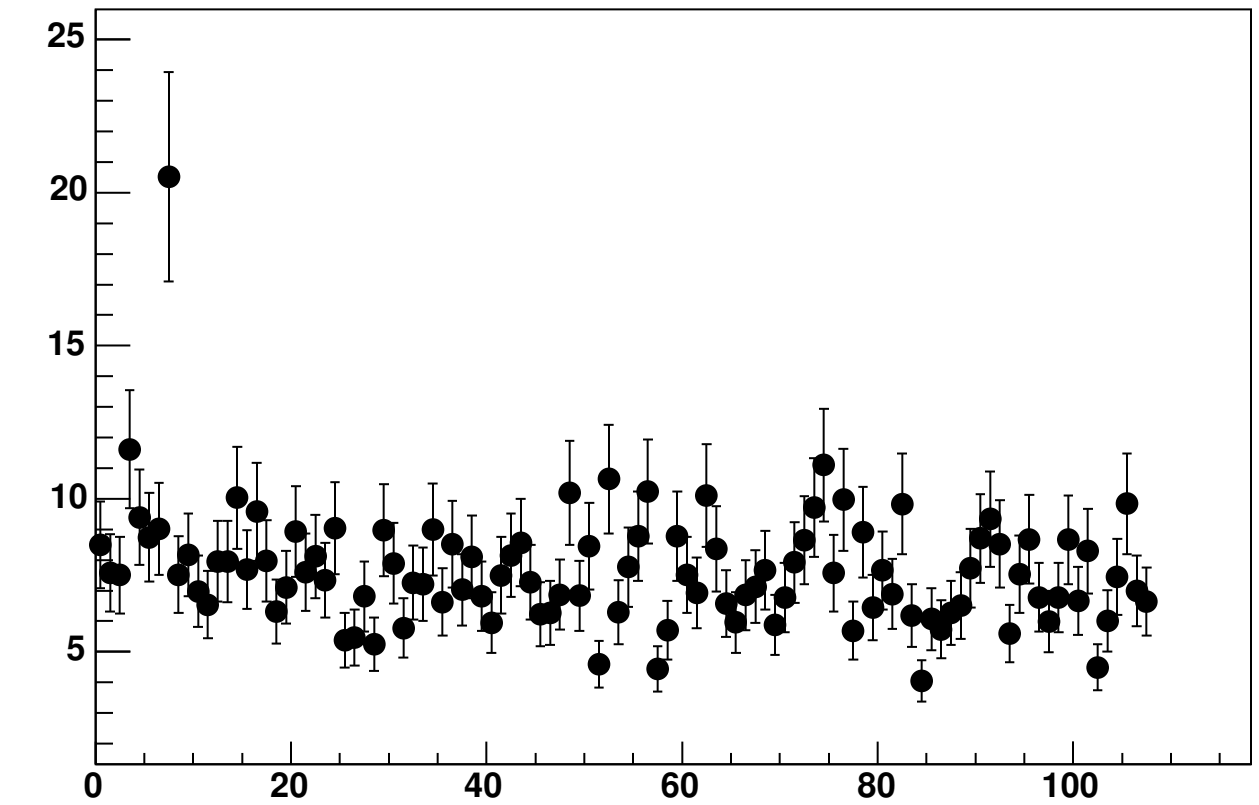
Enable 5, DAC=4000, Hold=110, ADC Noise vs 18*Chip+Chan



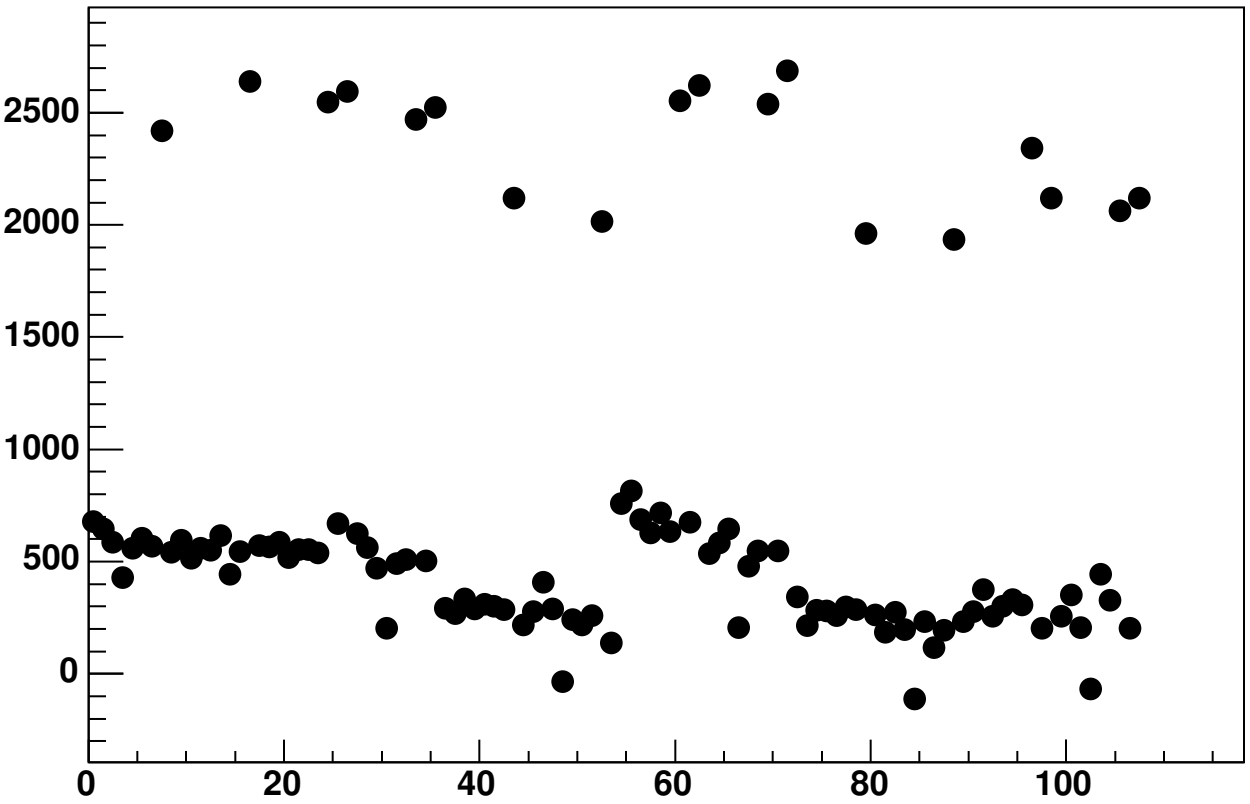
Enable 5, DAC=4000, Hold=115, ADC Mean vs 18*Chip+Chan



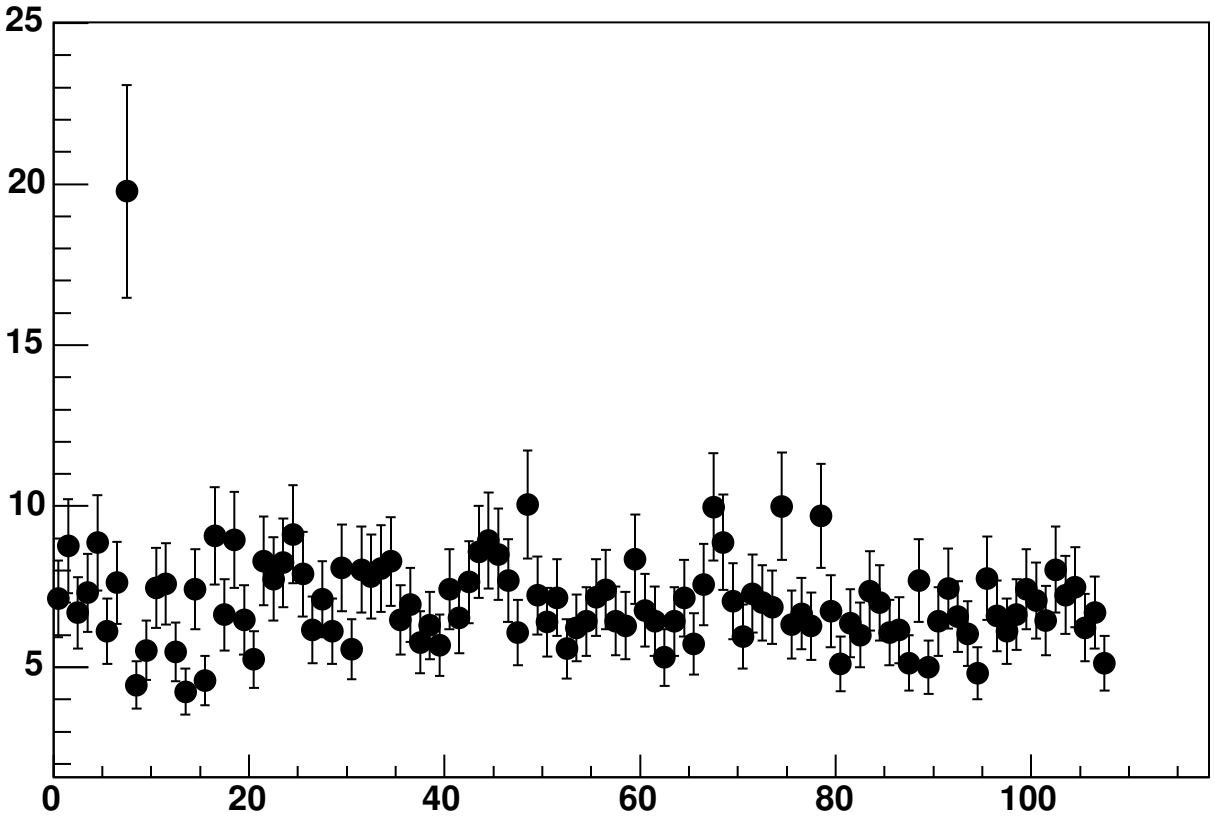
Enable 5, DAC=4000, Hold=115, ADC Noise vs 18*Chip+Chan



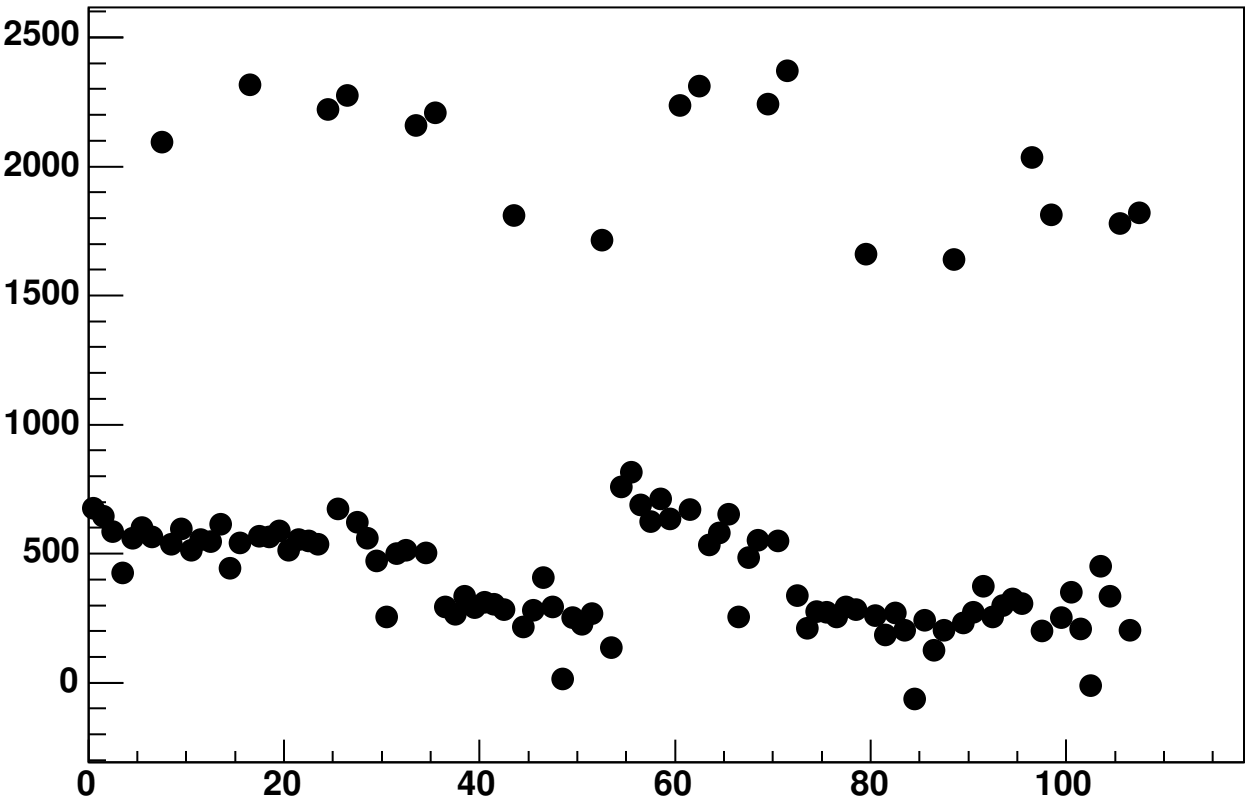
Enable 5, DAC=4000, Hold=120, ADC Mean vs 18*Chip+Chan



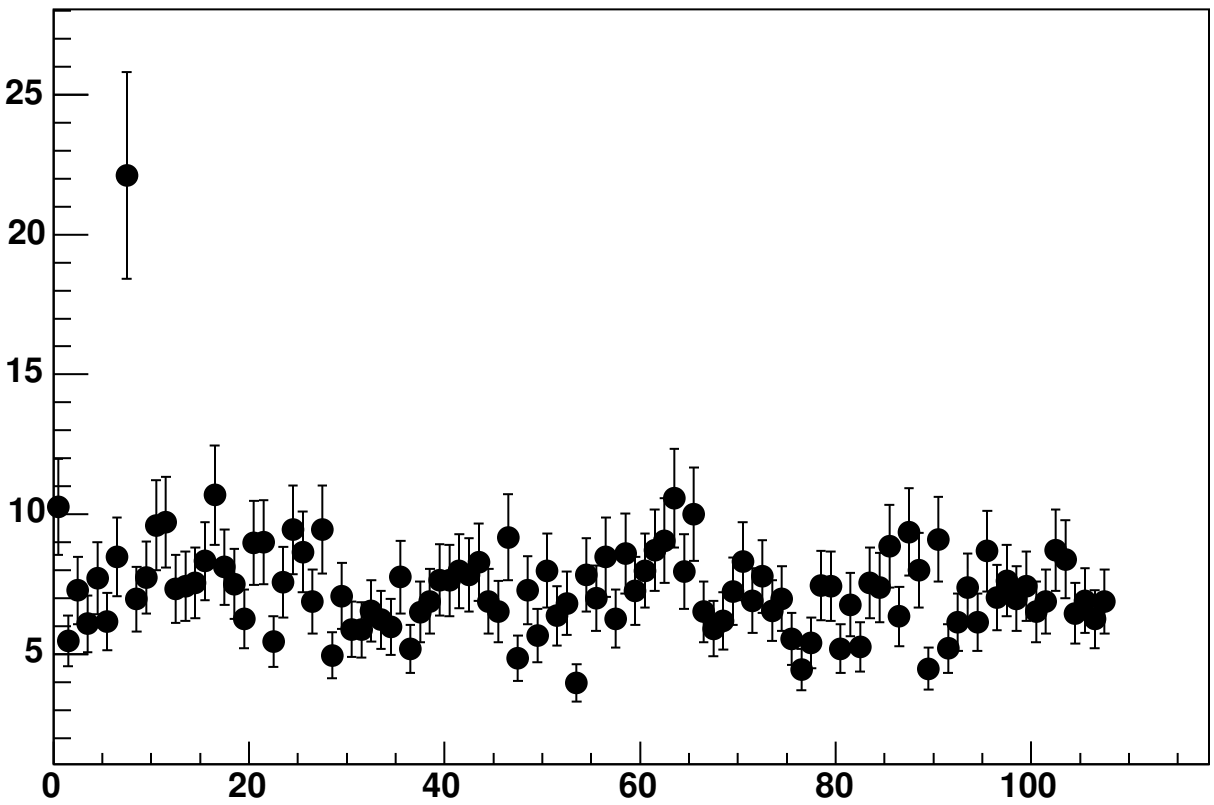
Enable 5, DAC=4000, Hold=120, ADC Noise vs 18*Chip+Chan



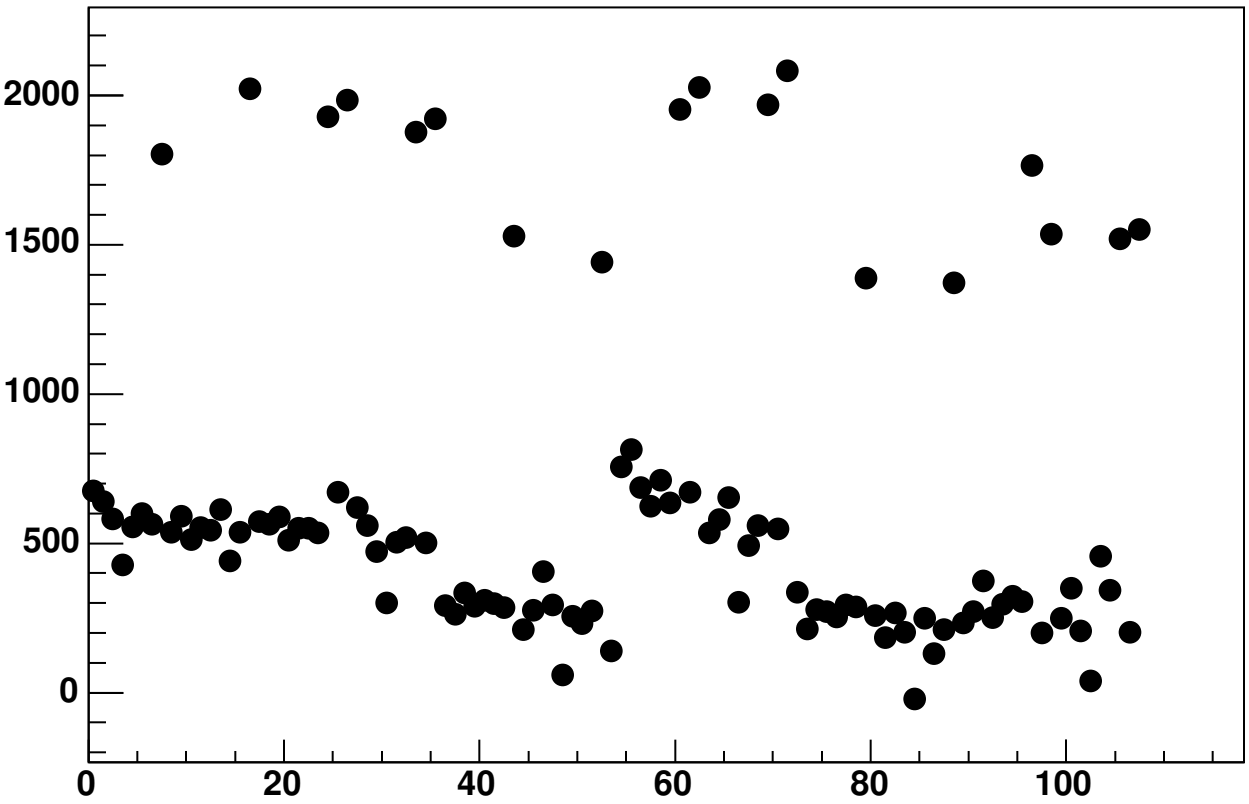
Enable 5, DAC=4000, Hold=125, ADC Mean vs 18*Chip+Chan



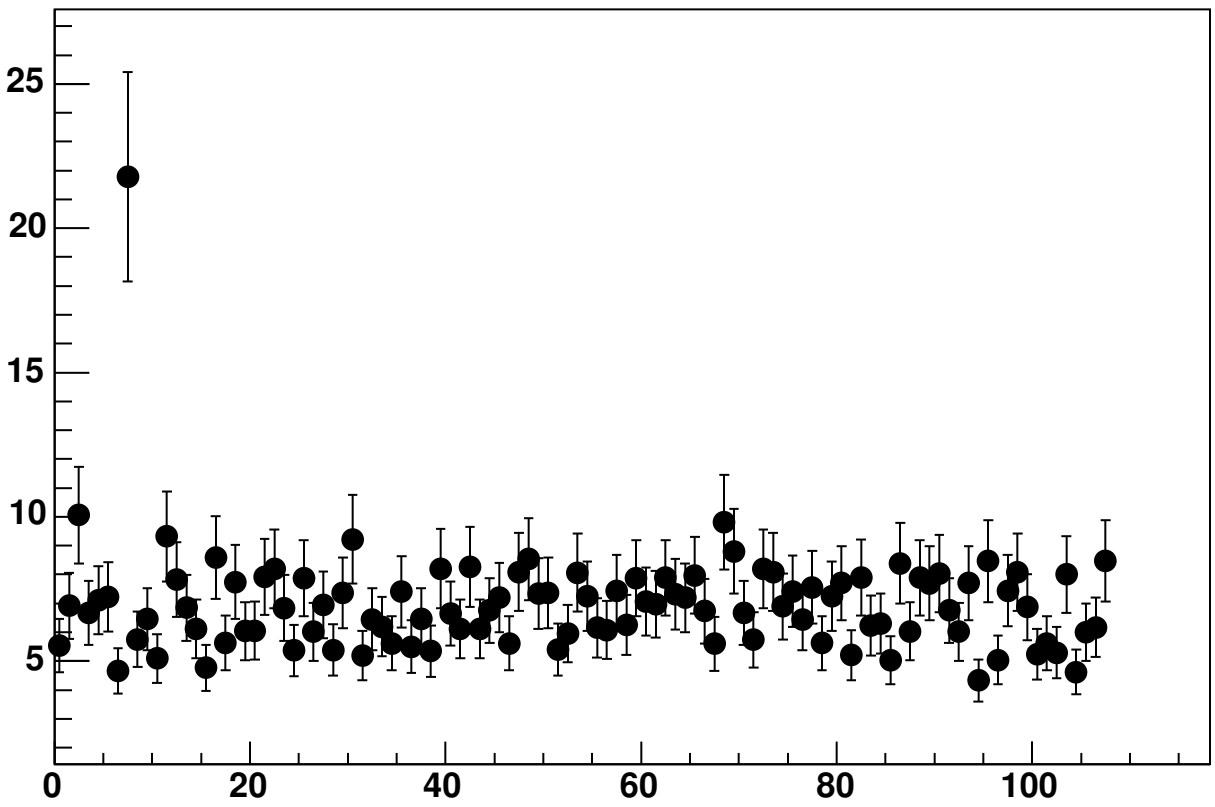
Enable 5, DAC=4000, Hold=125, ADC Noise vs 18*Chip+Chan



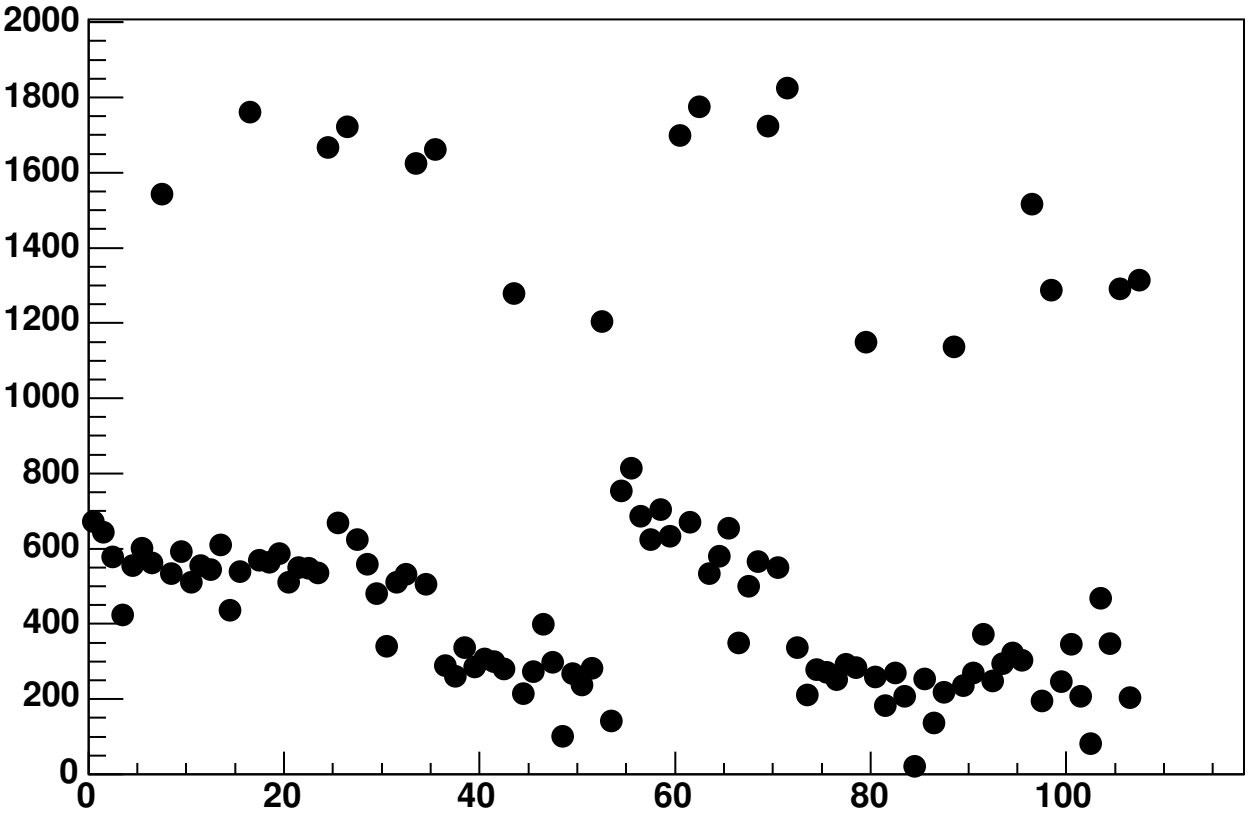
Enable 5, DAC=4000, Hold=130, ADC Mean vs 18*Chip+Chan



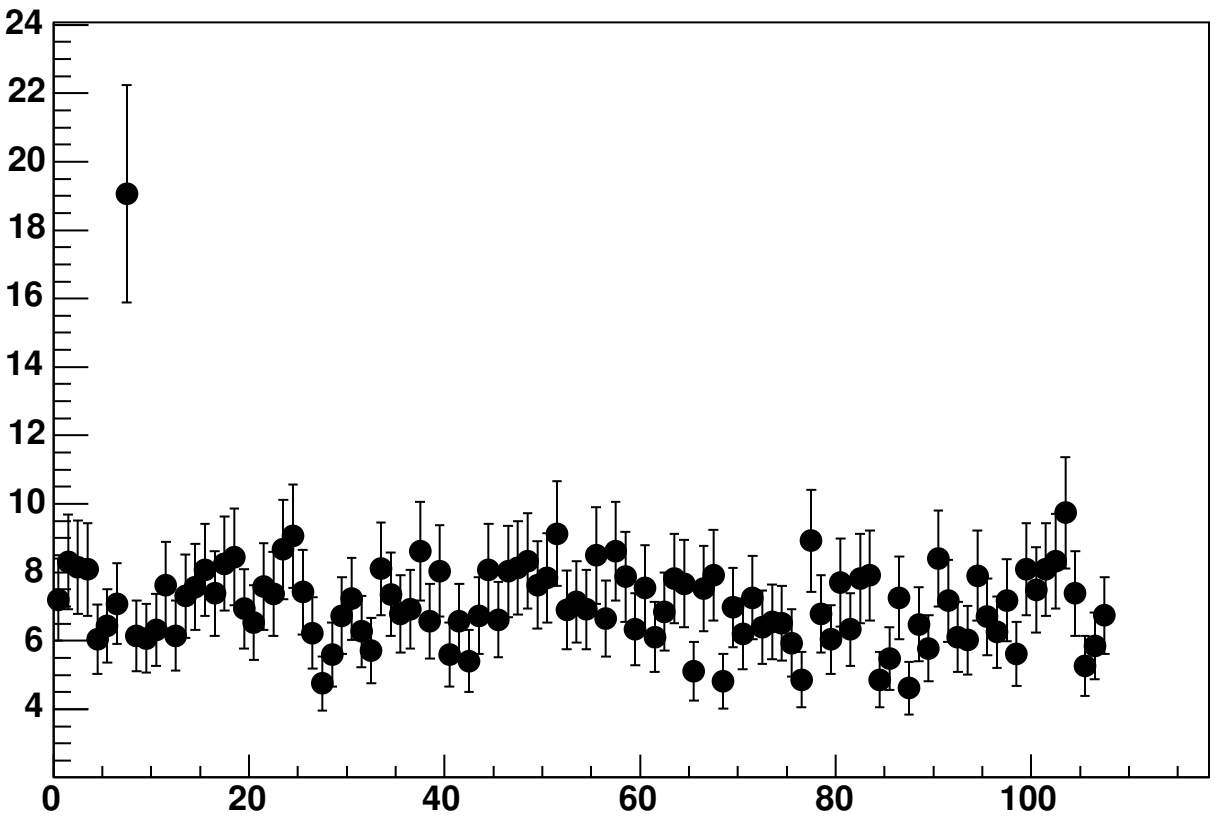
Enable 5, DAC=4000, Hold=130, ADC Noise vs 18*Chip+Chan



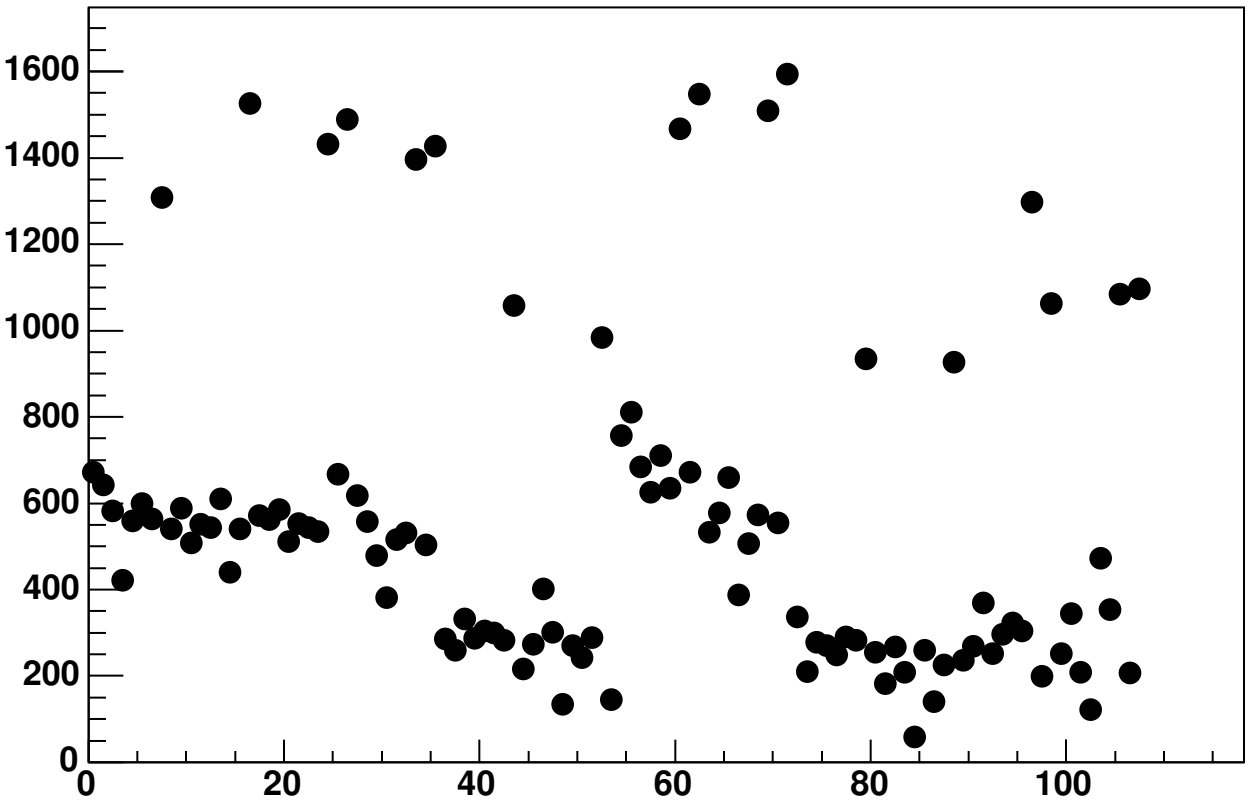
Enable 5, DAC=4000, Hold=135, ADC Mean vs 18*Chip+Chan



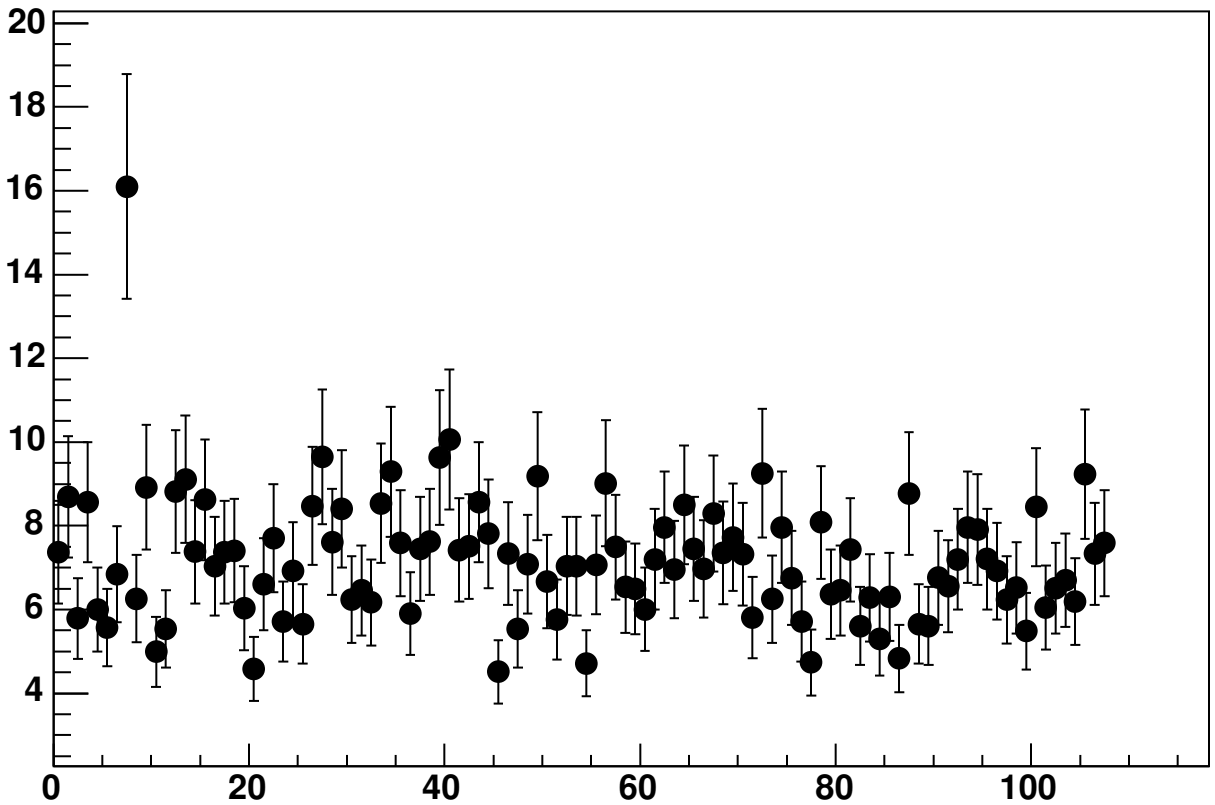
Enable 5, DAC=4000, Hold=135, ADC Noise vs 18*Chip+Chan



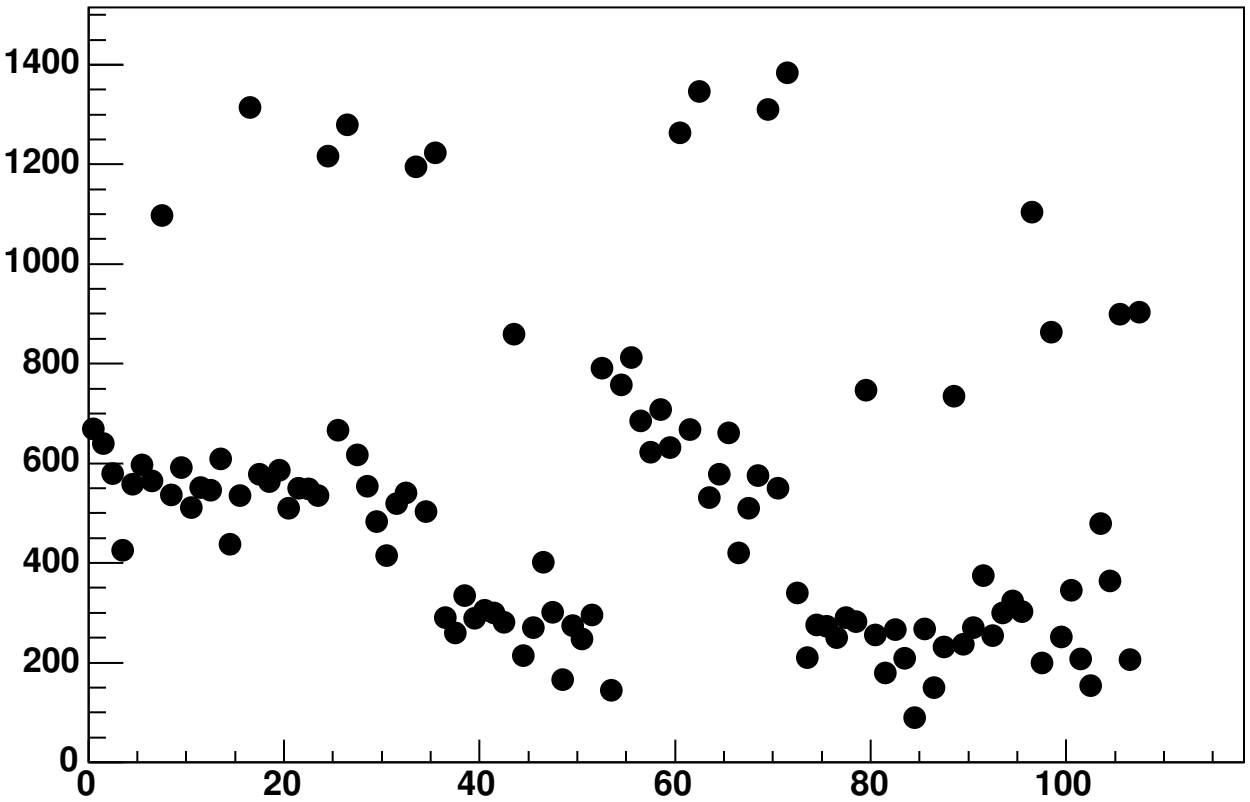
Enable 5, DAC=4000, Hold=140, ADC Mean vs 18*Chip+Chan



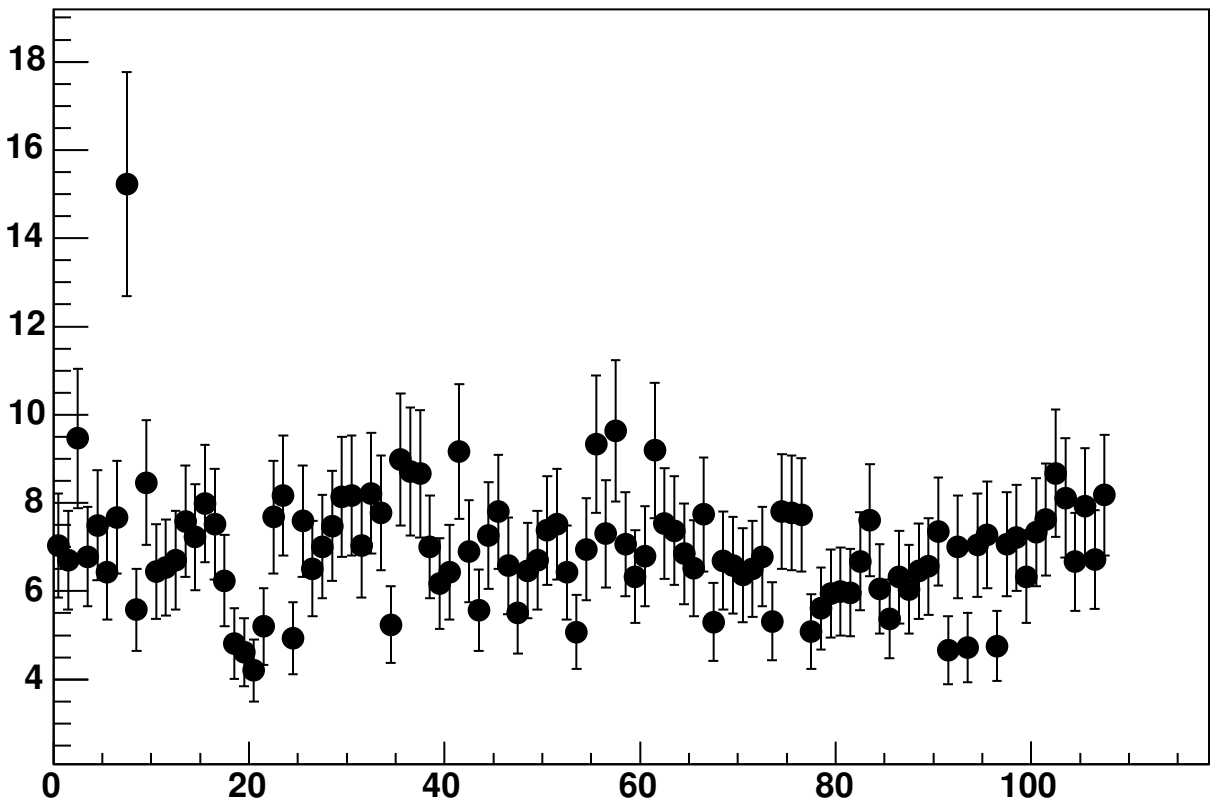
Enable 5, DAC=4000, Hold=140, ADC Noise vs 18*Chip+Chan



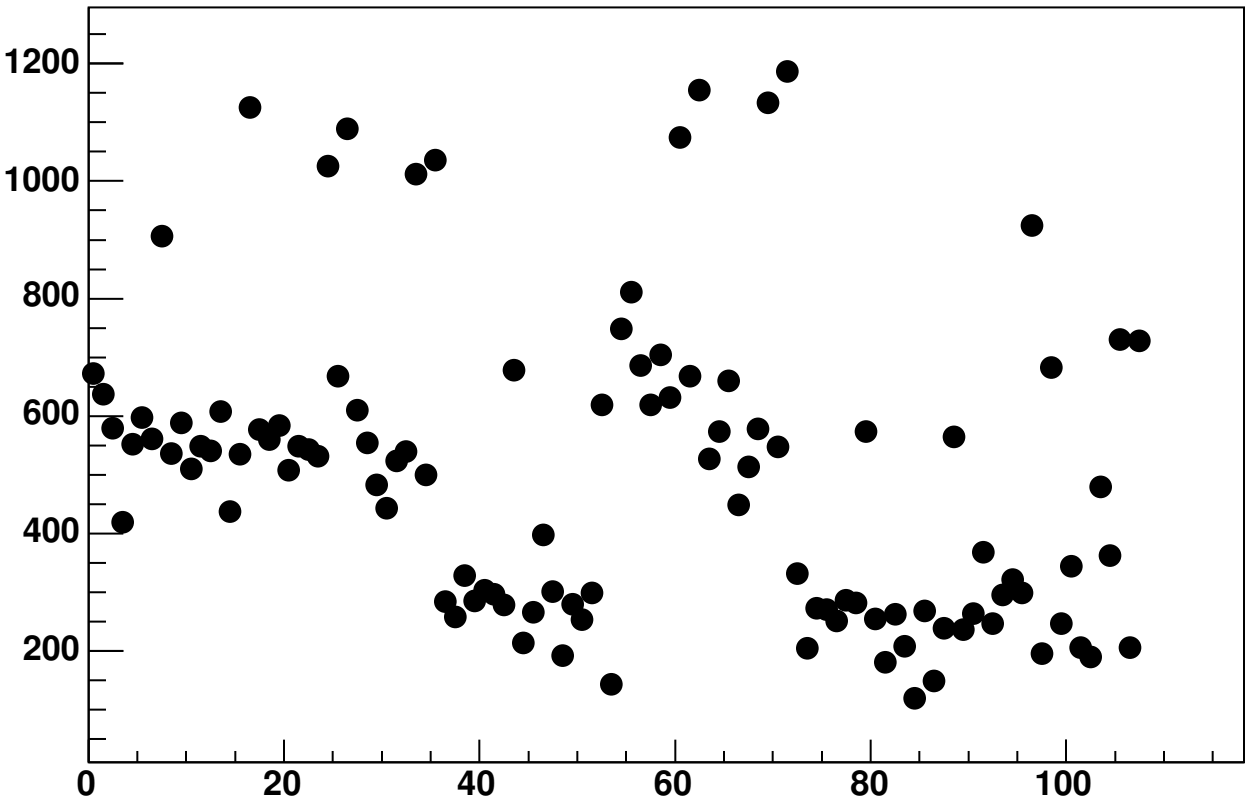
Enable 5, DAC=4000, Hold=145, ADC Mean vs 18*Chip+Chan



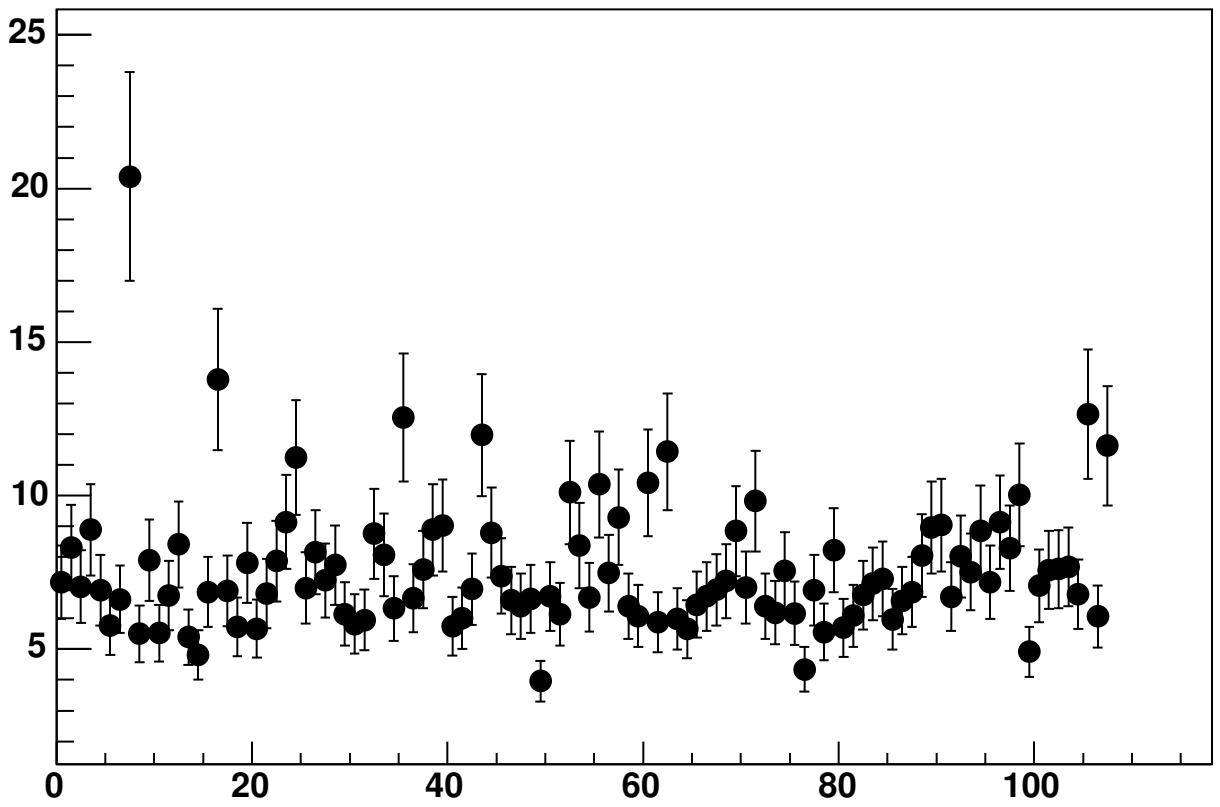
Enable 5, DAC=4000, Hold=145, ADC Noise vs 18*Chip+Chan



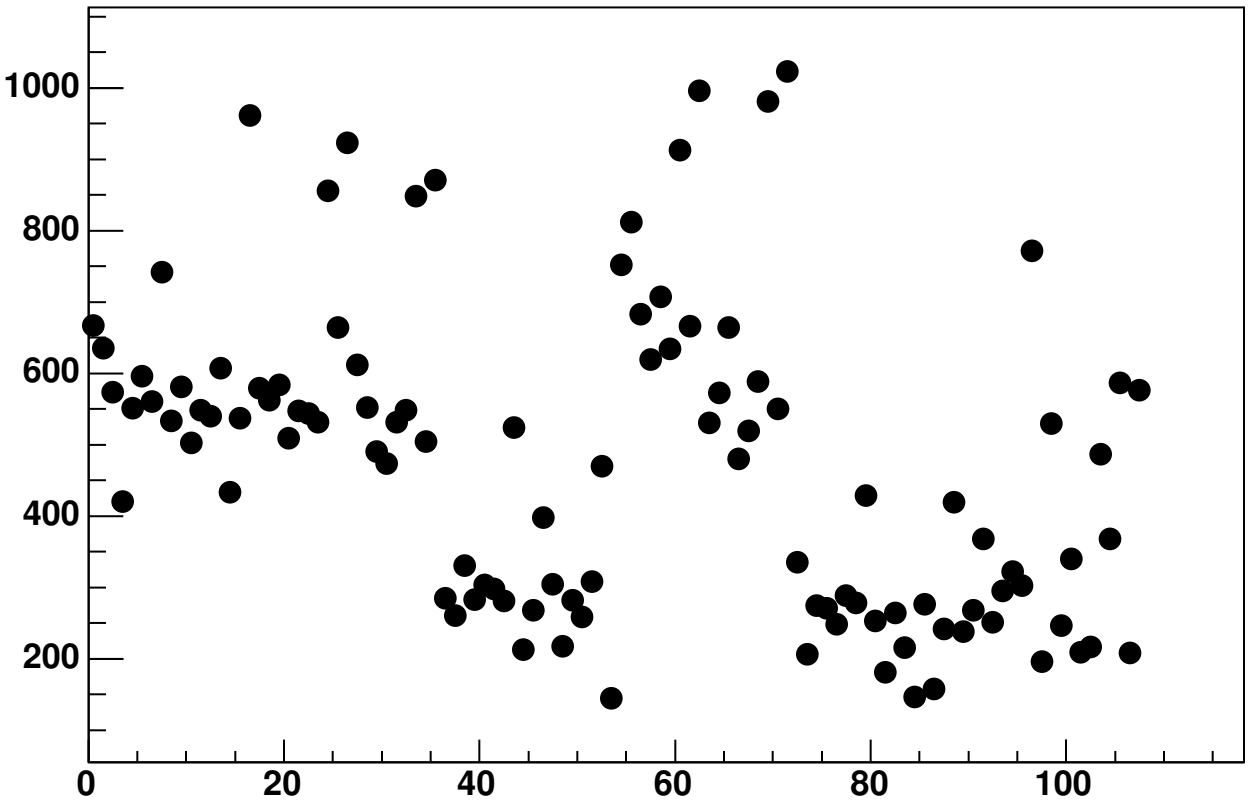
Enable 5, DAC=4000, Hold=150, ADC Mean vs 18*Chip+Chan



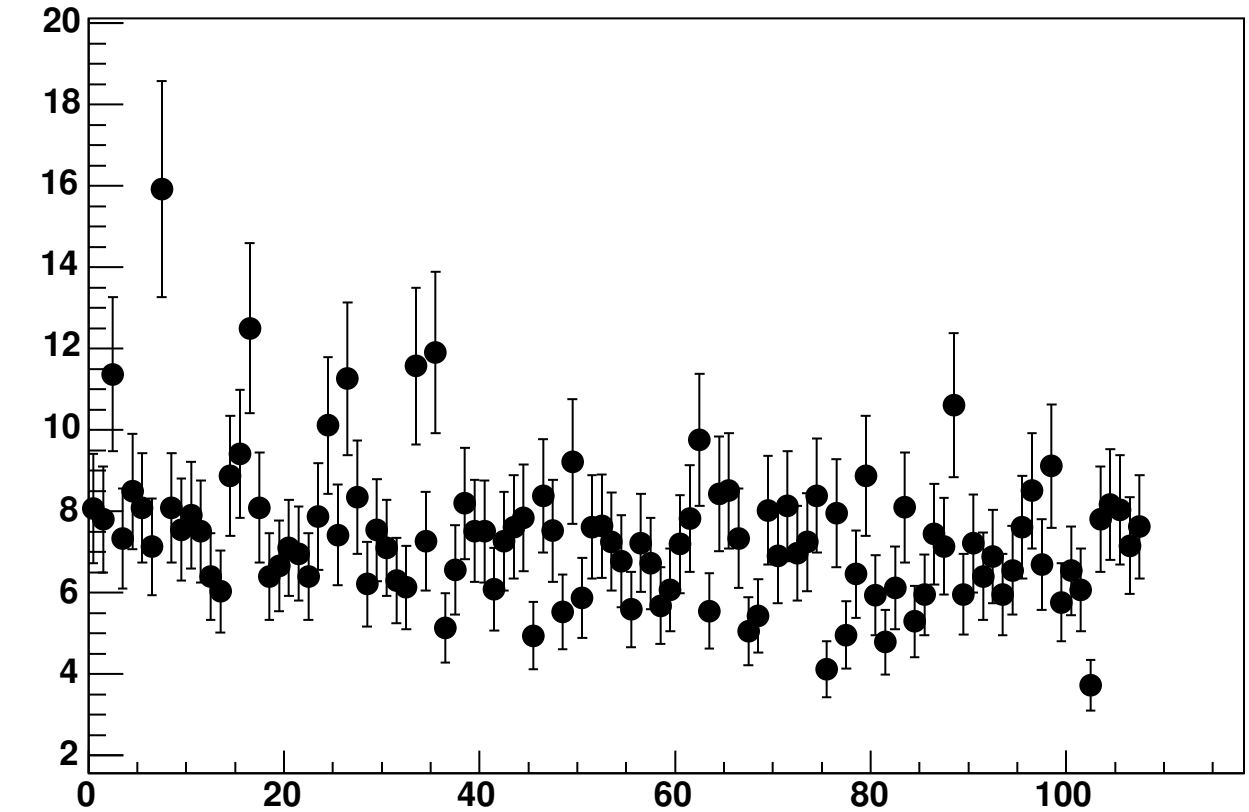
Enable 5, DAC=4000, Hold=150, ADC Noise vs 18*Chip+Chan



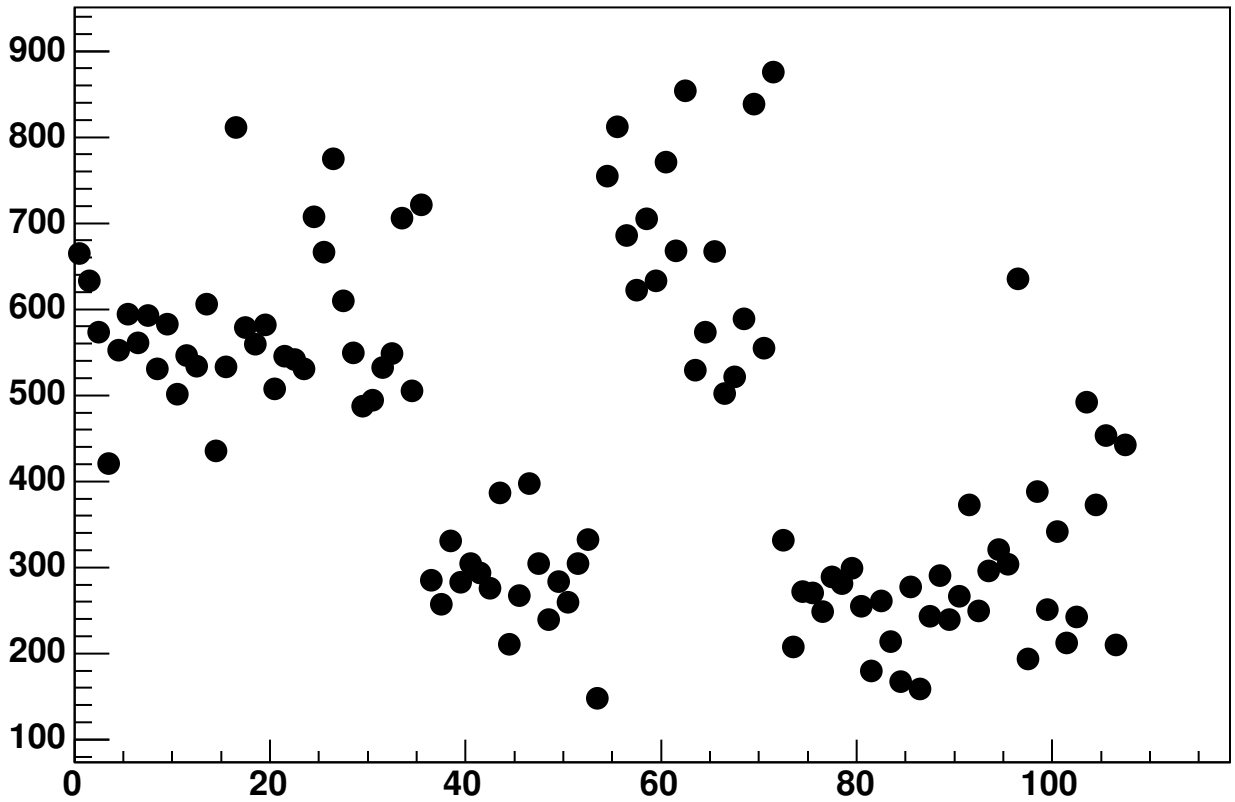
Enable 5, DAC=4000, Hold=155, ADC Mean vs 18*Chip+Chan



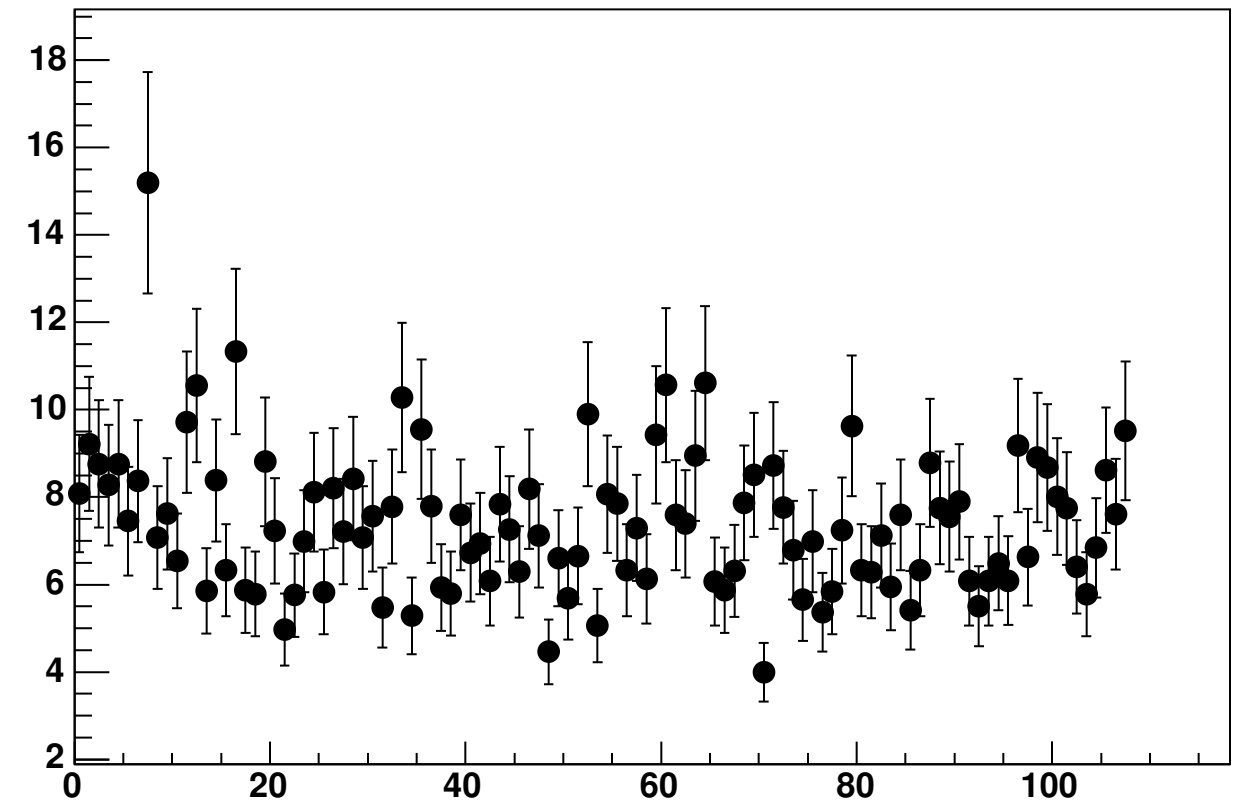
Enable 5, DAC=4000, Hold=155, ADC Noise vs 18*Chip+Chan



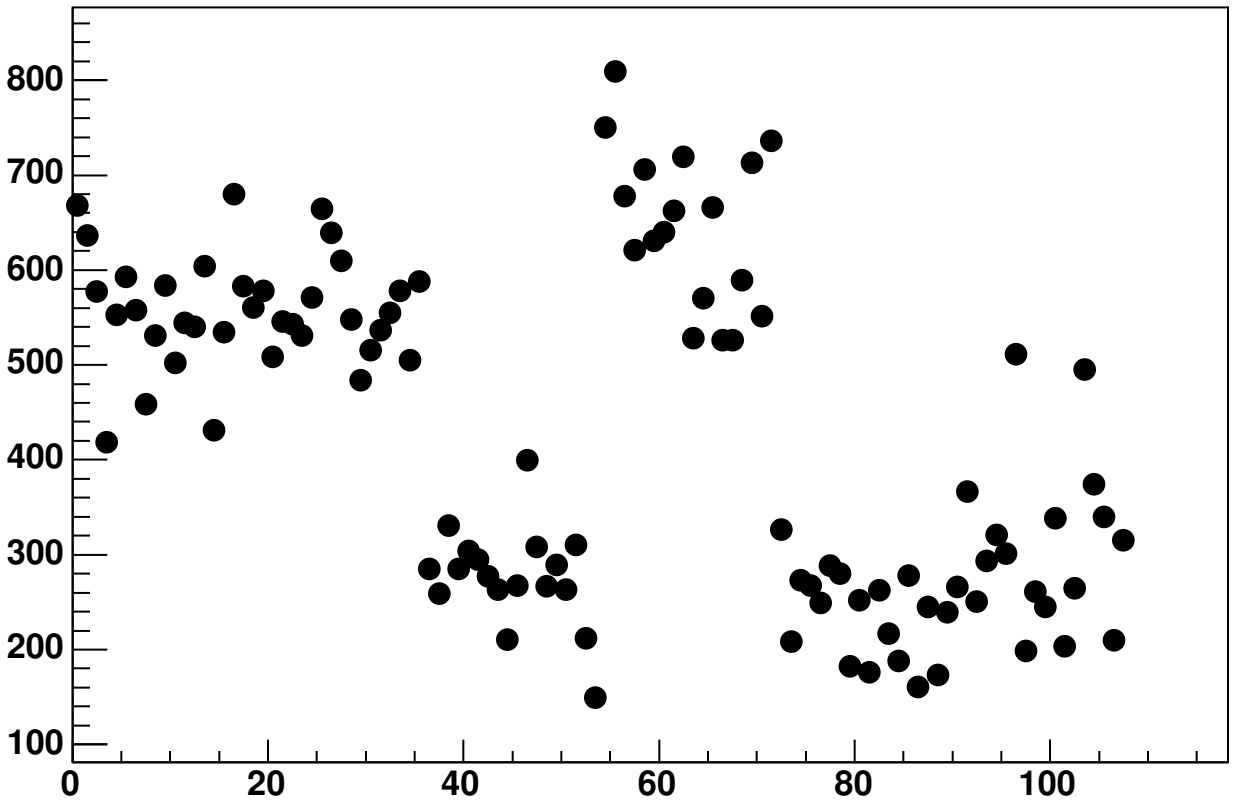
Enable 5, DAC=4000, Hold=160, ADC Mean vs 18*Chip+Chan



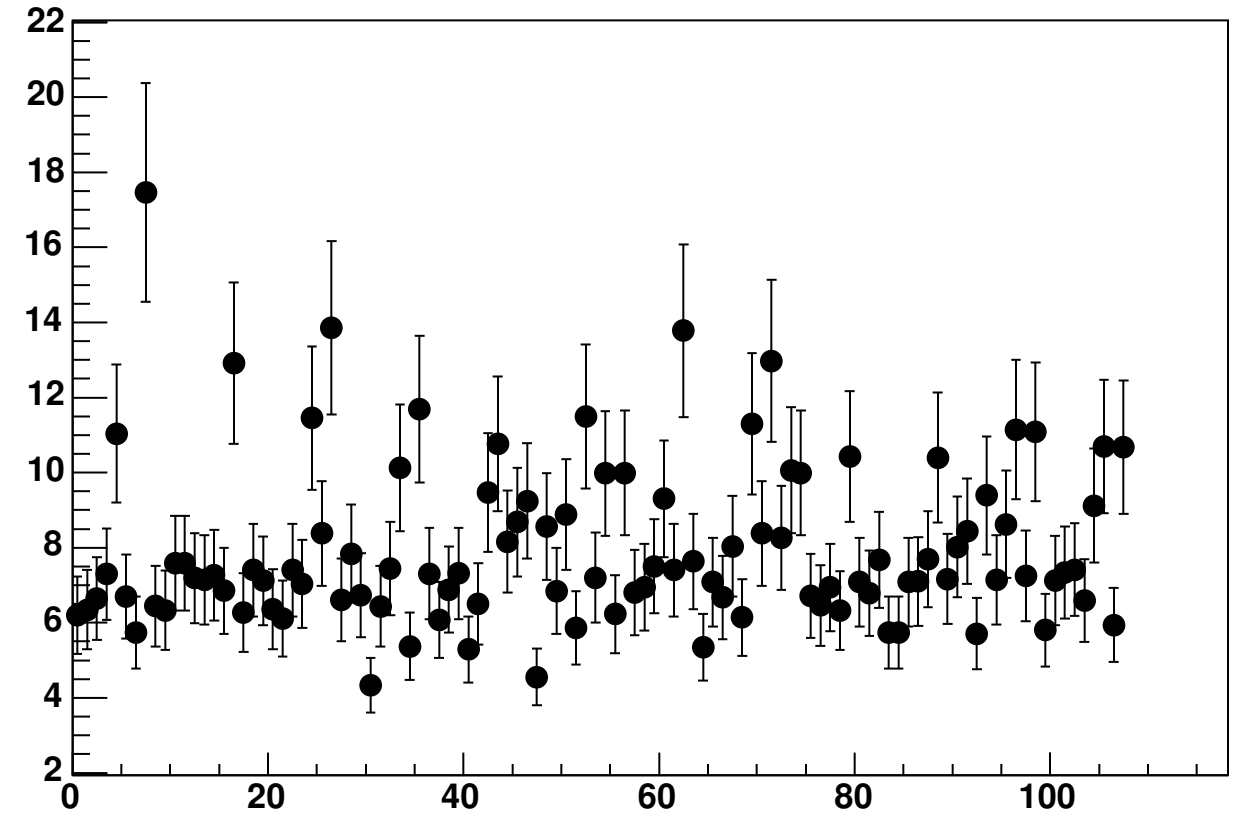
Enable 5, DAC=4000, Hold=160, ADC Noise vs 18*Chip+Chan



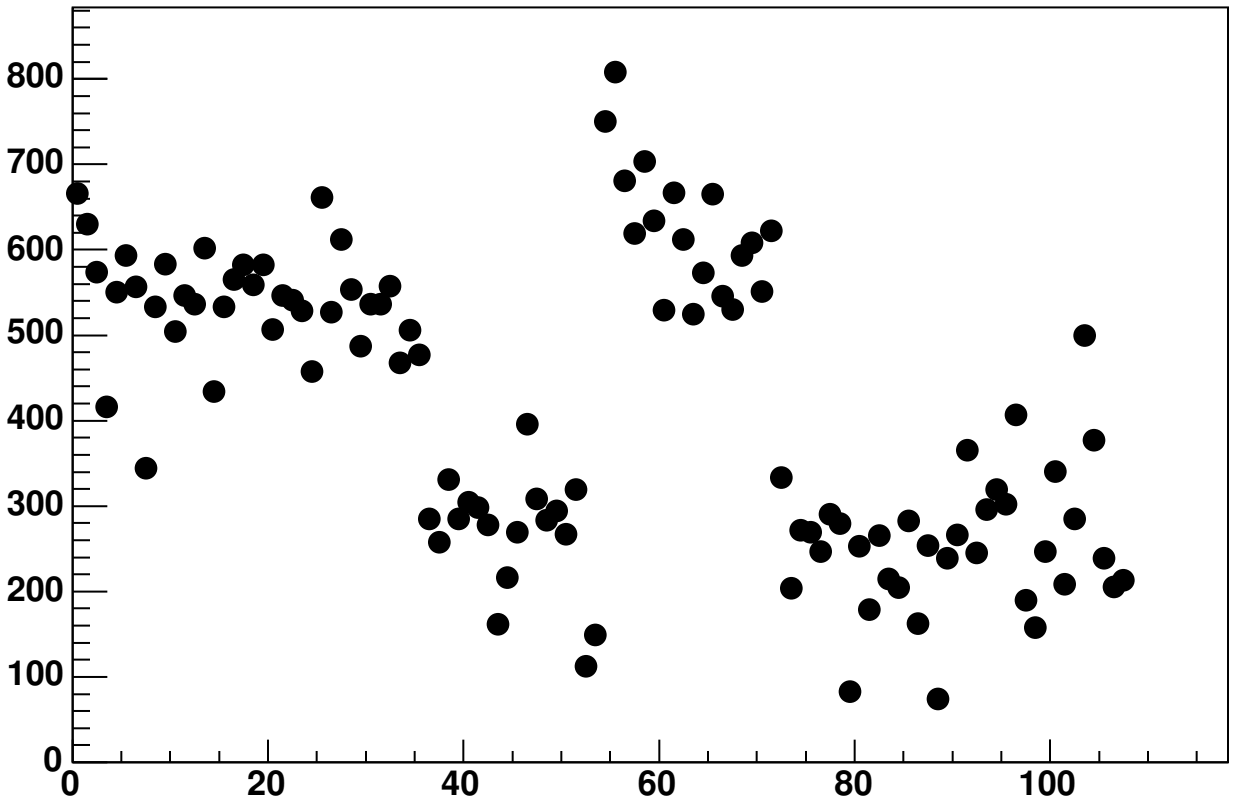
Enable 5, DAC=4000, Hold=165, ADC Mean vs 18*Chip+Chan



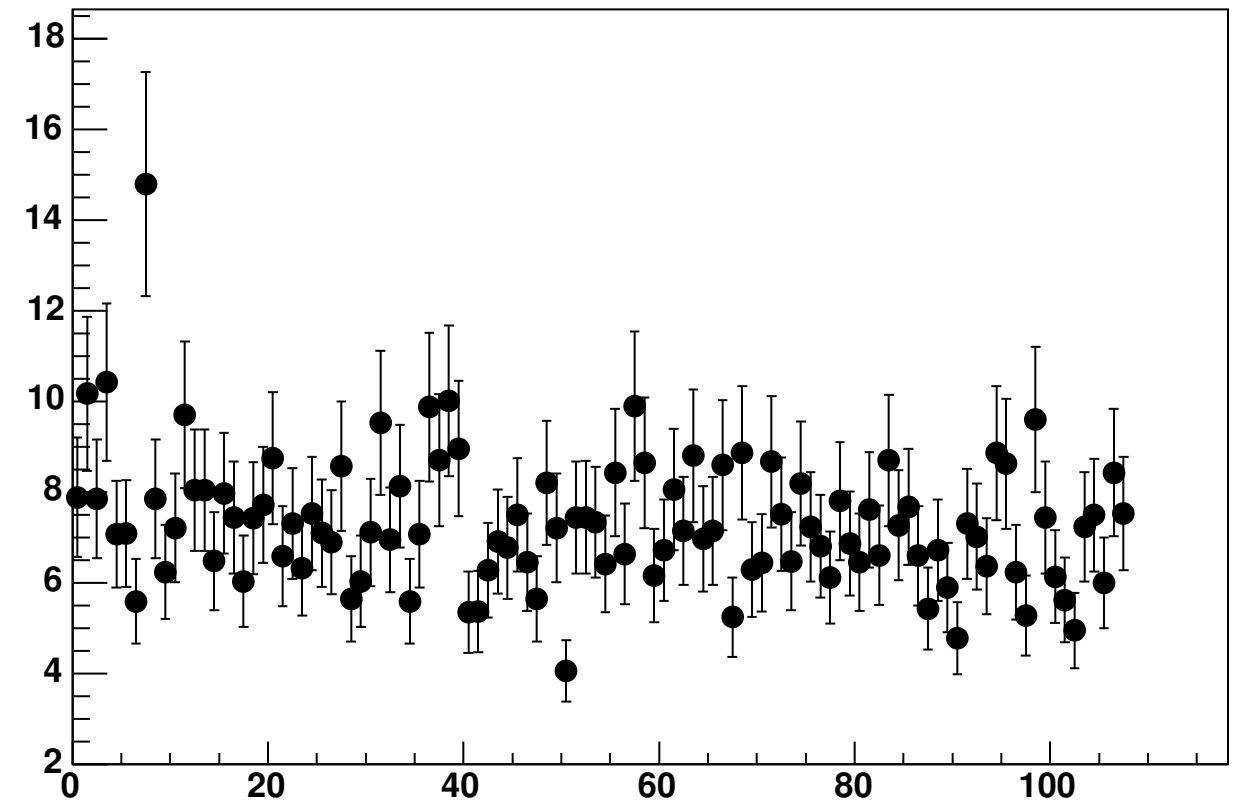
Enable 5, DAC=4000, Hold=165, ADC Noise vs 18*Chip+Chan



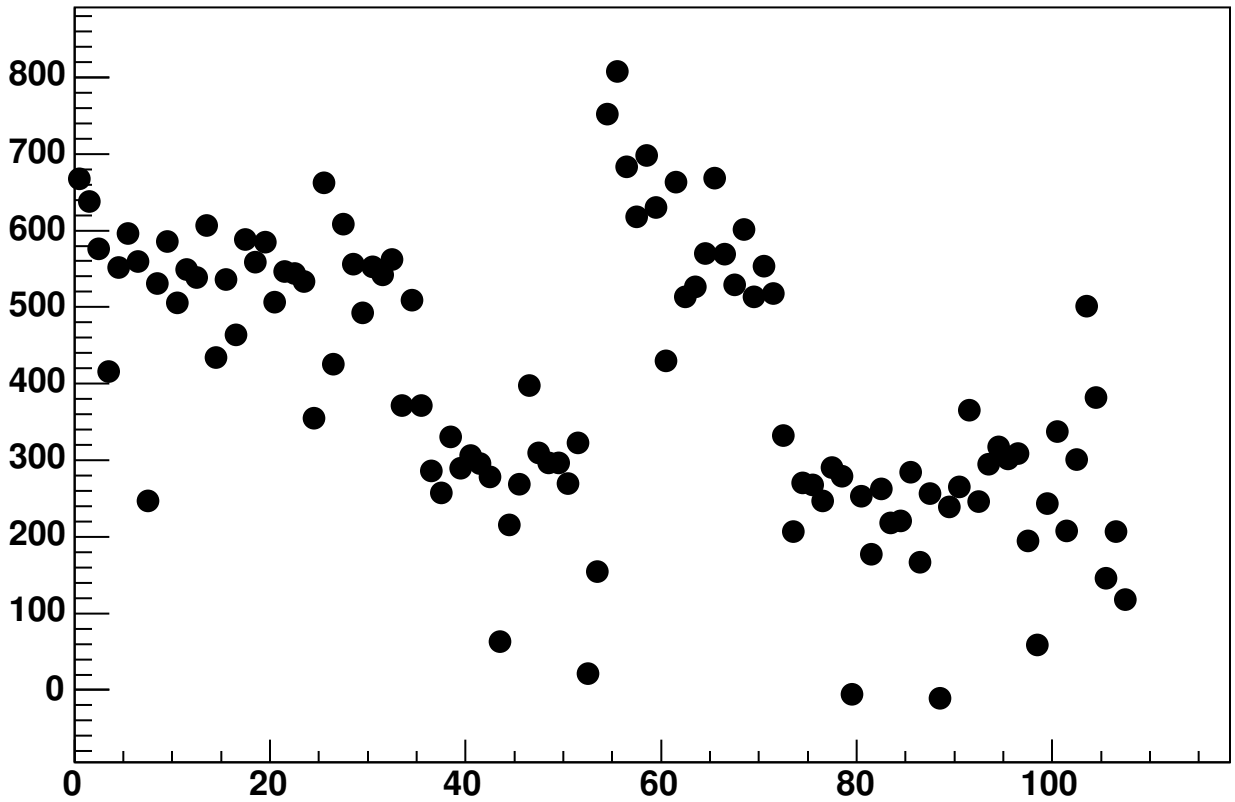
Enable 5, DAC=4000, Hold=170, ADC Mean vs 18*Chip+Chan



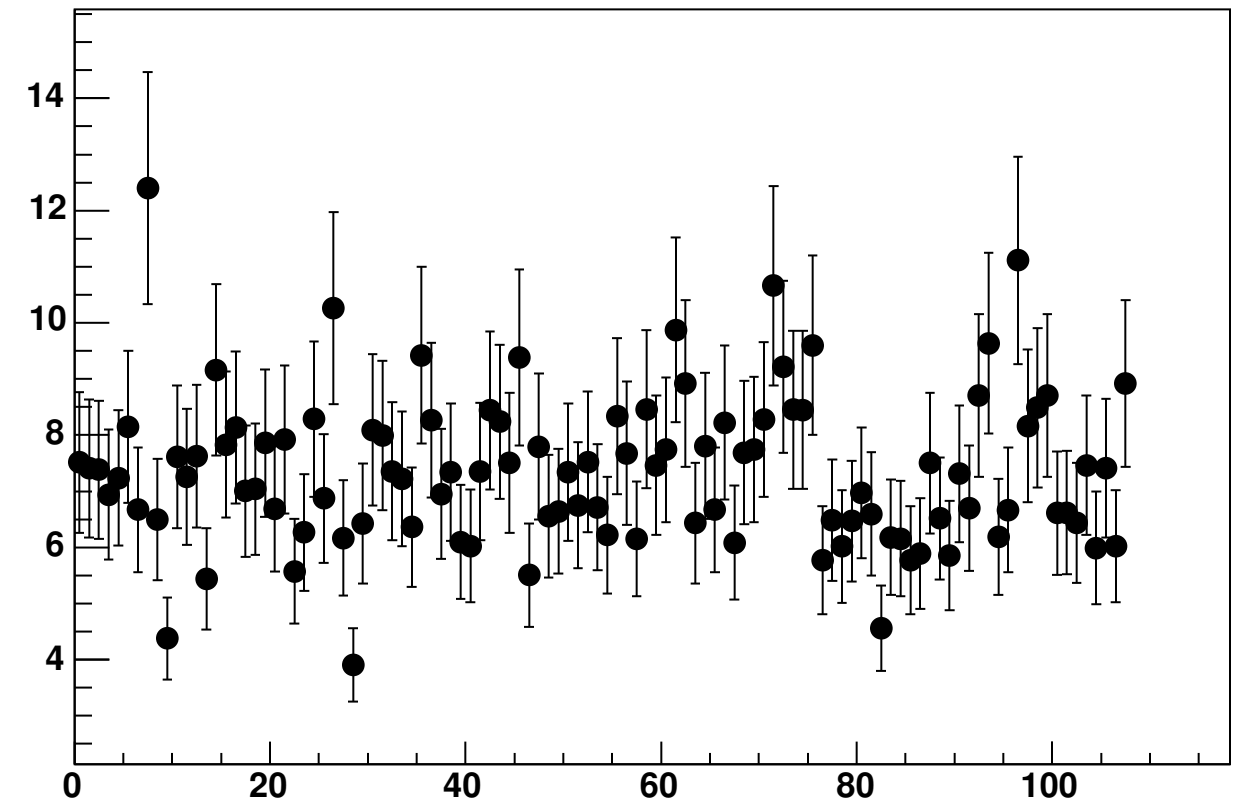
Enable 5, DAC=4000, Hold=170, ADC Noise vs 18*Chip+Chan



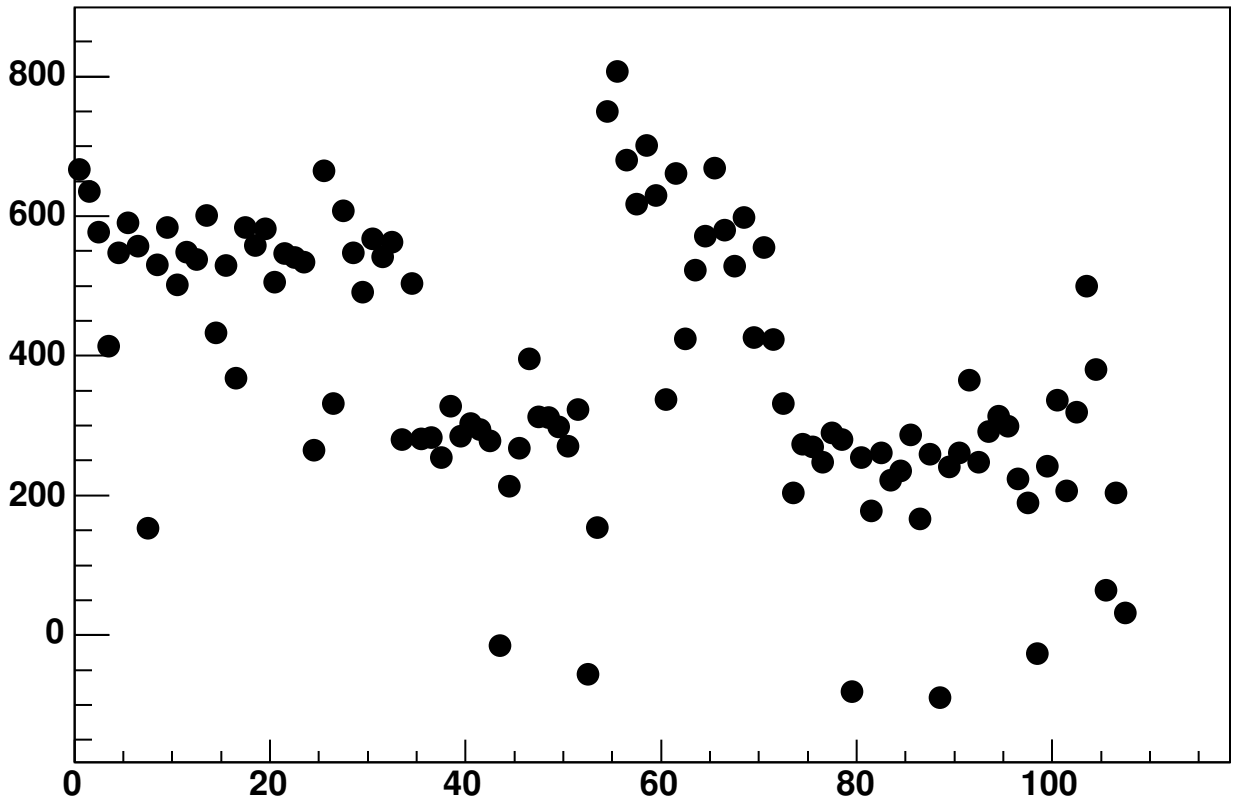
Enable 5, DAC=4000, Hold=175, ADC Mean vs 18*Chip+Chan



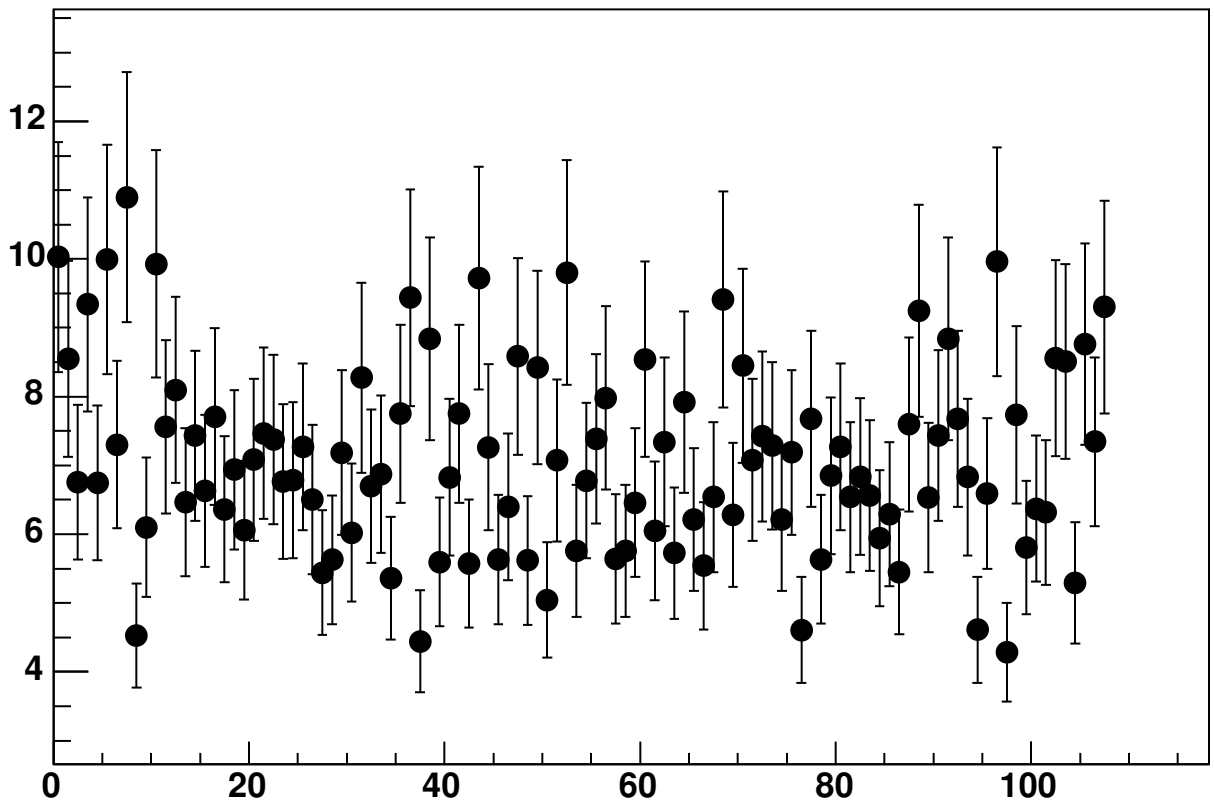
Enable 5, DAC=4000, Hold=175, ADC Noise vs 18*Chip+Chan



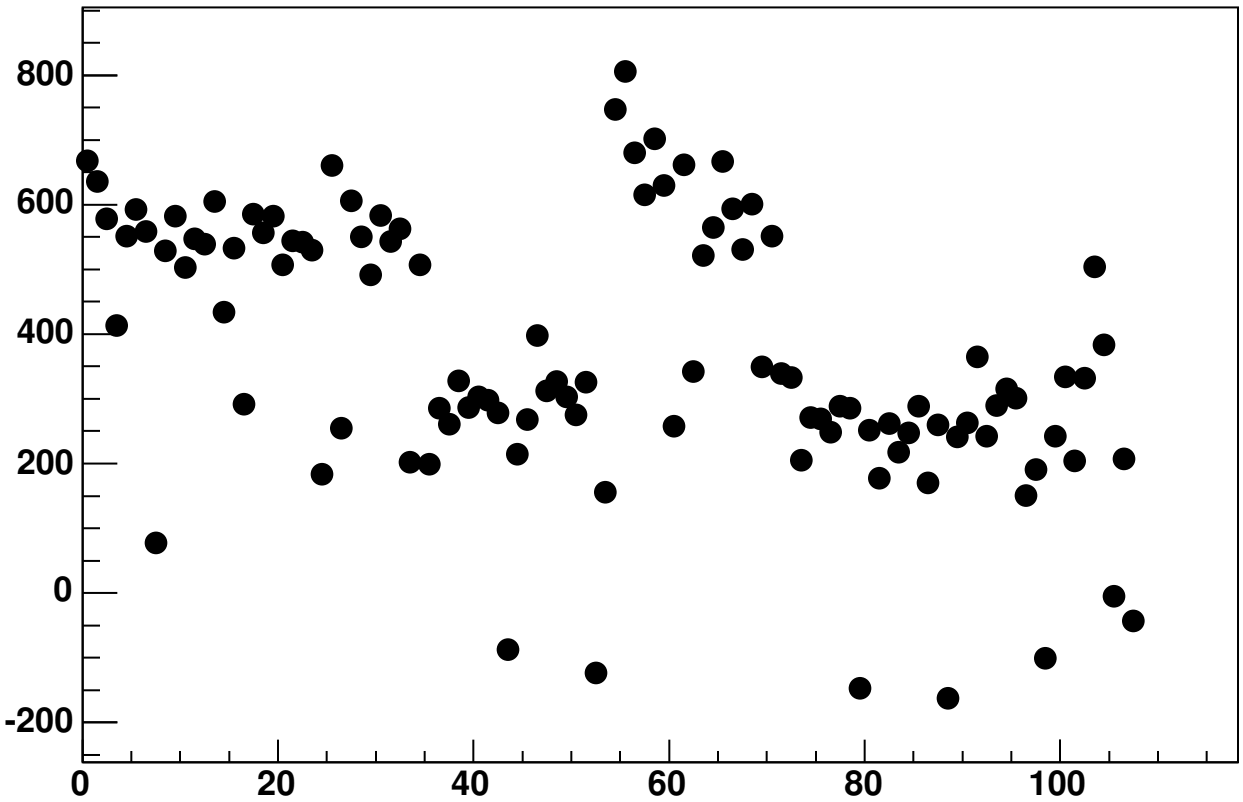
Enable 5, DAC=4000, Hold=180, ADC Mean vs 18*Chip+Chan



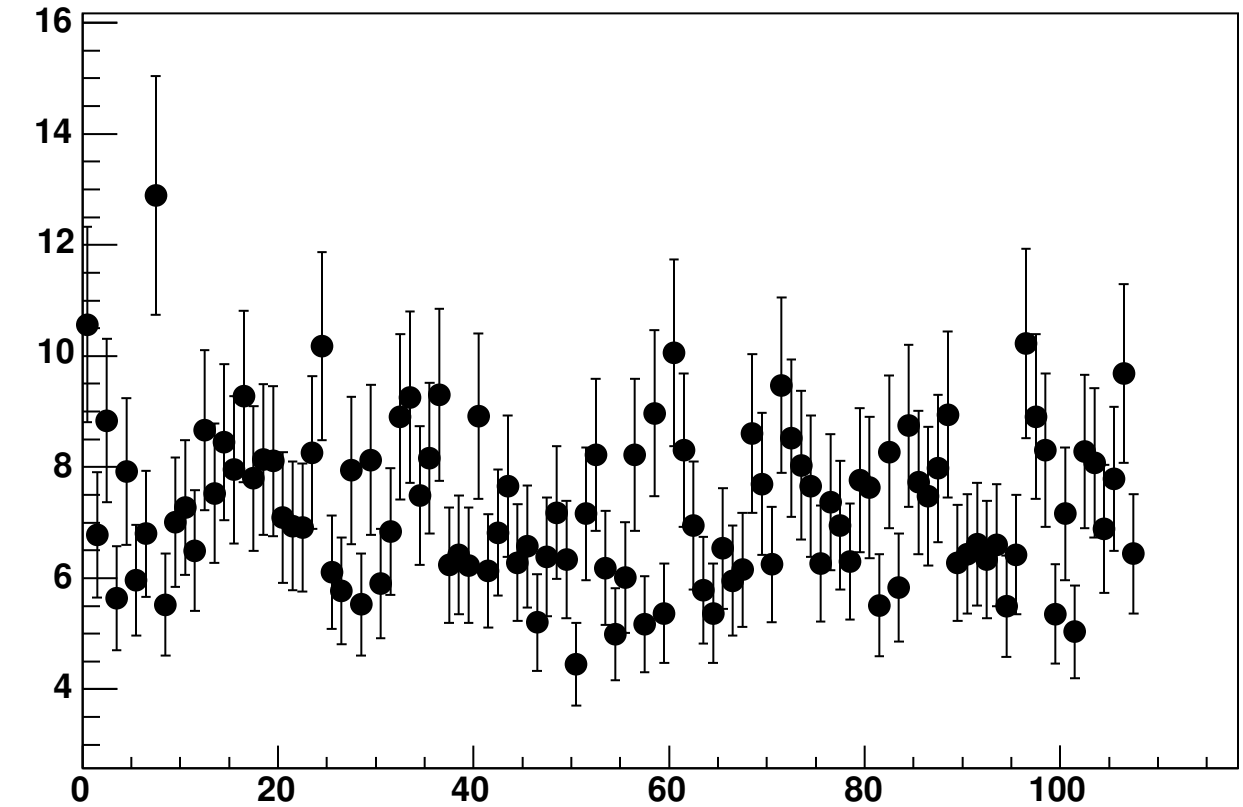
Enable 5, DAC=4000, Hold=180, ADC Noise vs 18*Chip+Chan



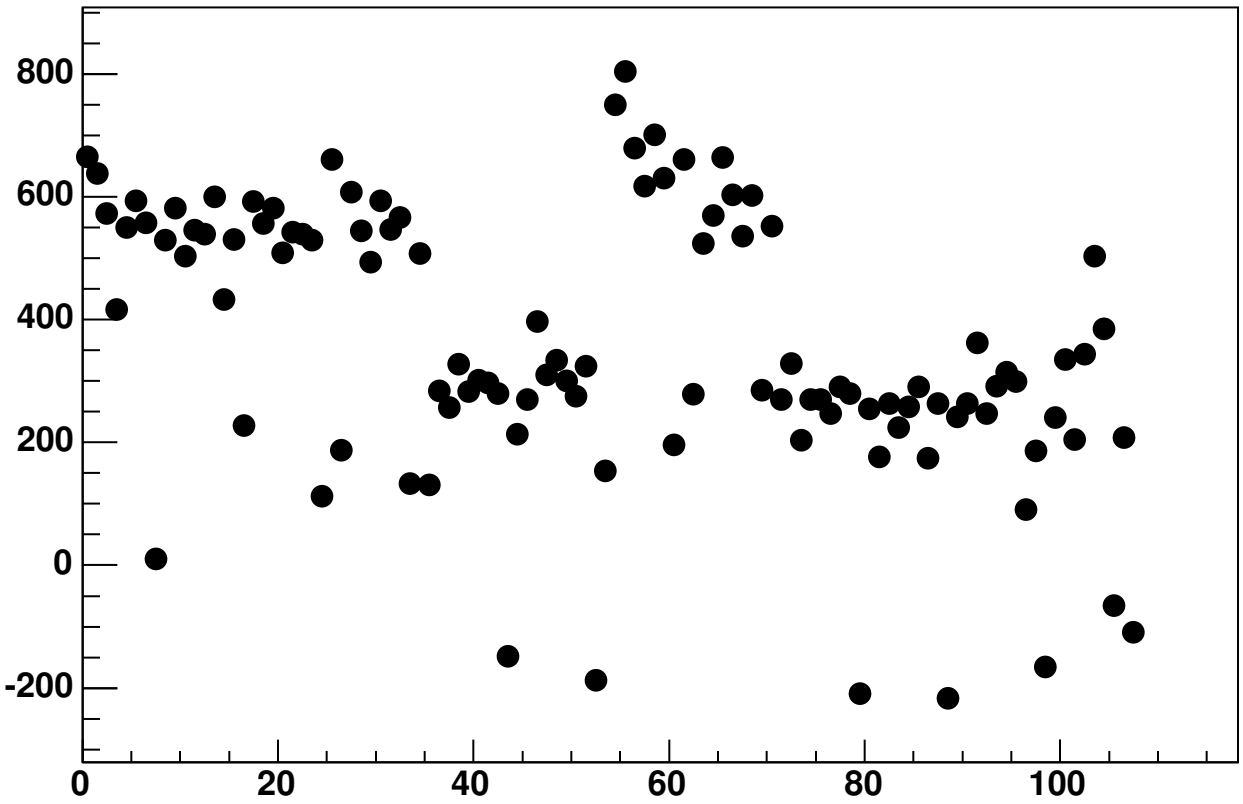
Enable 5, DAC=4000, Hold=185, ADC Mean vs 18*Chip+Chan



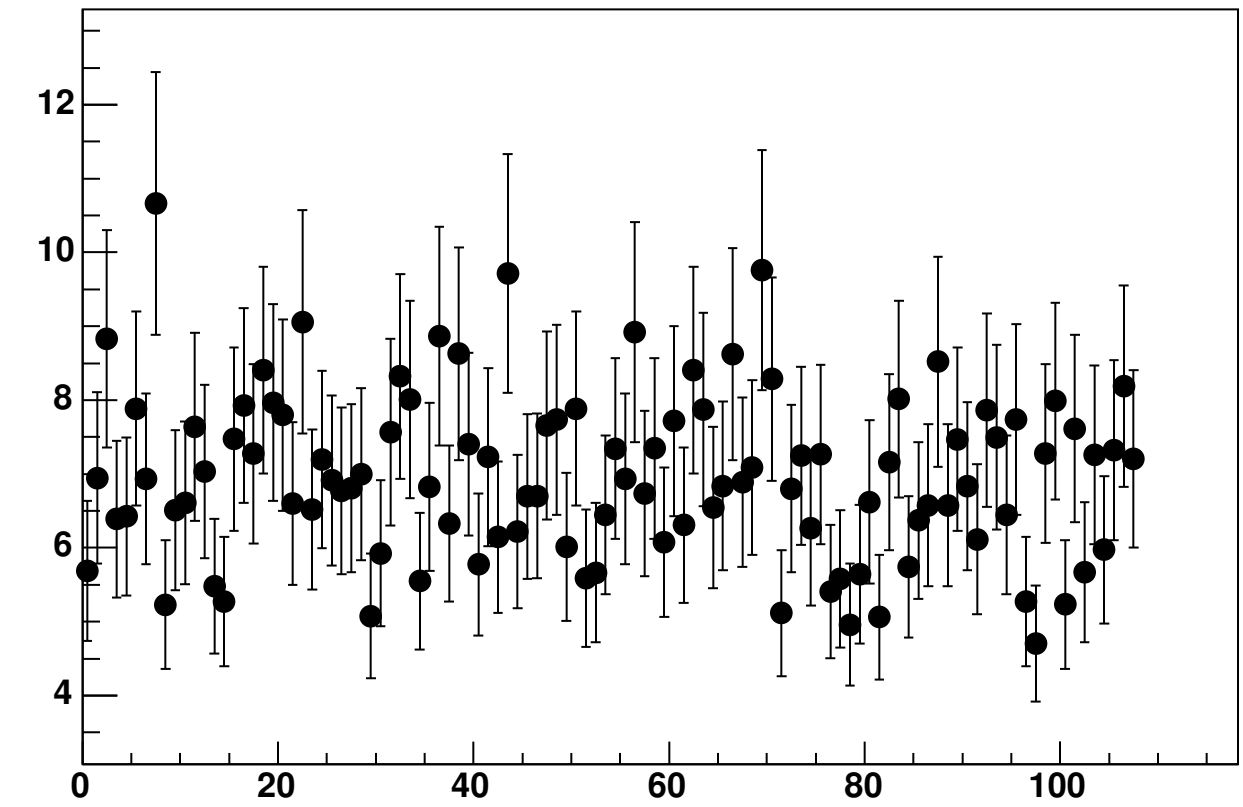
Enable 5, DAC=4000, Hold=185, ADC Noise vs 18*Chip+Chan



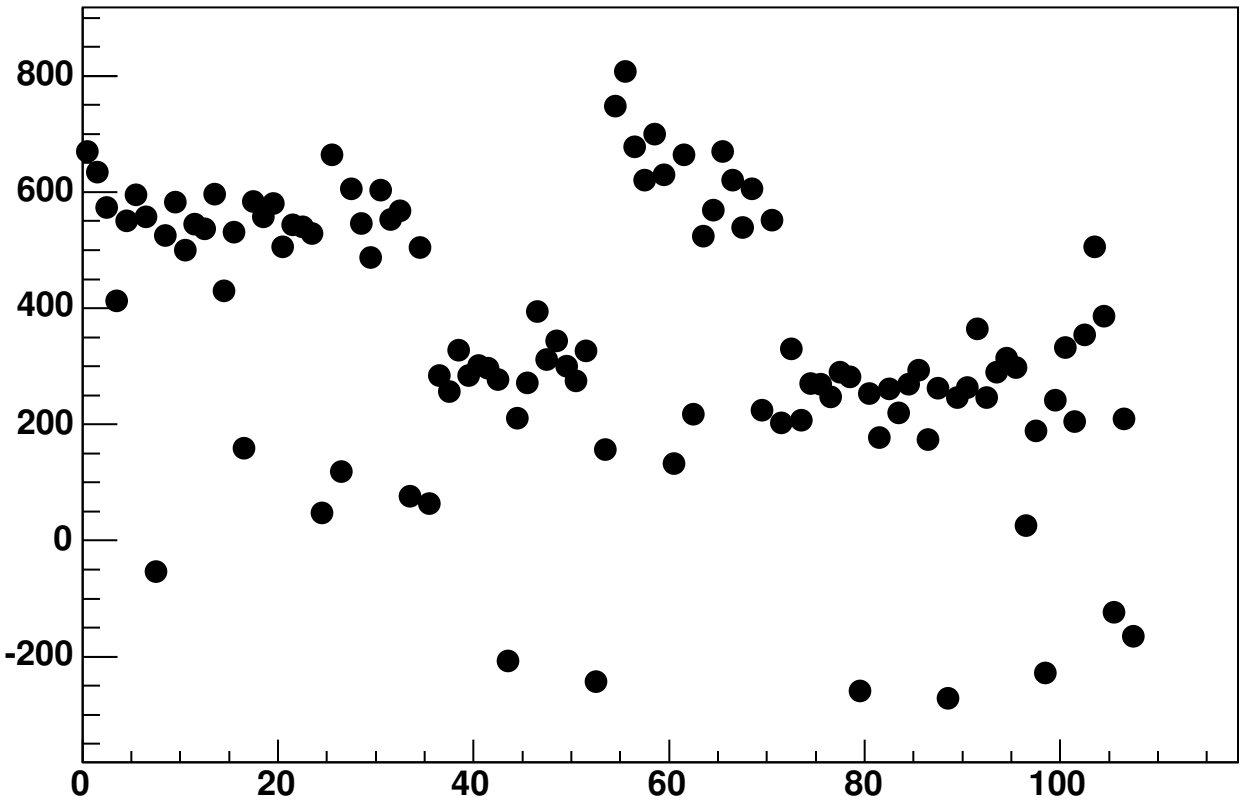
Enable 5, DAC=4000, Hold=190, ADC Mean vs 18*Chip+Chan



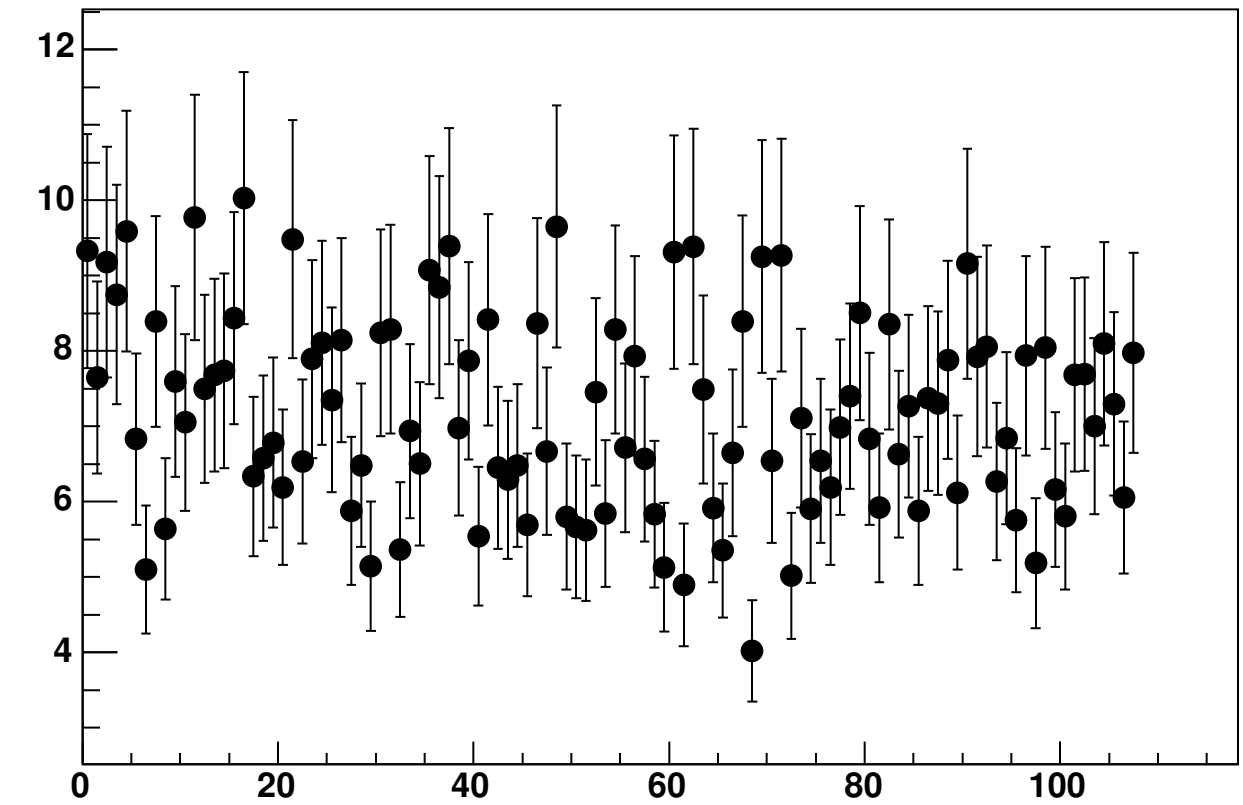
Enable 5, DAC=4000, Hold=190, ADC Noise vs 18*Chip+Chan



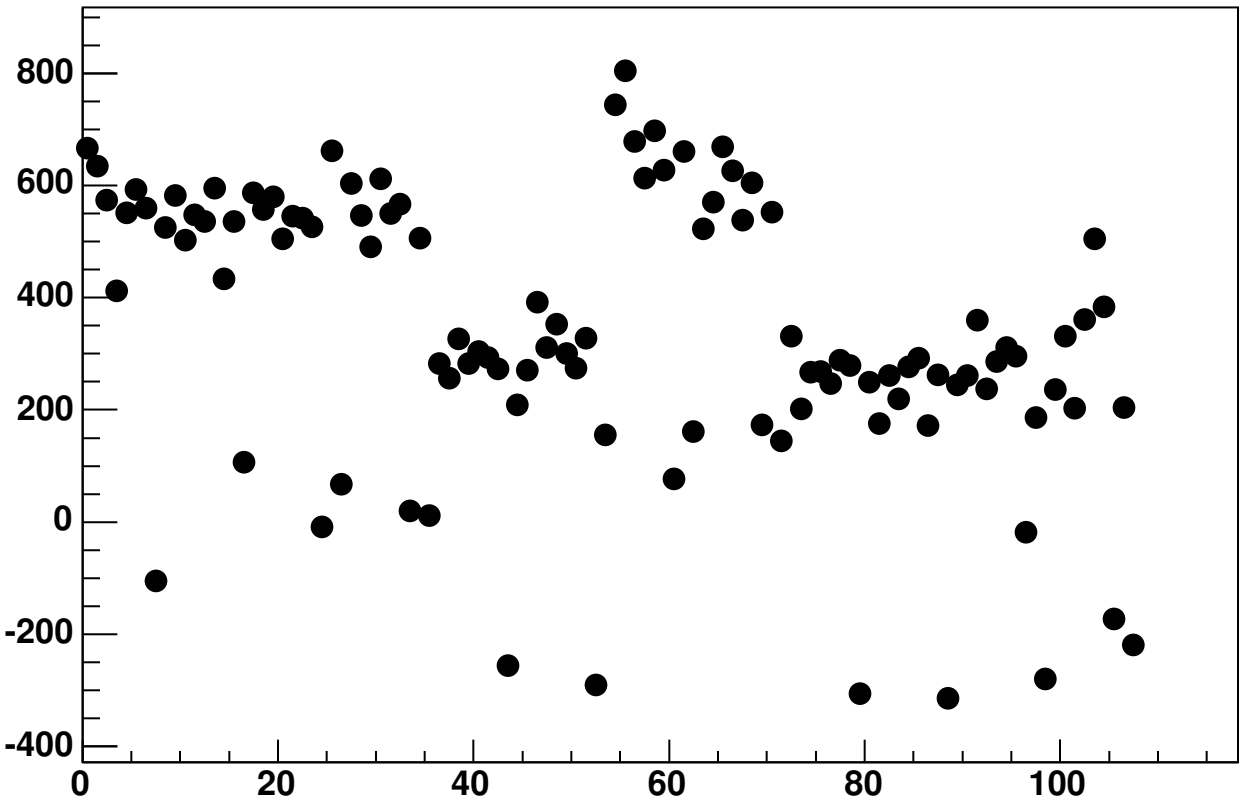
Enable 5, DAC=4000, Hold=195, ADC Mean vs 18*Chip+Chan



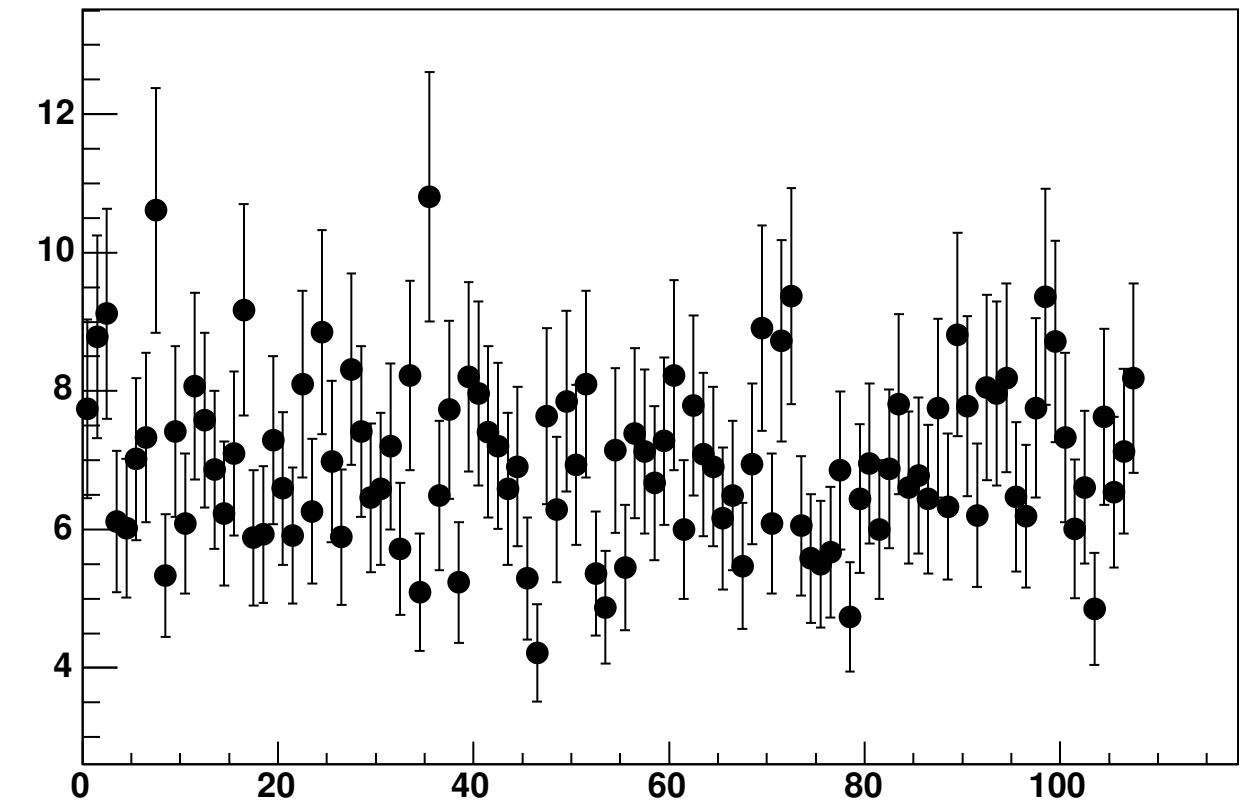
Enable 5, DAC=4000, Hold=195, ADC Noise vs 18*Chip+Chan



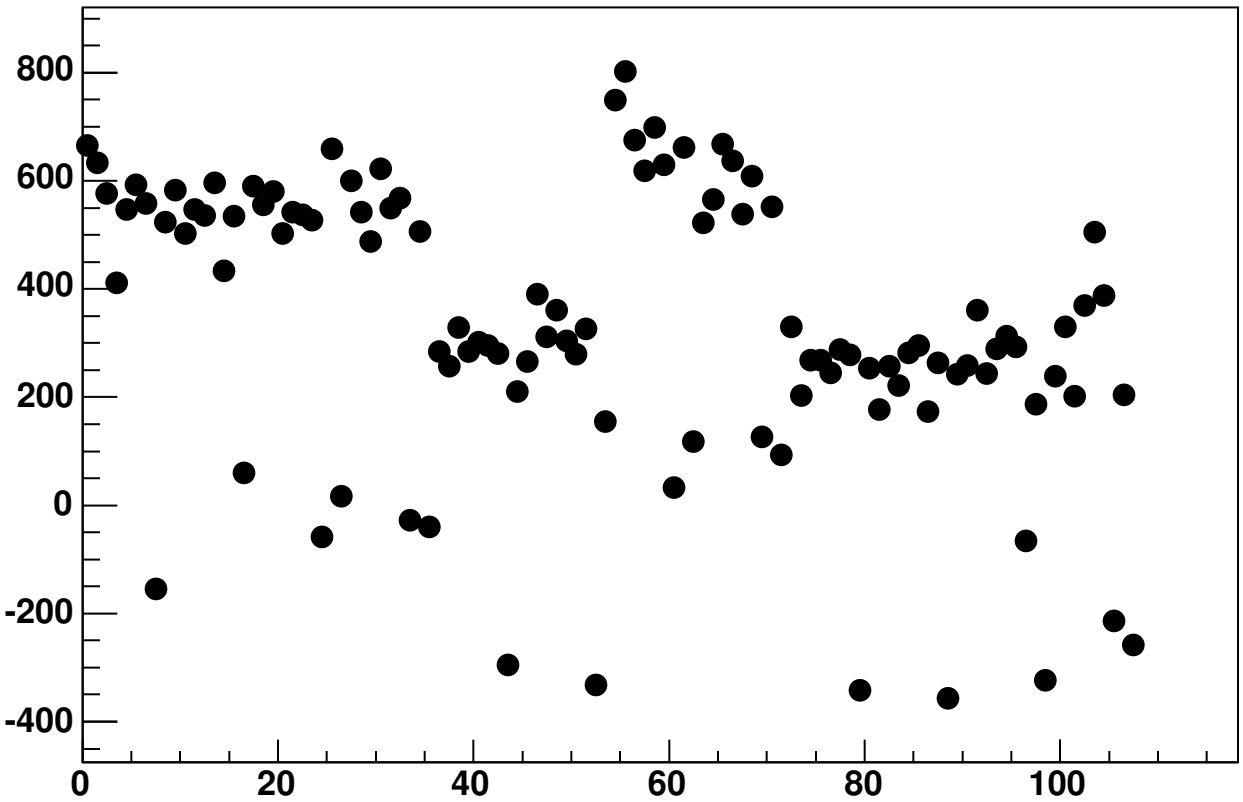
Enable 5, DAC=4000, Hold=200, ADC Mean vs 18*Chip+Chan



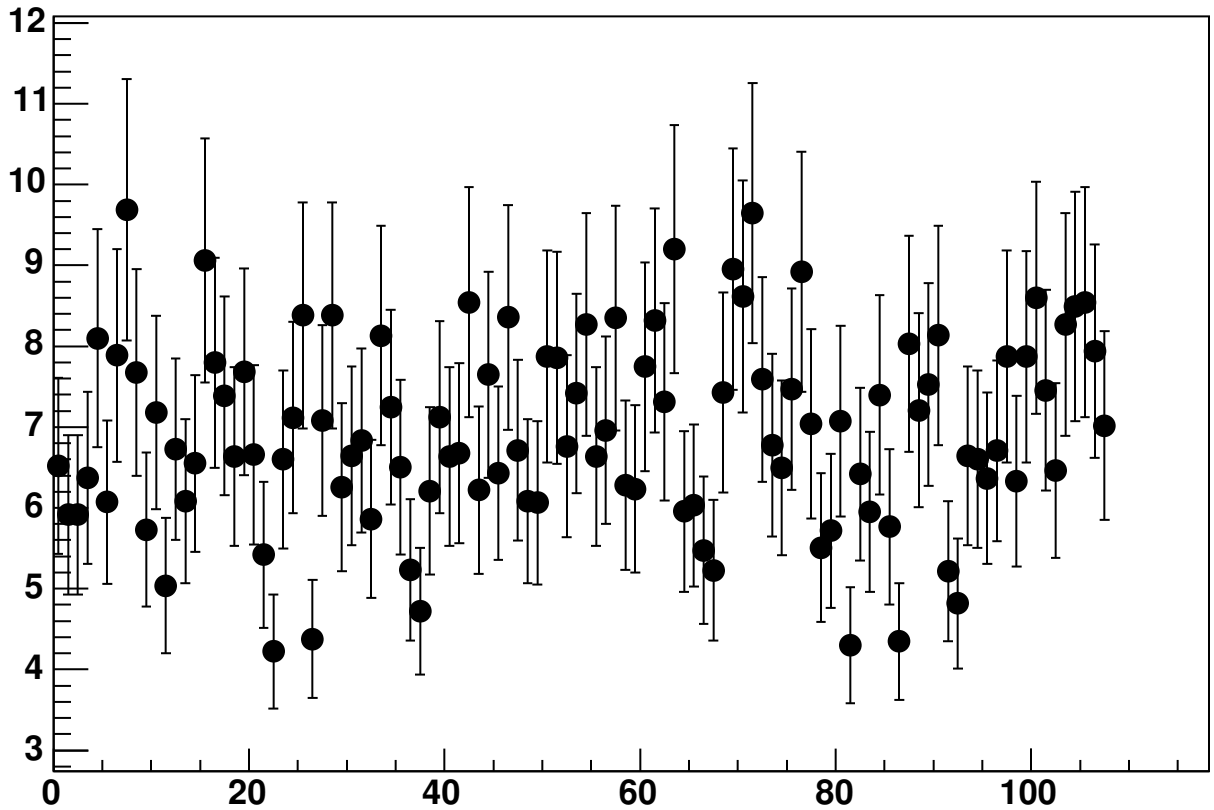
Enable 5, DAC=4000, Hold=200, ADC Noise vs 18*Chip+Chan



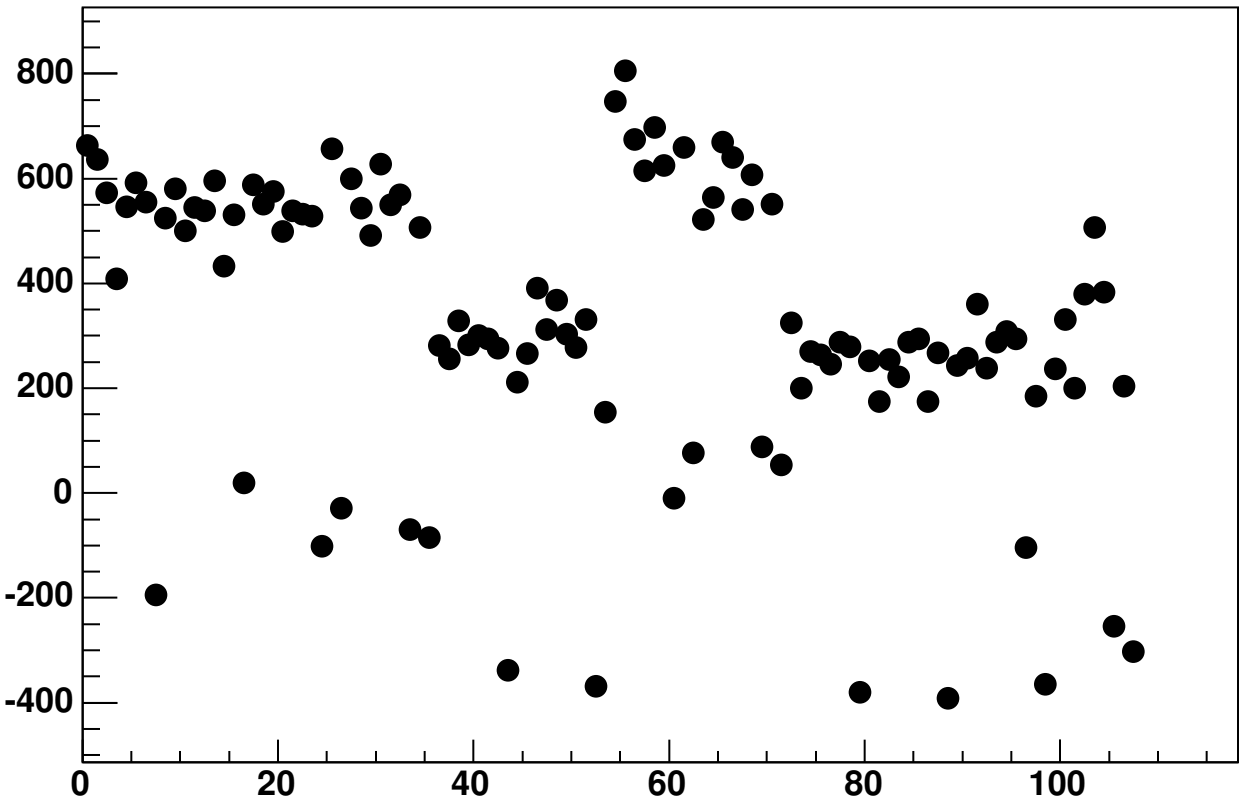
Enable 5, DAC=4000, Hold=205, ADC Mean vs 18*Chip+Chan



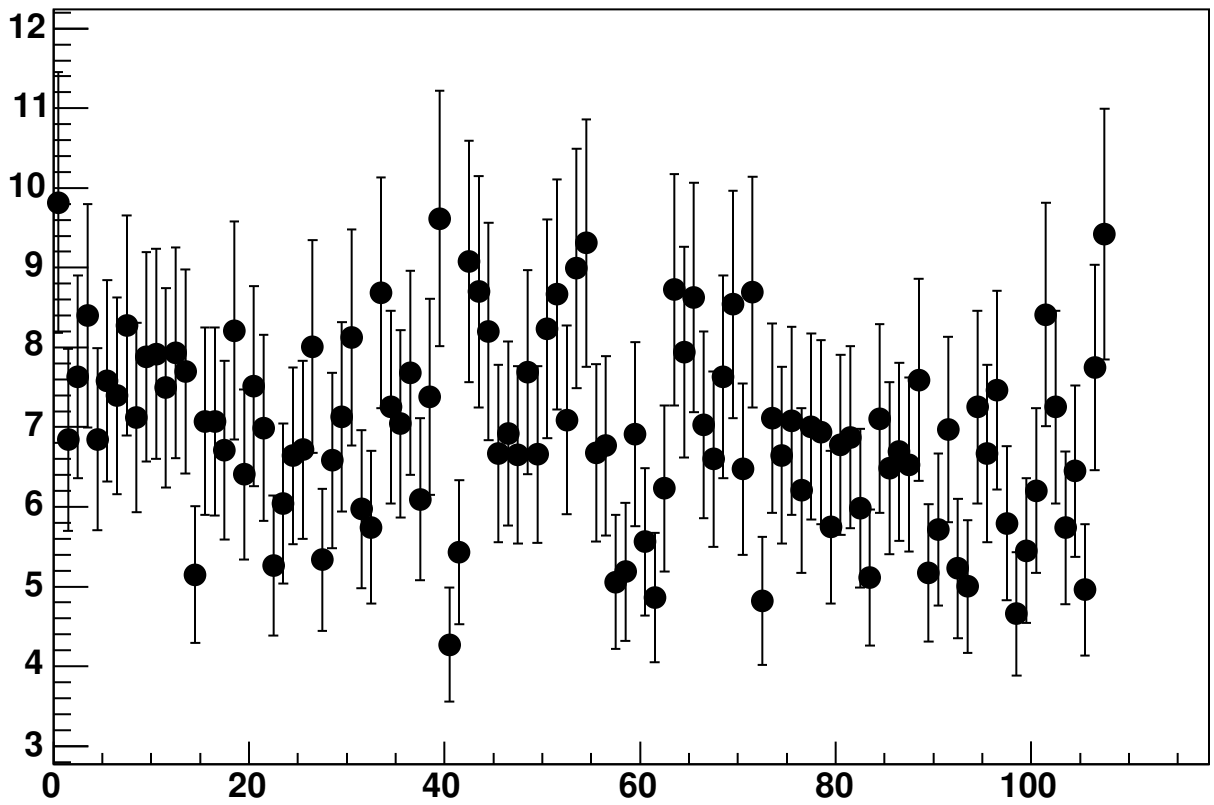
Enable 5, DAC=4000, Hold=205, ADC Noise vs 18*Chip+Chan



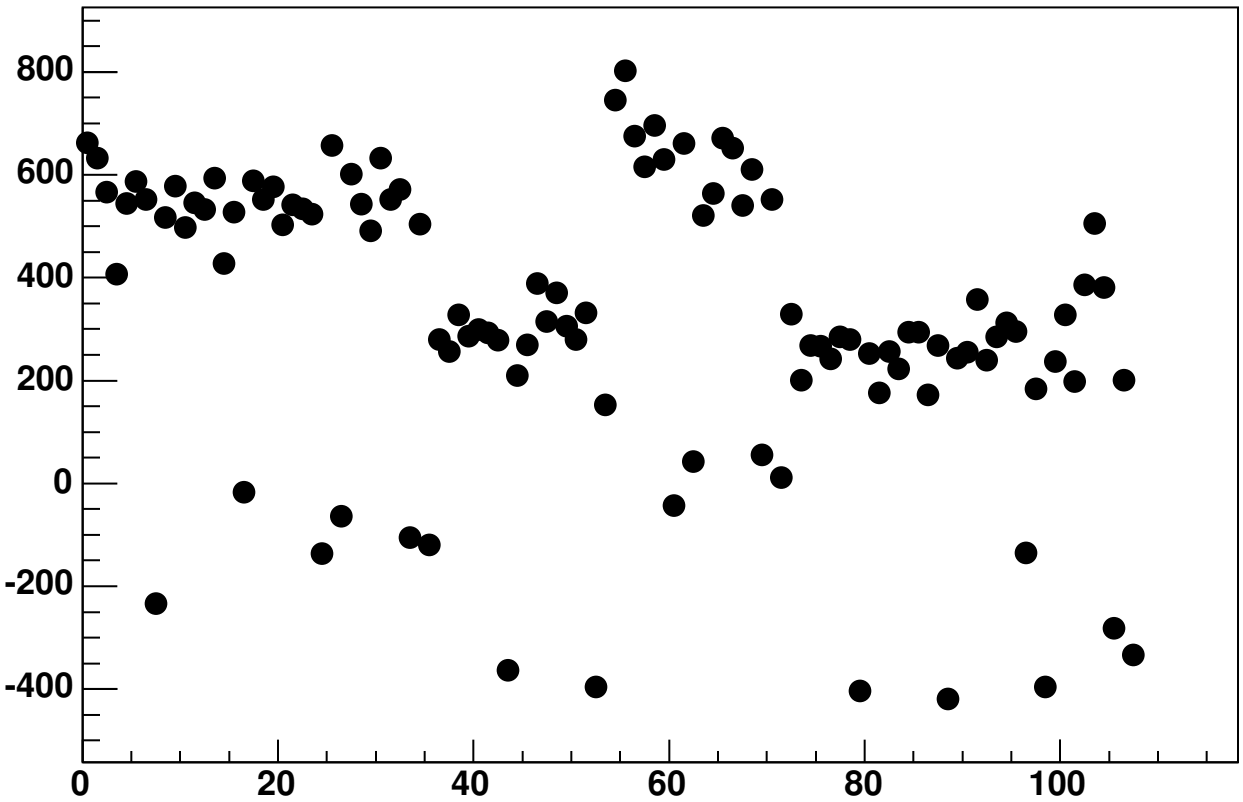
Enable 5, DAC=4000, Hold=210, ADC Mean vs 18*Chip+Chan



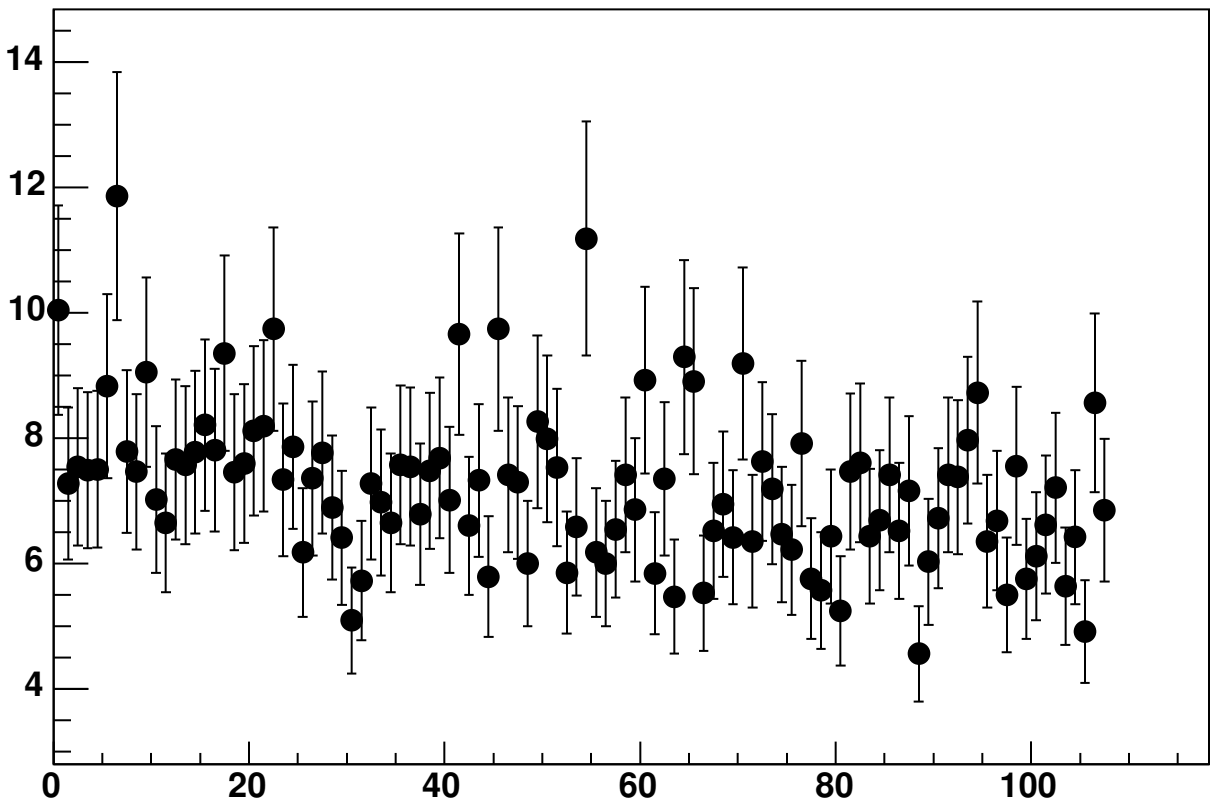
Enable 5, DAC=4000, Hold=210, ADC Noise vs 18*Chip+Chan



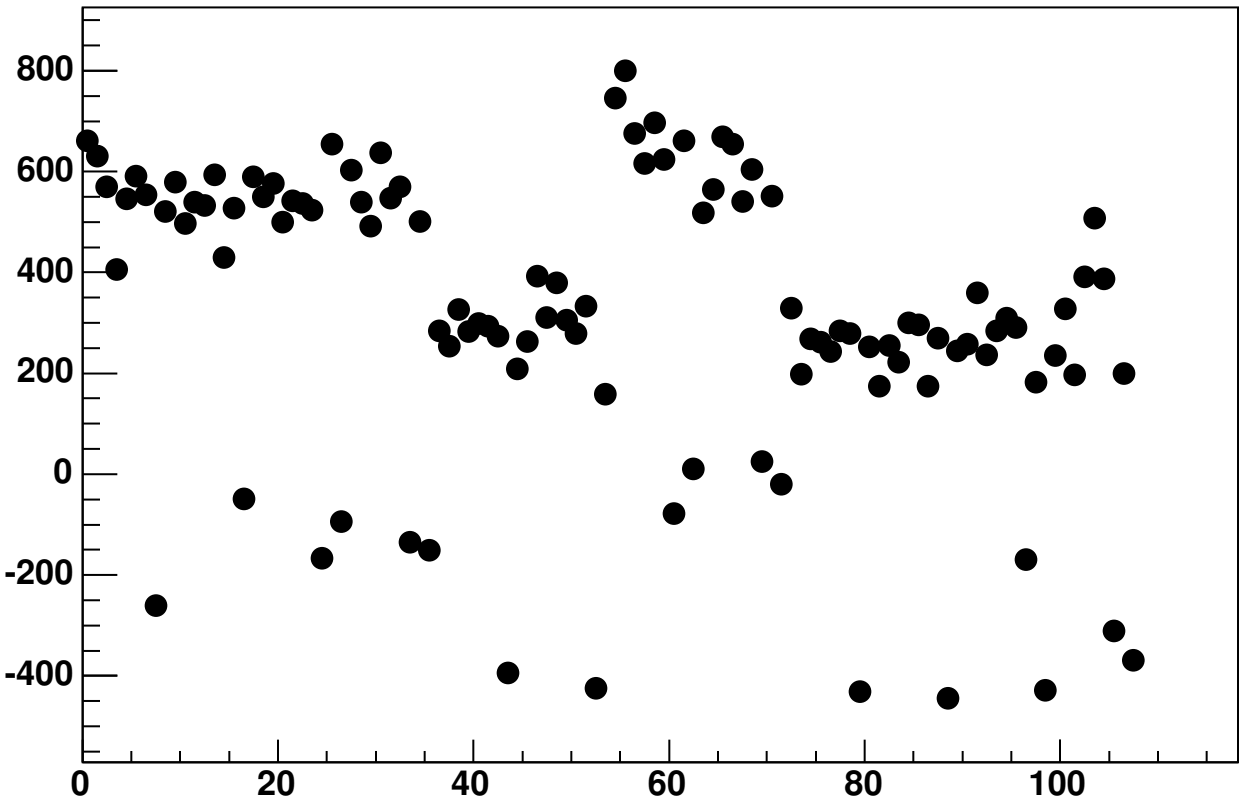
Enable 5, DAC=4000, Hold=215, ADC Mean vs 18*Chip+Chan



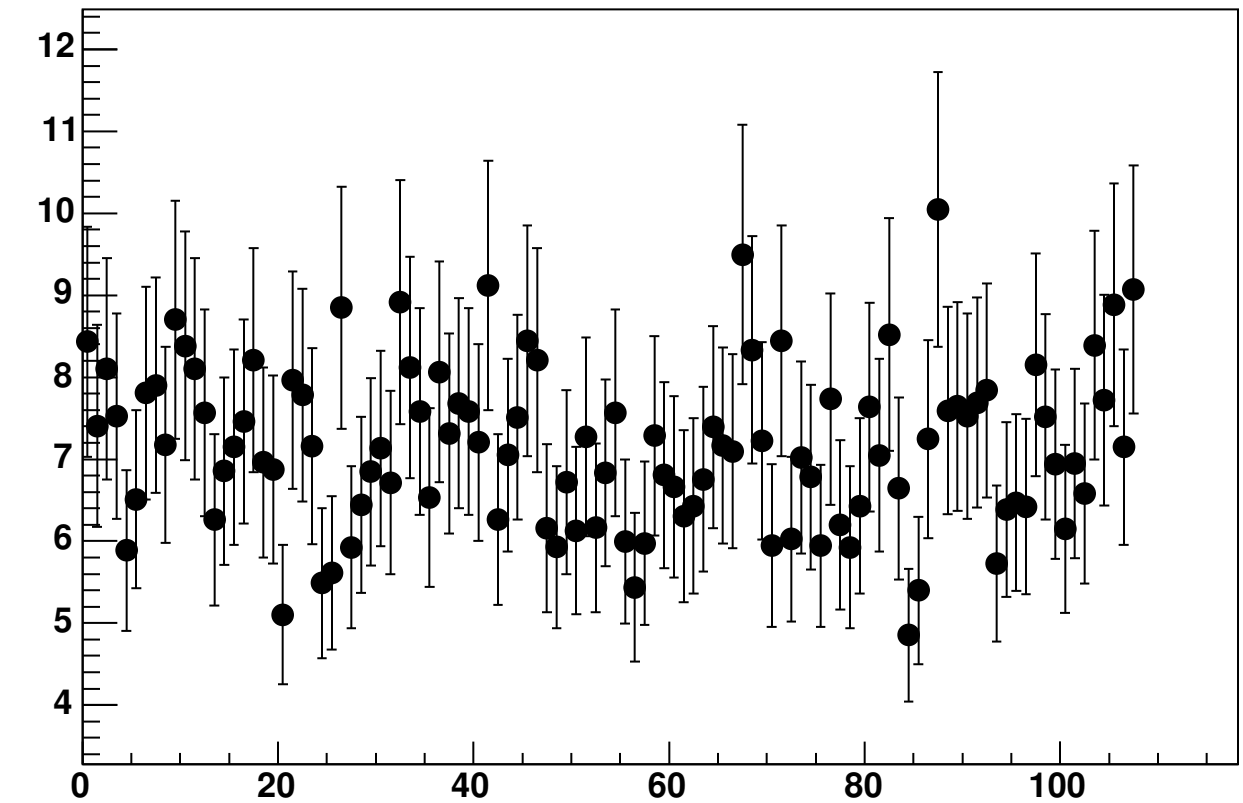
Enable 5, DAC=4000, Hold=215, ADC Noise vs 18*Chip+Chan



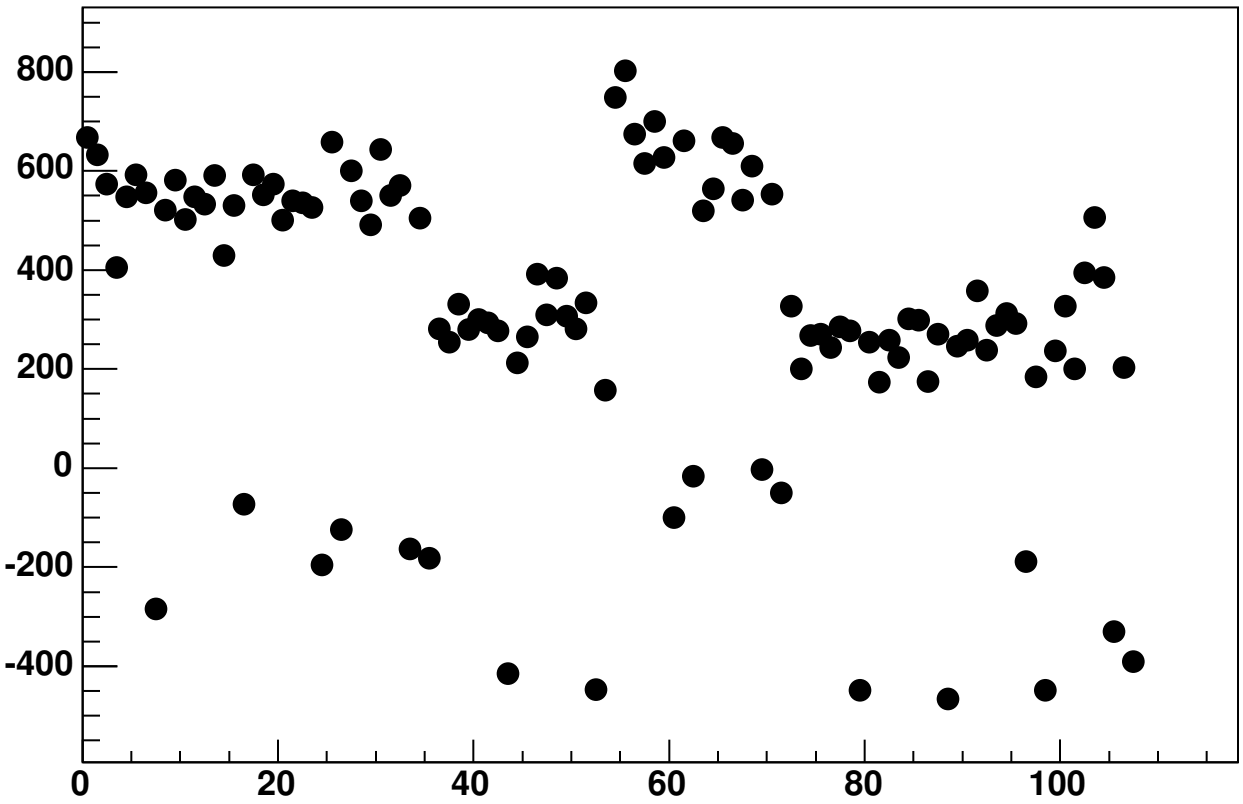
Enable 5, DAC=4000, Hold=220, ADC Mean vs 18*Chip+Chan



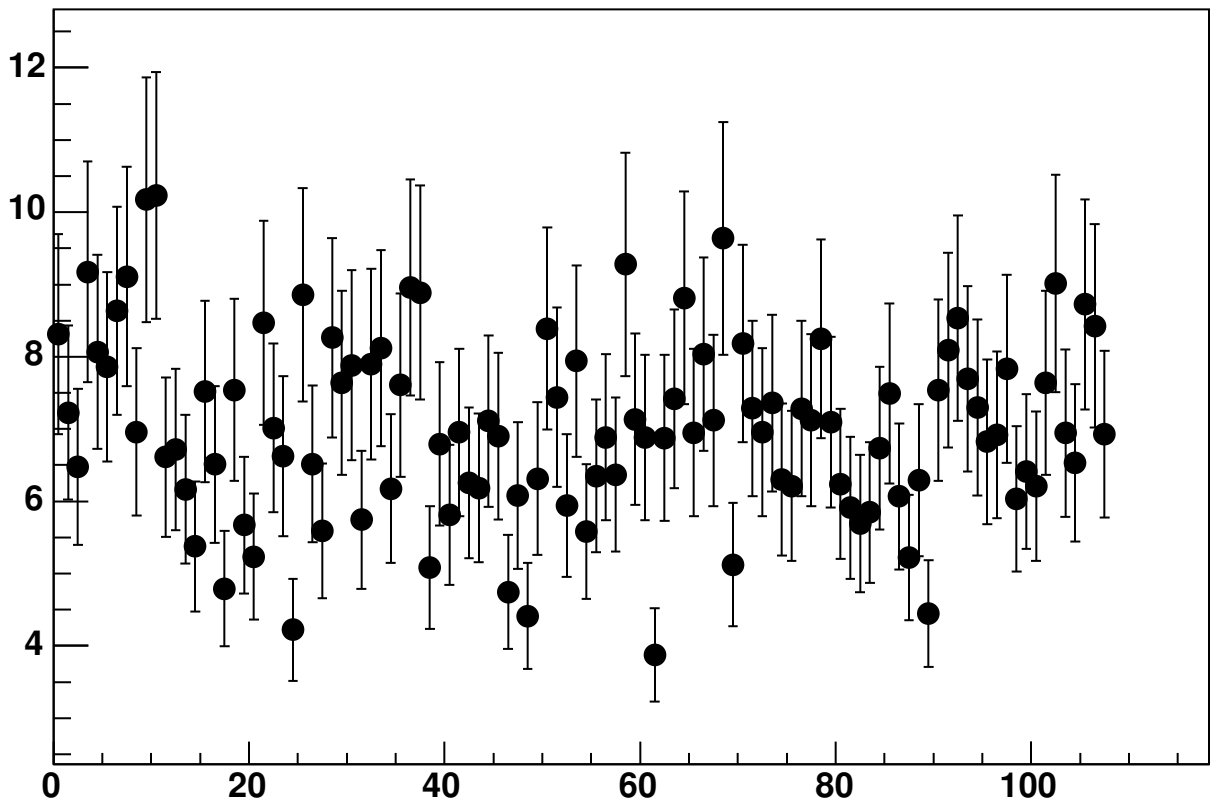
Enable 5, DAC=4000, Hold=220, ADC Noise vs 18*Chip+Chan



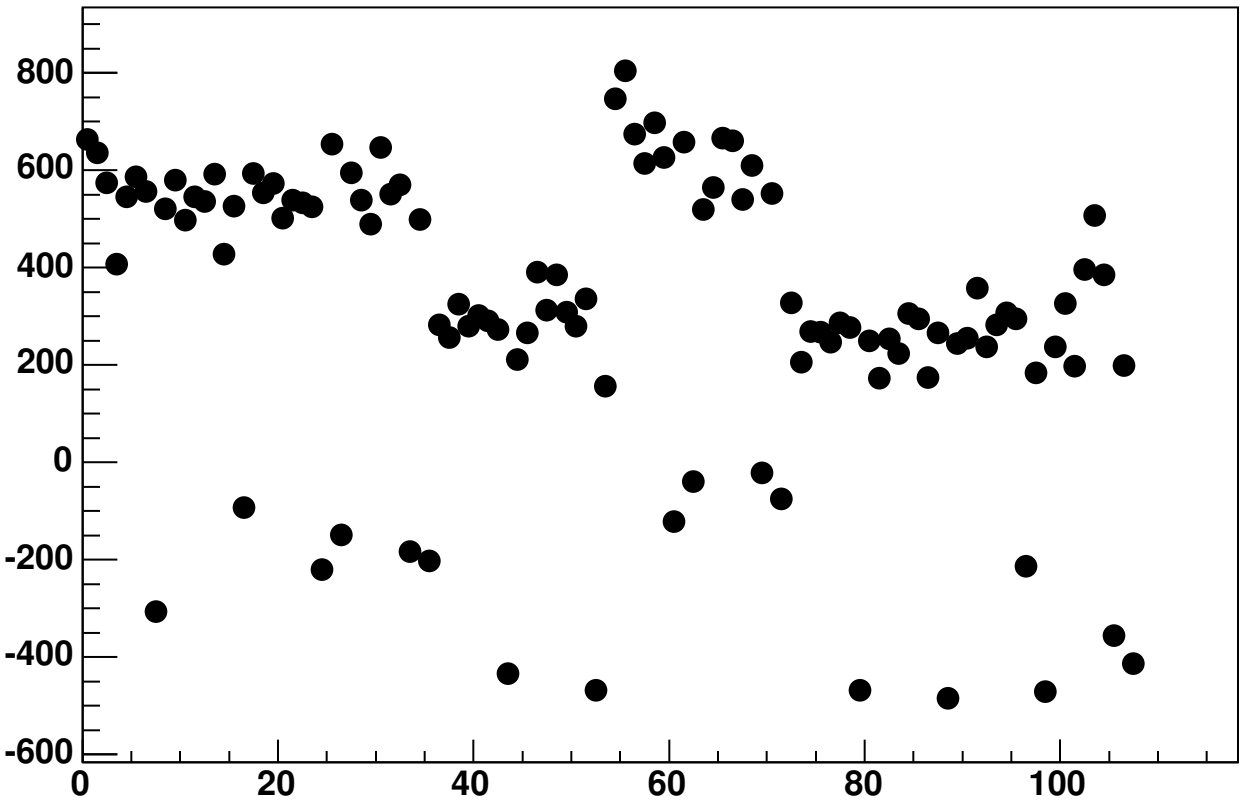
Enable 5, DAC=4000, Hold=225, ADC Mean vs 18*Chip+Chan



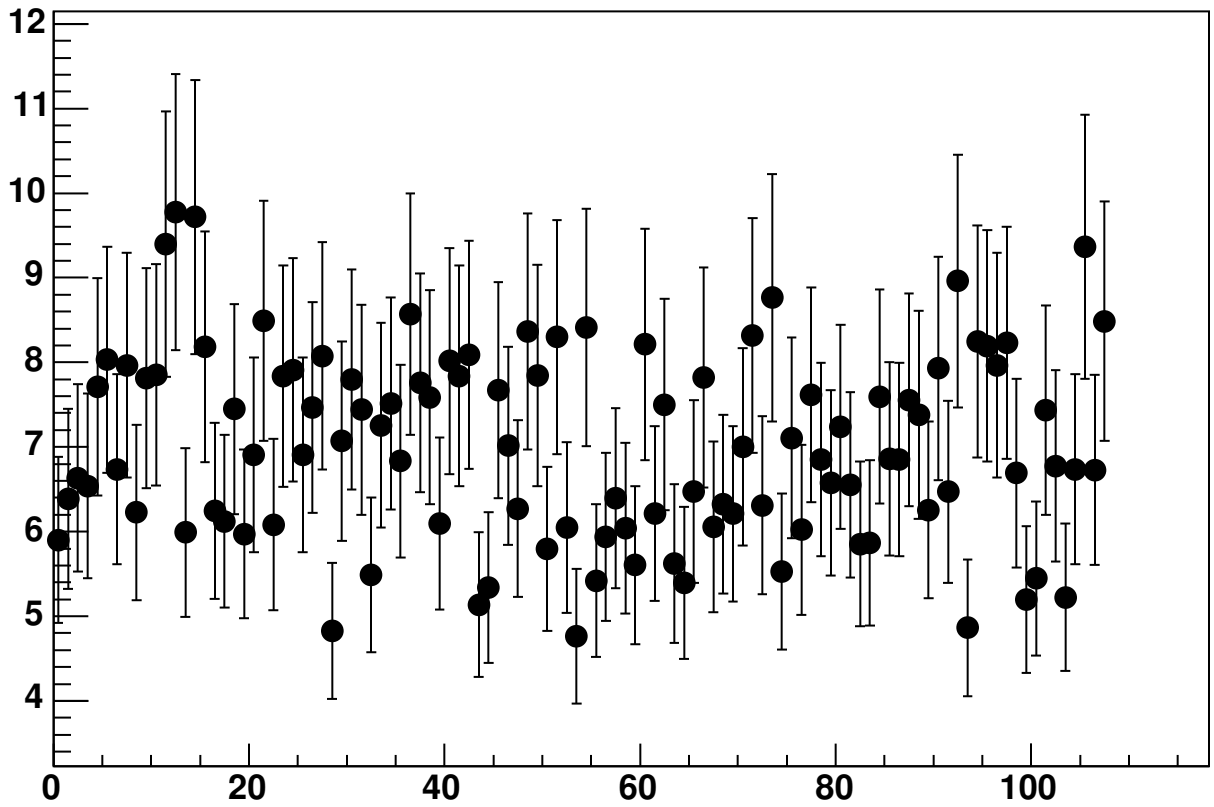
Enable 5, DAC=4000, Hold=225, ADC Noise vs 18*Chip+Chan



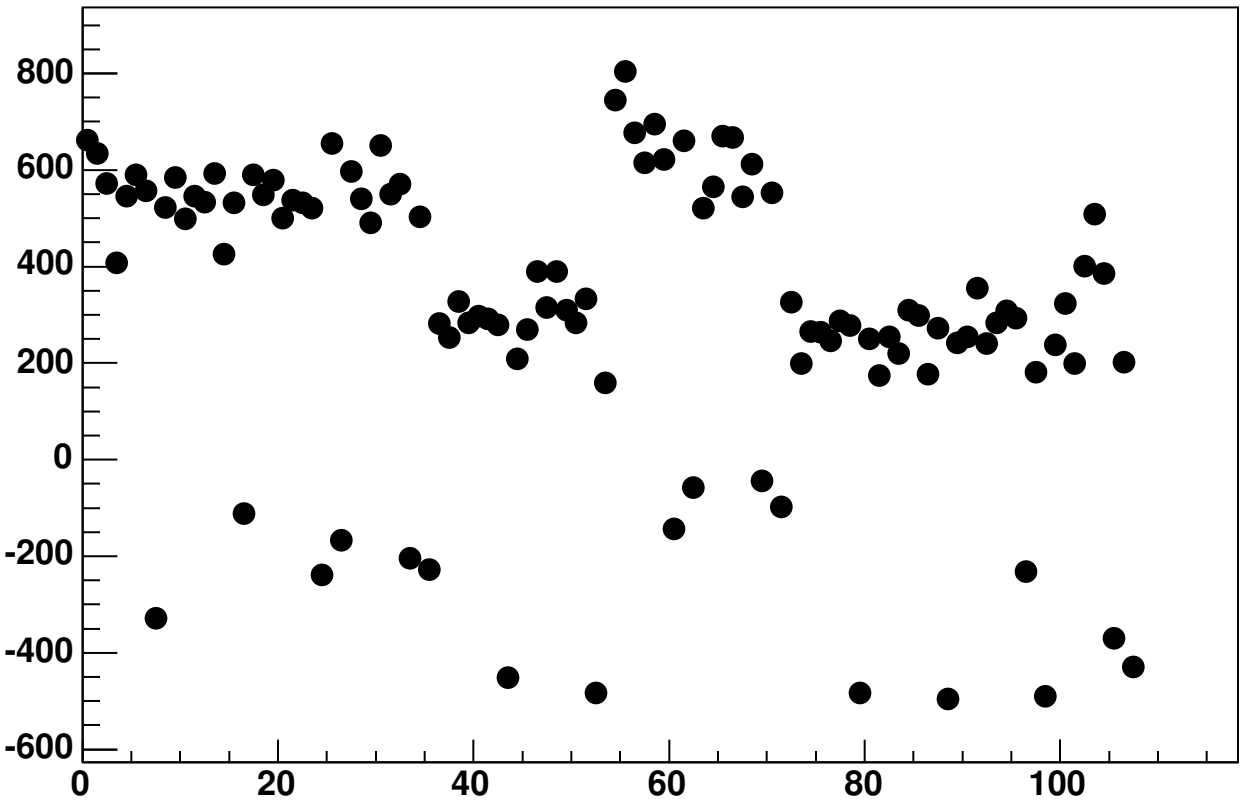
Enable 5, DAC=4000, Hold=230, ADC Mean vs 18*Chip+Chan



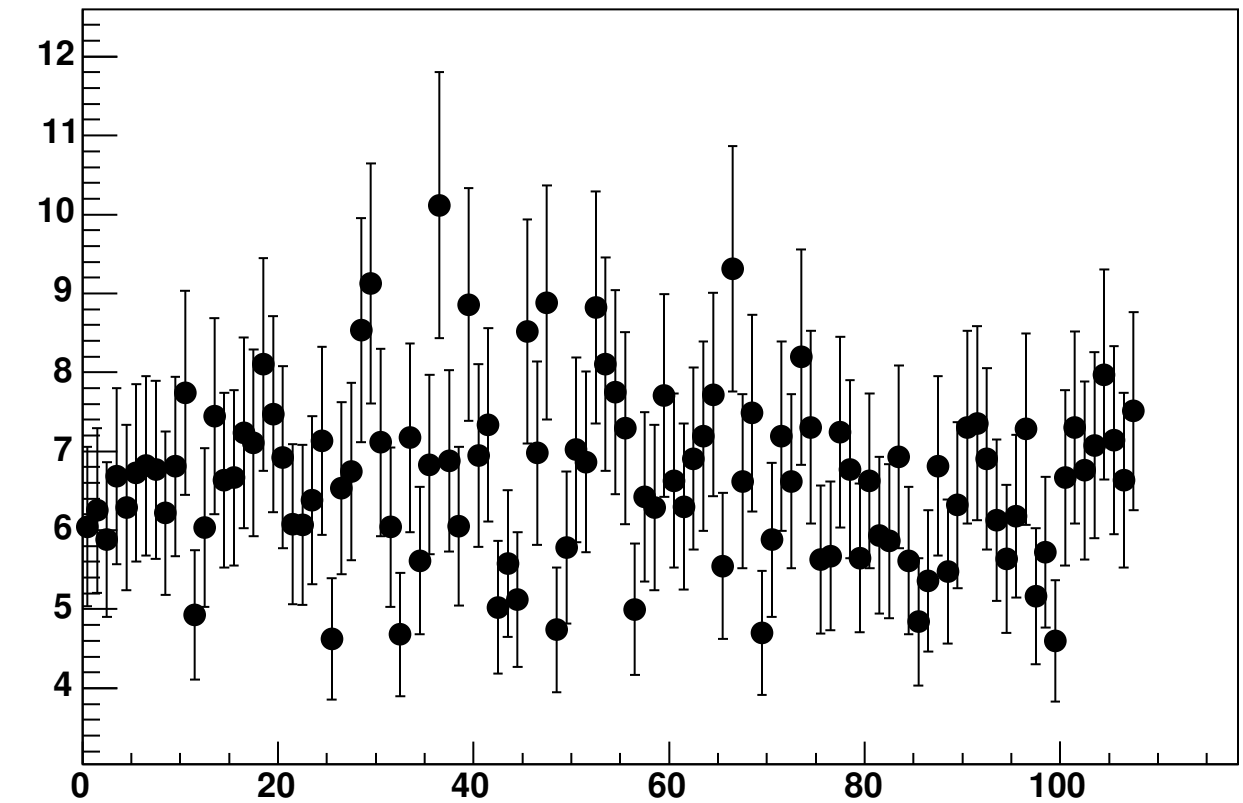
Enable 5, DAC=4000, Hold=230, ADC Noise vs 18*Chip+Chan



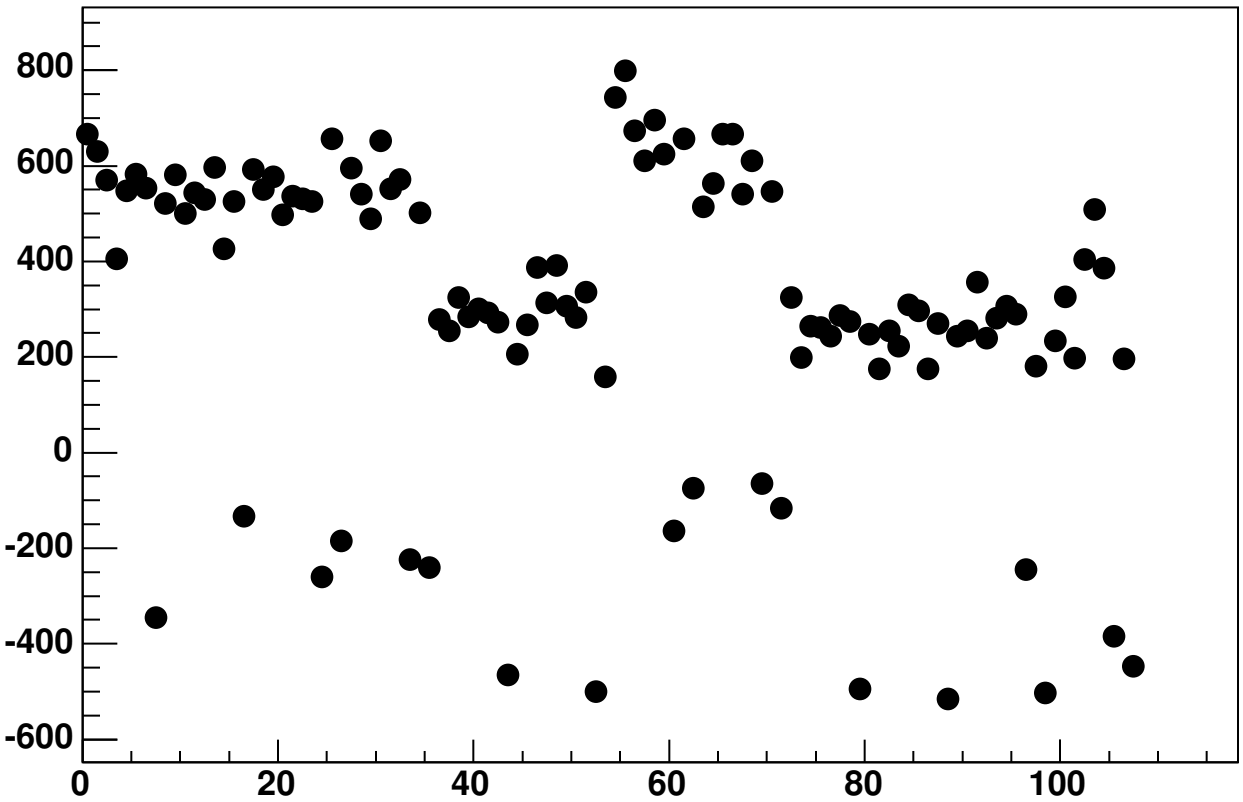
Enable 5, DAC=4000, Hold=235, ADC Mean vs 18*Chip+Chan



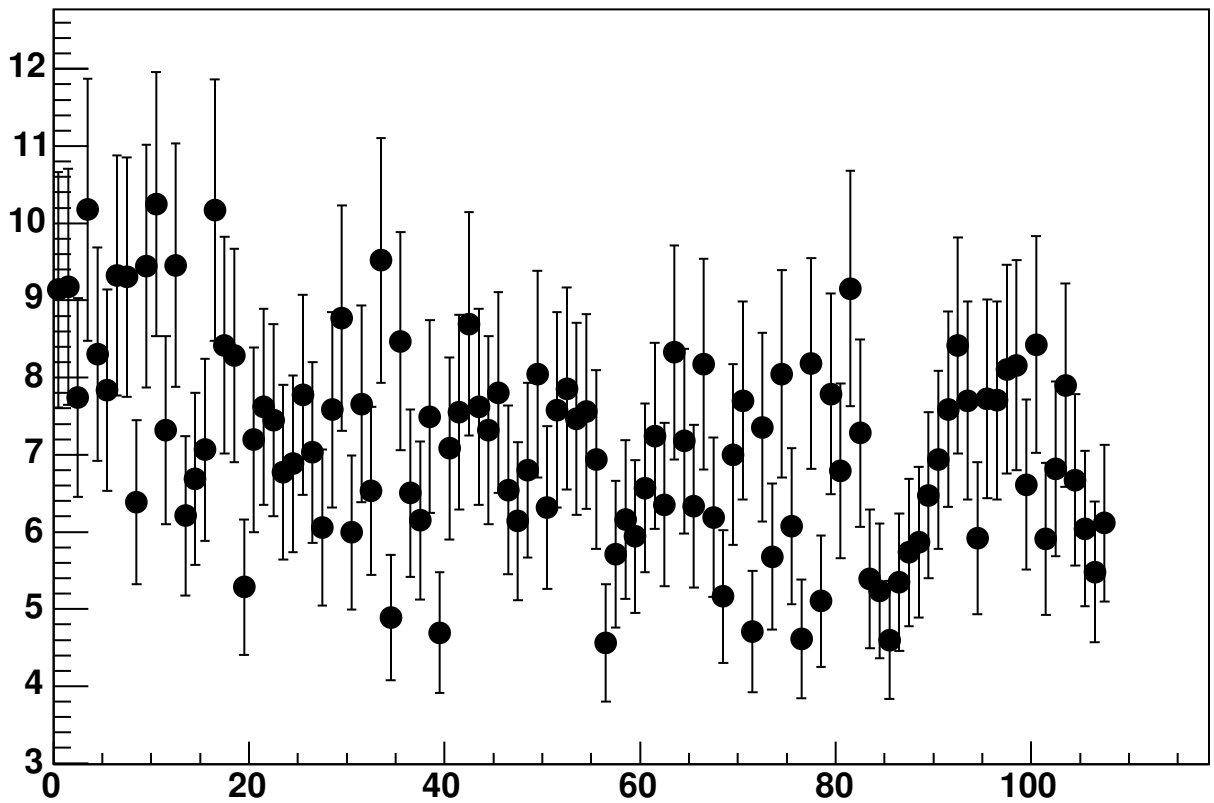
Enable 5, DAC=4000, Hold=235, ADC Noise vs 18*Chip+Chan

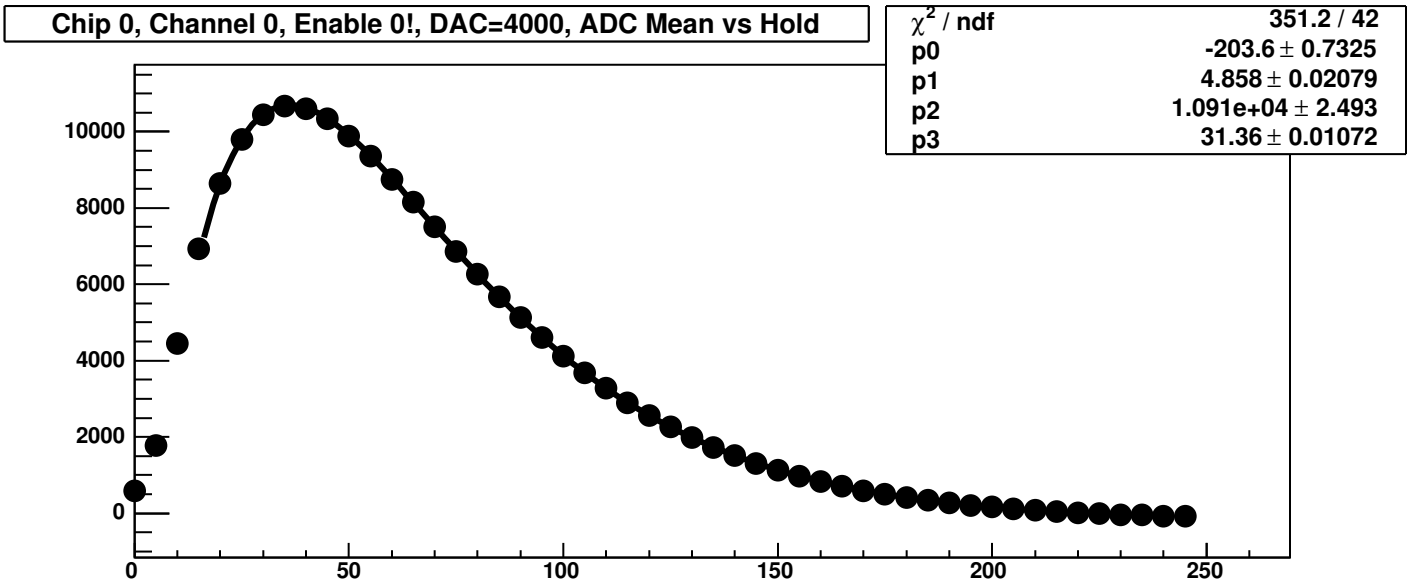


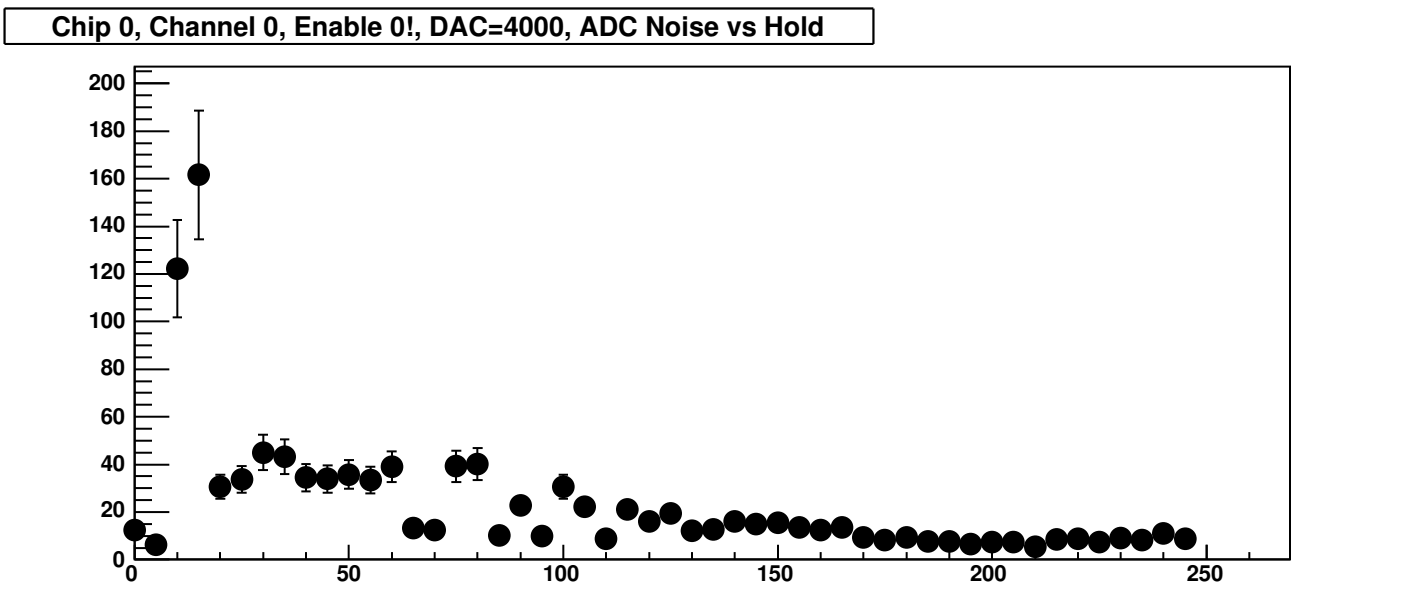
Enable 5, DAC=4000, Hold=240, ADC Mean vs 18*Chip+Chan

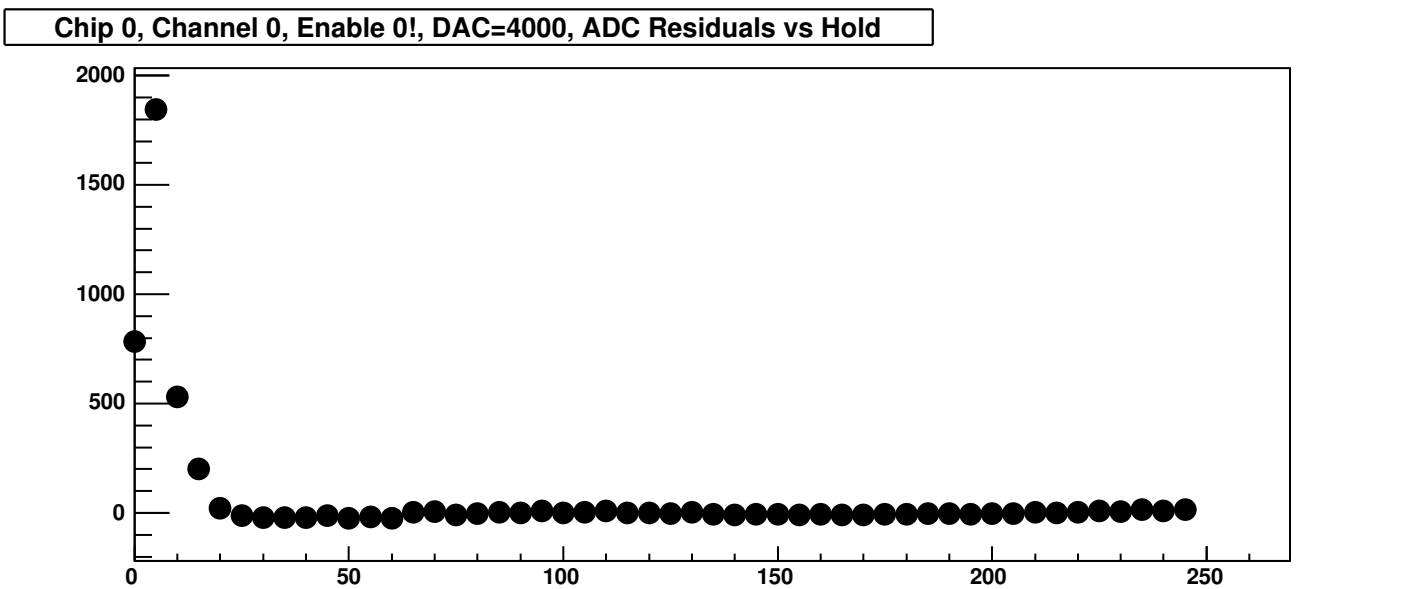


Enable 5, DAC=4000, Hold=240, ADC Noise vs 18*Chip+Chan

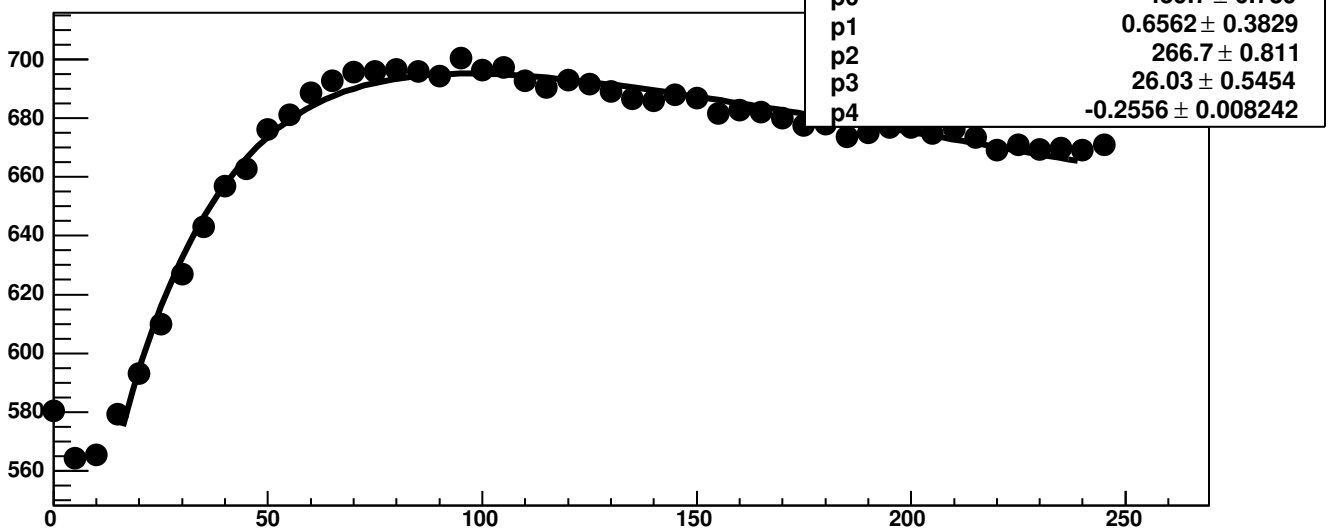



Chip 0, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 351.2 / 42
p0 -203.6 ± 0.7325
p1 4.858 ± 0.02079
p2 1.091e+04 ± 2.493
p3 31.36 ± 0.01072
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

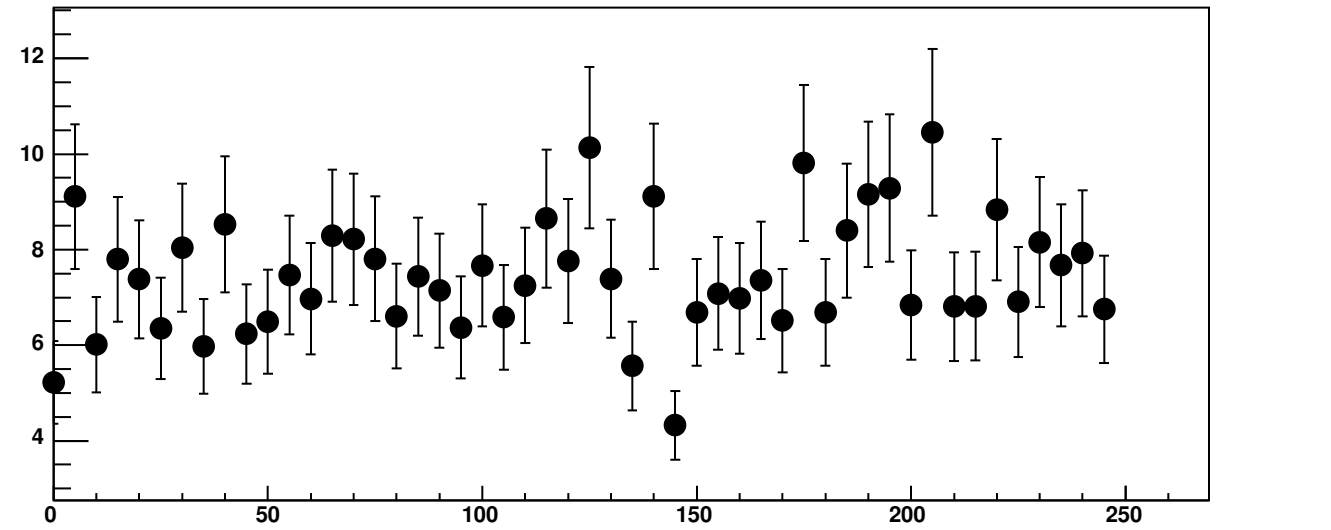

Chip 0, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
0
50
100
150
200
250

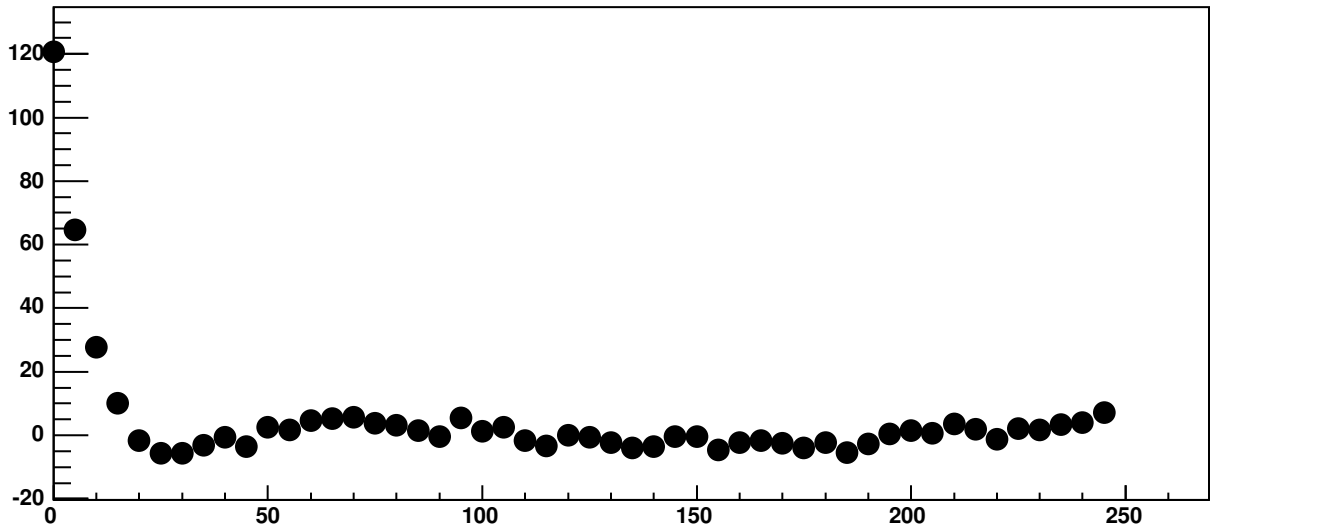
Chip 0, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold

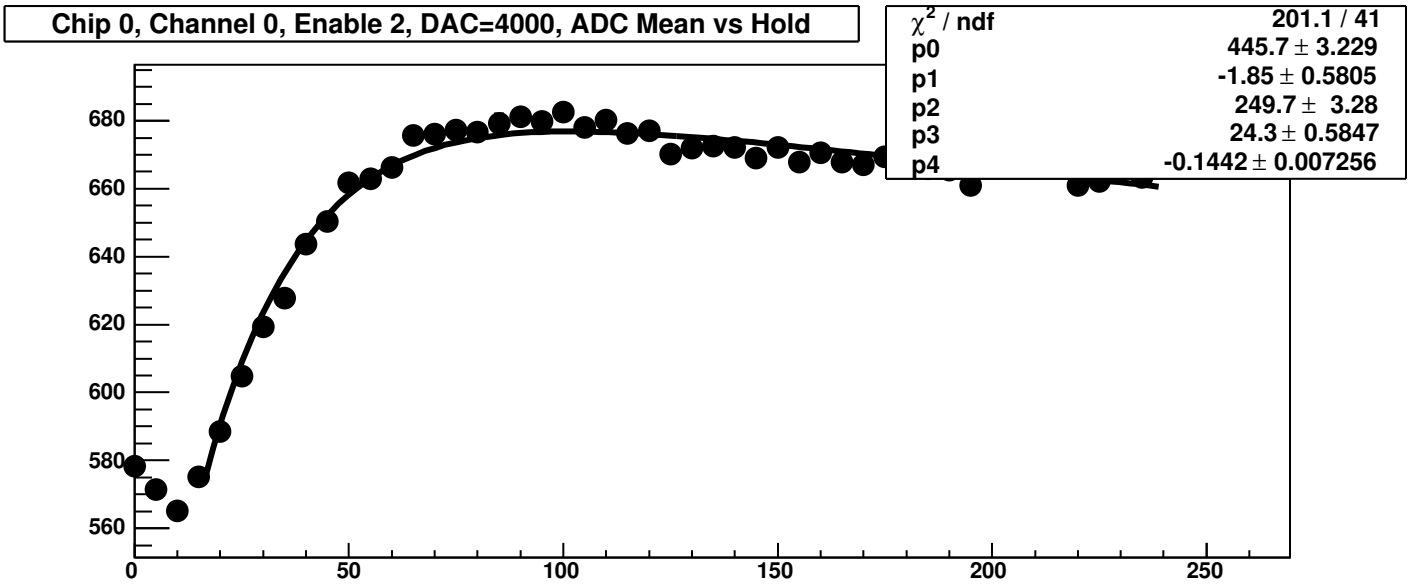


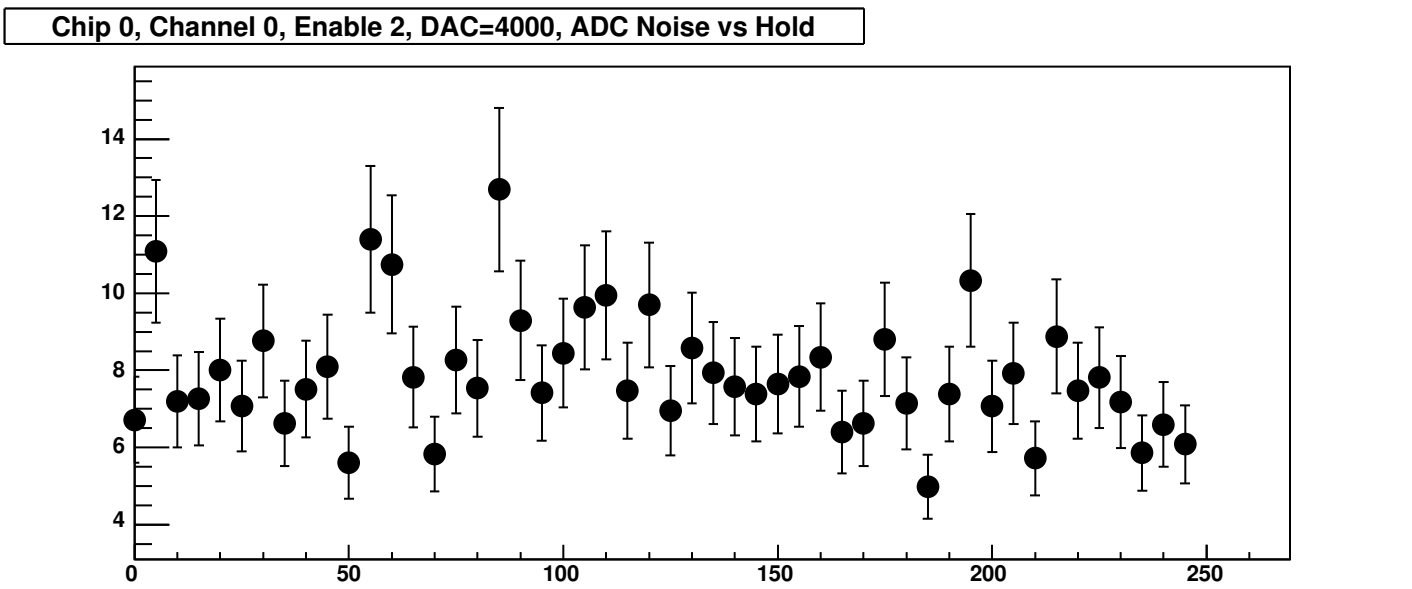
Chip 0, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold

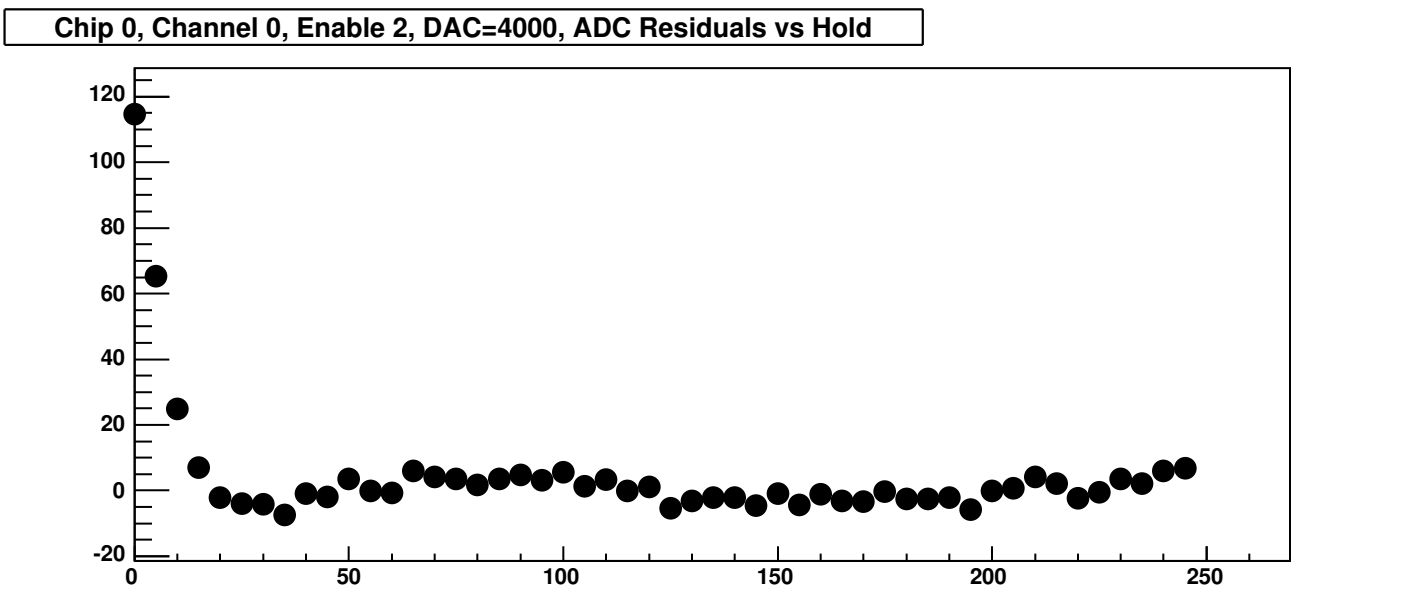


Chip 0, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold

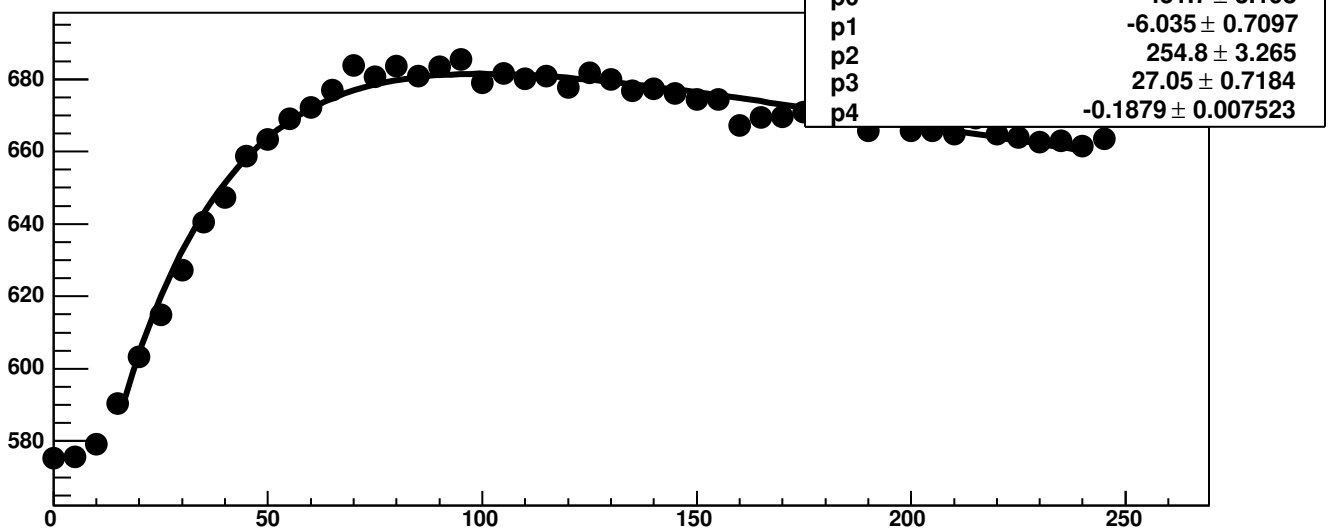



Chip 0, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 201.1 / 41
p0 445.7 ± 3.229
p1 -1.85 ± 0.5805
p2 249.7 ± 3.28
p3 24.3 ± 0.5847
p4 -0.1442 ± 0.007256
680
660
640
620
600
580
560
0
50
100
150
200
250

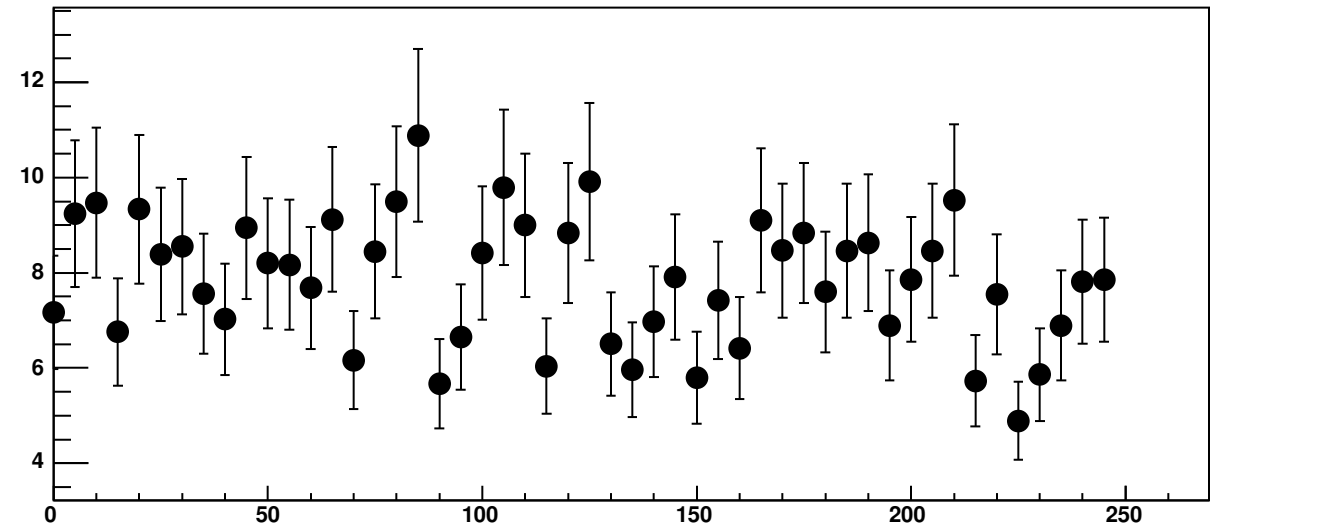

Chip 0, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 0, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold
120
100
80
60
40
20
0
-20
0
50
100
150
200
250

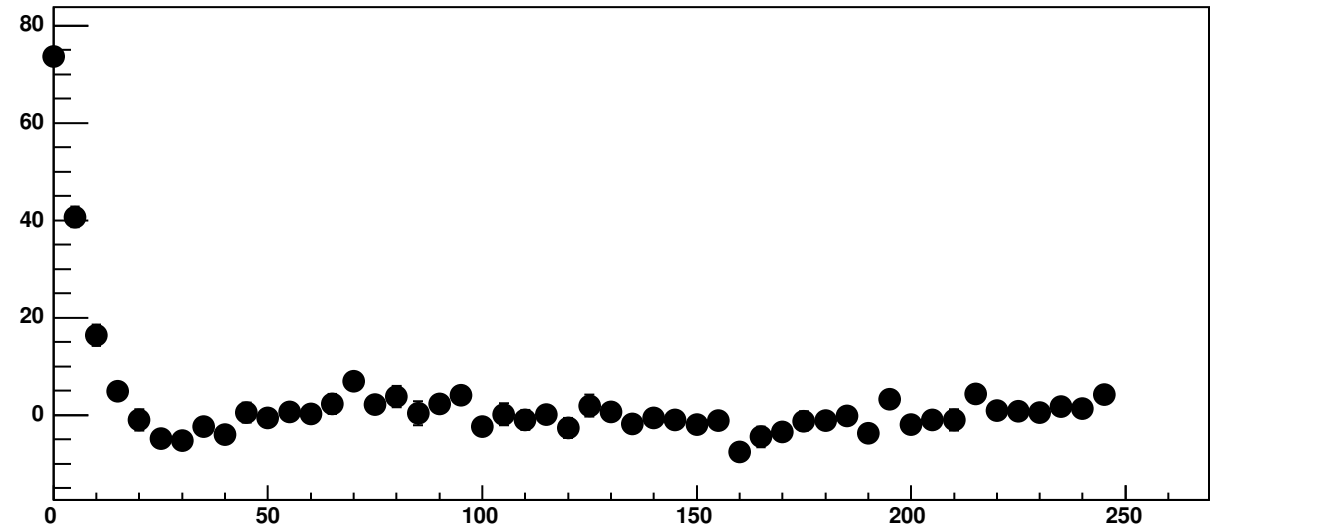
Chip 0, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold

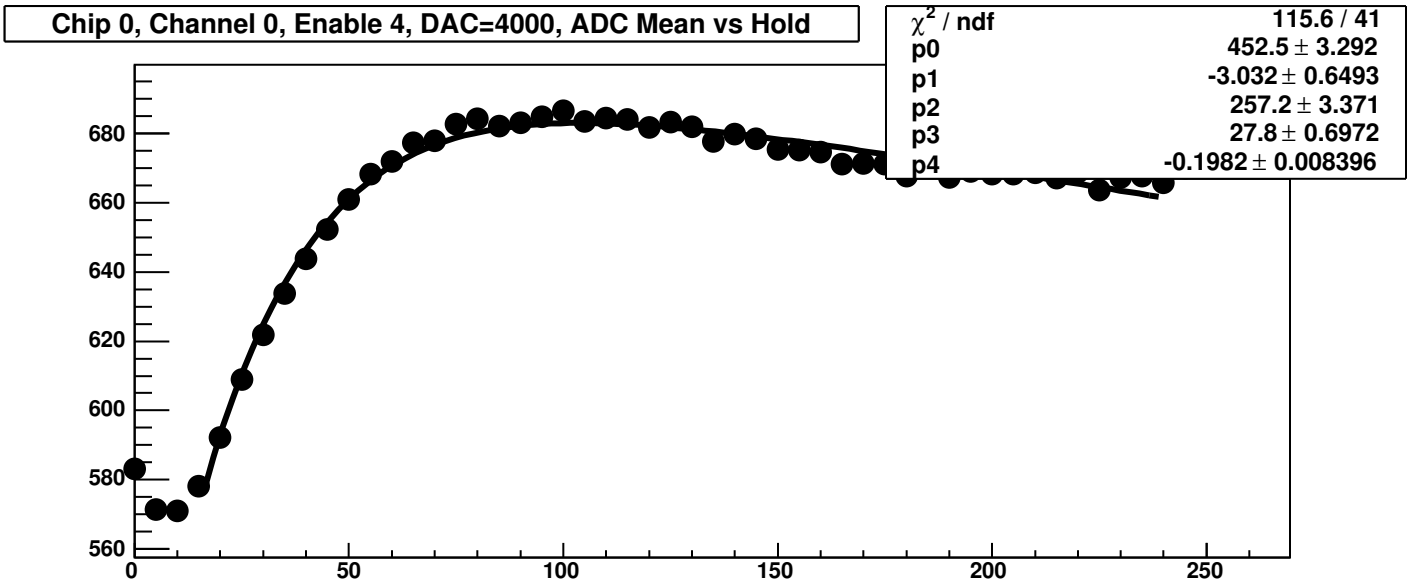


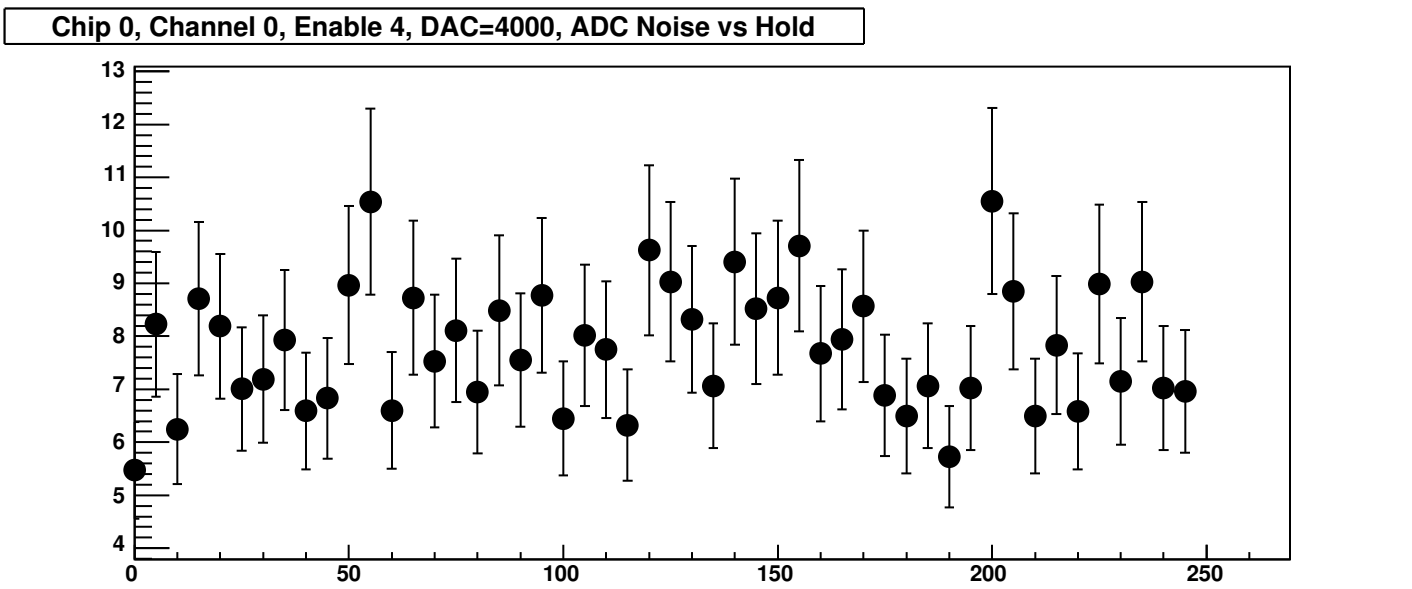
Chip 0, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold

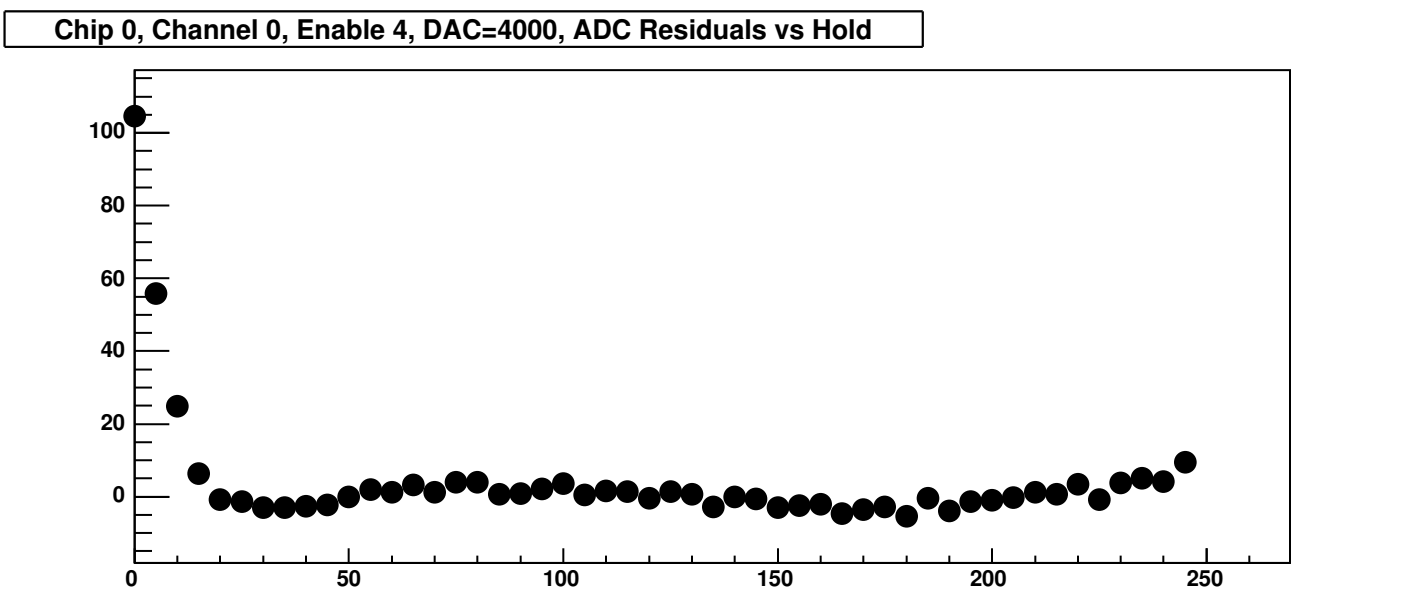


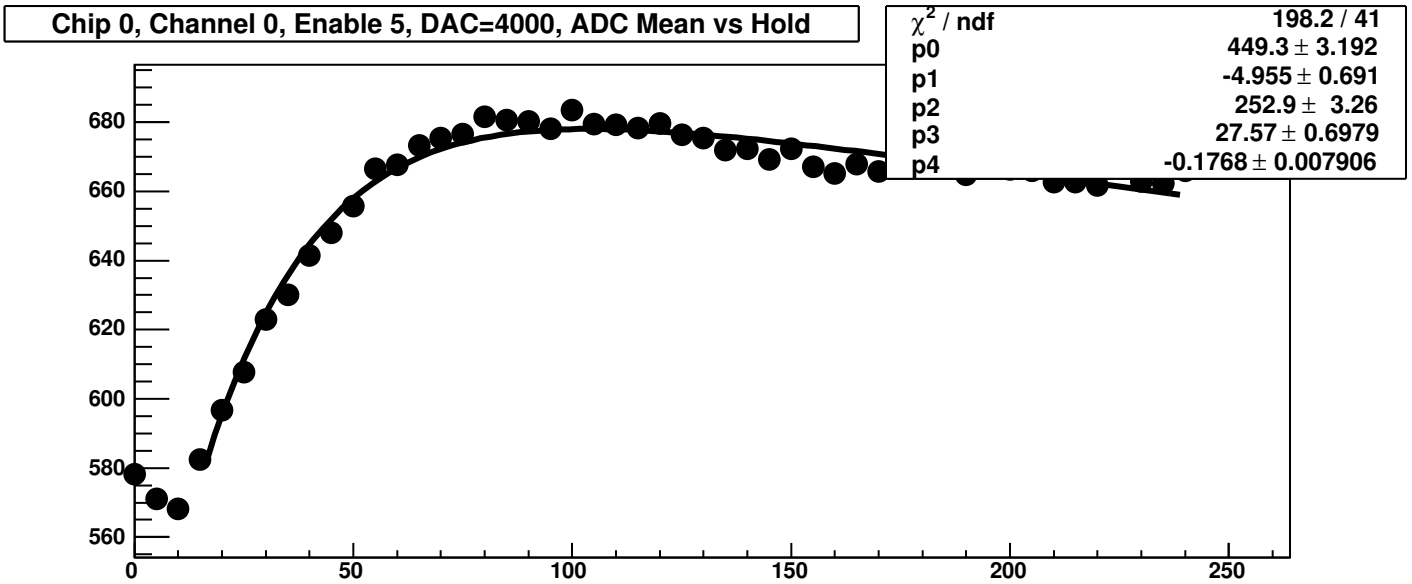
Chip 0, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold

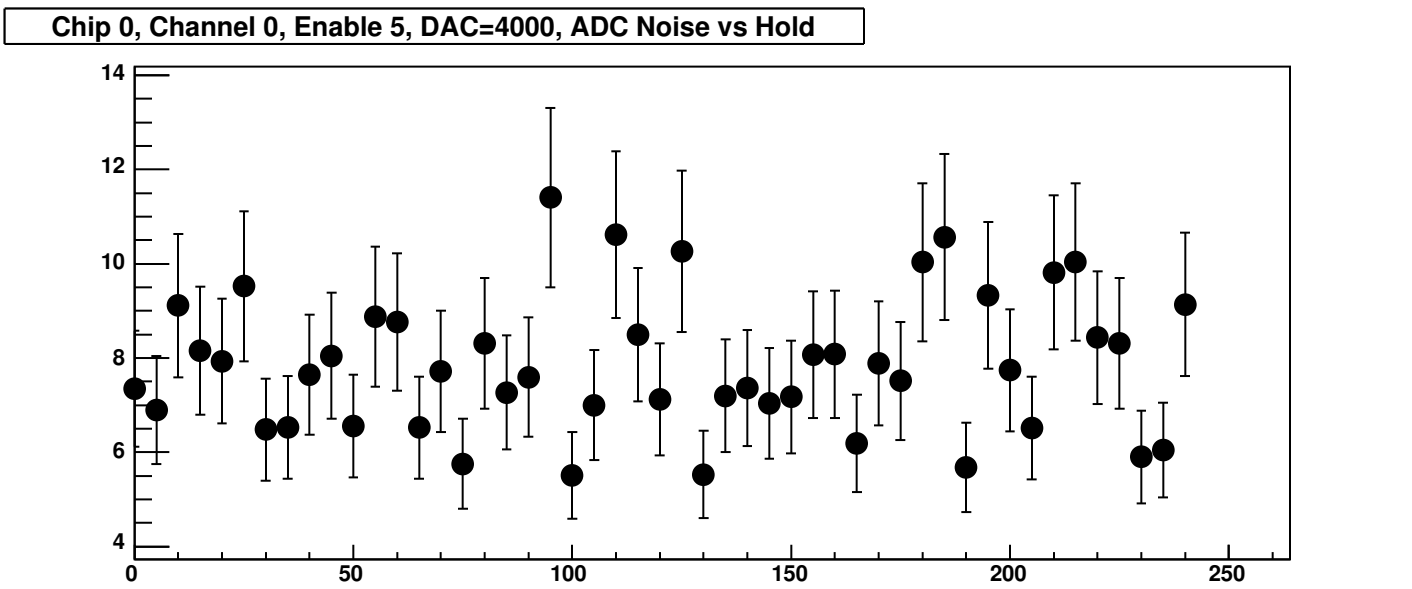


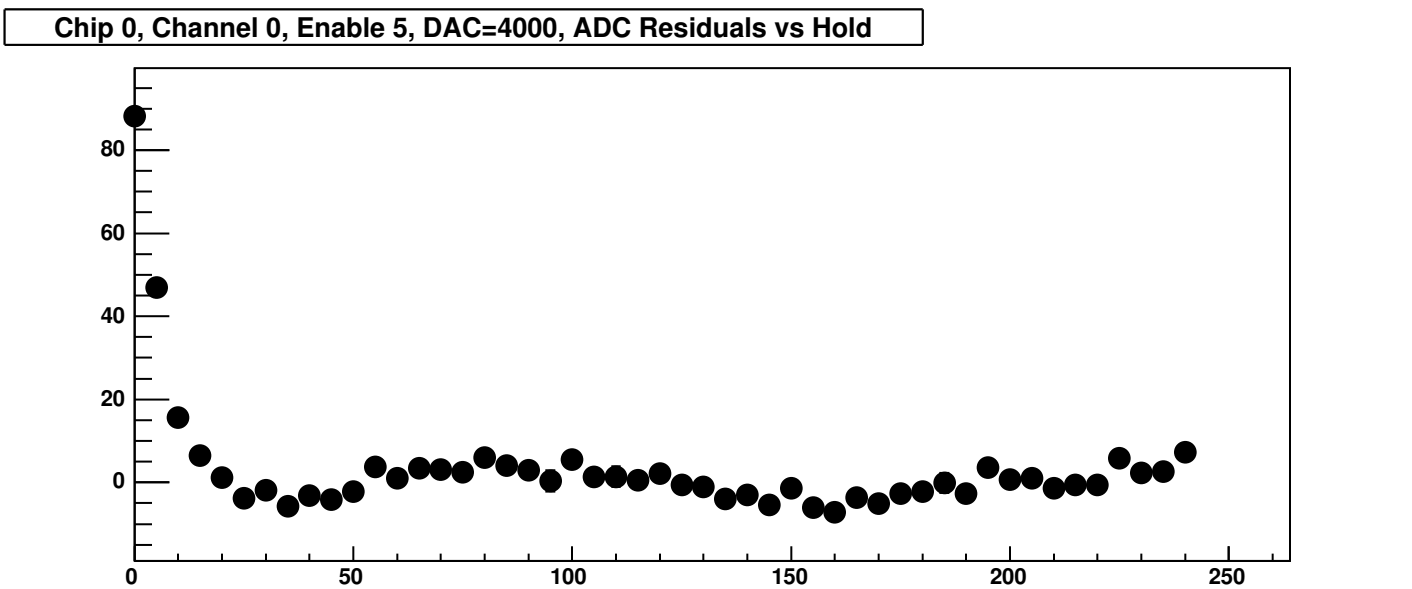

Chip 0, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 115.6 / 41
p0 452.5 ± 3.292
p1 -3.032 ± 0.6493
p2 257.2 ± 3.371
p3 27.8 ± 0.6972
p4 -0.1982 ± 0.008396
680
660
640
620
600
580
560
0
50
100
150
200
250


Chip 0, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

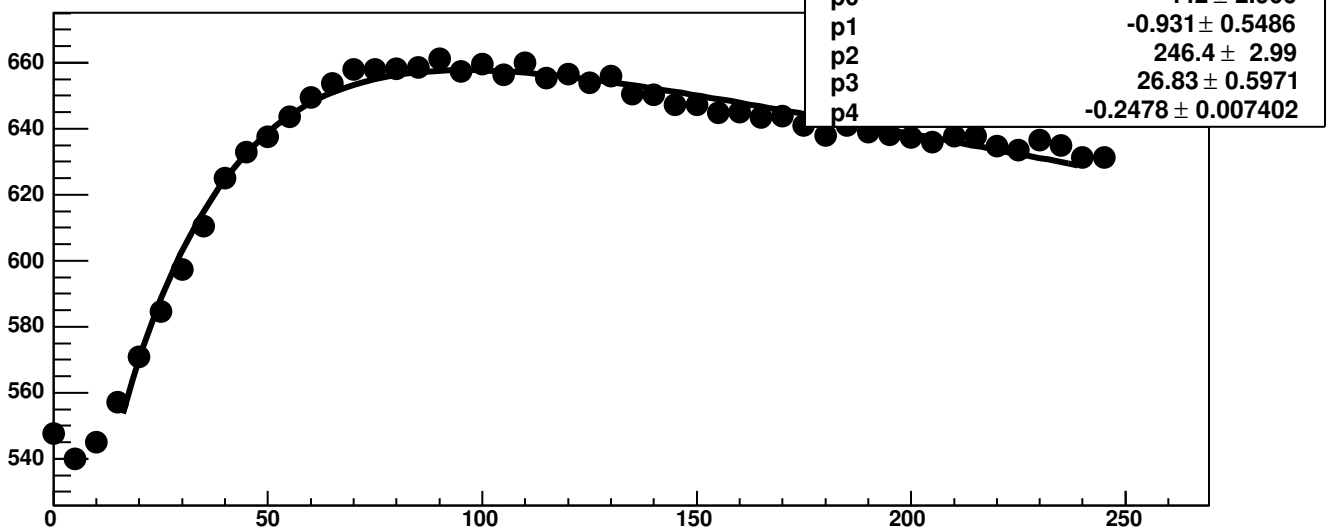

Chip 0, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 198.2 / 41
p0 449.3 ± 3.192
p1 -4.955 ± 0.691
p2 252.9 ± 3.26
p3 27.57 ± 0.6979
p4 -0.1768 ± 0.007906
680
660
640
620
600
580
560
0
50
100
150
200
250

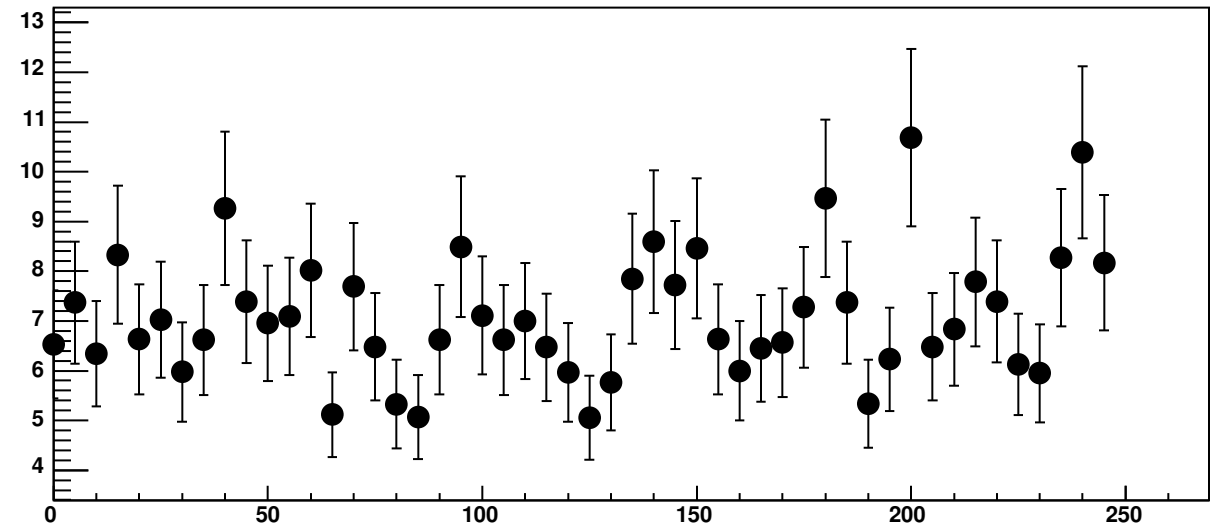

Chip 0, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 0, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold
80
60
40
20
0
0
50
100
150
200
250

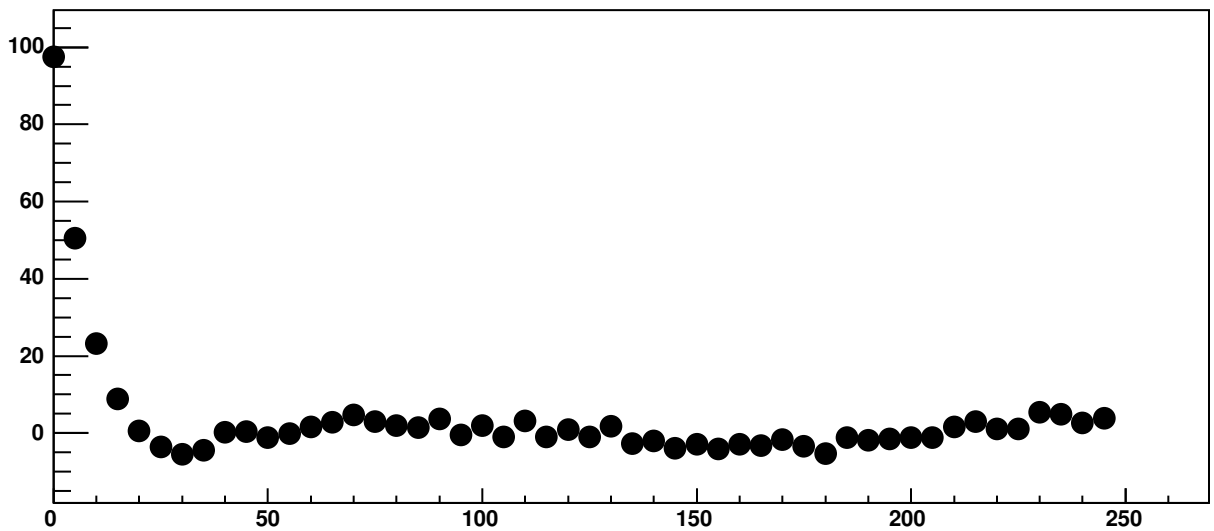
Chip 0, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold

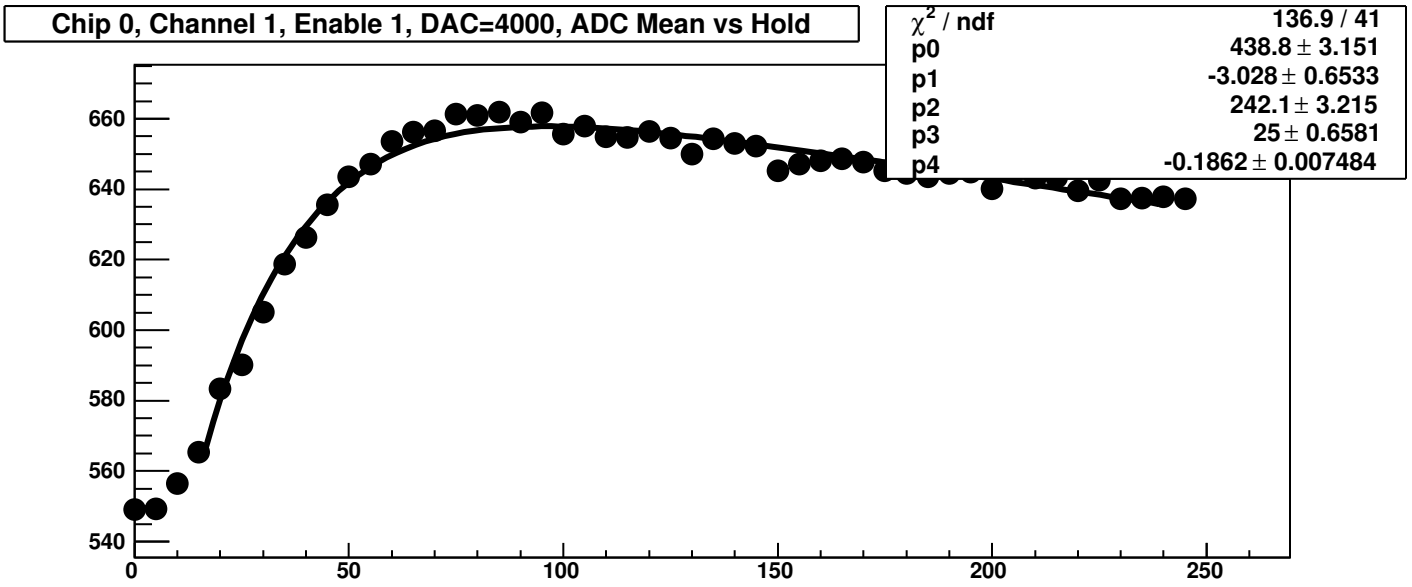


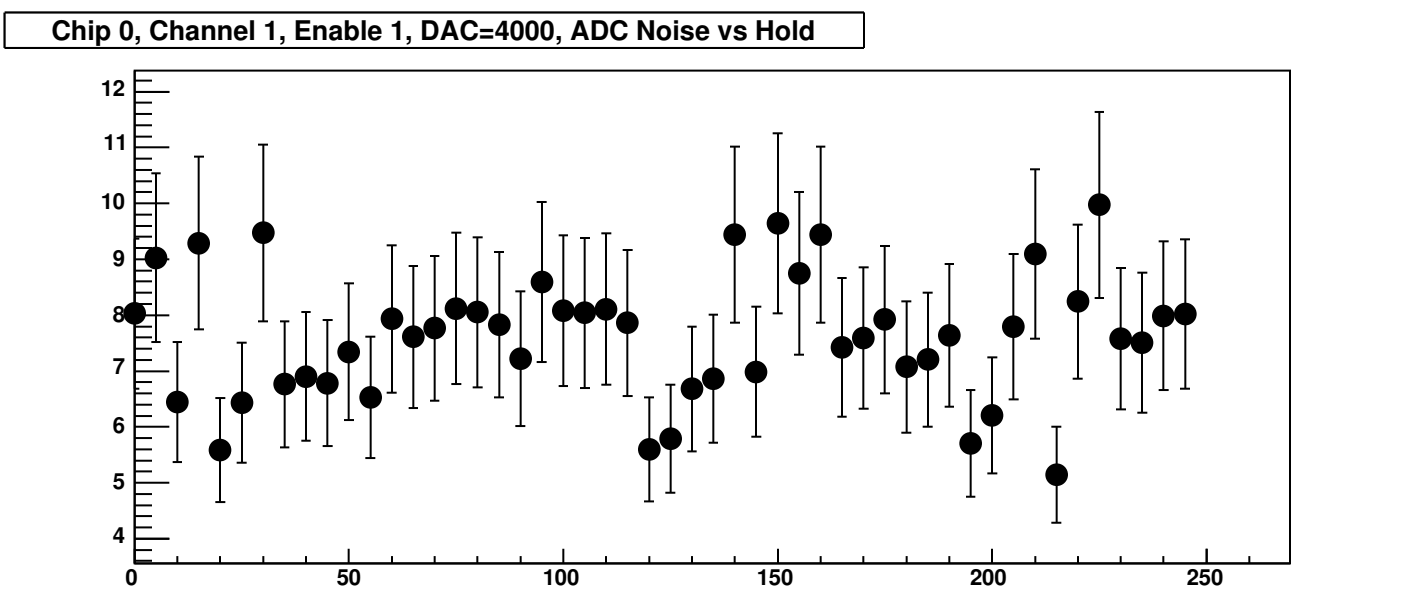
Chip 0, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold

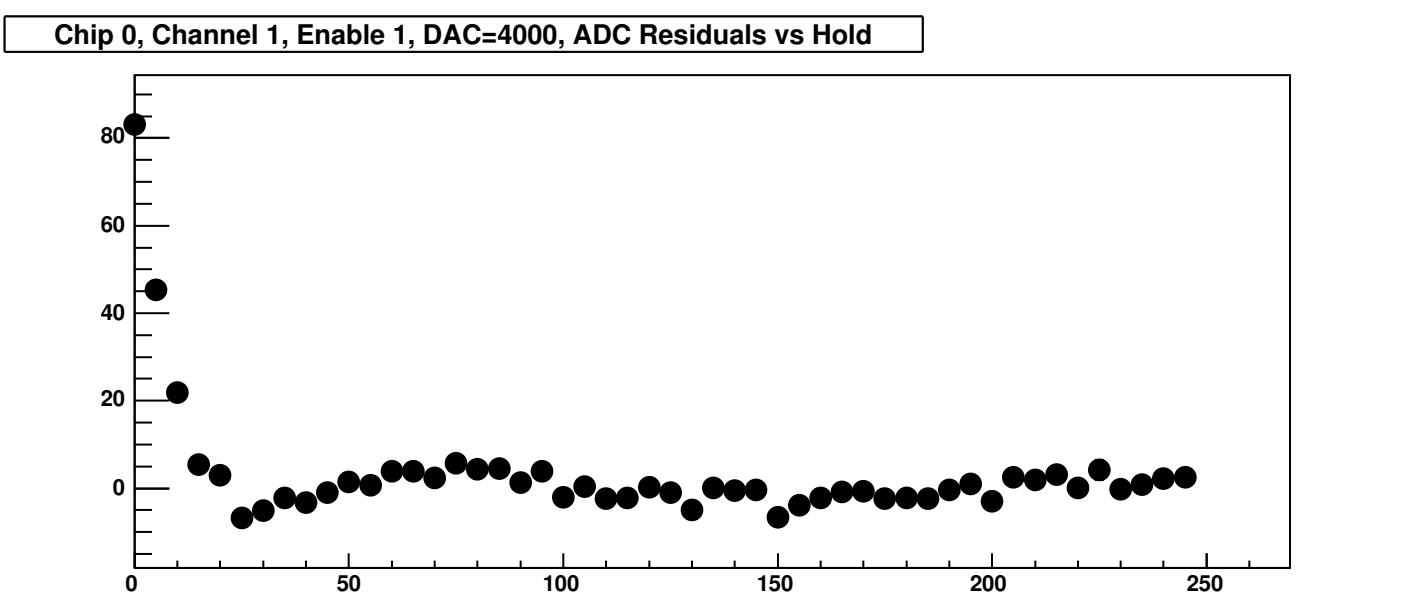


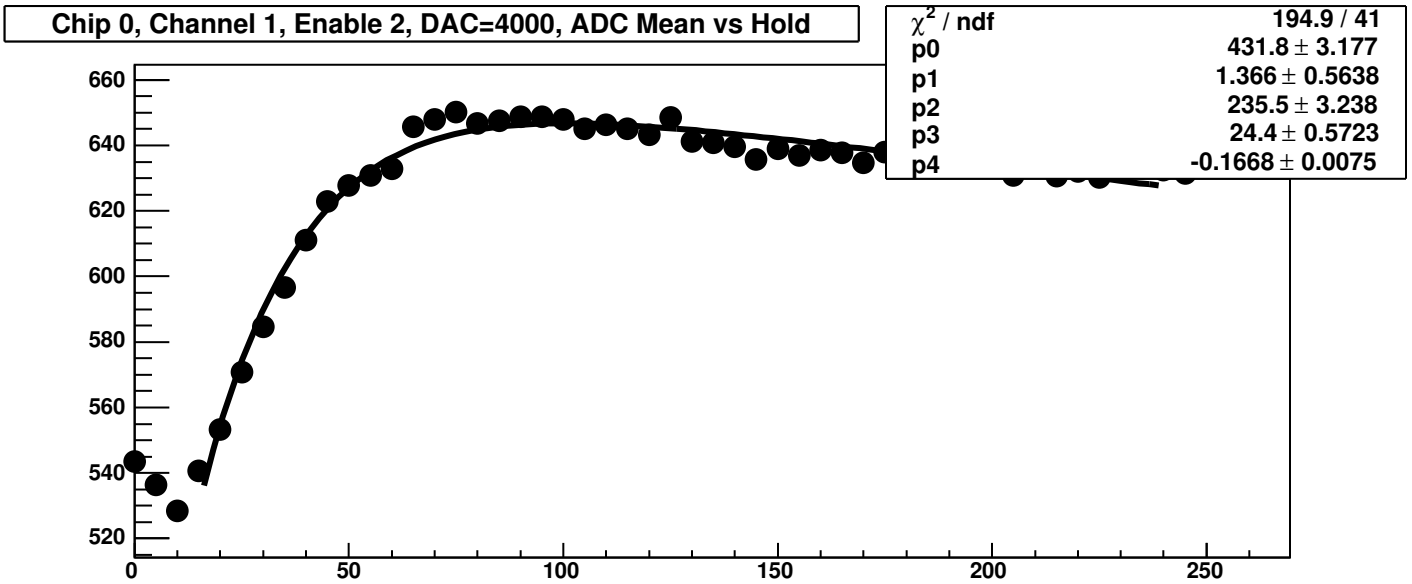
Chip 0, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold

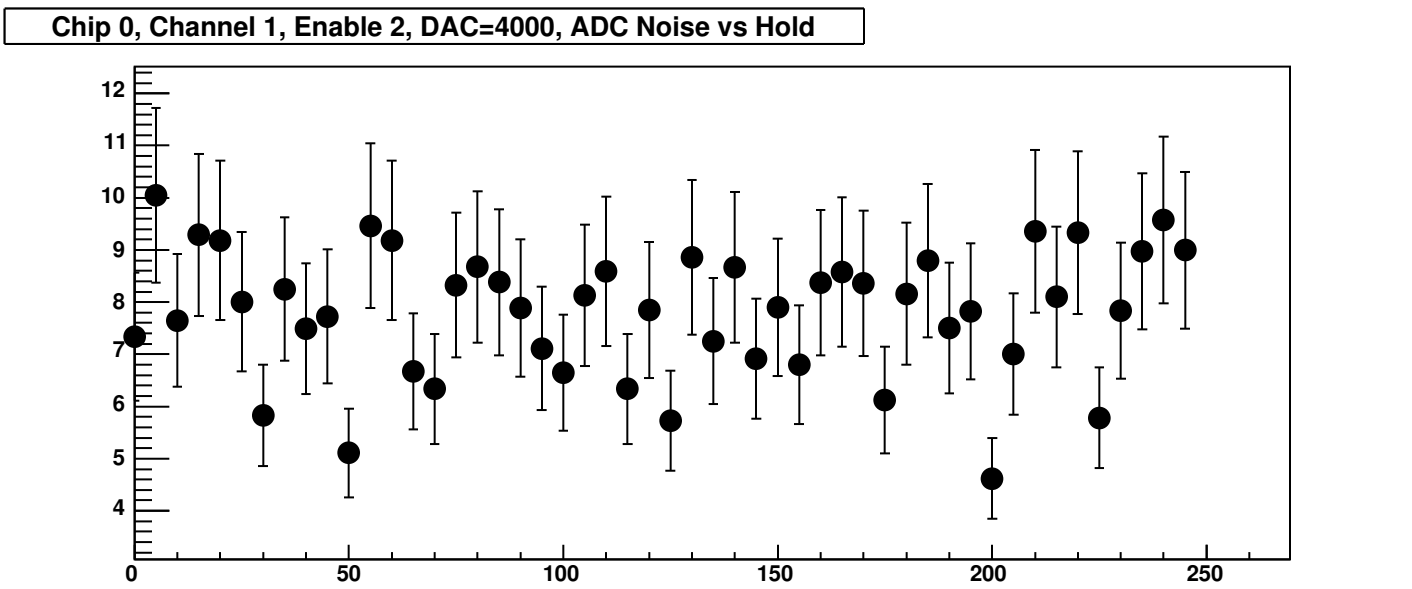


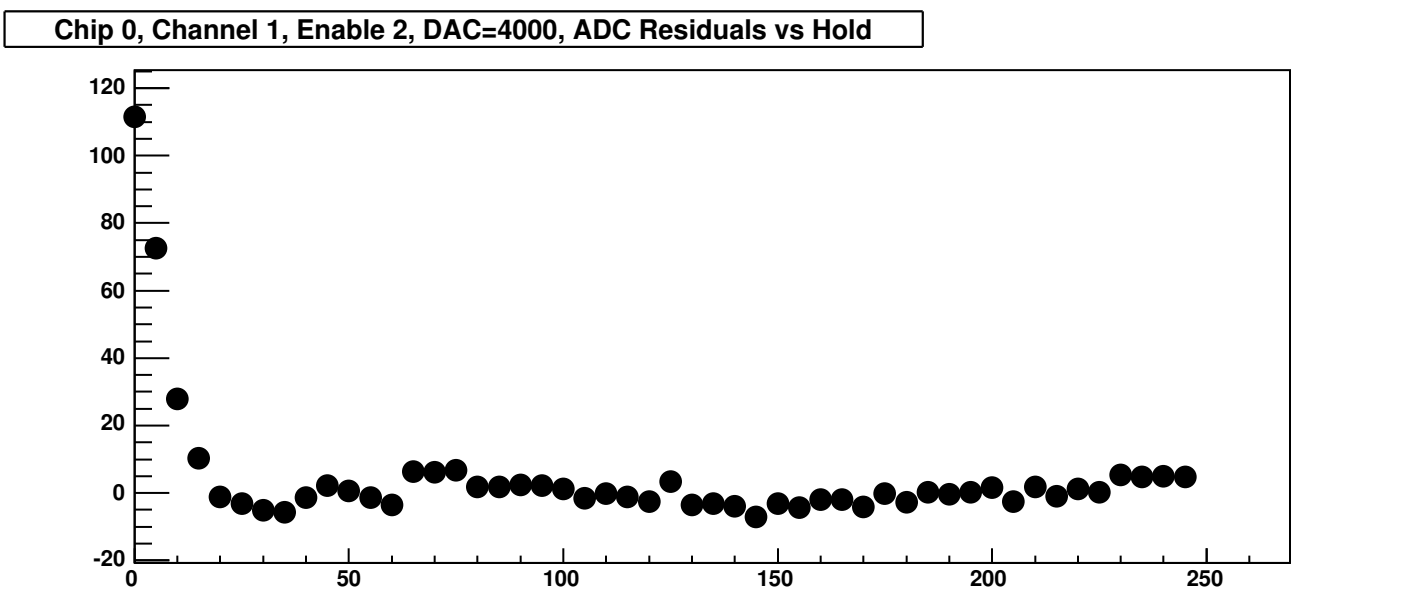

Chip 0, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 136.9 / 41
p0 438.8 ± 3.151
p1 -3.028 ± 0.6533
p2 242.1 ± 3.215
p3 25 ± 0.6581
p4 -0.1862 ± 0.007484
660
640
620
600
580
560
540
0
50
100
150
200
250


Chip 0, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 0, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold
80
60
40
20
0
0
50
100
150
200
250

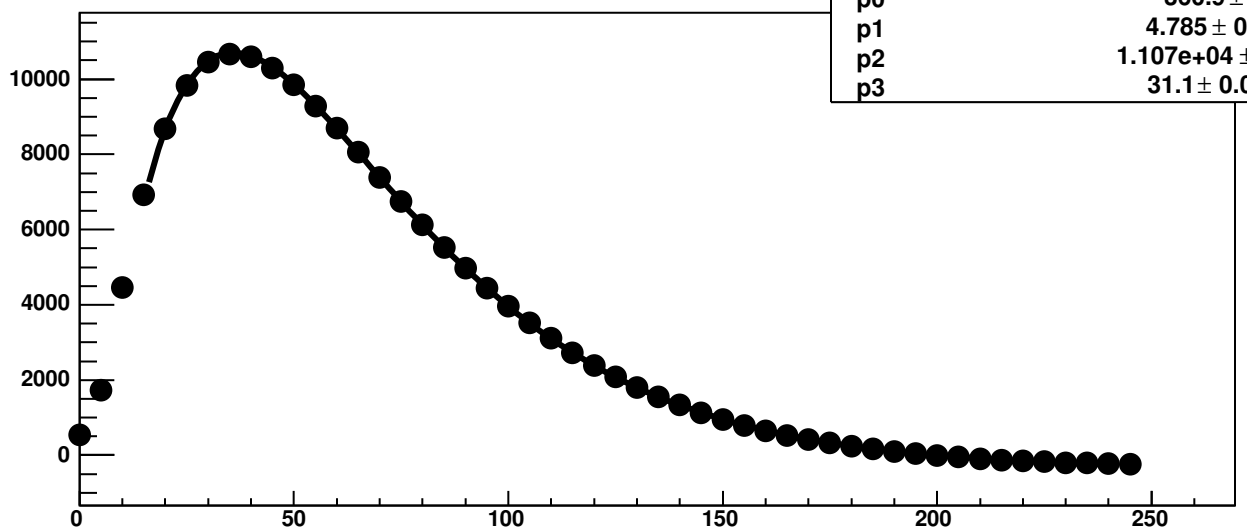

Chip 0, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 194.9 / 41
p0 431.8 ± 3.177
p1 1.366 ± 0.5638
p2 235.5 ± 3.238
p3 24.4 ± 0.5723
p4 -0.1668 ± 0.0075
660
640
620
600
580
560
540
520
0
50
100
150
200
250


Chip 0, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

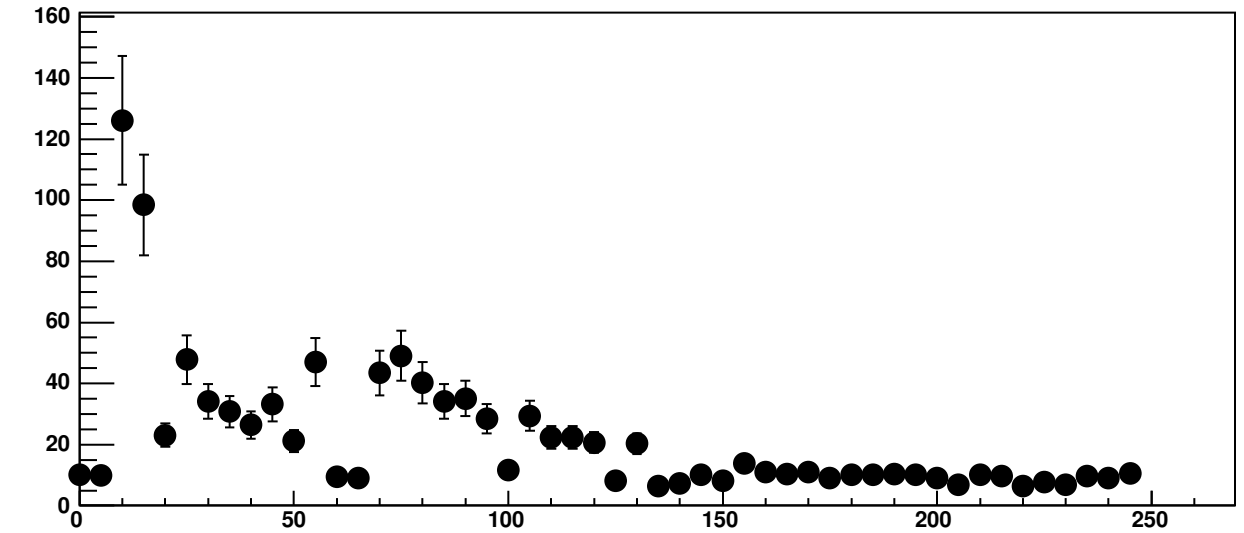

Chip 0, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold
120
100
80
60
40
20
0
-20
0
50
100
150
200
250

Chip 0, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold

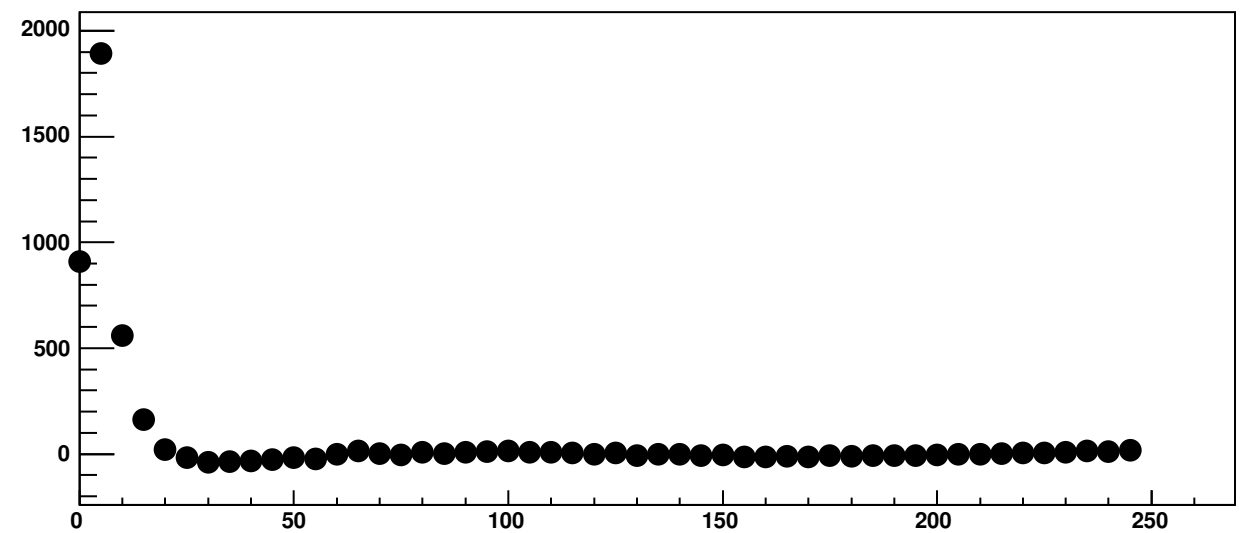
χ^2 / ndf	597.8 / 42
p0	-360.9 ± 0.7371
p1	4.785 ± 0.01586
p2	1.107e+04 ± 1.955
p3	31.1 ± 0.008516

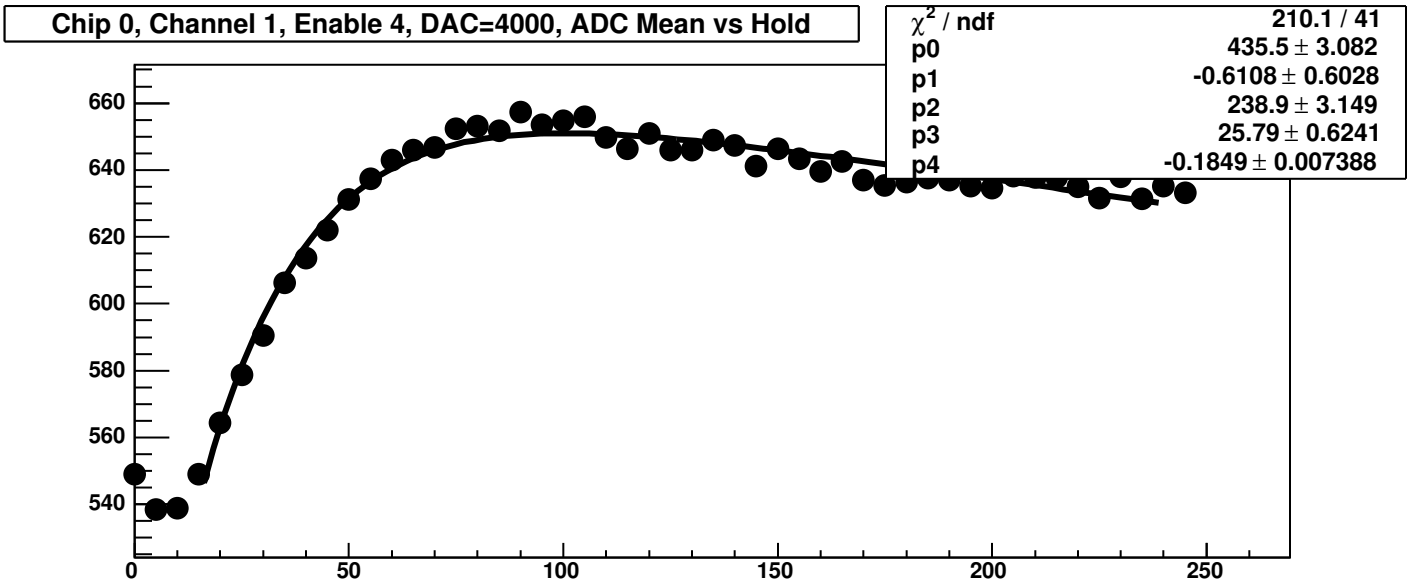


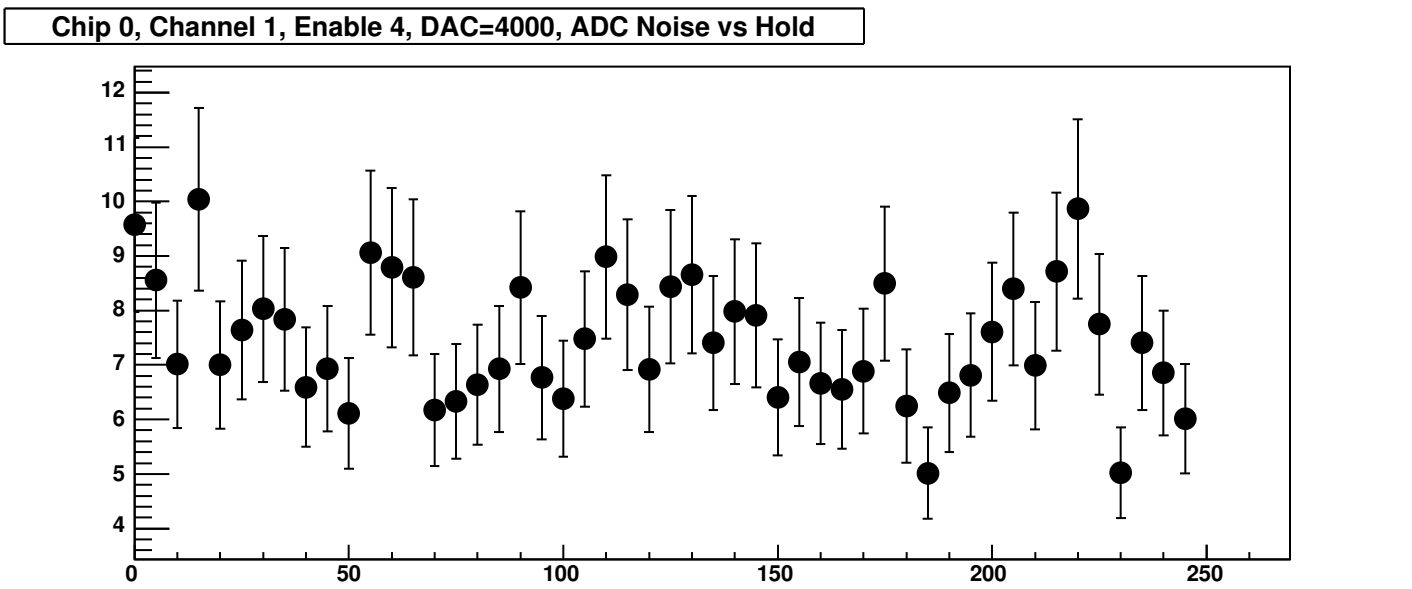
Chip 0, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold

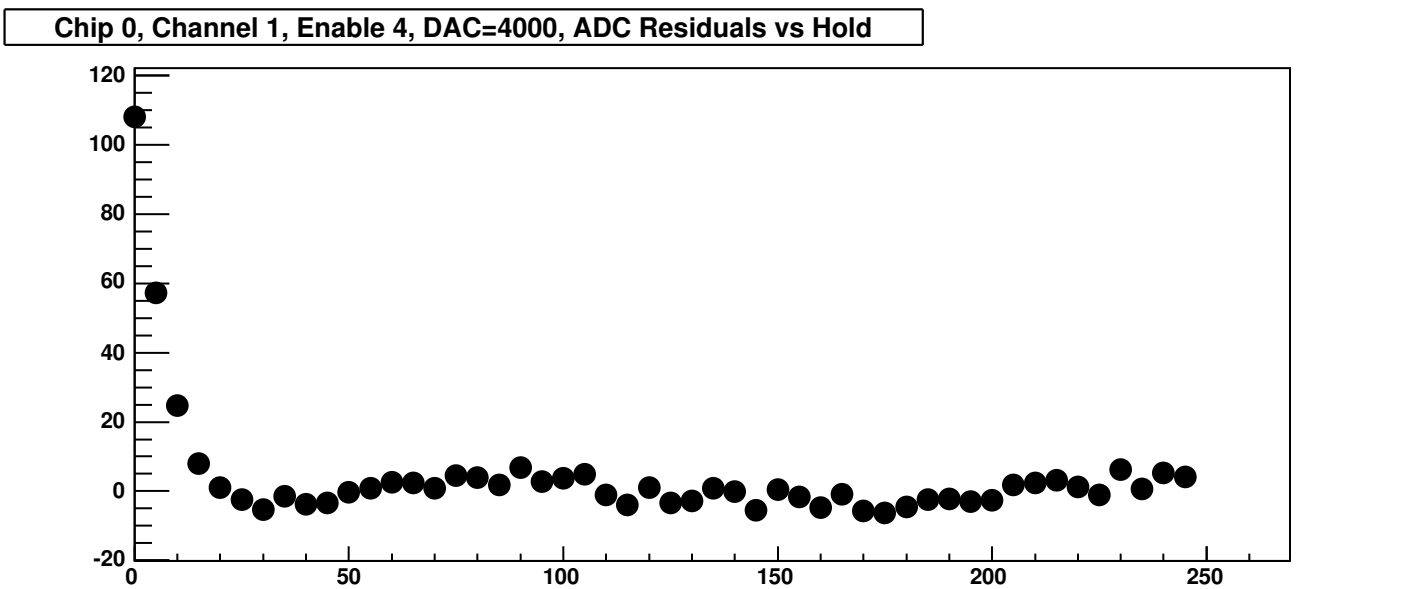


Chip 0, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold

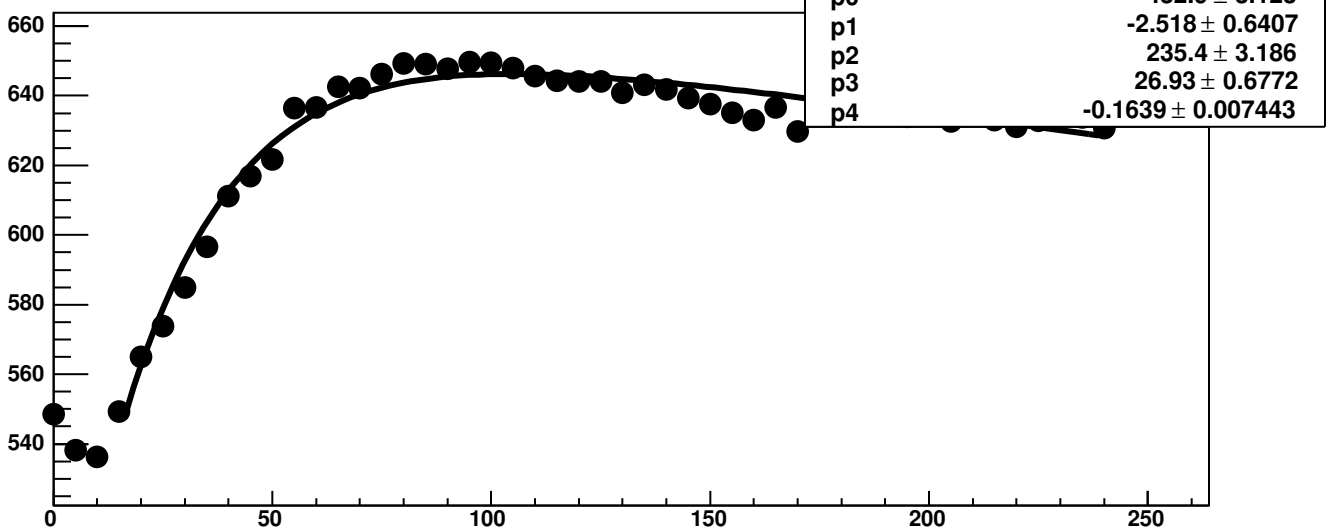



Chip 0, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 210.1 / 41
p0 435.5 ± 3.082
p1 -0.6108 ± 0.6028
p2 238.9 ± 3.149
p3 25.79 ± 0.6241
p4 -0.1849 ± 0.007388
660
640
620
600
580
560
540
0
50
100
150
200
250

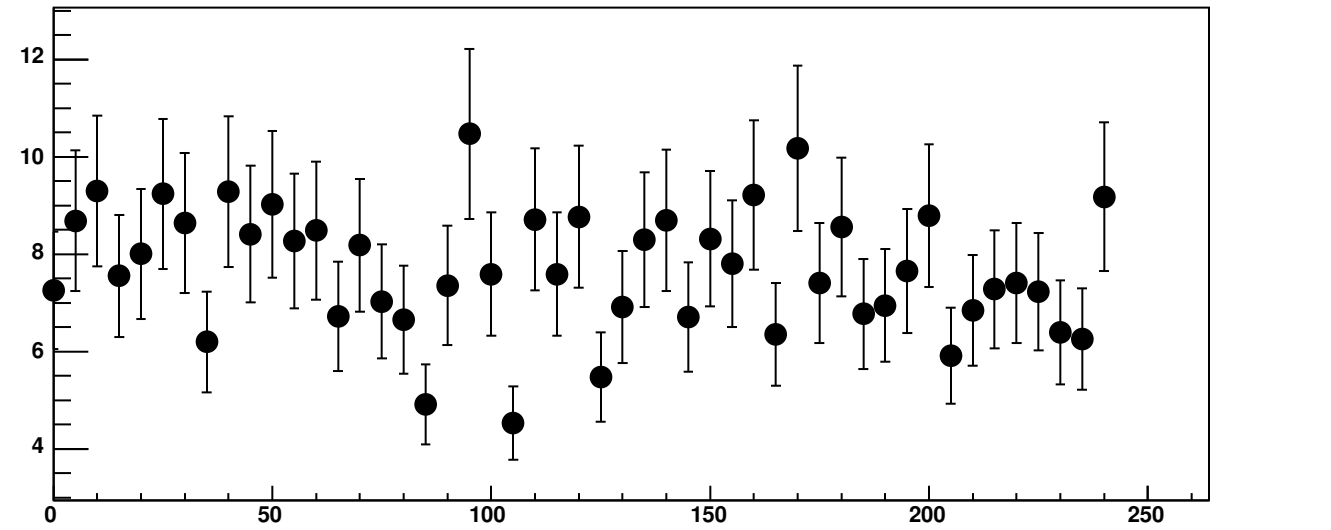

Chip 0, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 0, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold
120
100
80
60
40
20
0
-20
0
50
100
150
200
250

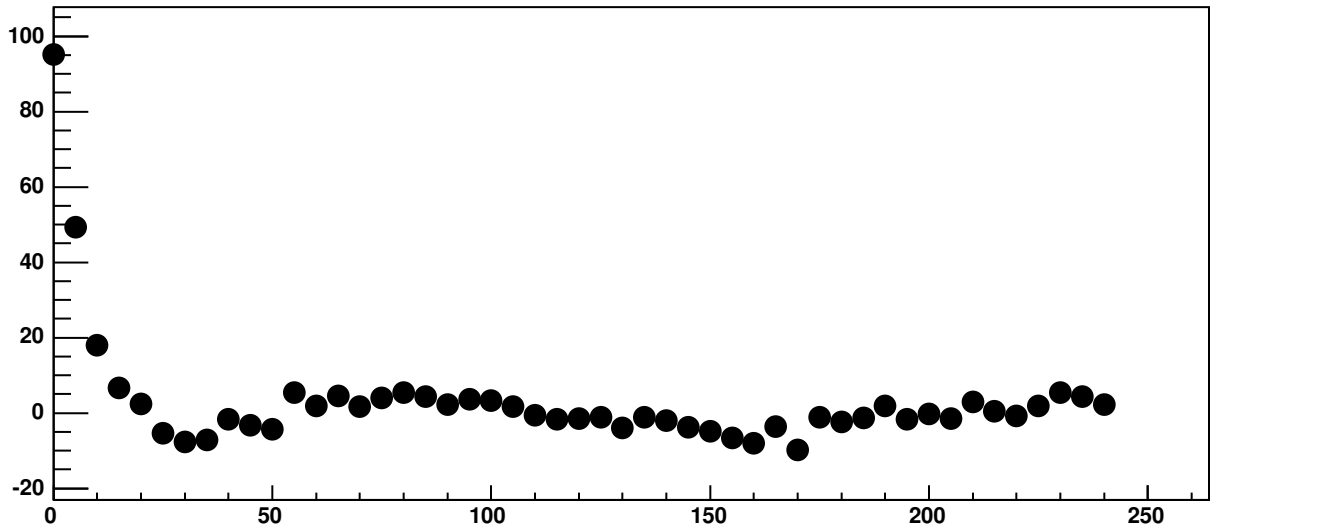
Chip 0, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold



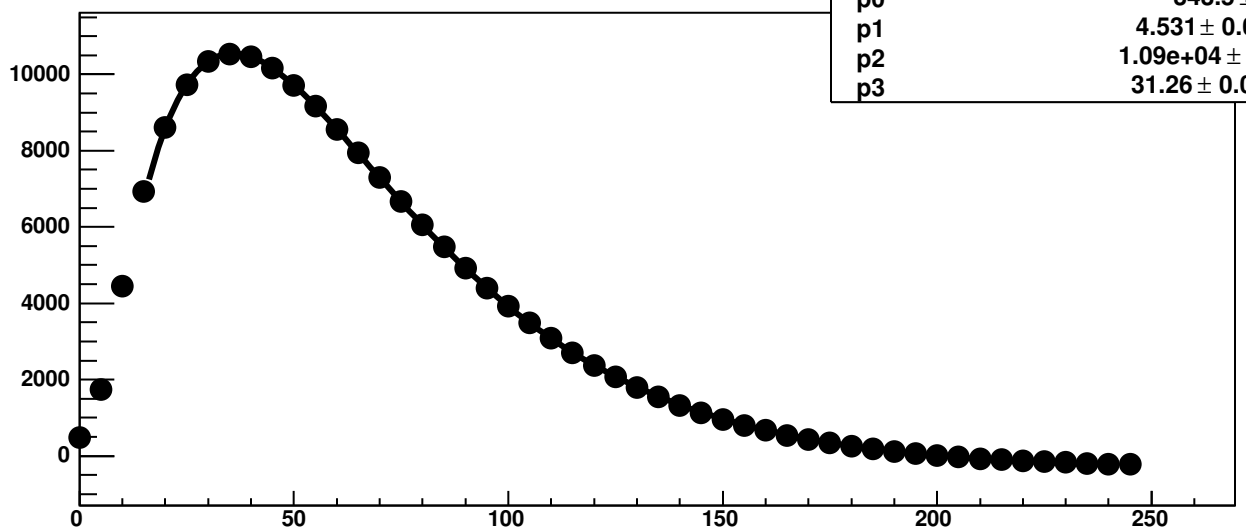
Chip 0, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



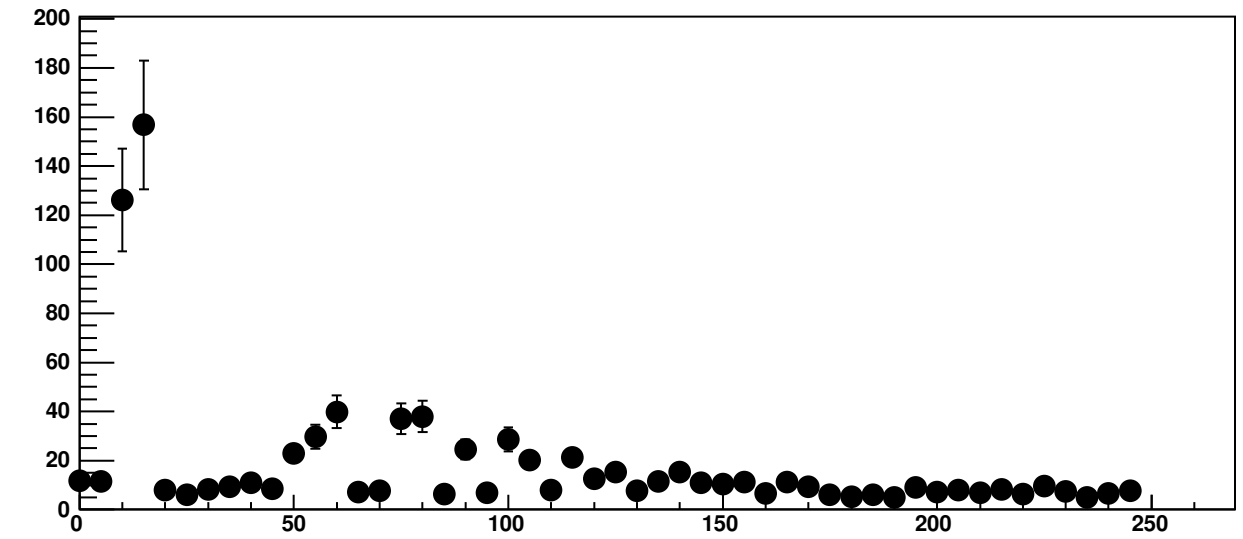
Chip 0, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold



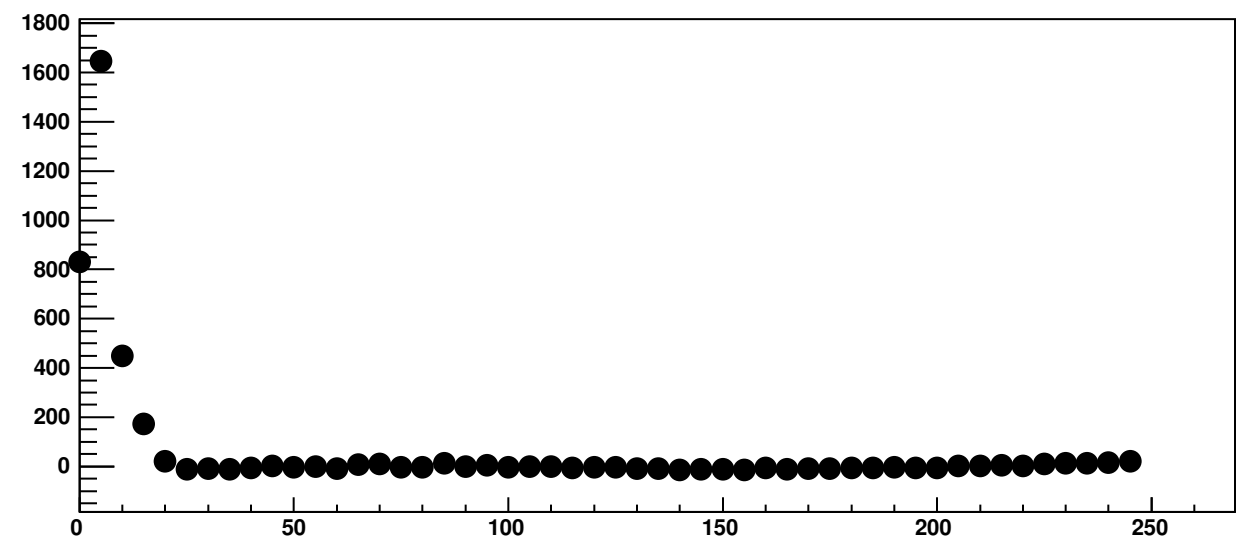
Chip 0, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold

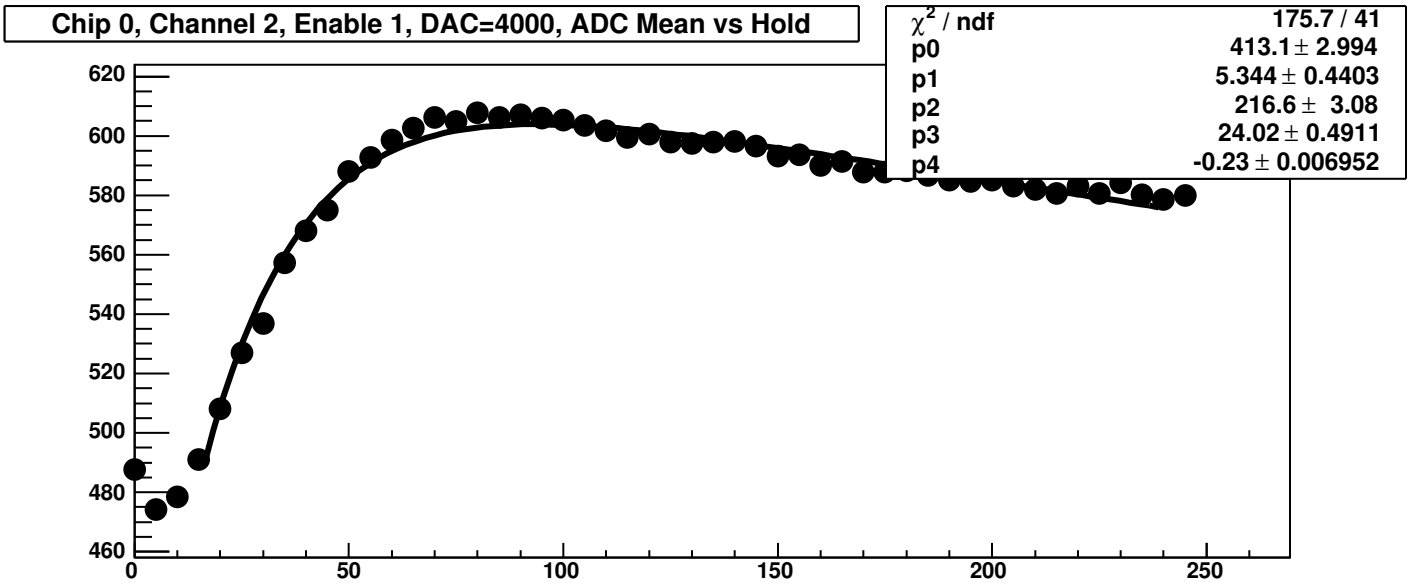


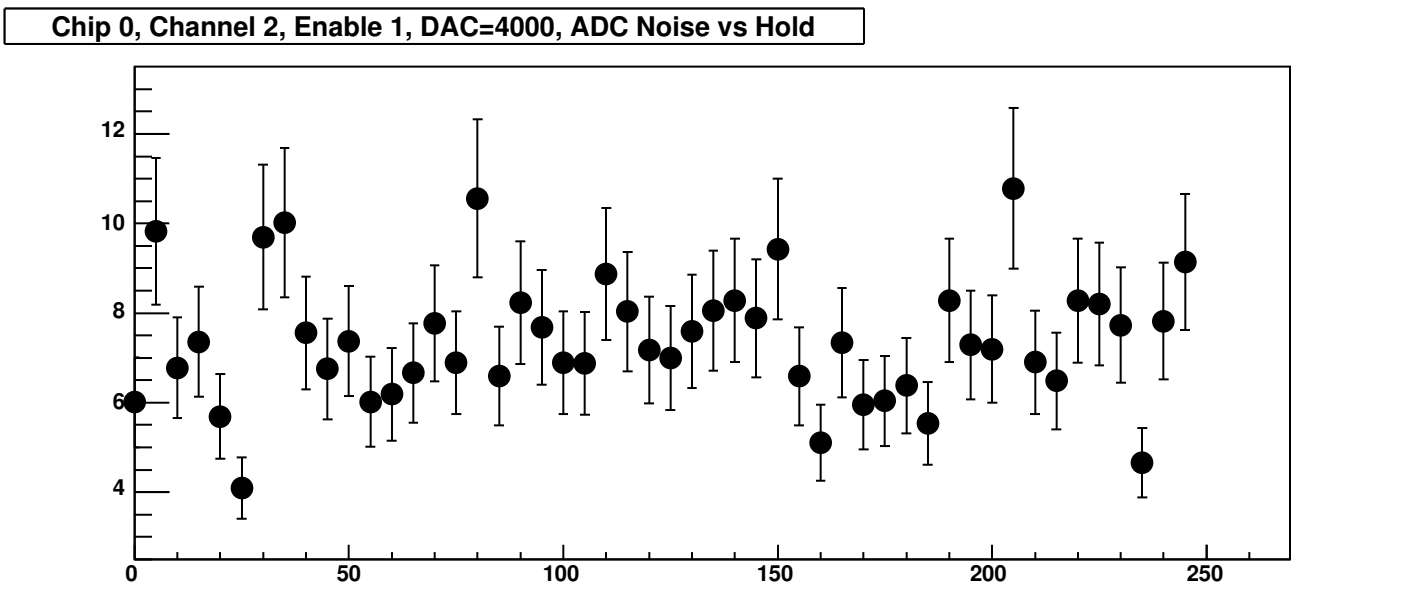
Chip 0, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold

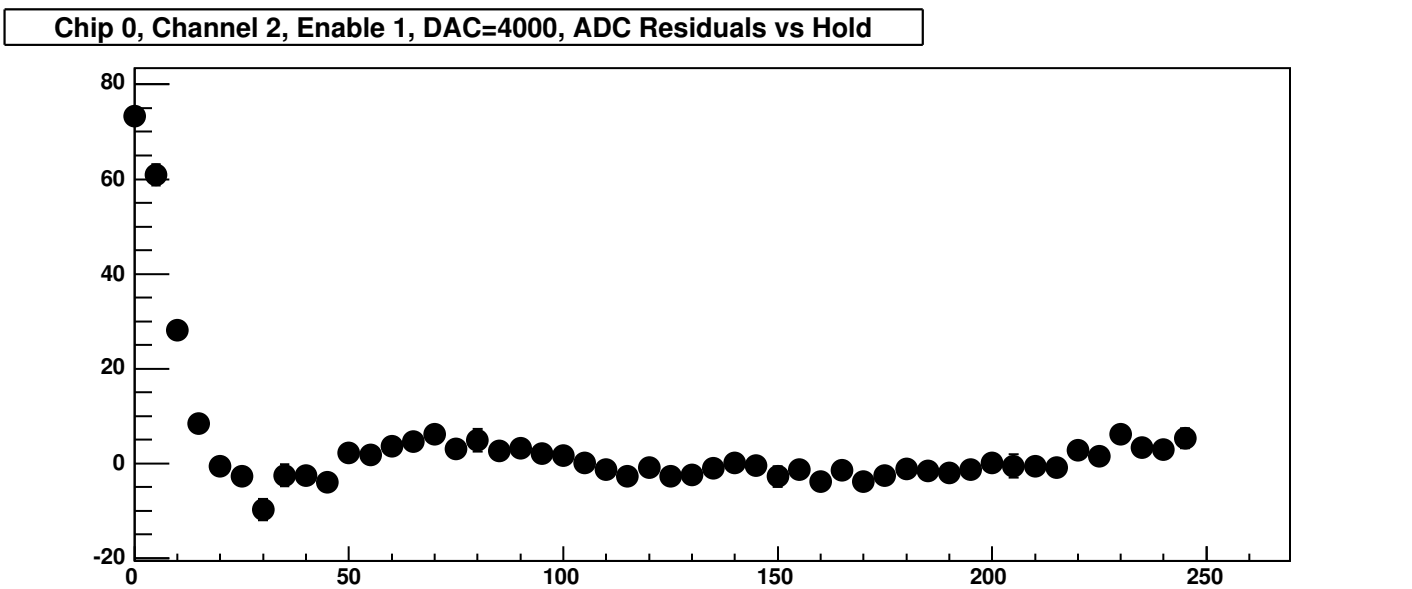


Chip 0, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold

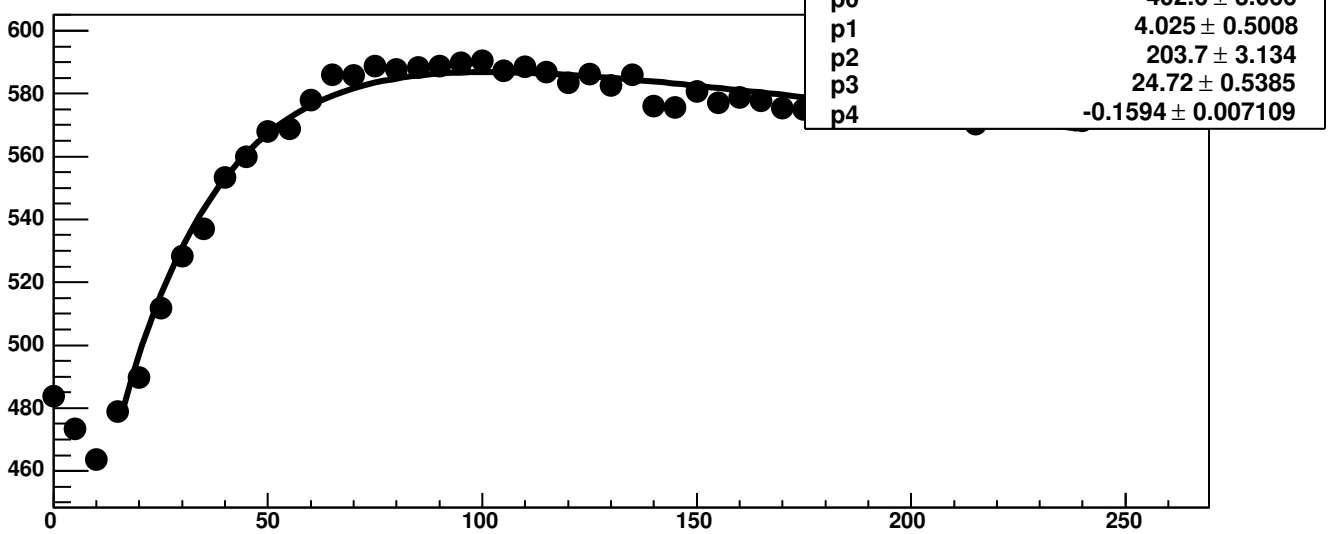



Chip 0, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 175.7 / 41
p0 413.1 ± 2.994
p1 5.344 ± 0.4403
p2 216.6 ± 3.08
p3 24.02 ± 0.4911
p4 -0.23 ± 0.006952
620
600
580
560
540
520
500
480
460
0
50
100
150
200
250

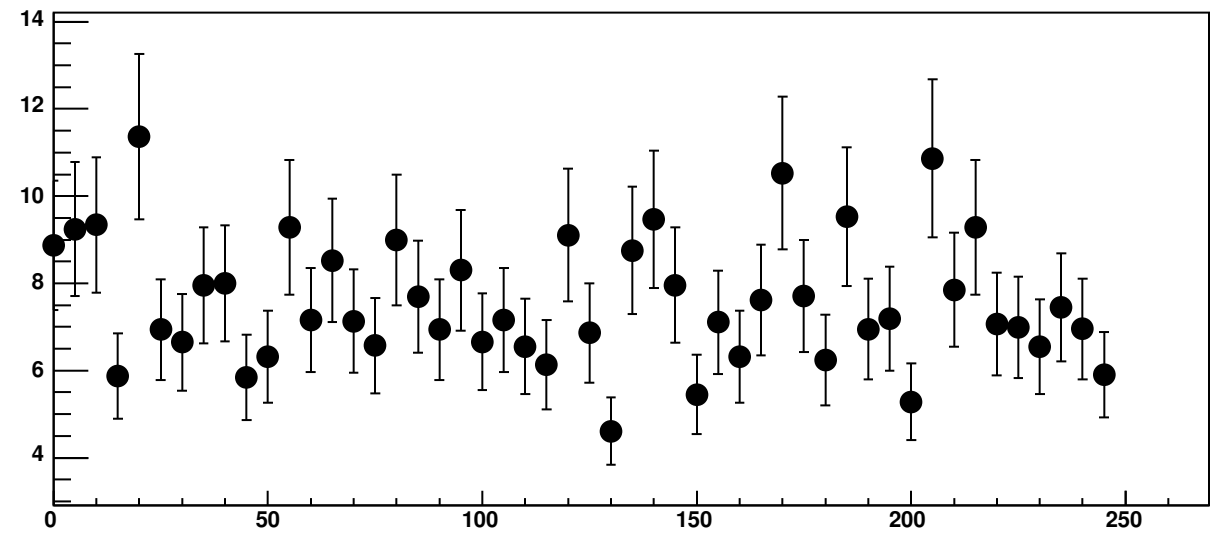

Chip 0, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 0, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold
80
60
40
20
0
-20
0
50
100
150
200
250

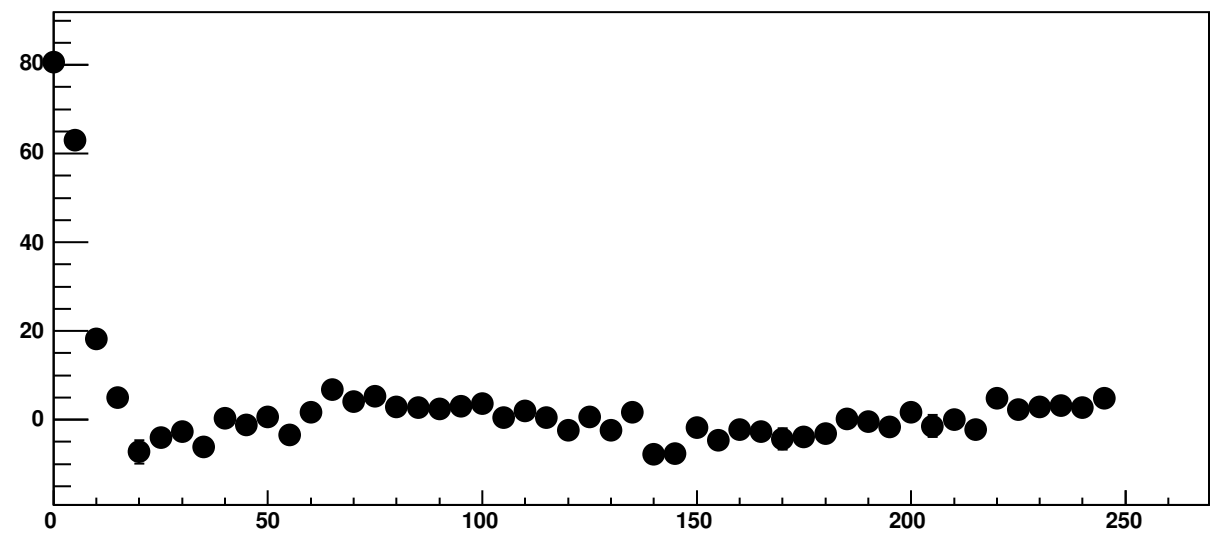
Chip 0, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold



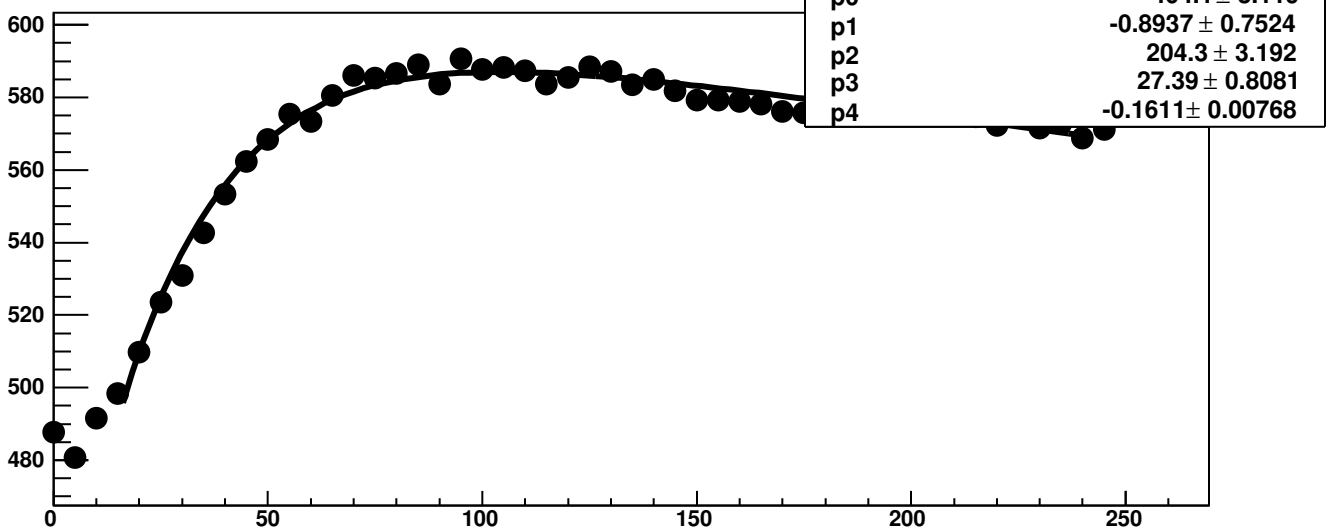
Chip 0, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



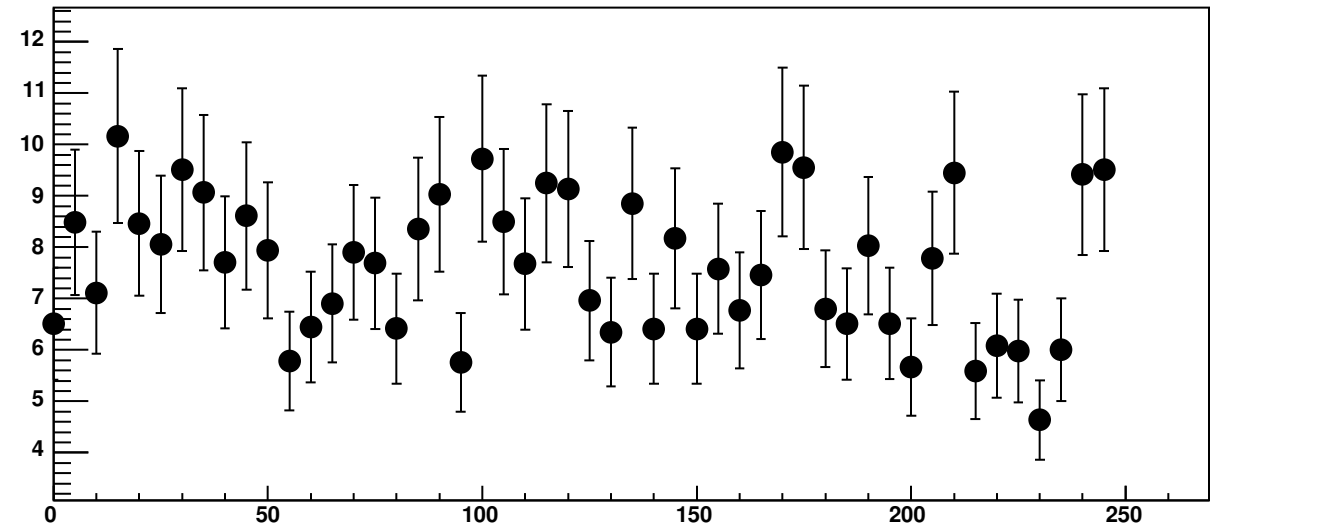
Chip 0, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold



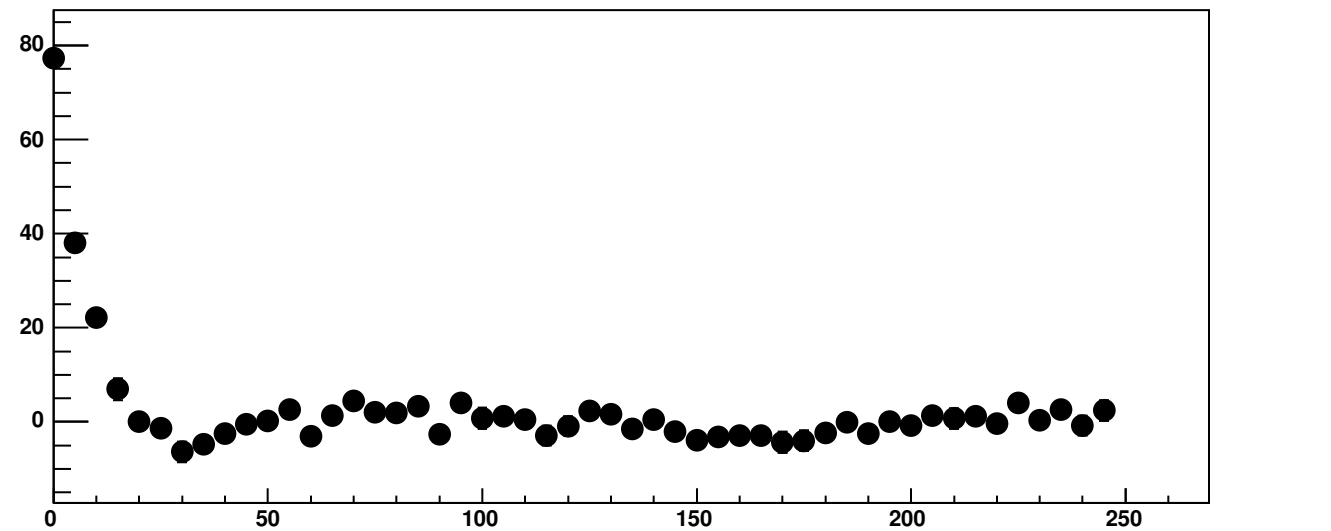
Chip 0, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold



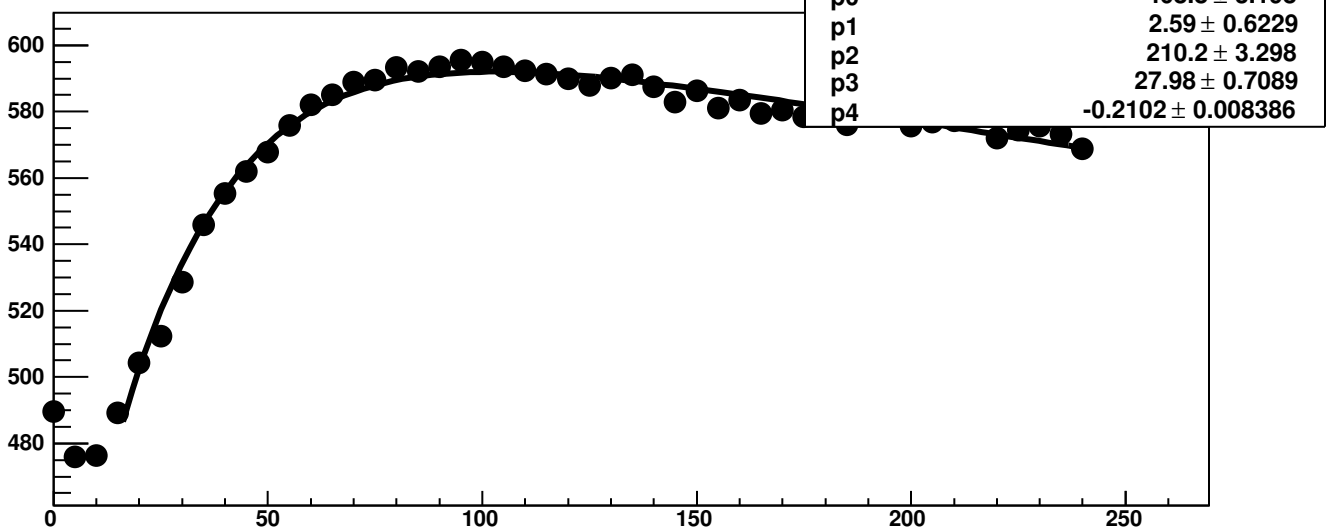
Chip 0, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold



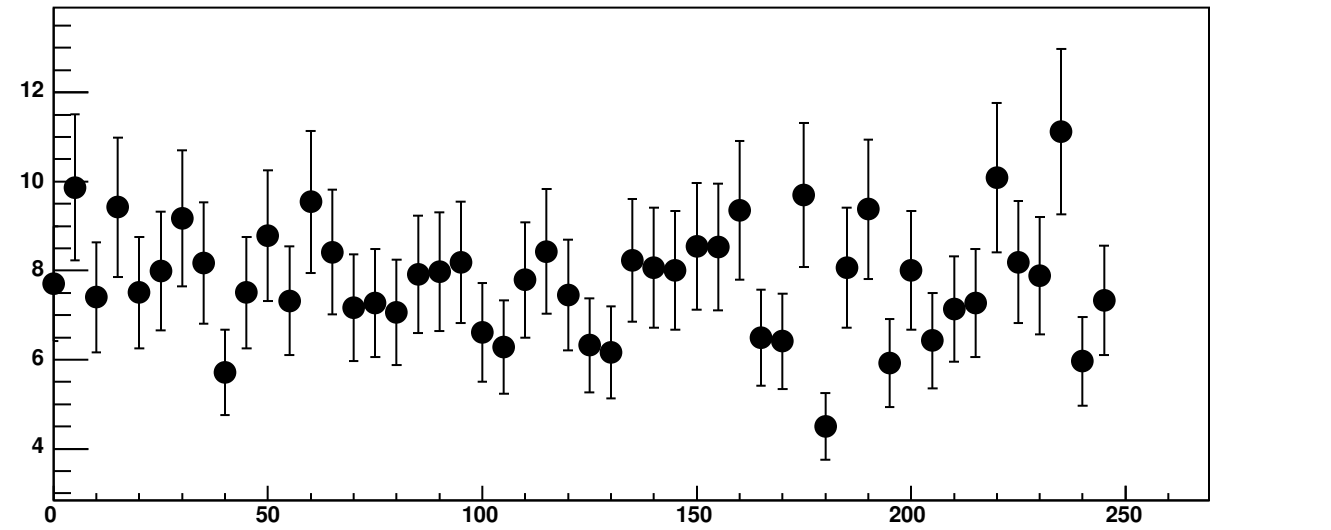
Chip 0, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold



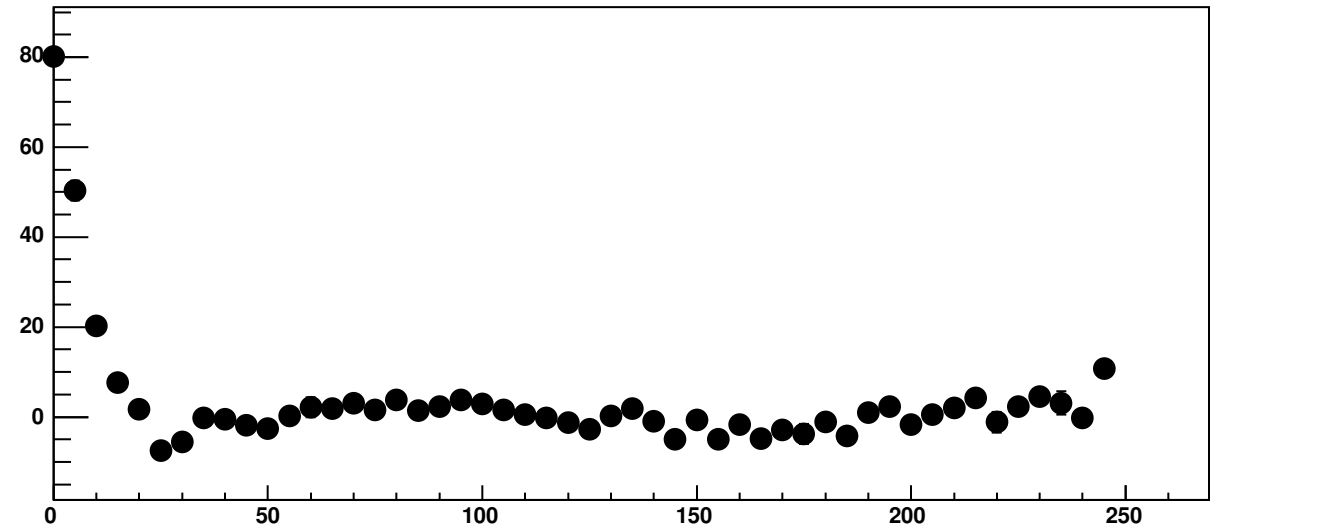
Chip 0, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold



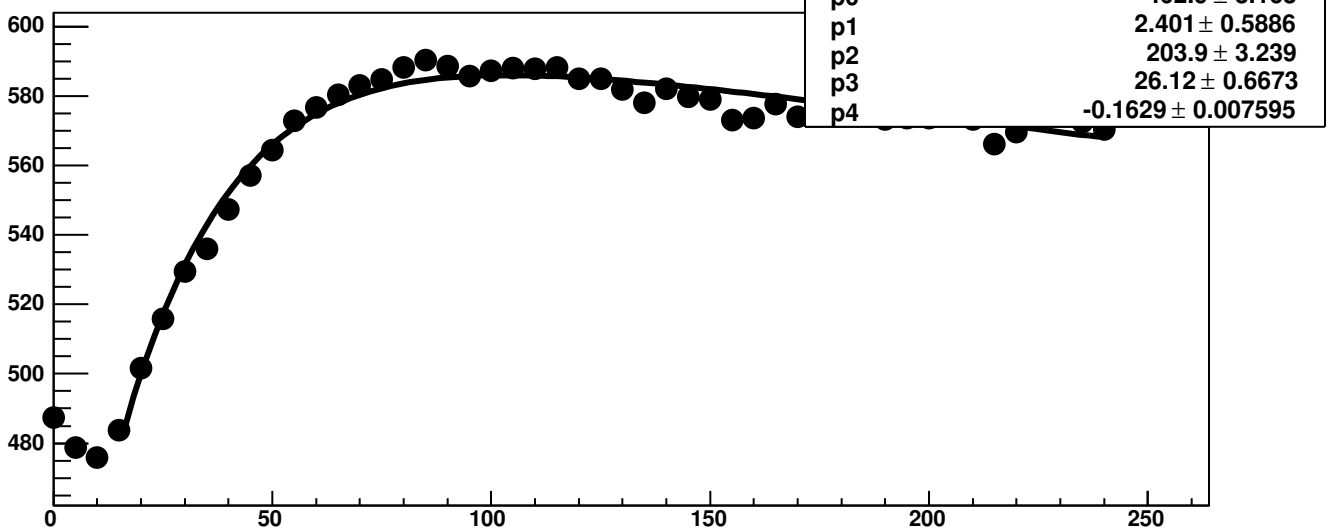
Chip 0, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



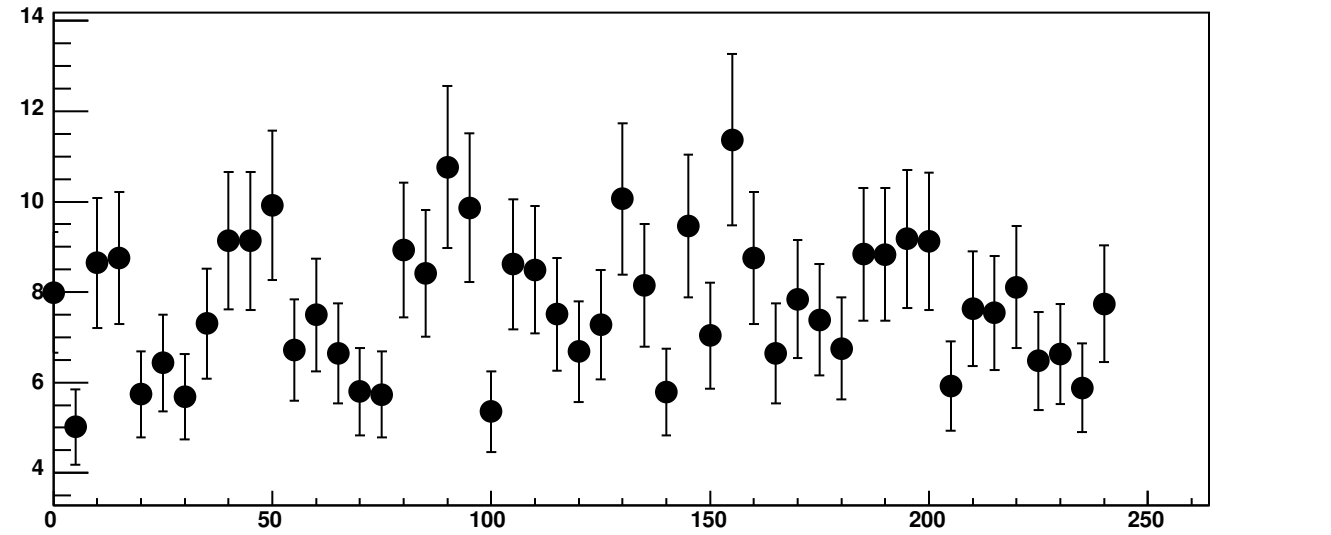
Chip 0, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold



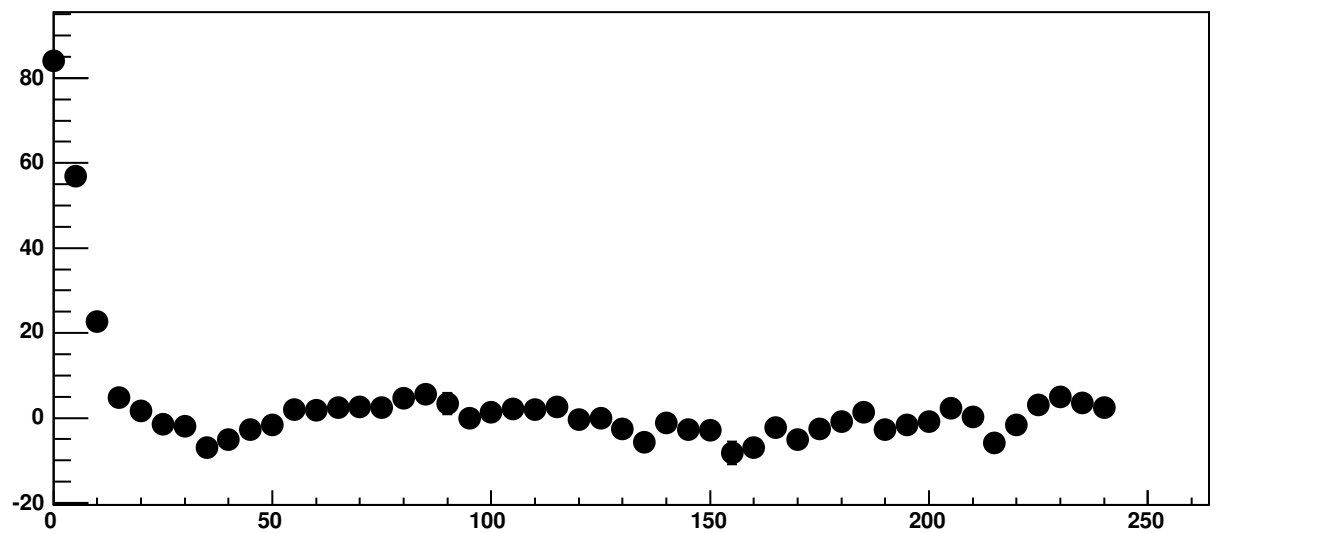
Chip 0, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

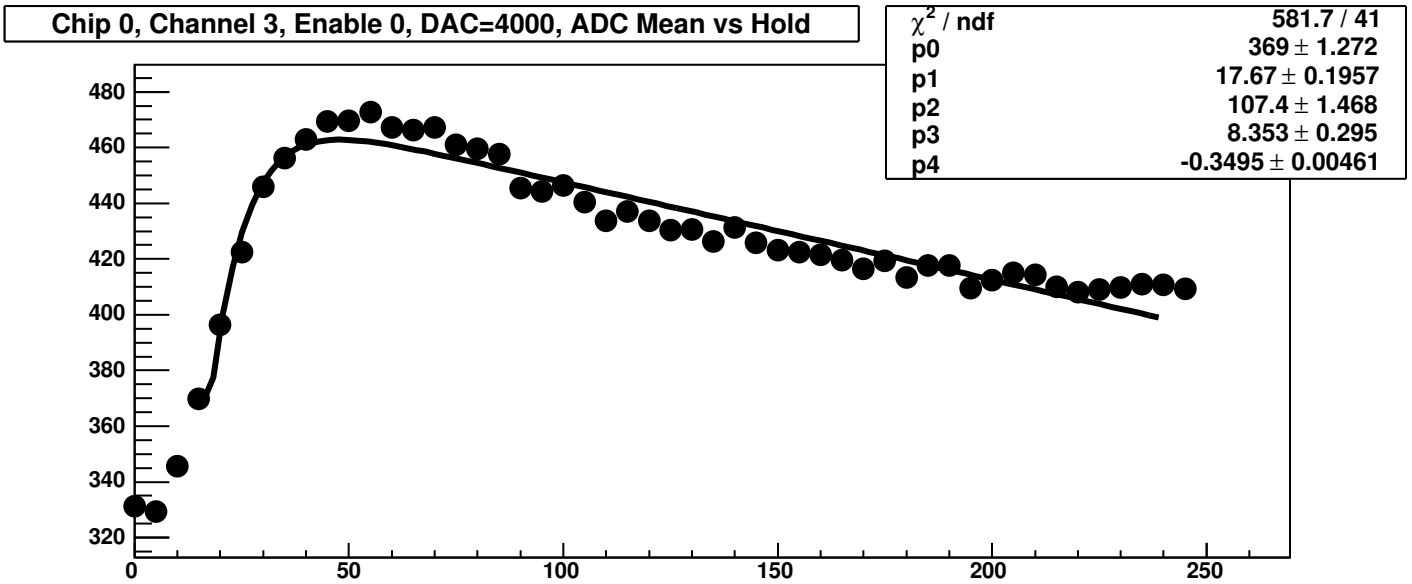


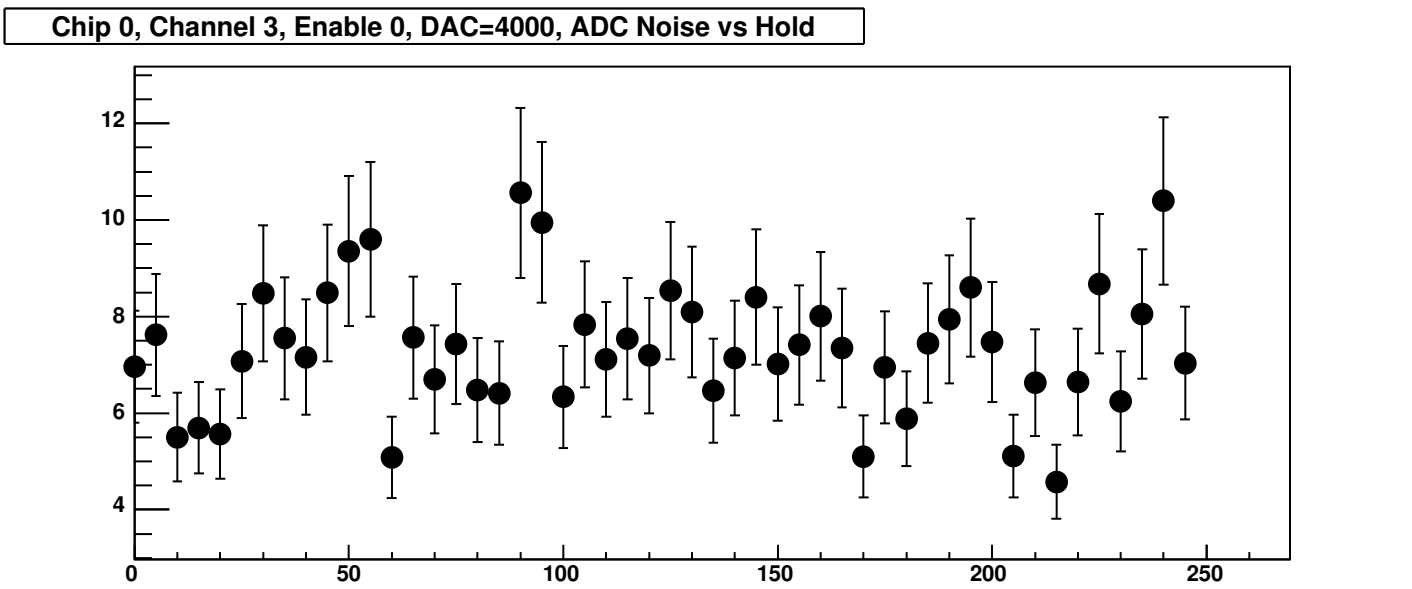
Chip 0, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold

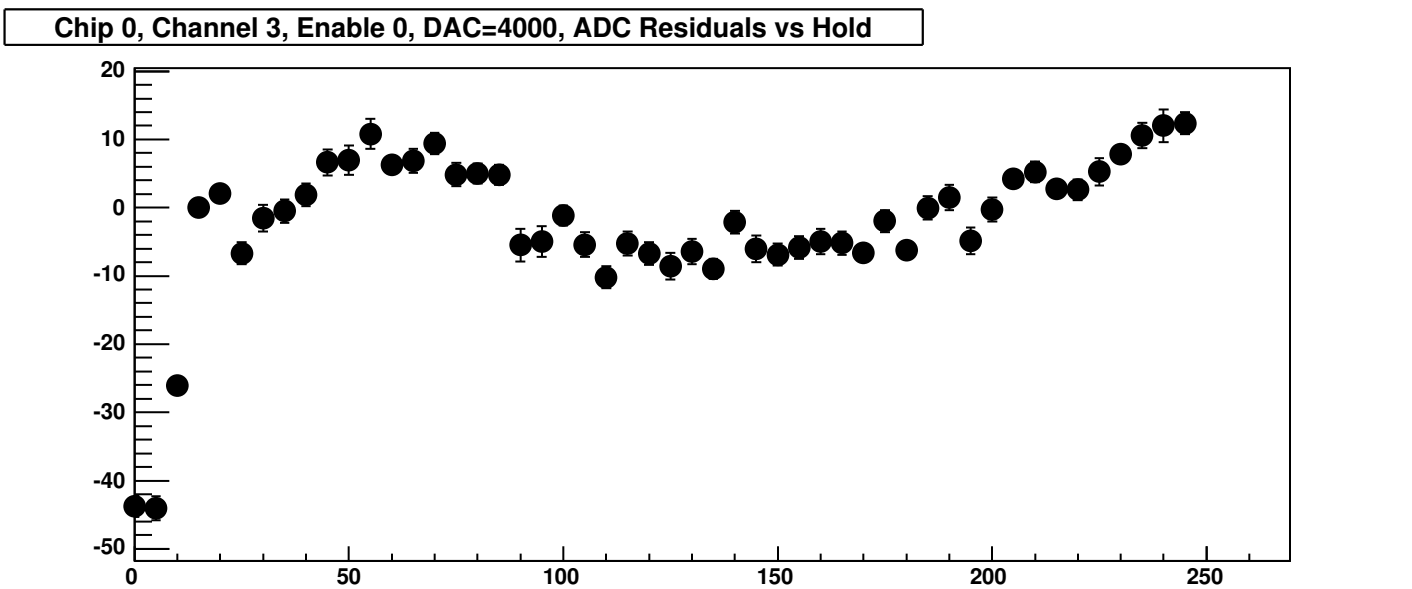


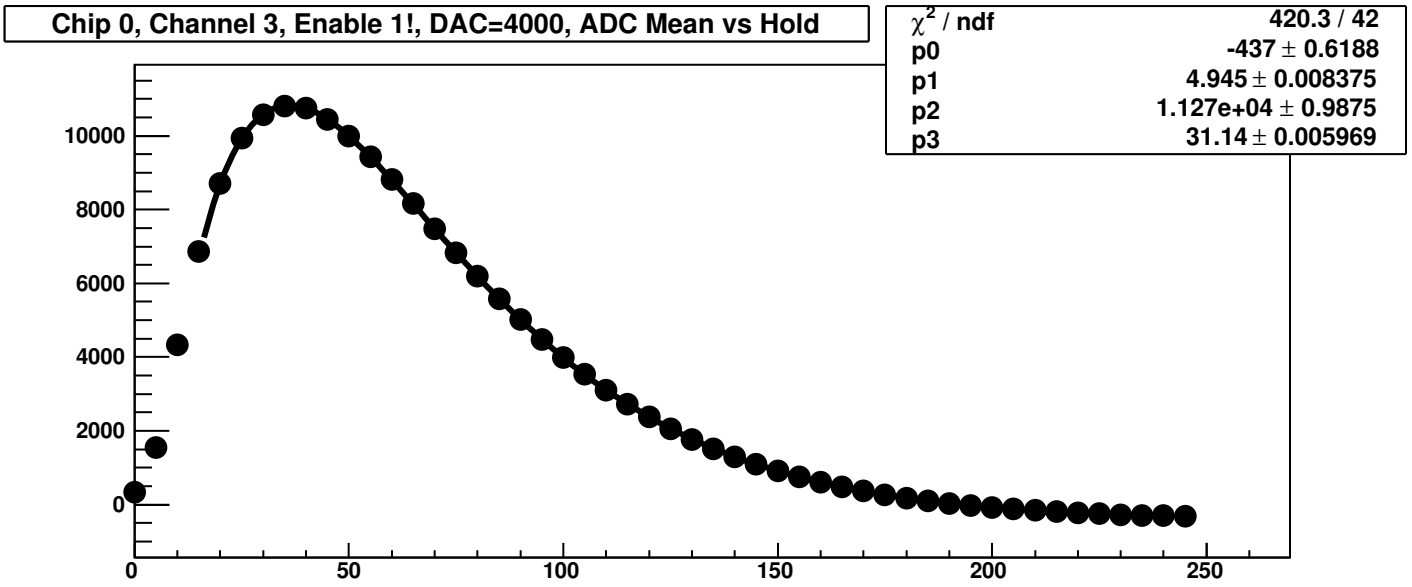
Chip 0, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold

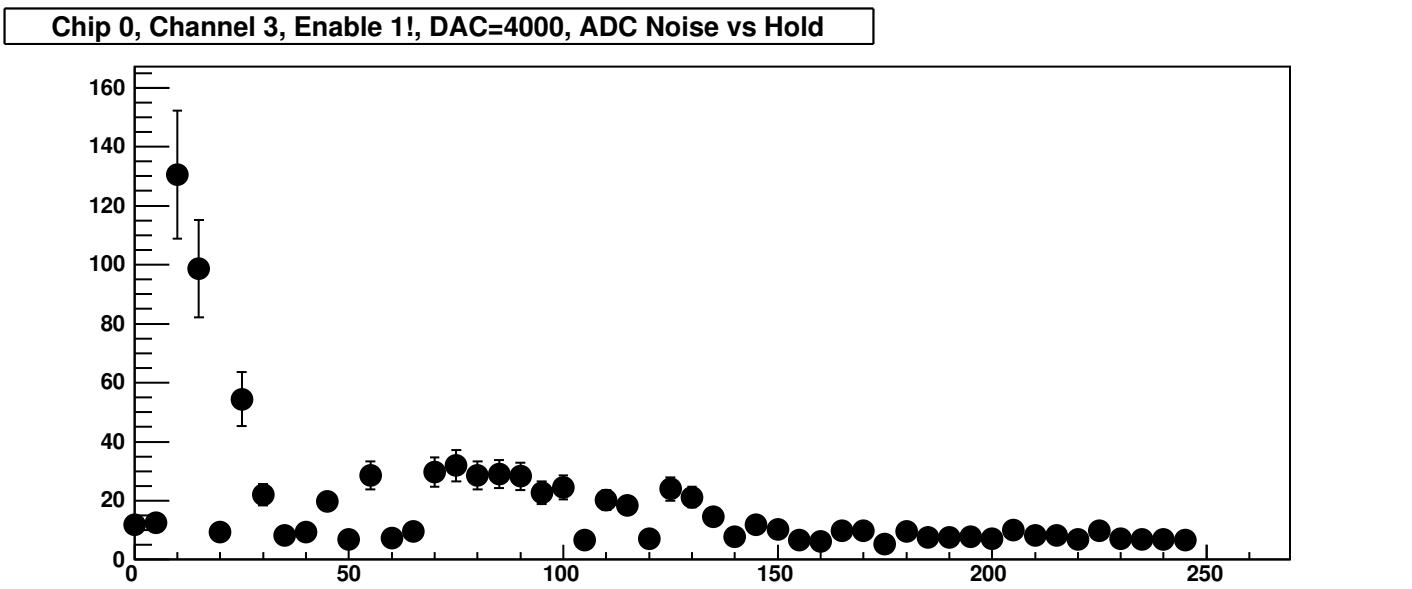


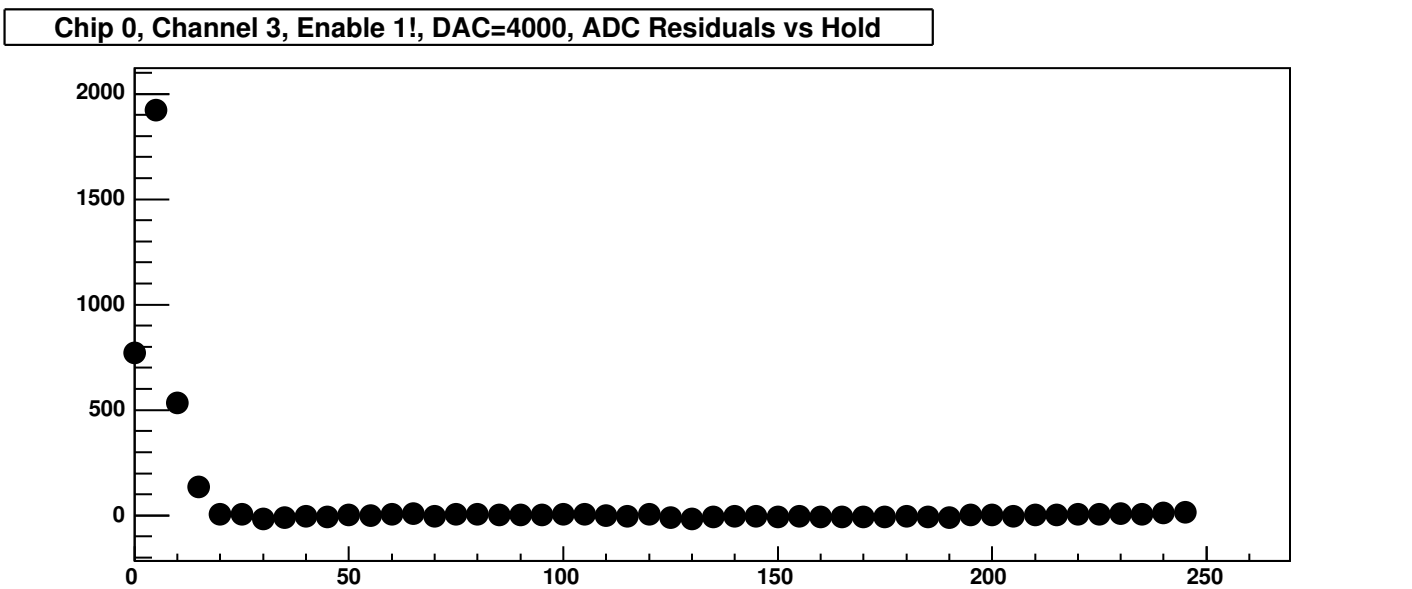

Chip 0, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 581.7 / 41
p0 369 ± 1.272
p1 17.67 ± 0.1957
p2 107.4 ± 1.468
p3 8.353 ± 0.295
p4 -0.3495 ± 0.00461
480
460
440
420
400
380
360
340
320
0
50
100
150
200
250


Chip 0, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

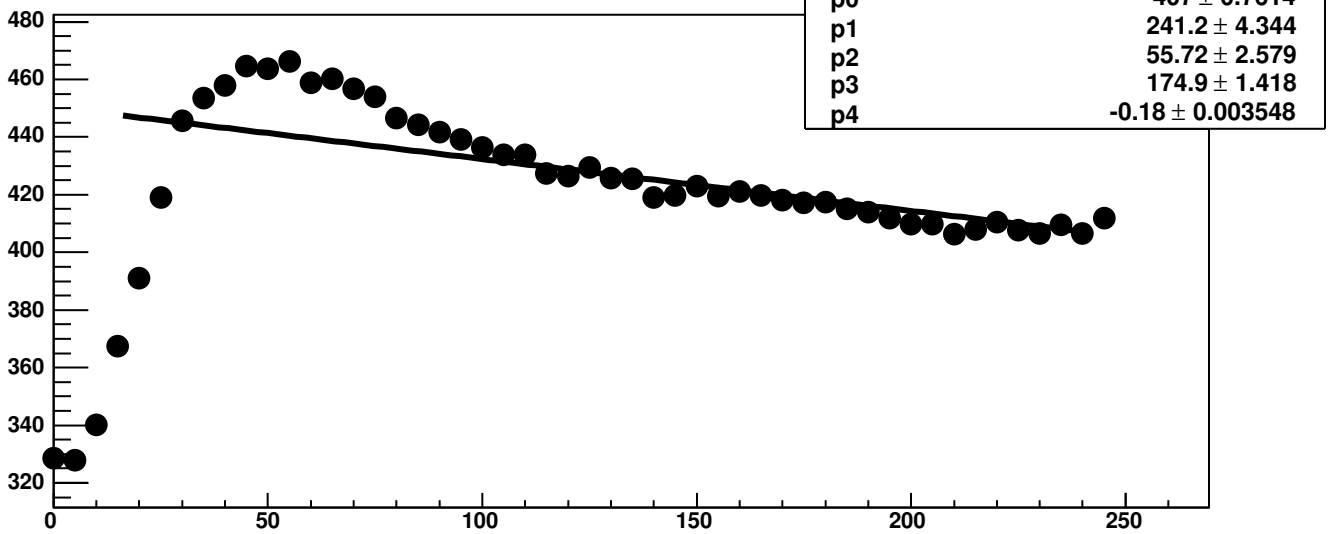

Chip 0, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
-50
0
50
100
150
200
250


Chip 0, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 420.3 / 42
p0 -437 ± 0.6188
p1 4.945 ± 0.008375
p2 1.127e+04 ± 0.9875
p3 31.14 ± 0.005969
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

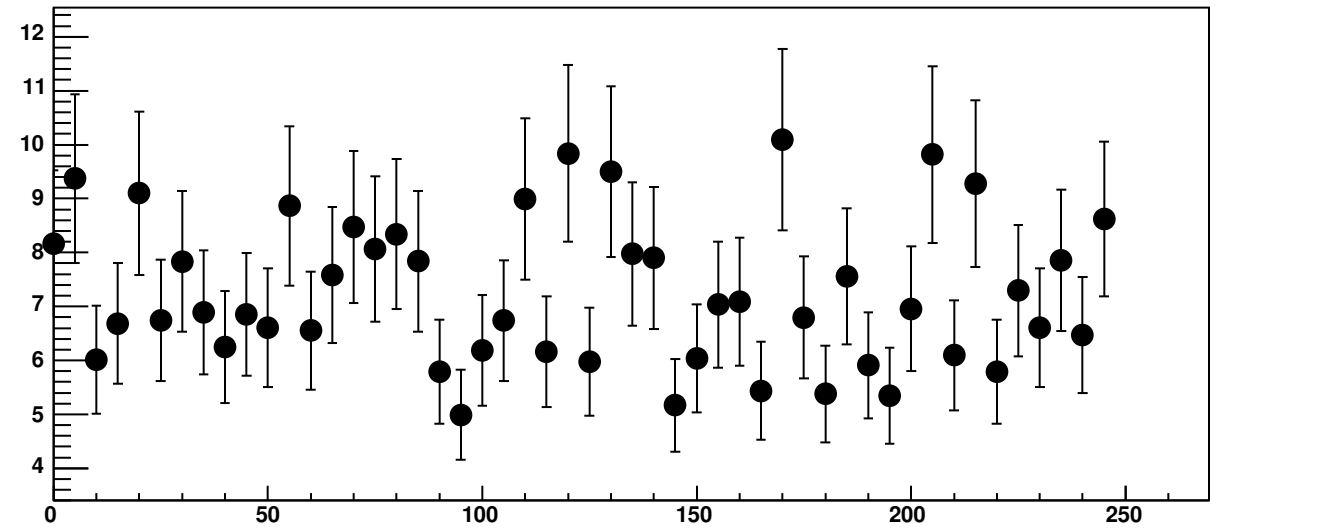

Chip 0, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
0
50
100
150
200
250

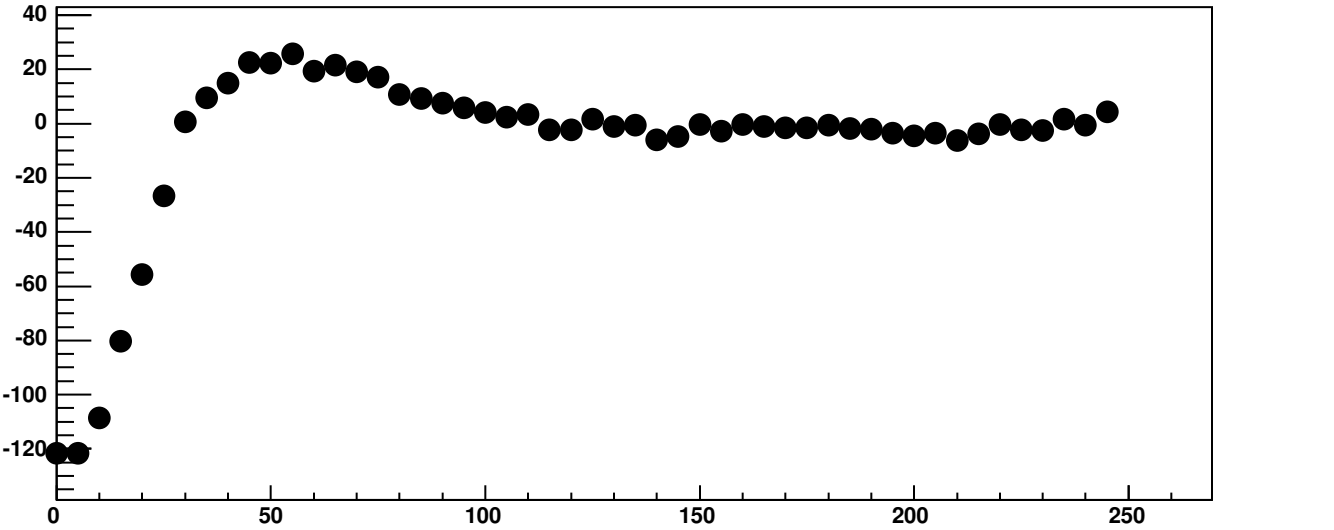
Chip 0, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold



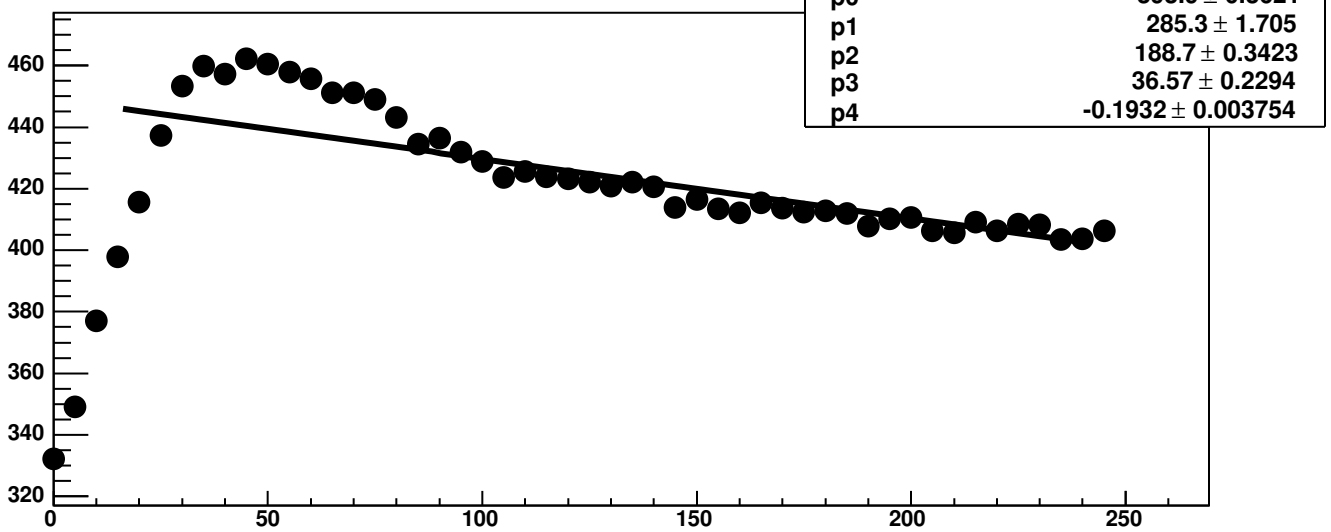
Chip 0, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



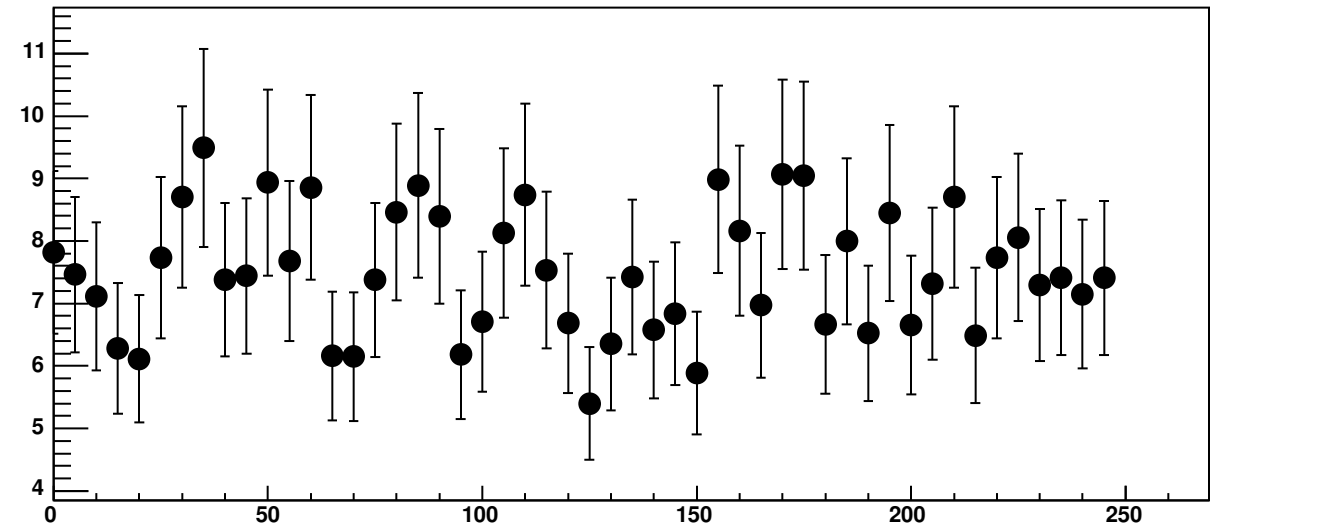
Chip 0, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold



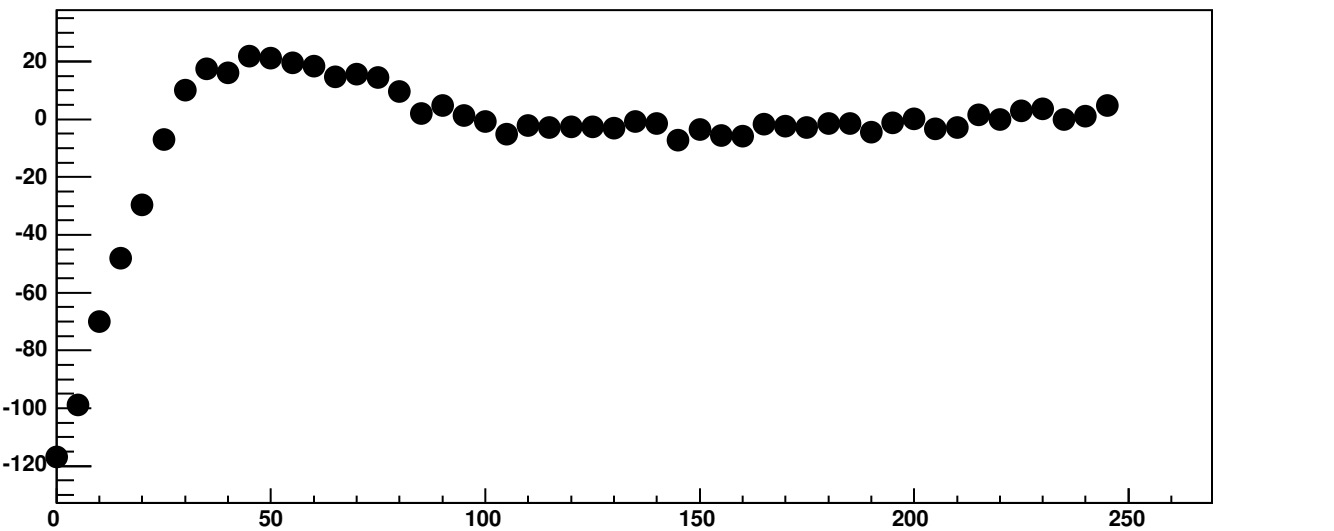
Chip 0, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold



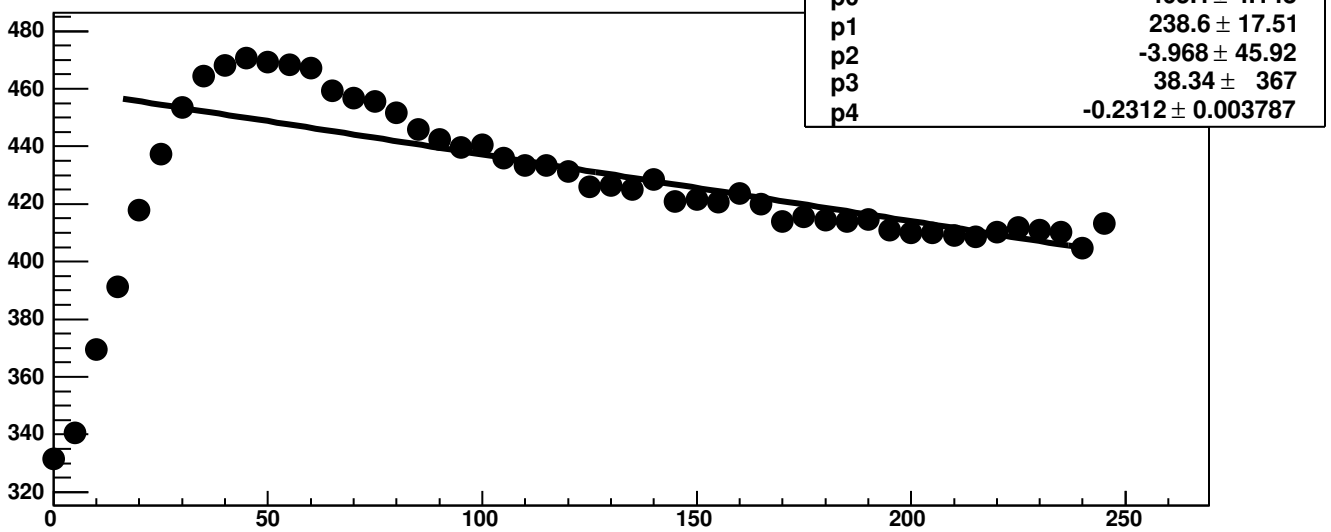
Chip 0, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



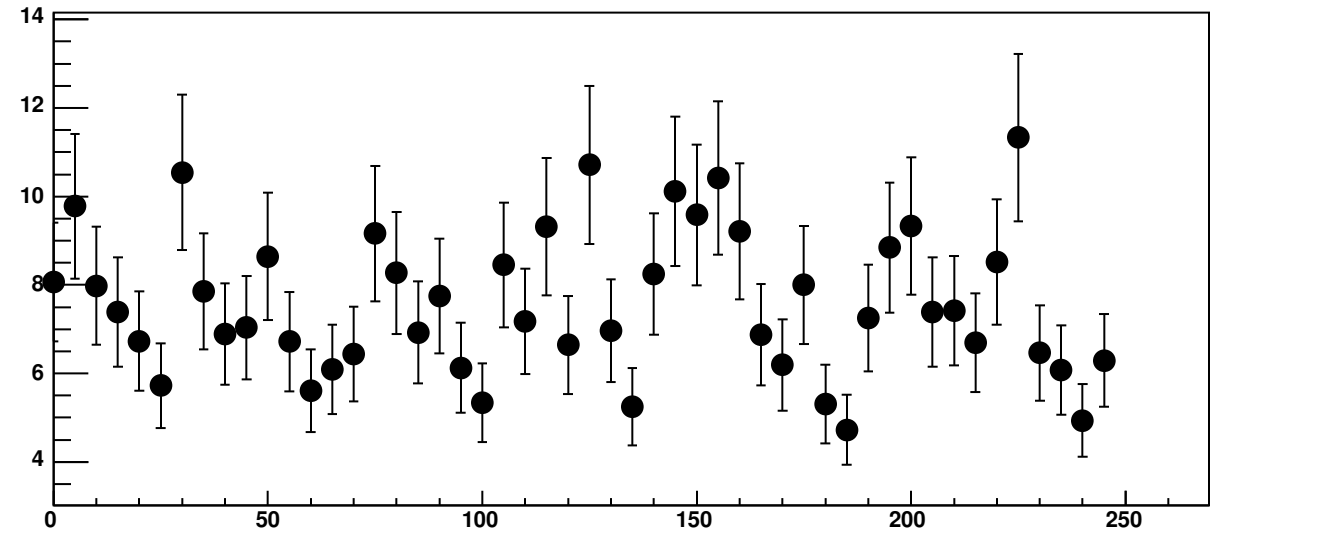
Chip 0, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold



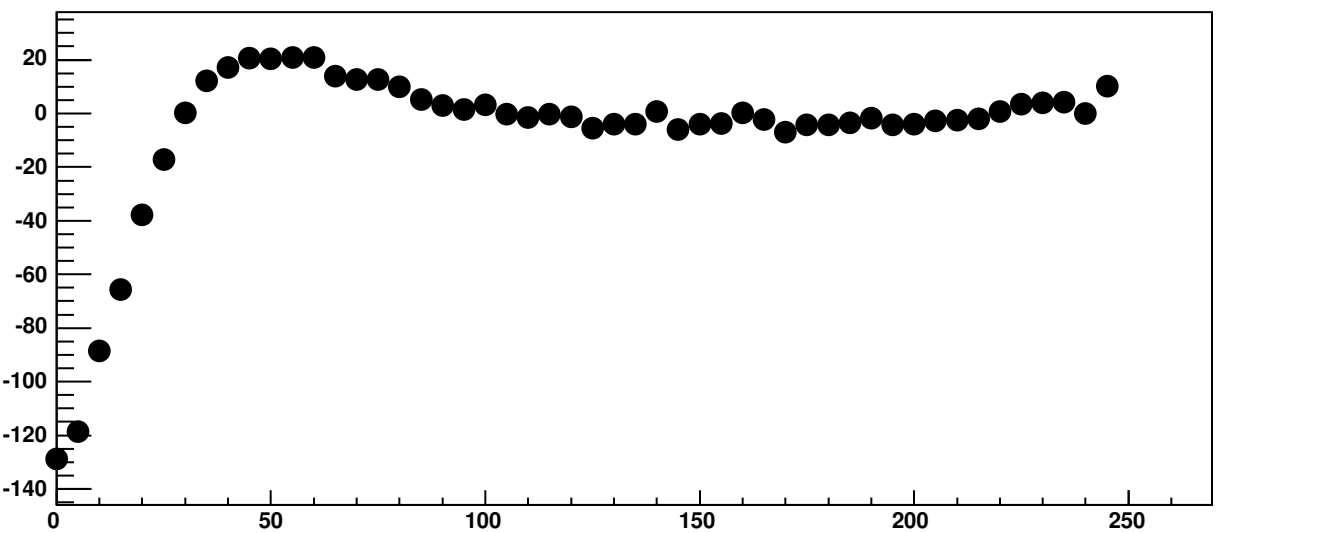
Chip 0, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold



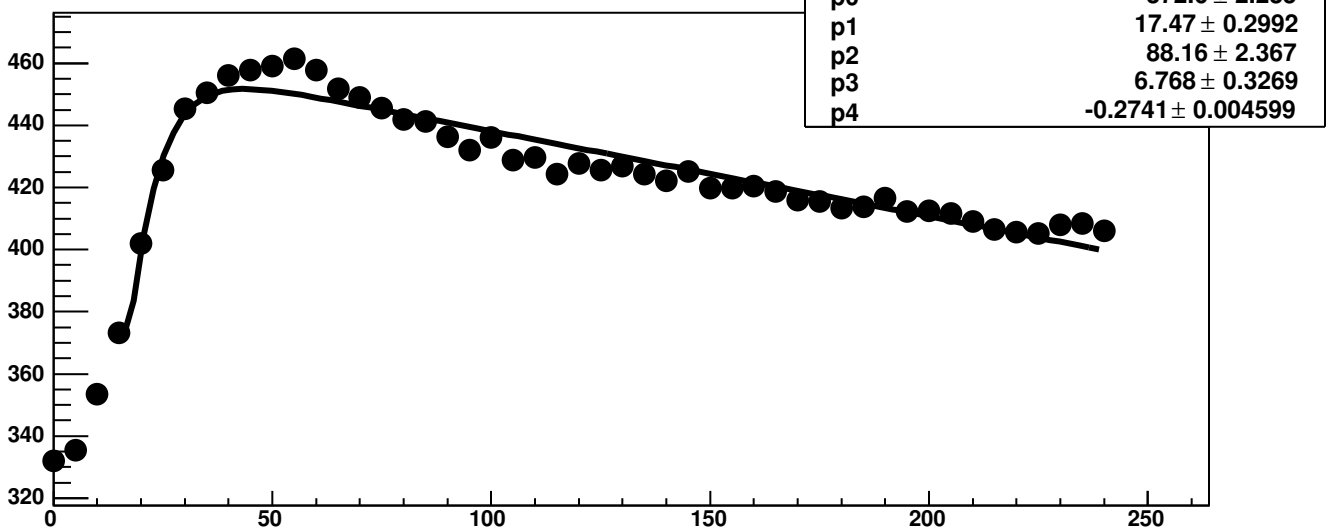
Chip 0, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold



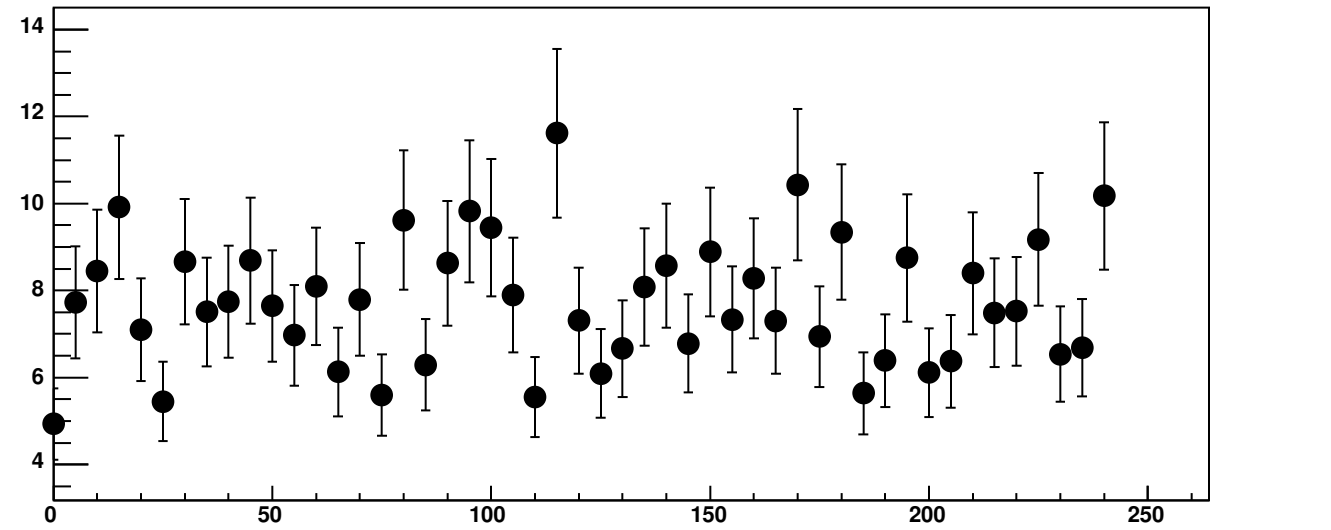
Chip 0, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold



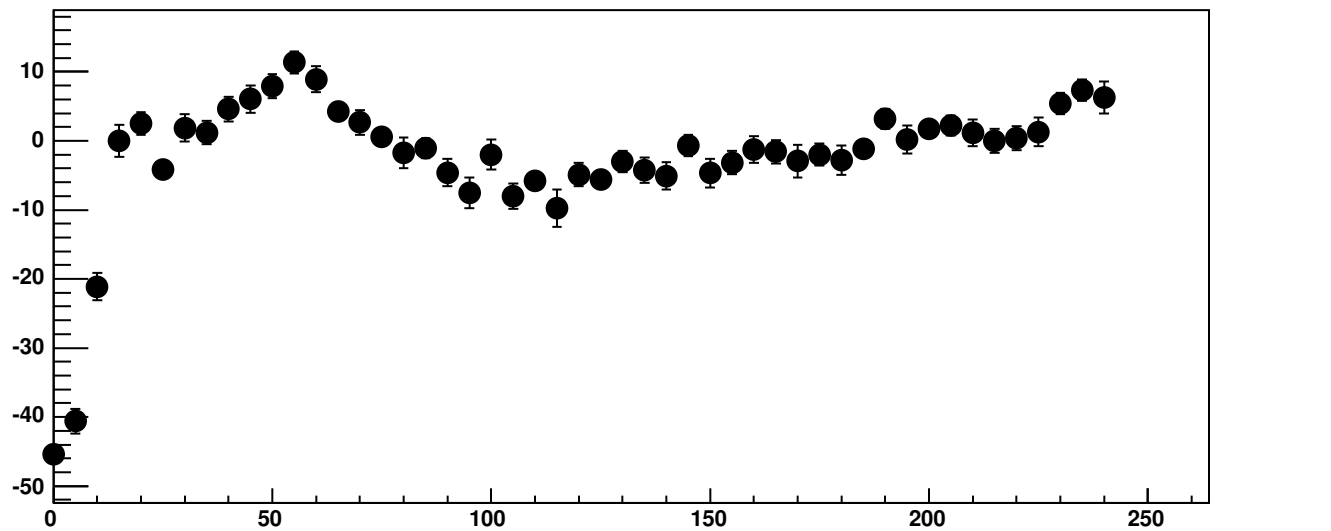
Chip 0, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold



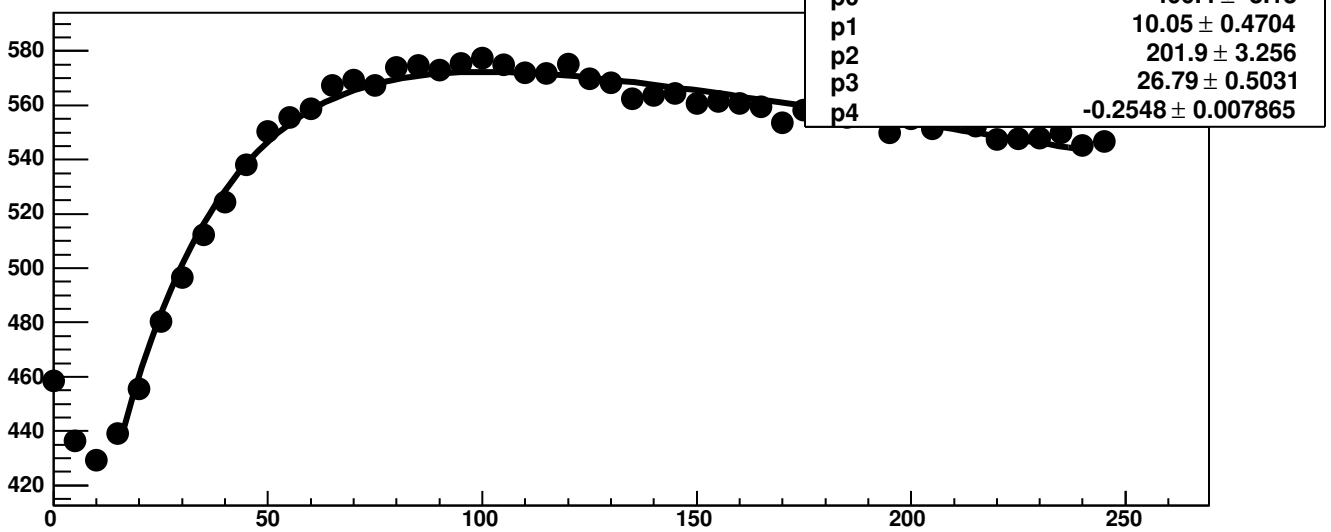
Chip 0, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



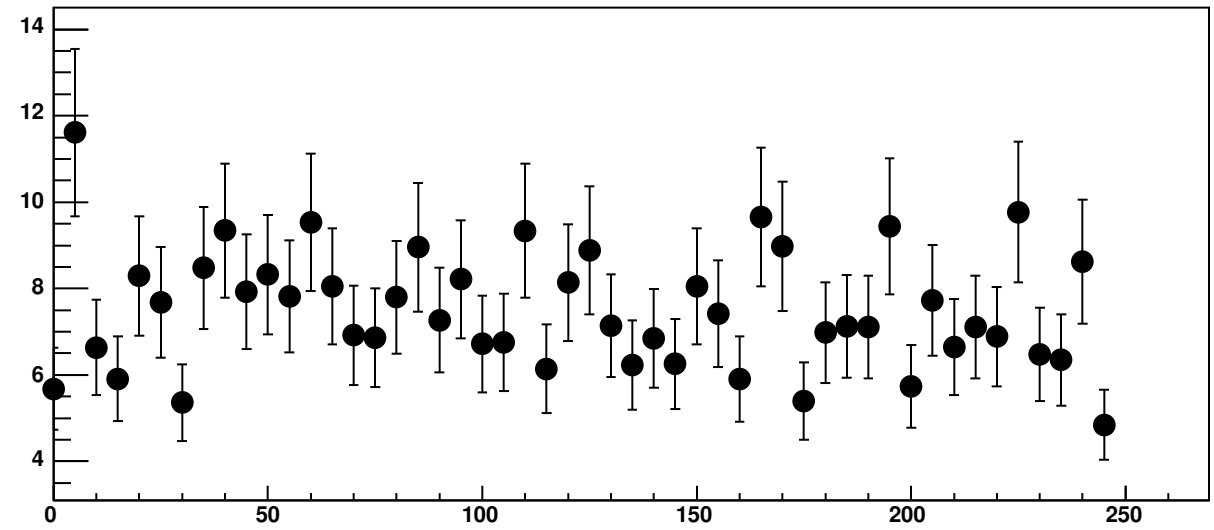
Chip 0, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold



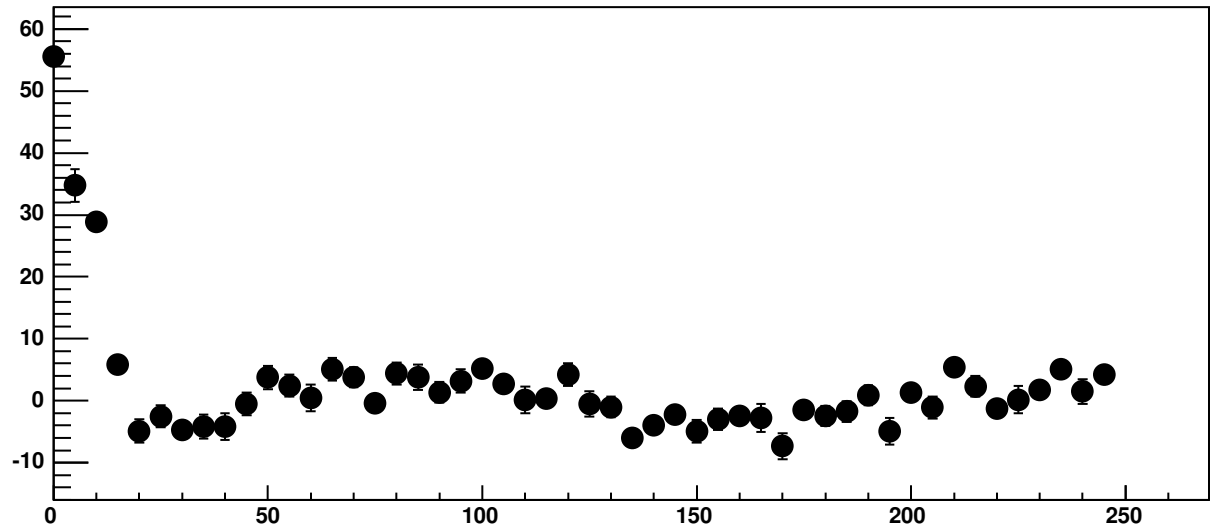
Chip 0, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

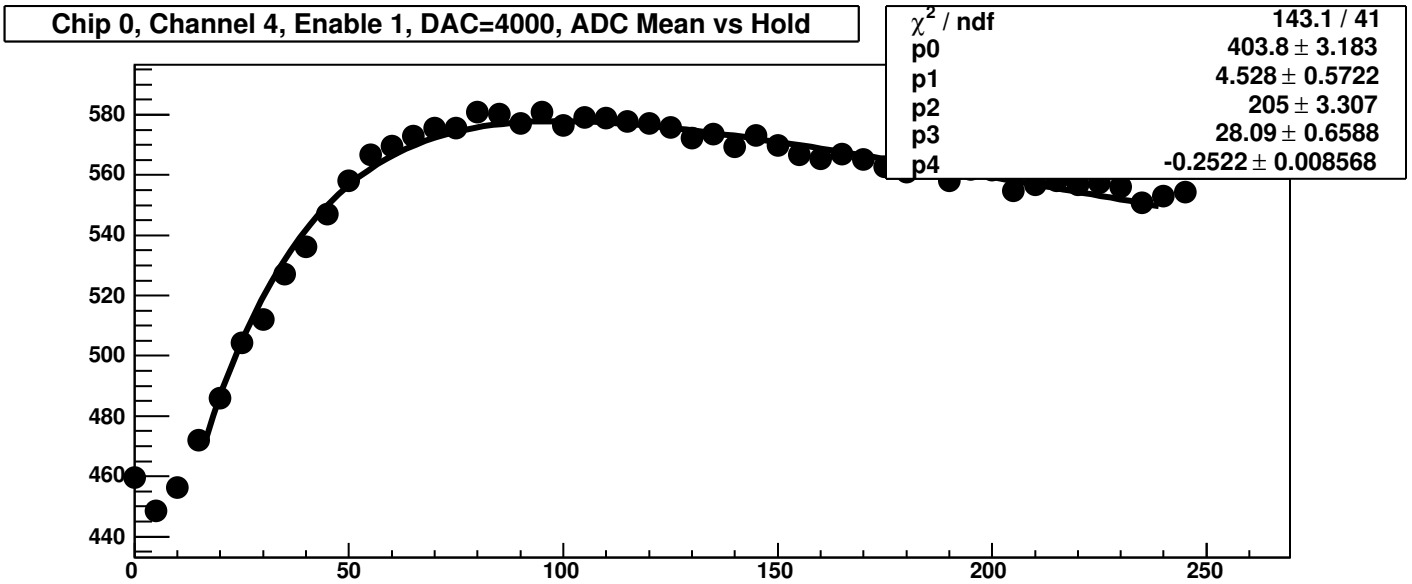


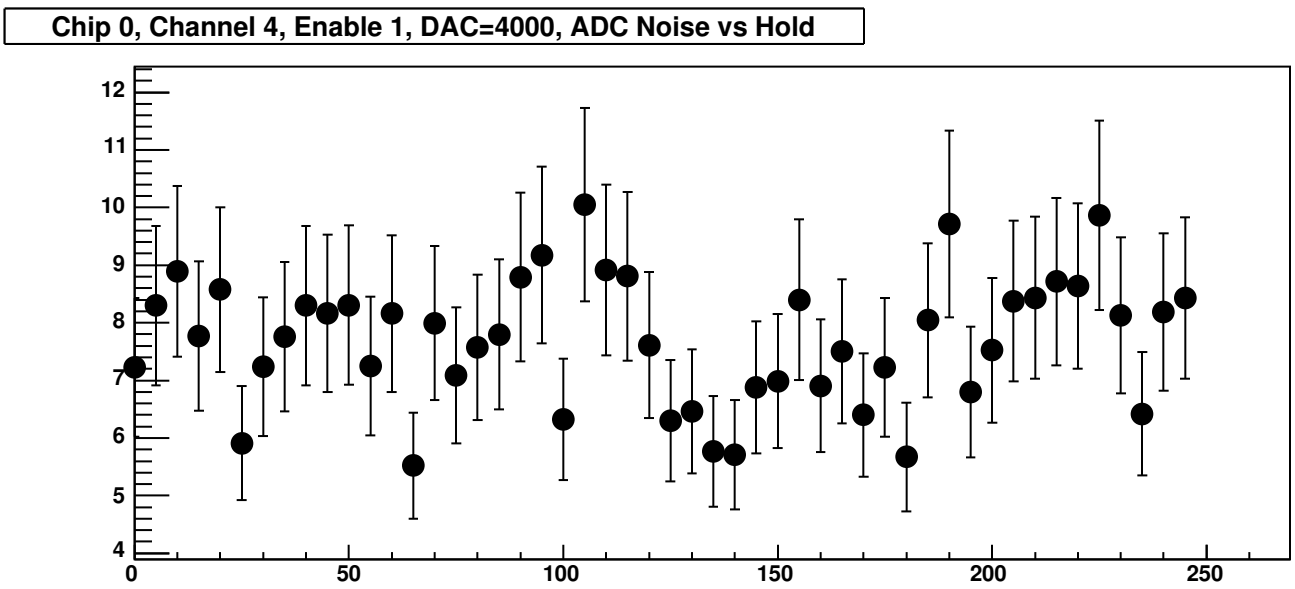
Chip 0, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold

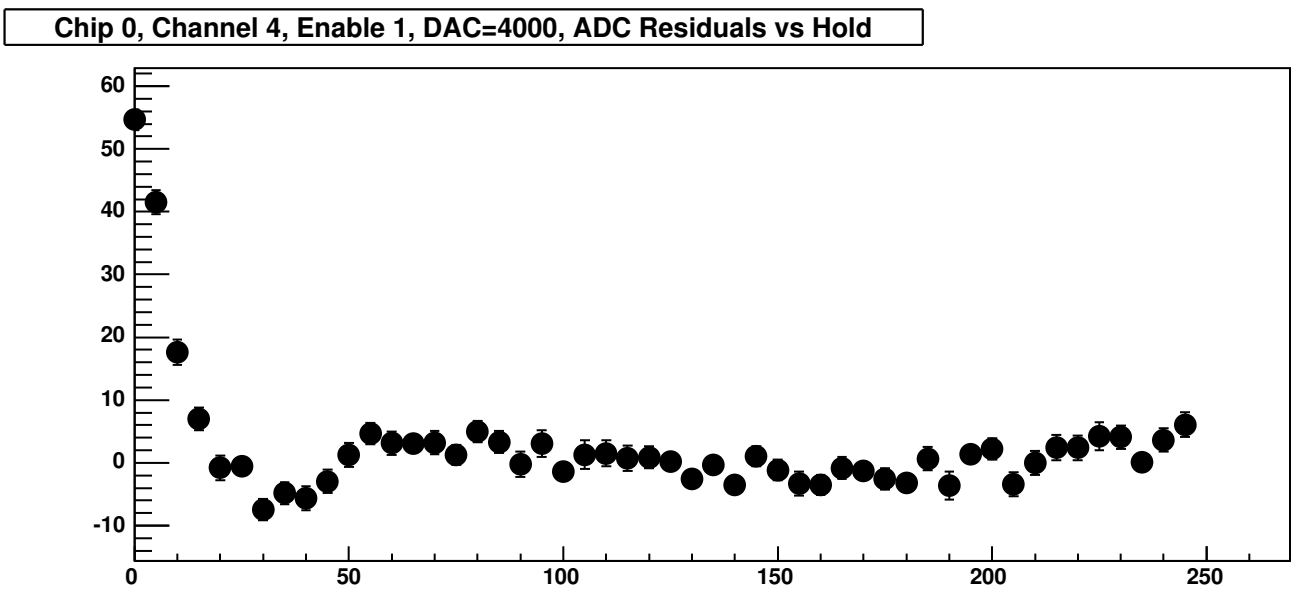


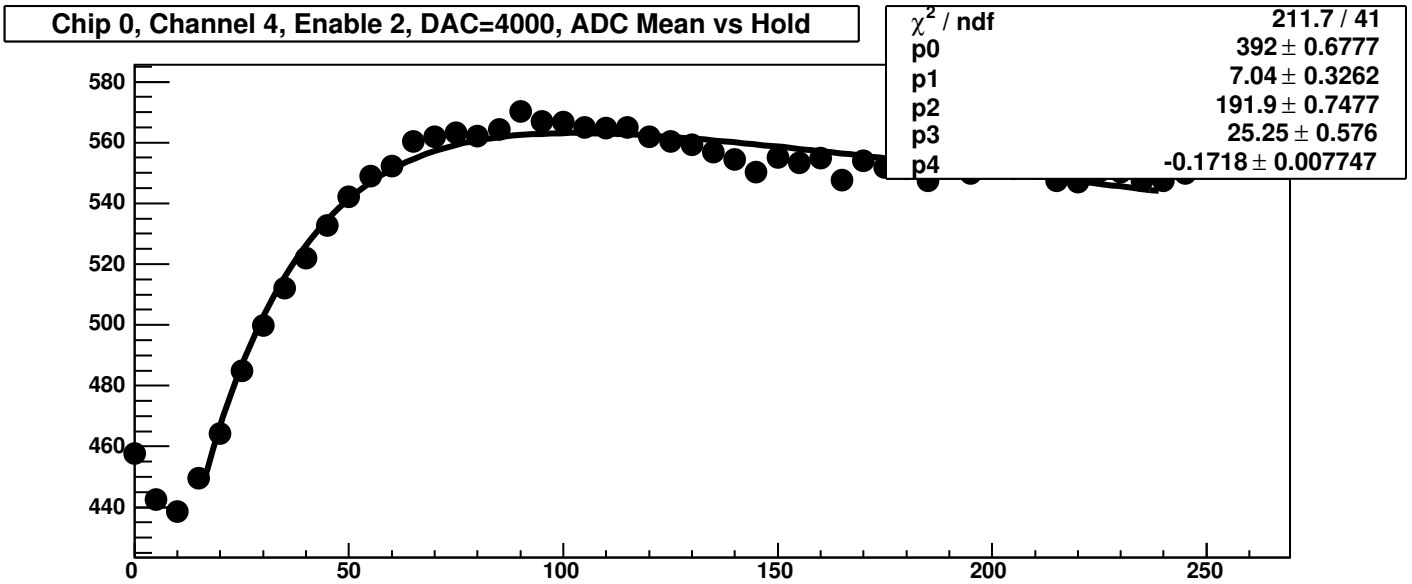
Chip 0, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

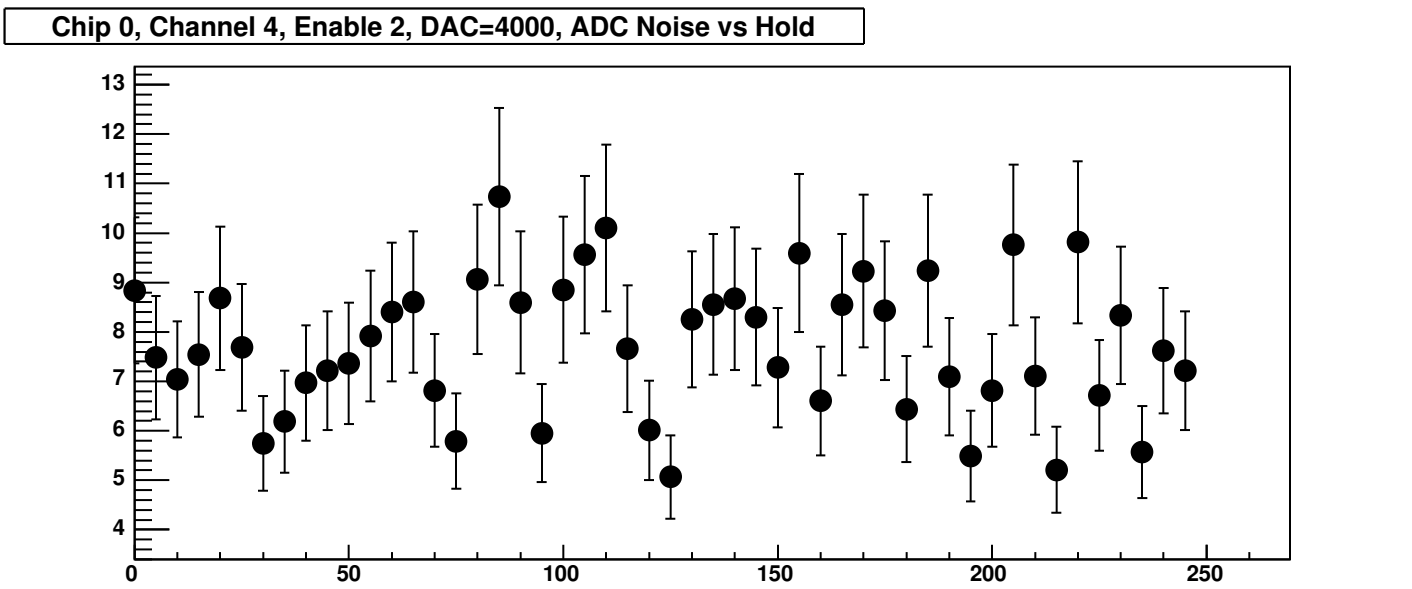


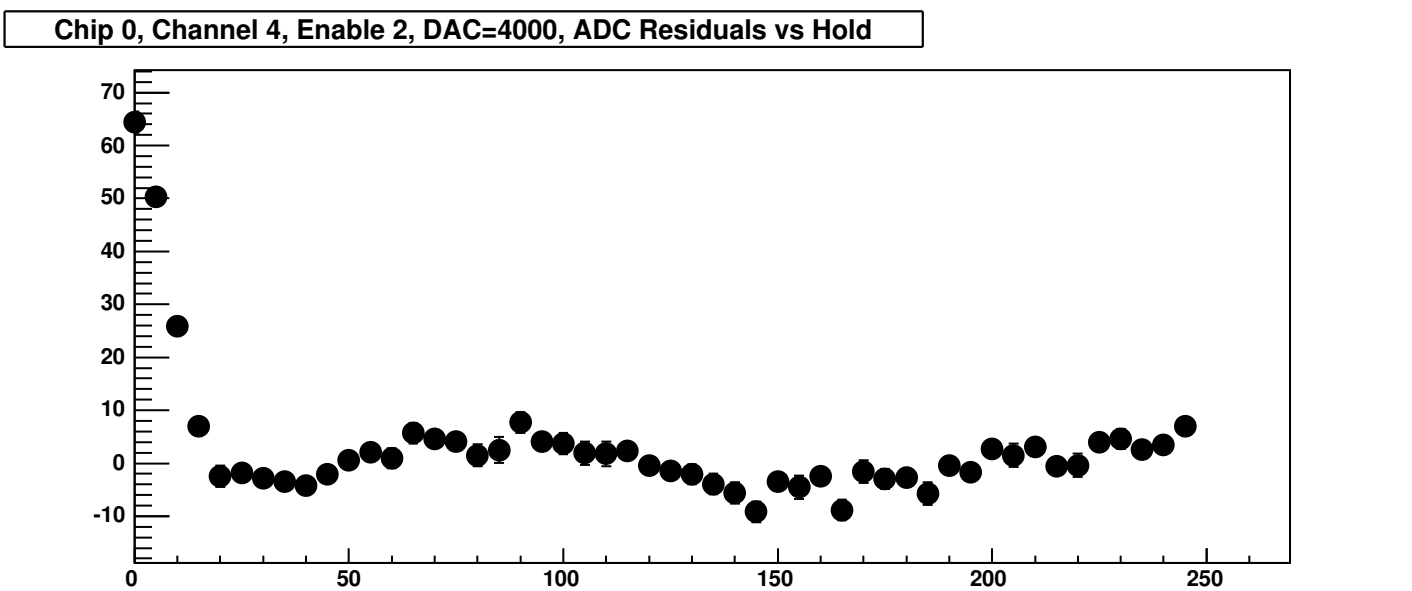

Chip 0, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 143.1 / 41
p0 403.8 ± 3.183
p1 4.528 ± 0.5722
p2 205 ± 3.307
p3 28.09 ± 0.6588
p4 -0.2522 ± 0.008568
580
560
540
520
500
480
460
440
0
50
100
150
200
250

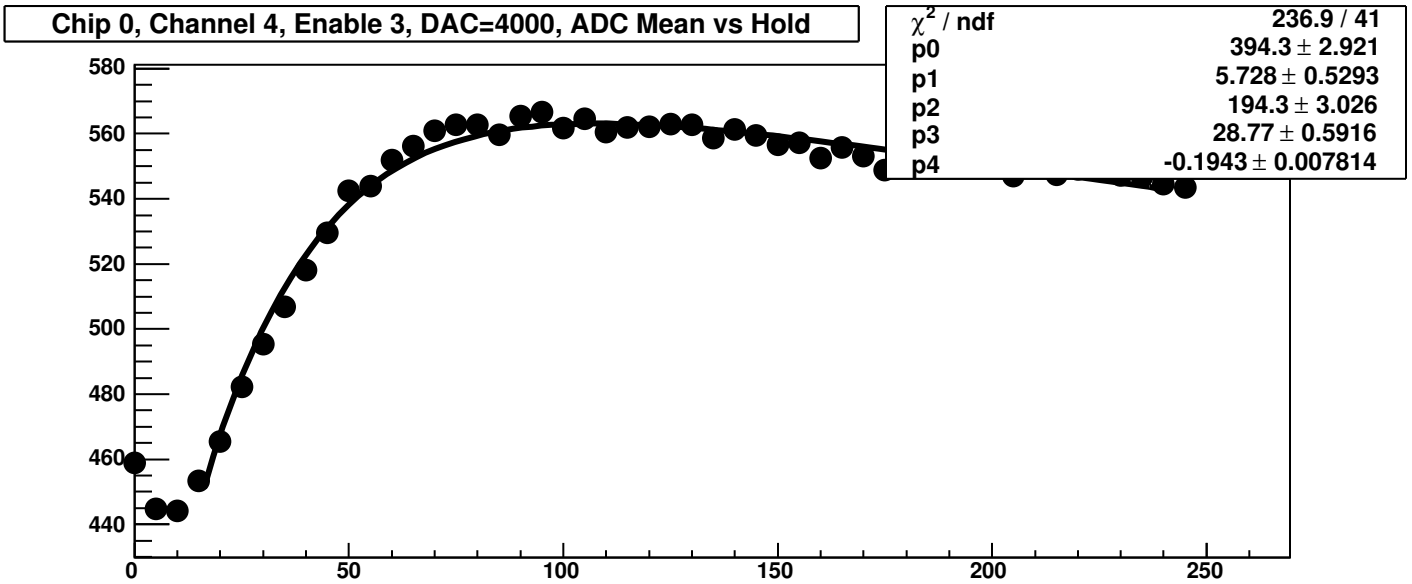

Chip 0, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

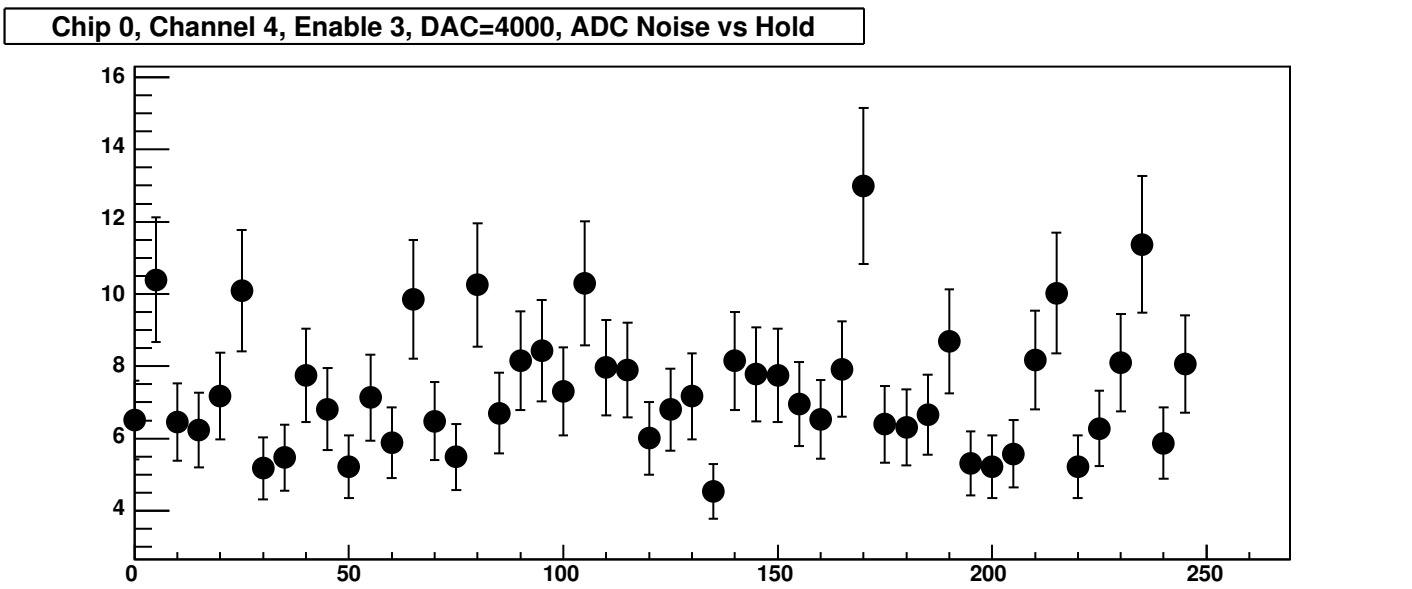

Chip 0, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

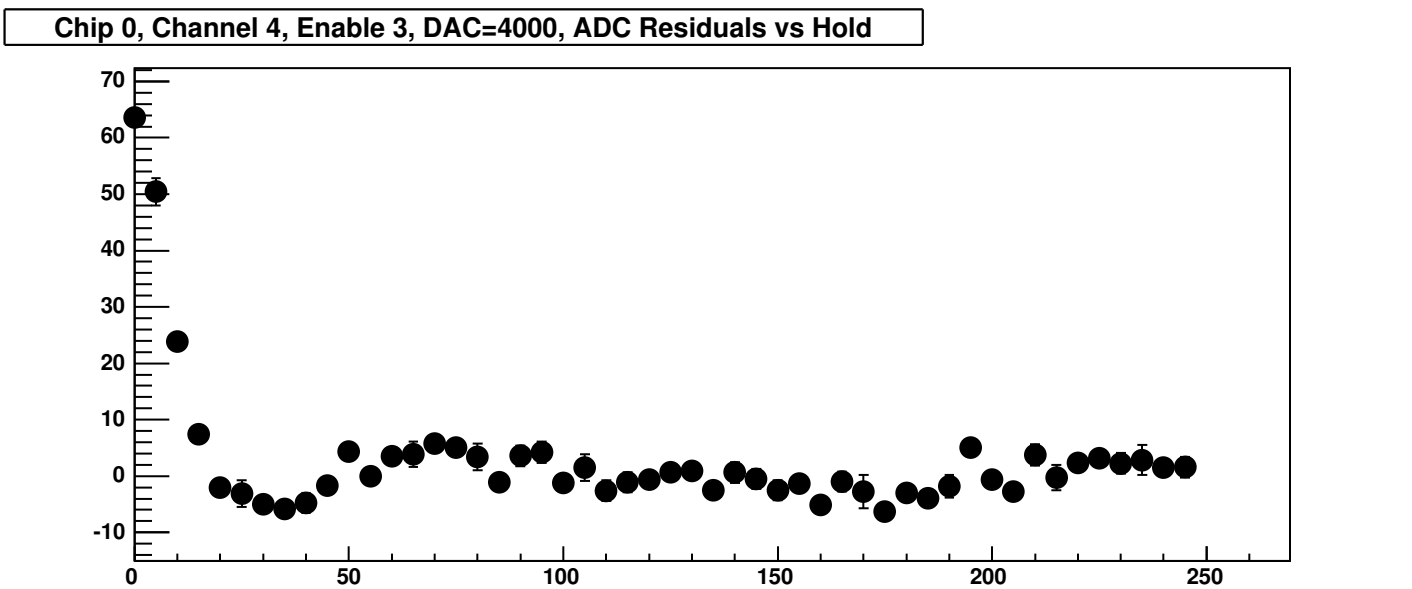

Chip 0, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 211.7 / 41
p0 392 ± 0.6777
p1 7.04 ± 0.3262
p2 191.9 ± 0.7477
p3 25.25 ± 0.576
p4 -0.1718 ± 0.007747
580
560
540
520
500
480
460
440
0
50
100
150
200
250

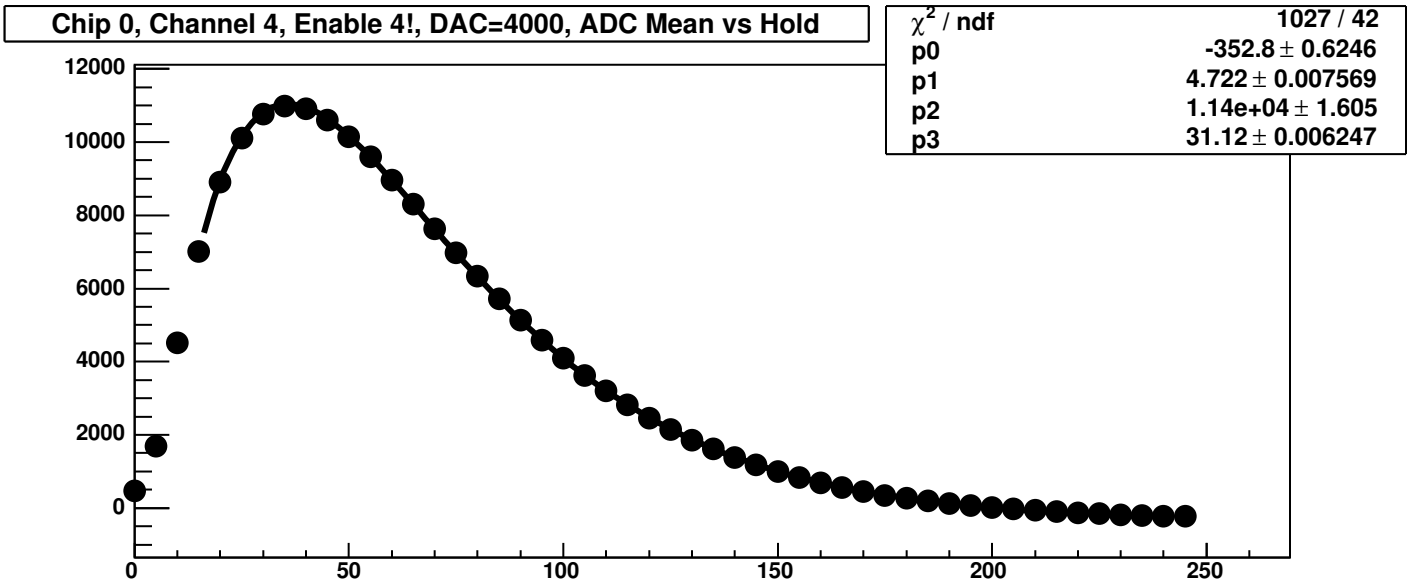

Chip 0, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

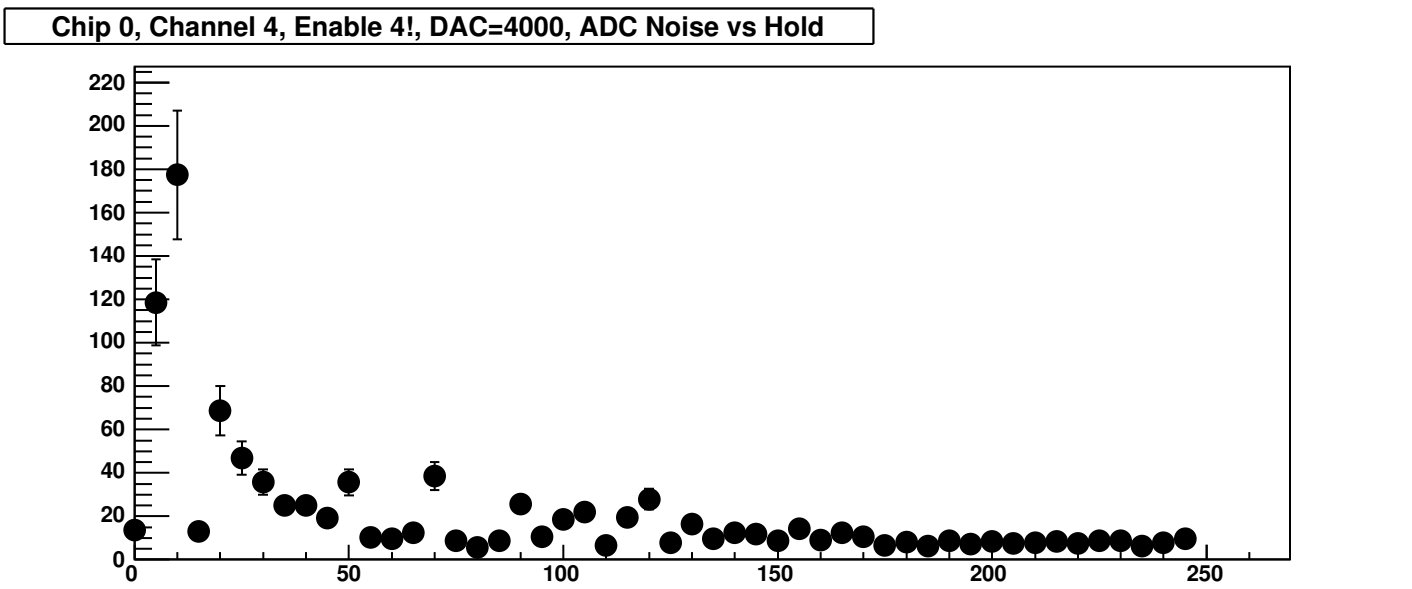

Chip 0, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

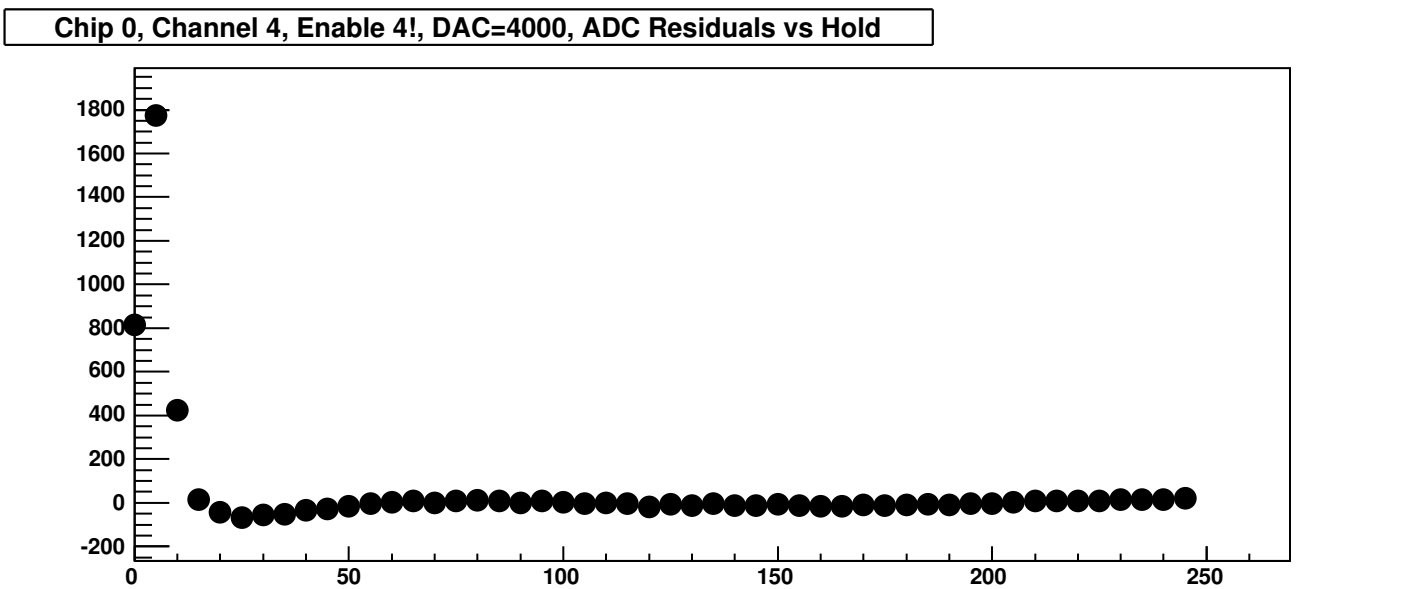

Chip 0, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 236.9 / 41
p0 394.3 ± 2.921
p1 5.728 ± 0.5293
p2 194.3 ± 3.026
p3 28.77 ± 0.5916
p4 -0.1943 ± 0.007814
580
560
540
520
500
480
460
440
0
50
100
150
200
250

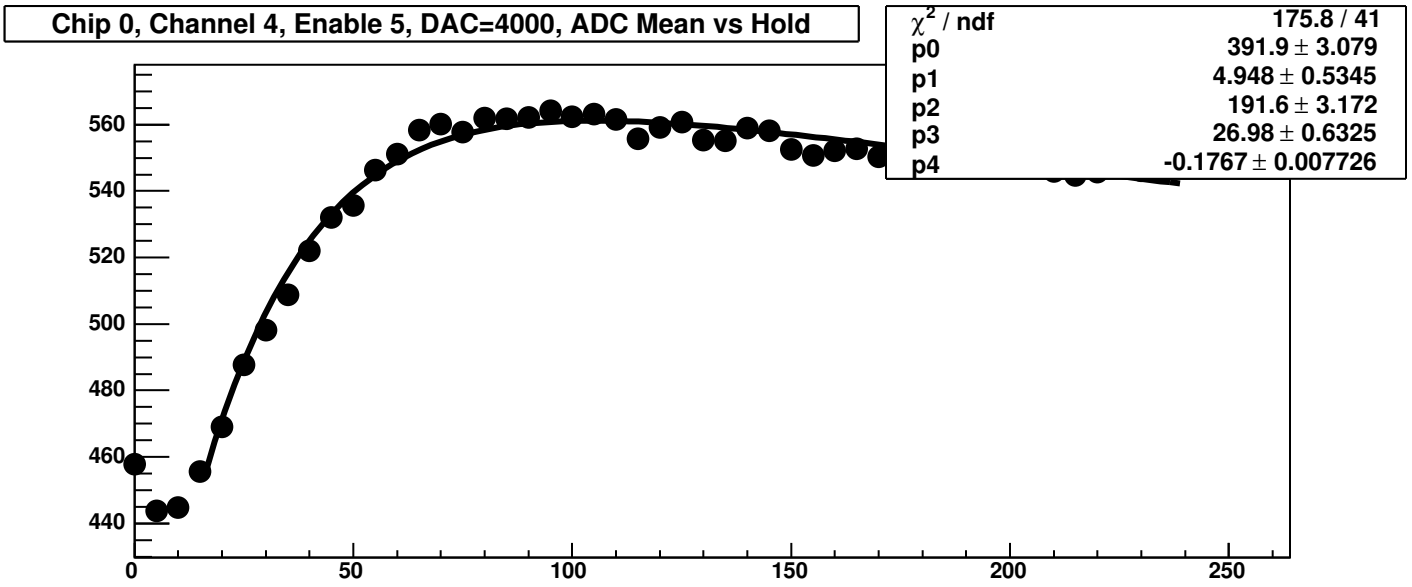

Chip 0, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold
16
14
12
10
8
6
4
0
50
100
150
200
250

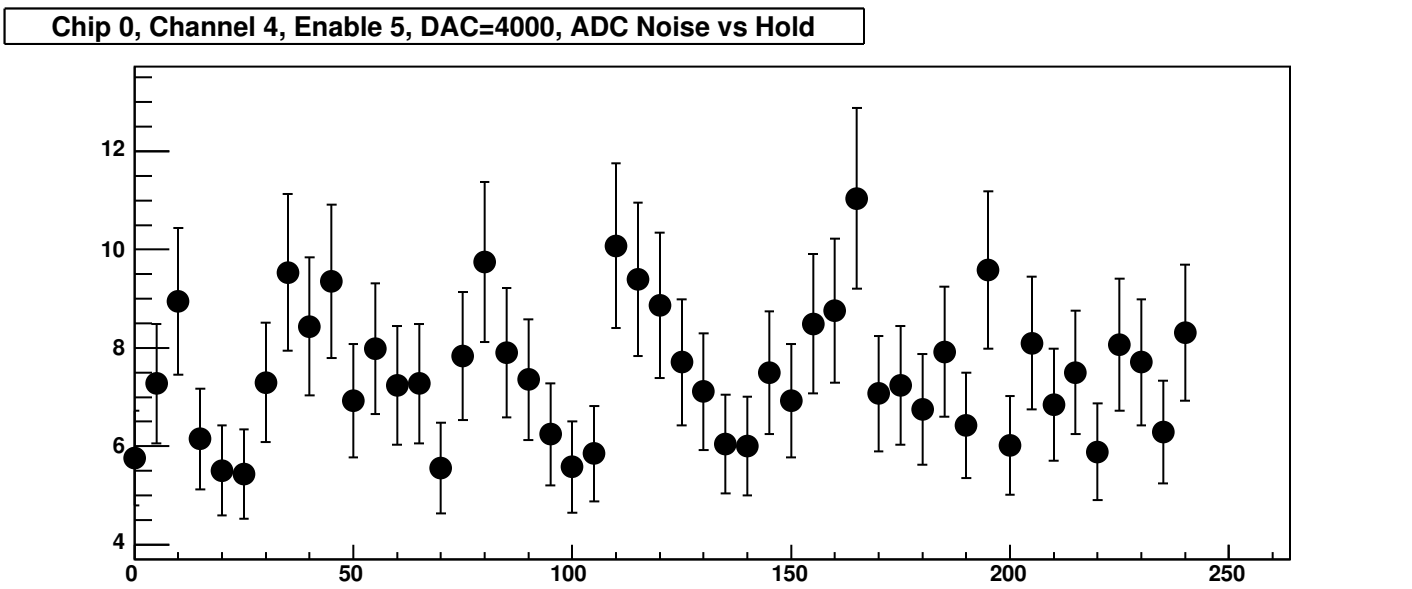

Chip 0, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

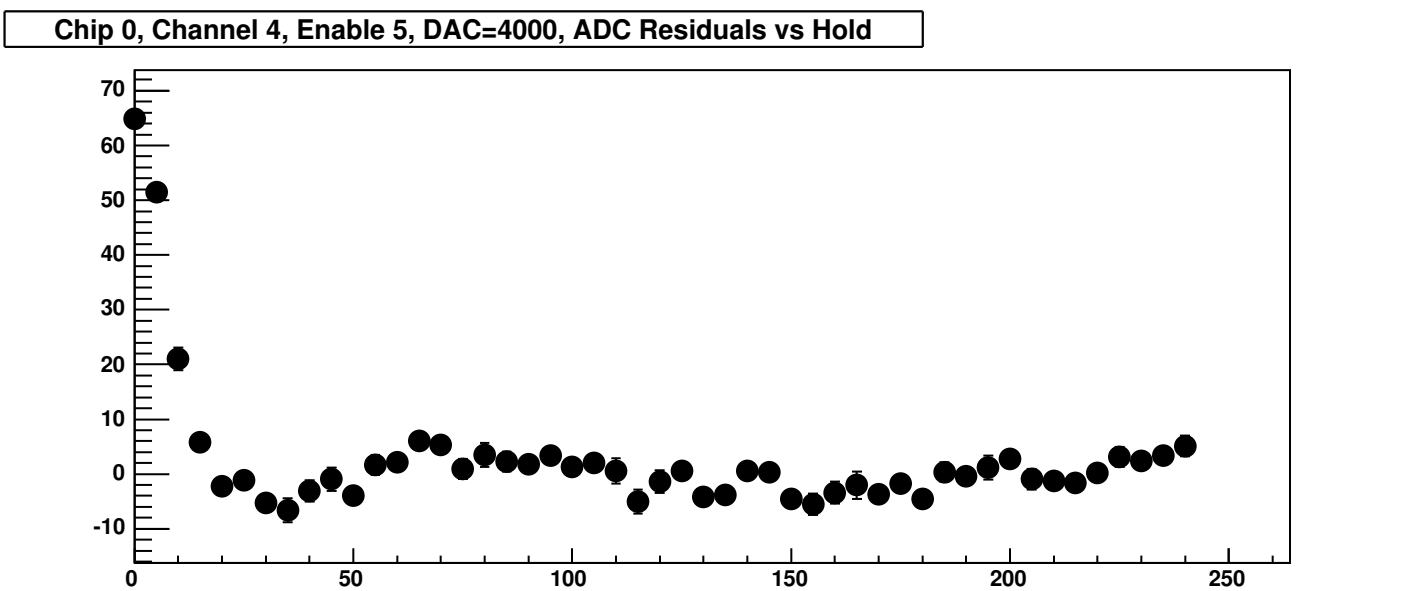

Chip 0, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1027 / 42
p0 -352.8 ± 0.6246
p1 4.722 ± 0.007569
p2 1.14e+04 ± 1.605
p3 31.12 ± 0.006247
12000
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

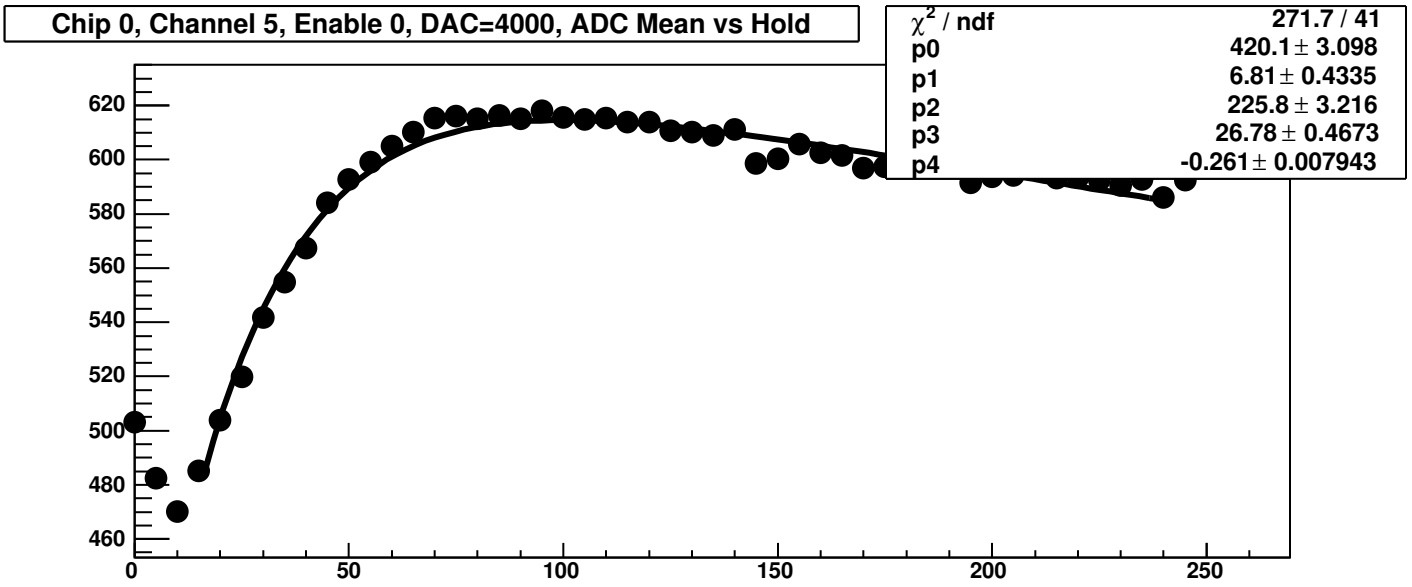

Chip 0, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

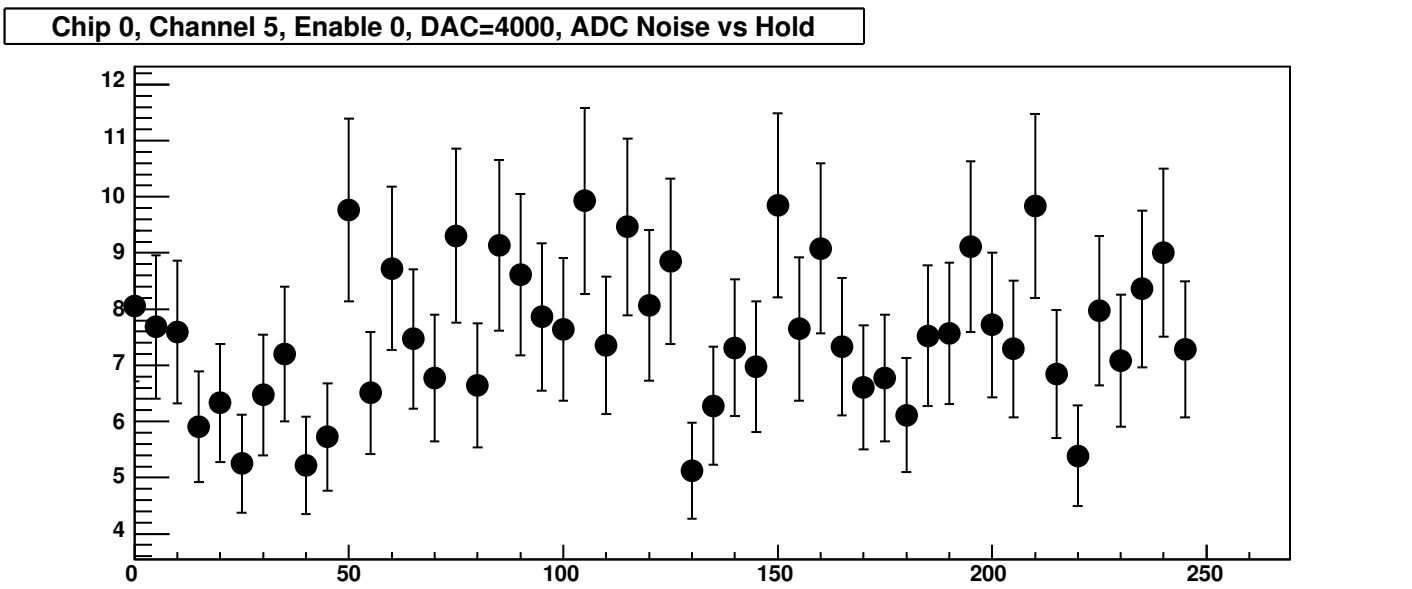

Chip 0, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

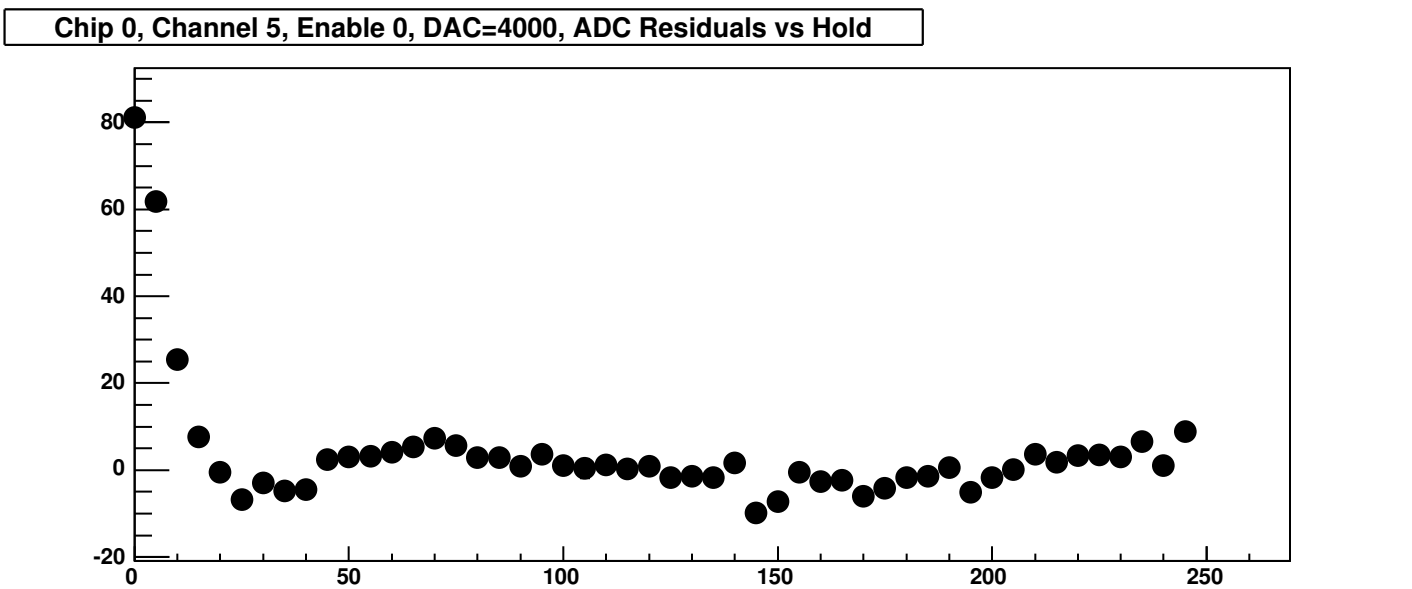

Chip 0, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 175.8 / 41
p0 391.9 ± 3.079
p1 4.948 ± 0.5345
p2 191.6 ± 3.172
p3 26.98 ± 0.6325
p4 -0.1767 ± 0.007726
560
540
520
500
480
460
440
0
50
100
150
200
250

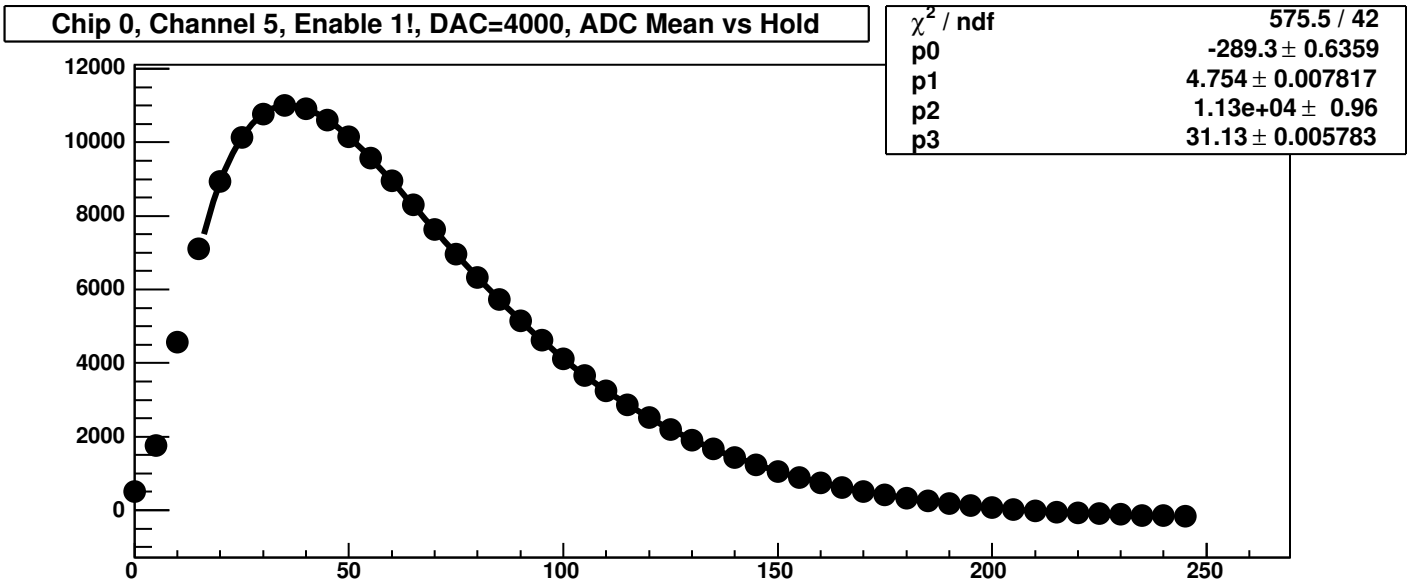

Chip 0, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

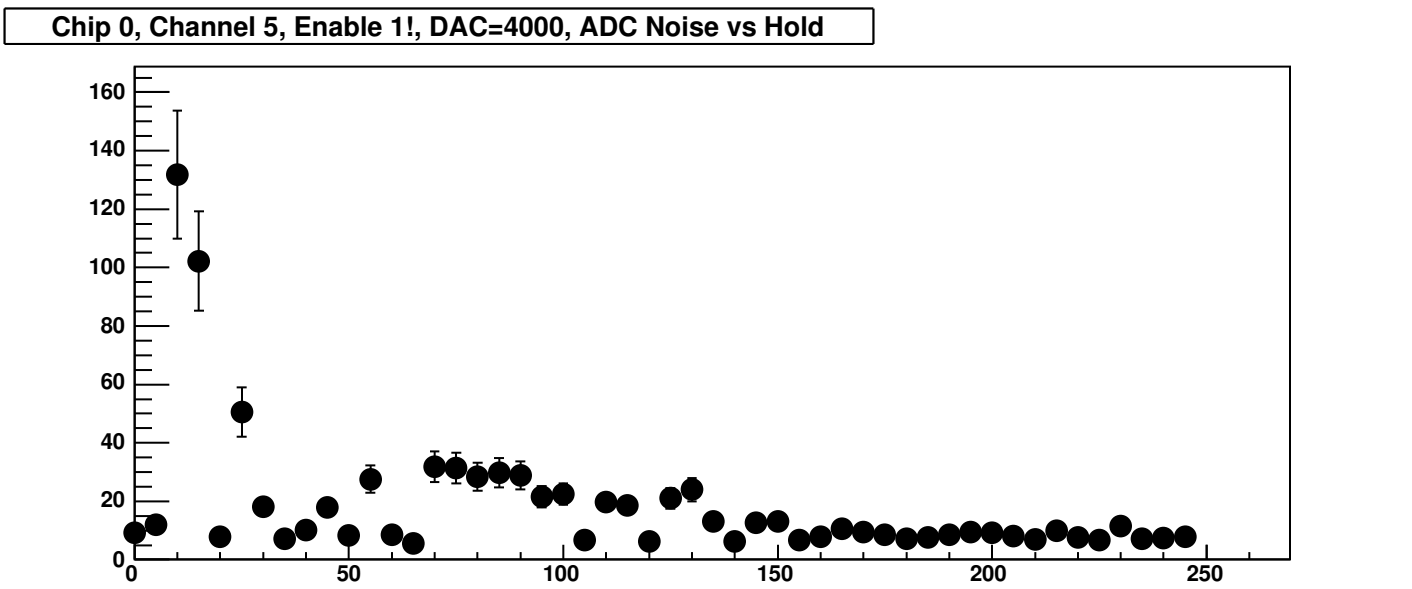

Chip 0, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

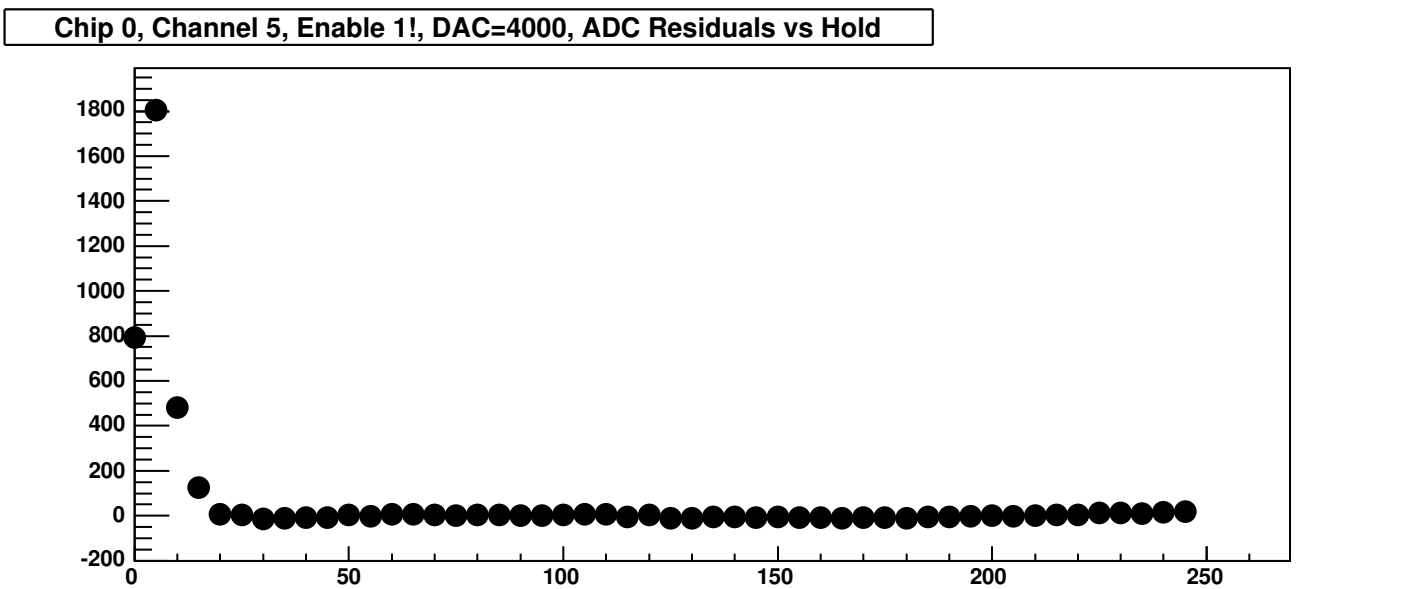

Chip 0, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 271.7 / 41
p0 420.1 ± 3.098
p1 6.81 ± 0.4335
p2 225.8 ± 3.216
p3 26.78 ± 0.4673
p4 -0.261 ± 0.007943
620
600
580
560
540
520
500
480
460
0
50
100
150
200
250

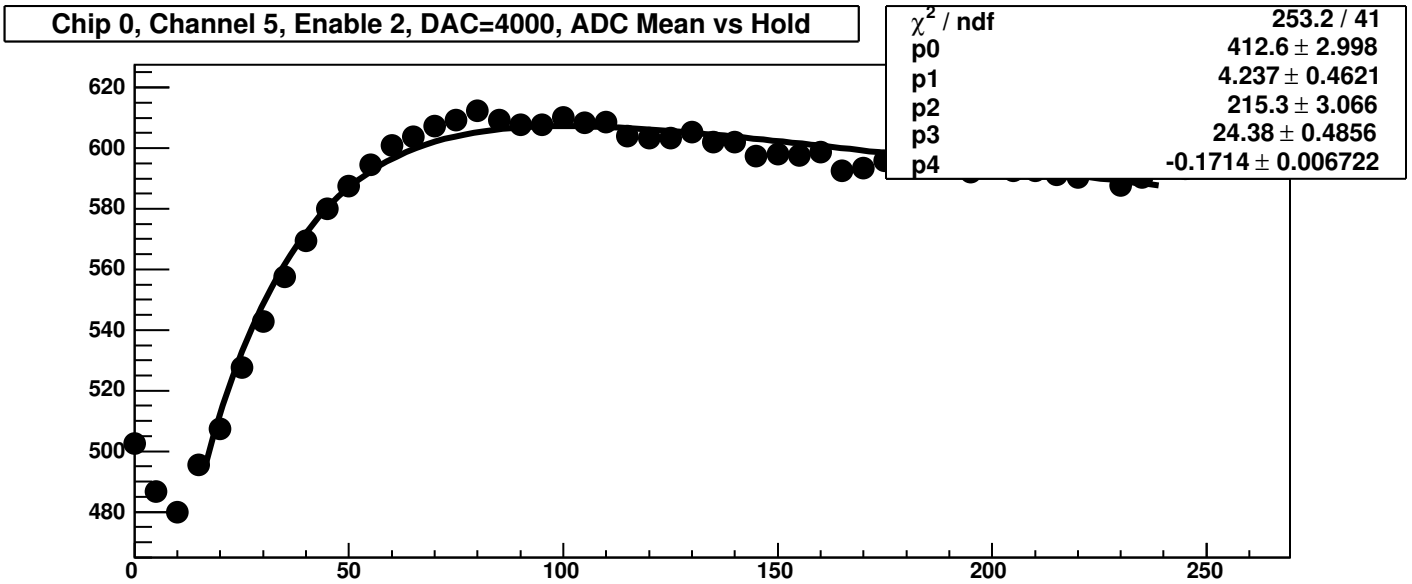

Chip 0, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

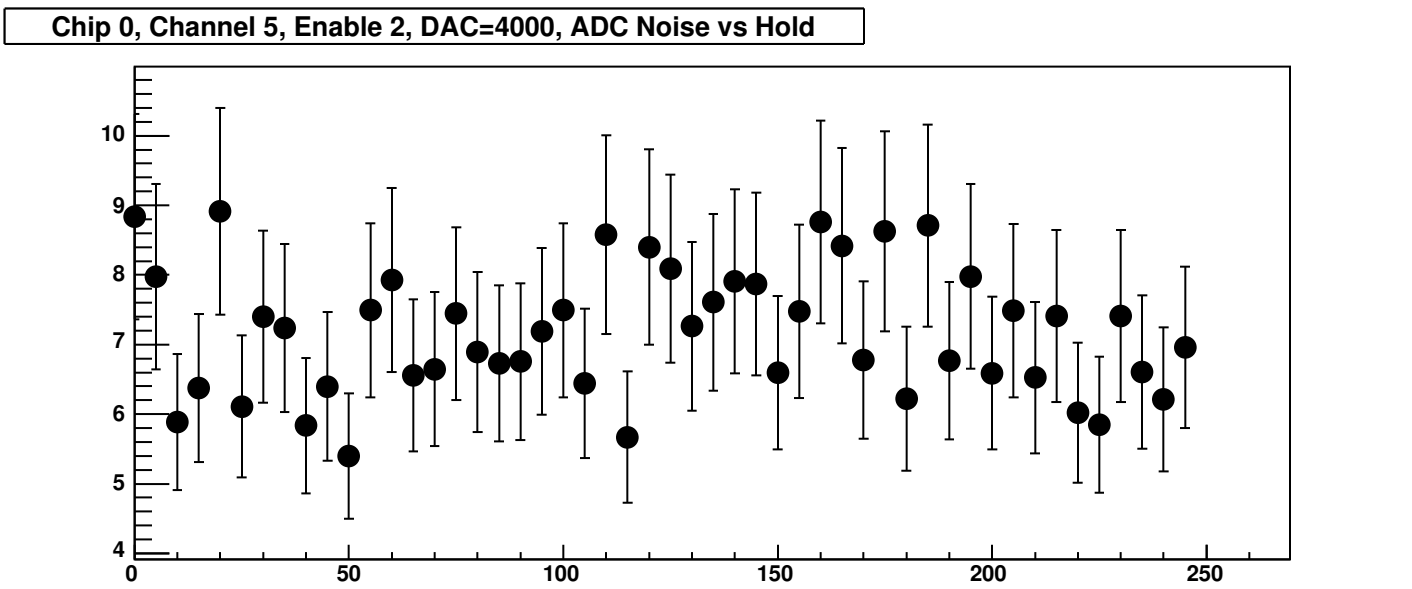

Chip 0, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold
80
60
40
20
0
-20
0
50
100
150
200
250

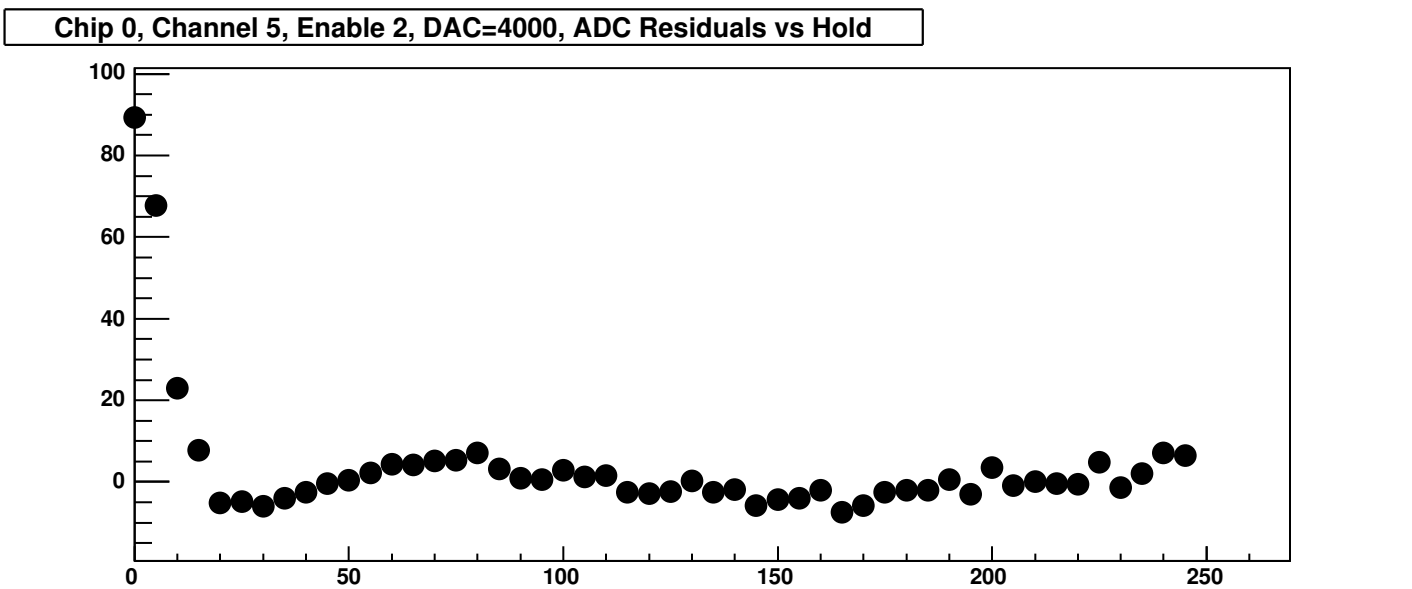

Chip 0, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 575.5 / 42
p0 -289.3 ± 0.6359
p1 4.754 ± 0.007817
p2 1.13e+04 ± 0.96
p3 31.13 ± 0.005783
12000
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

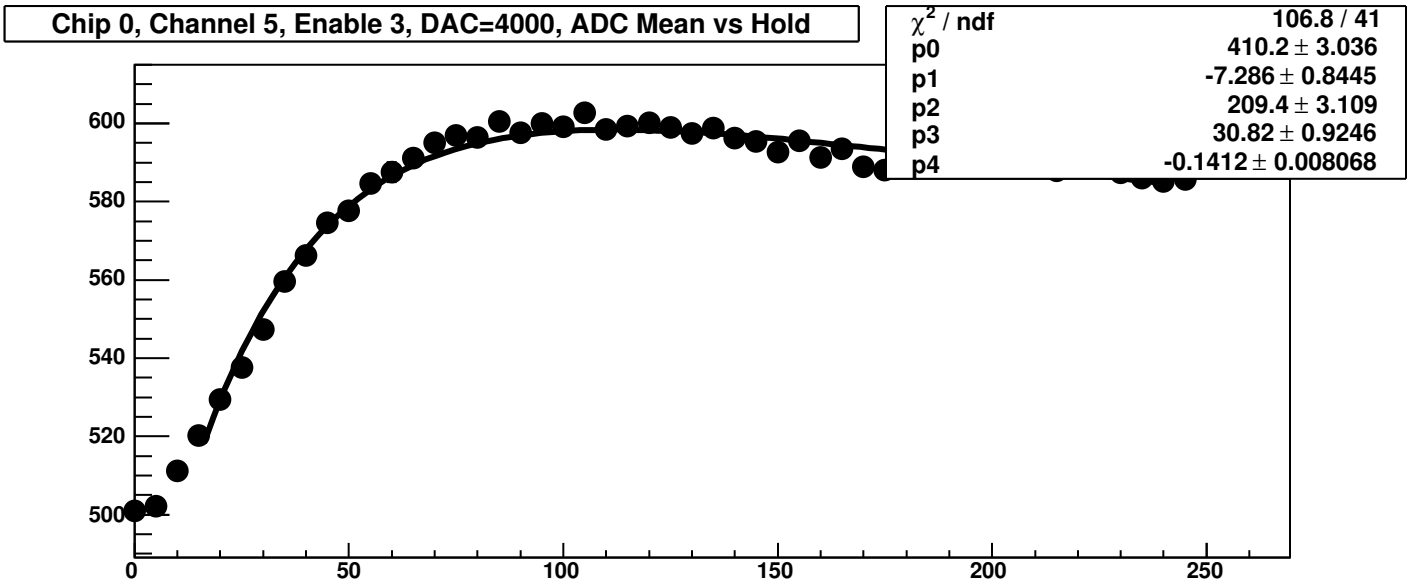

Chip 0, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

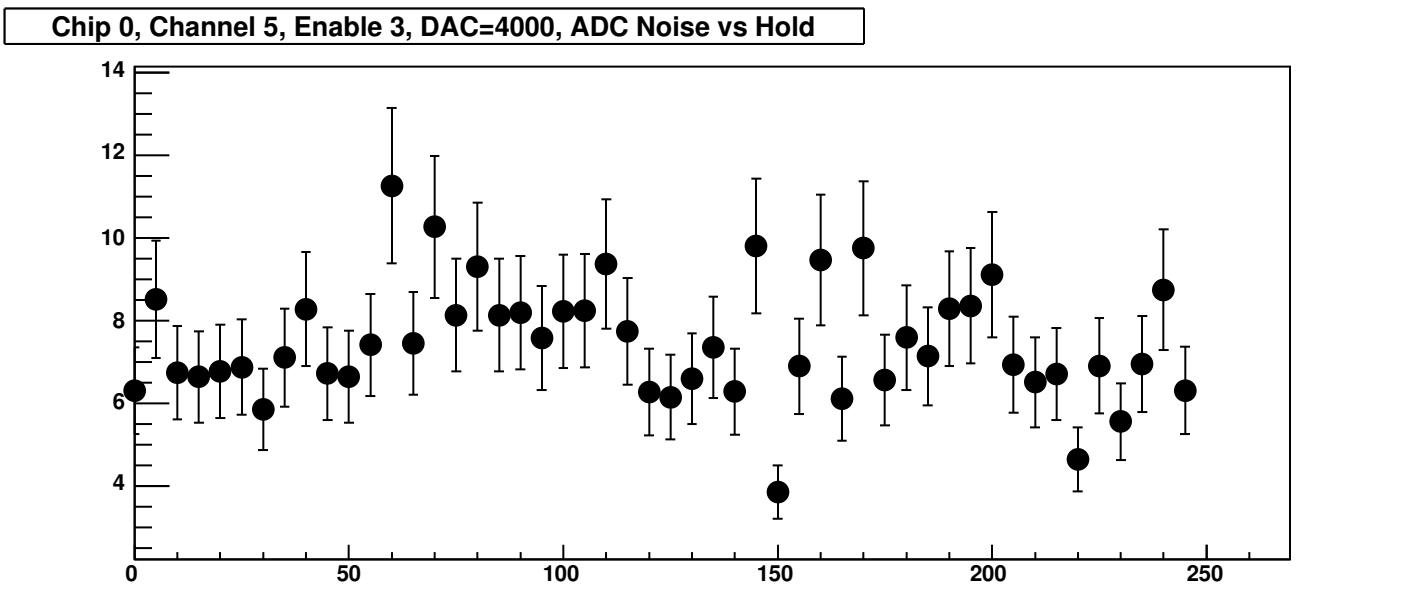

Chip 0, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

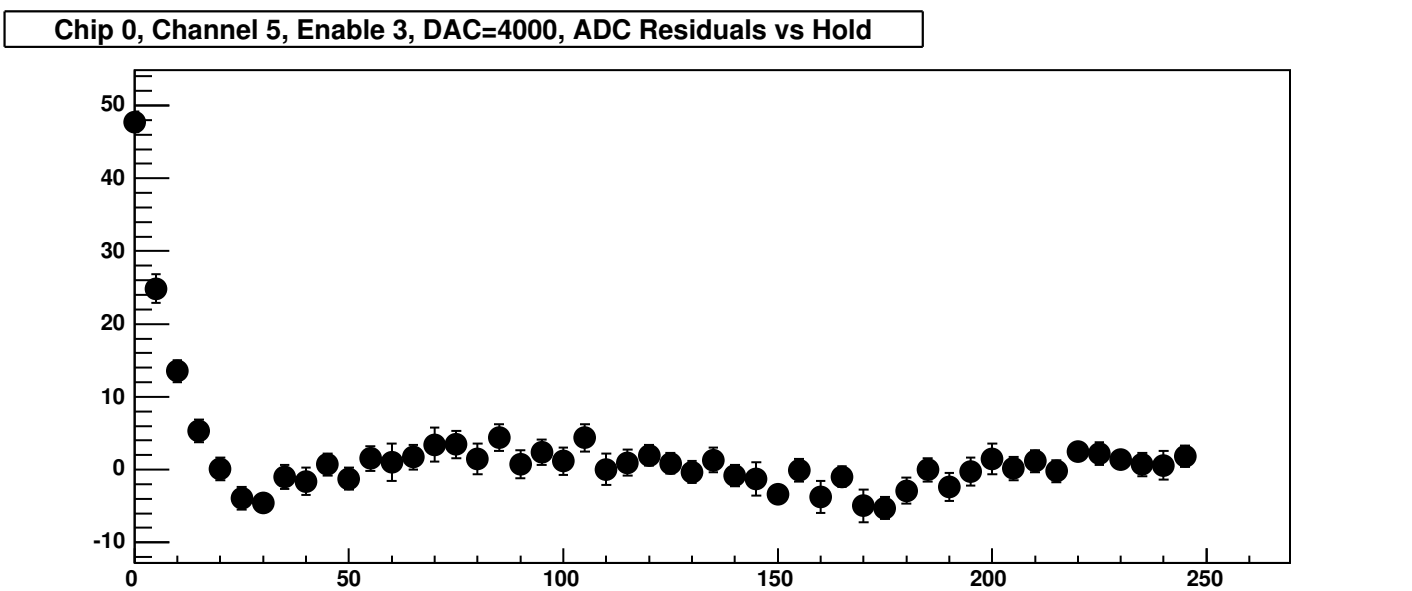

Chip 0, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 253.2 / 41
p0 412.6 ± 2.998
p1 4.237 ± 0.4621
p2 215.3 ± 3.066
p3 24.38 ± 0.4856
p4 -0.1714 ± 0.006722
620
600
580
560
540
520
500
480
0
50
100
150
200
250


Chip 0, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold
10
9
8
7
6
5
4
0
50
100
150
200
250

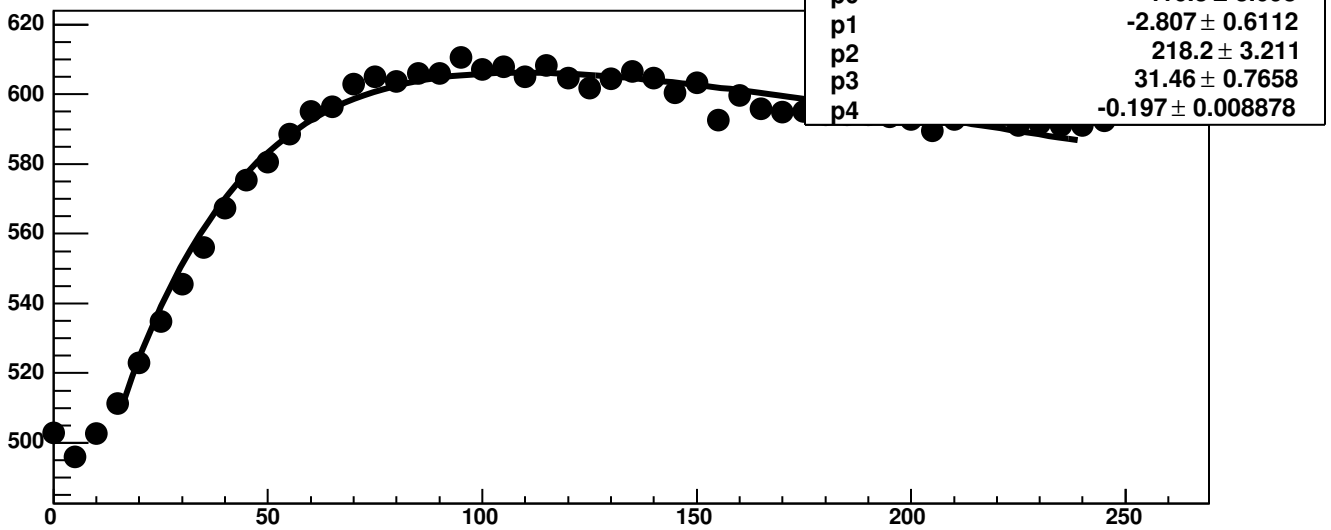

Chip 0, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 106.8 / 41
p0 410.2 ± 3.036
p1 -7.286 ± 0.8445
p2 209.4 ± 3.109
p3 30.82 ± 0.9246
p4 -0.1412 ± 0.008068
600
580
560
540
520
500
0
50
100
150
200
250

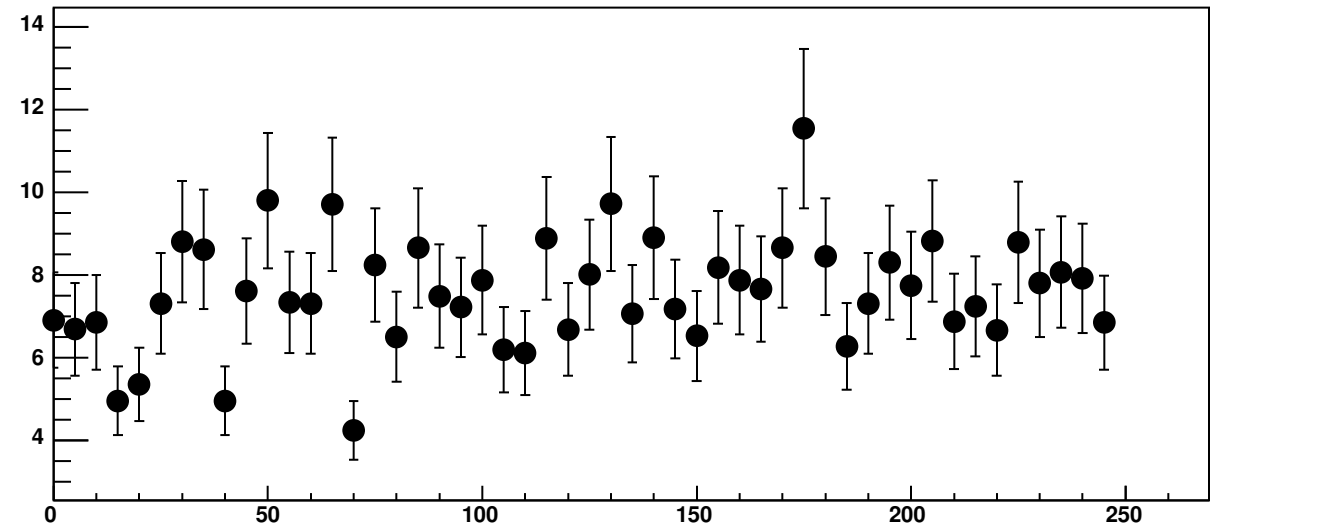

Chip 0, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 0, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250

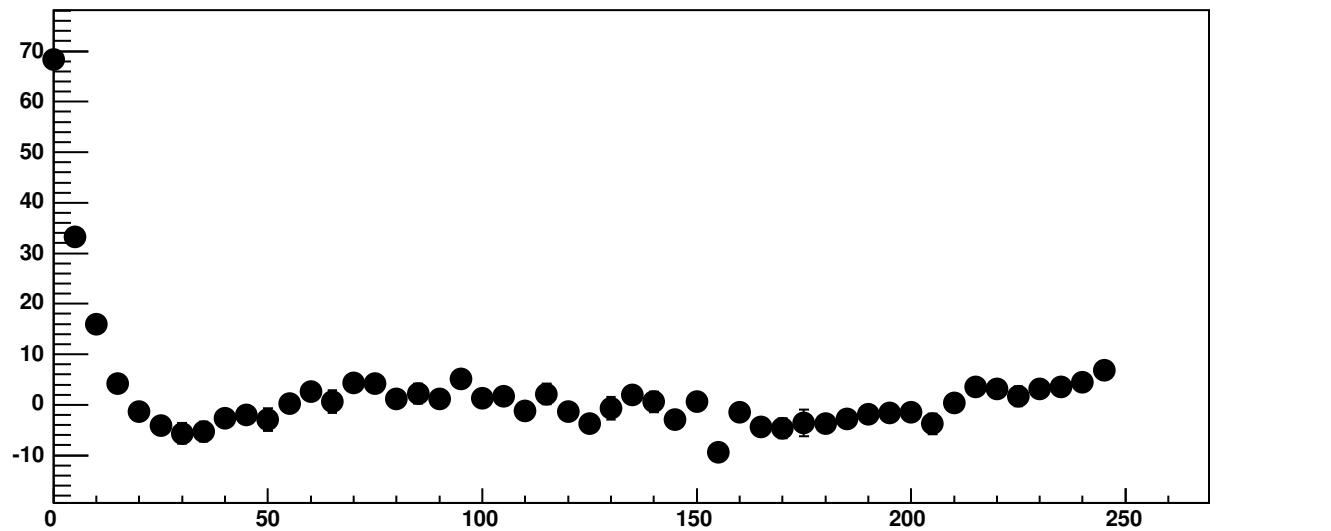
Chip 0, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold

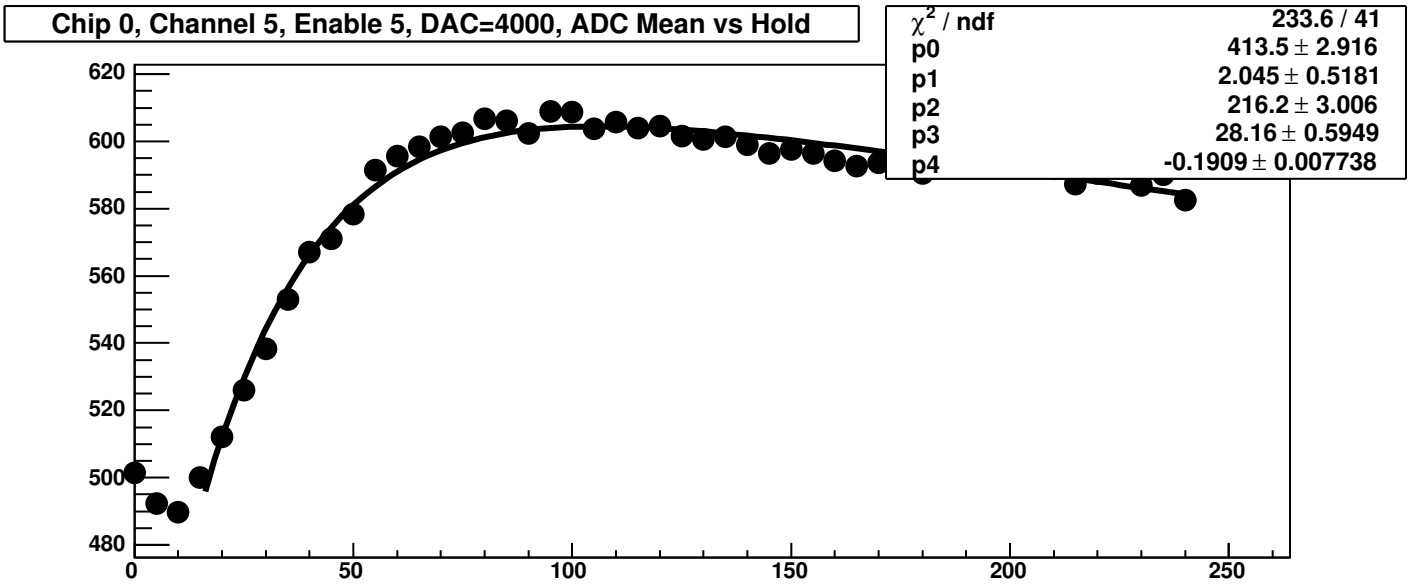


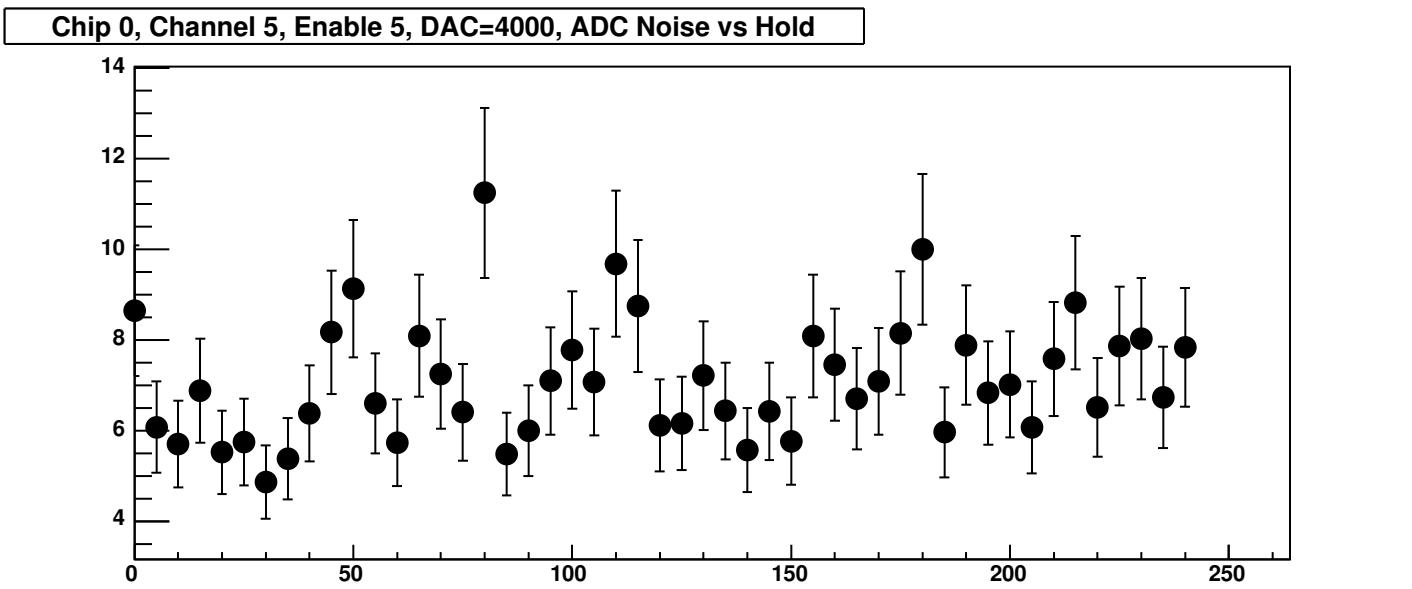
Chip 0, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold

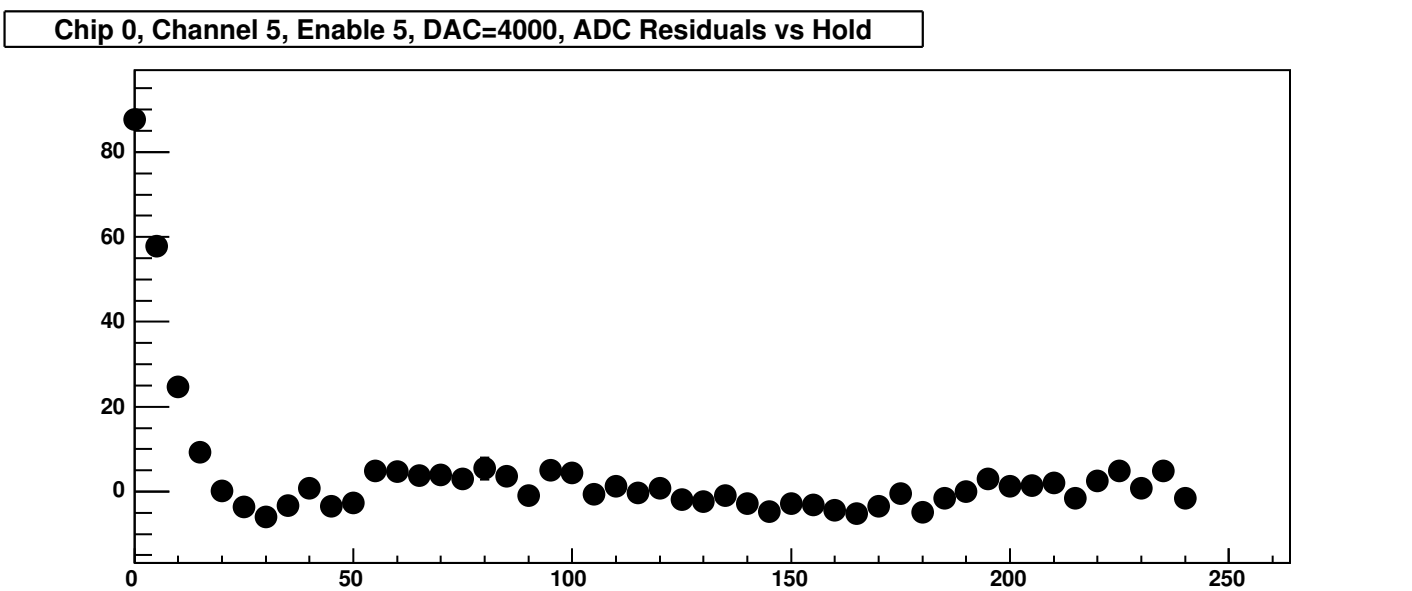


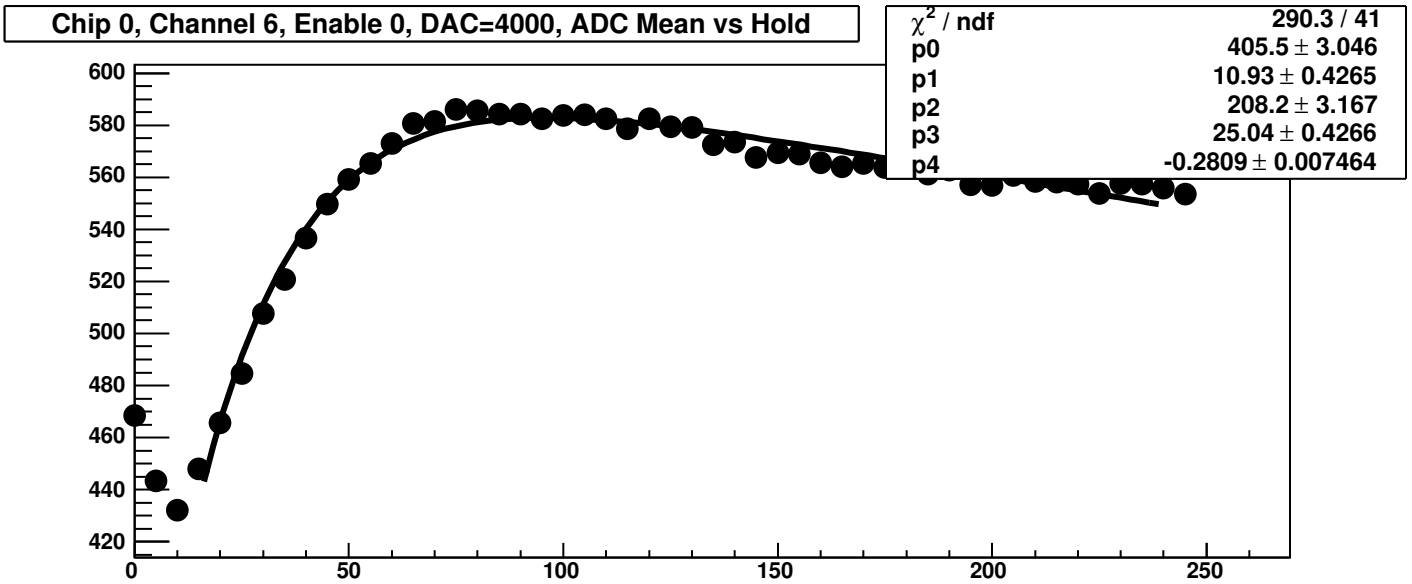
Chip 0, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold

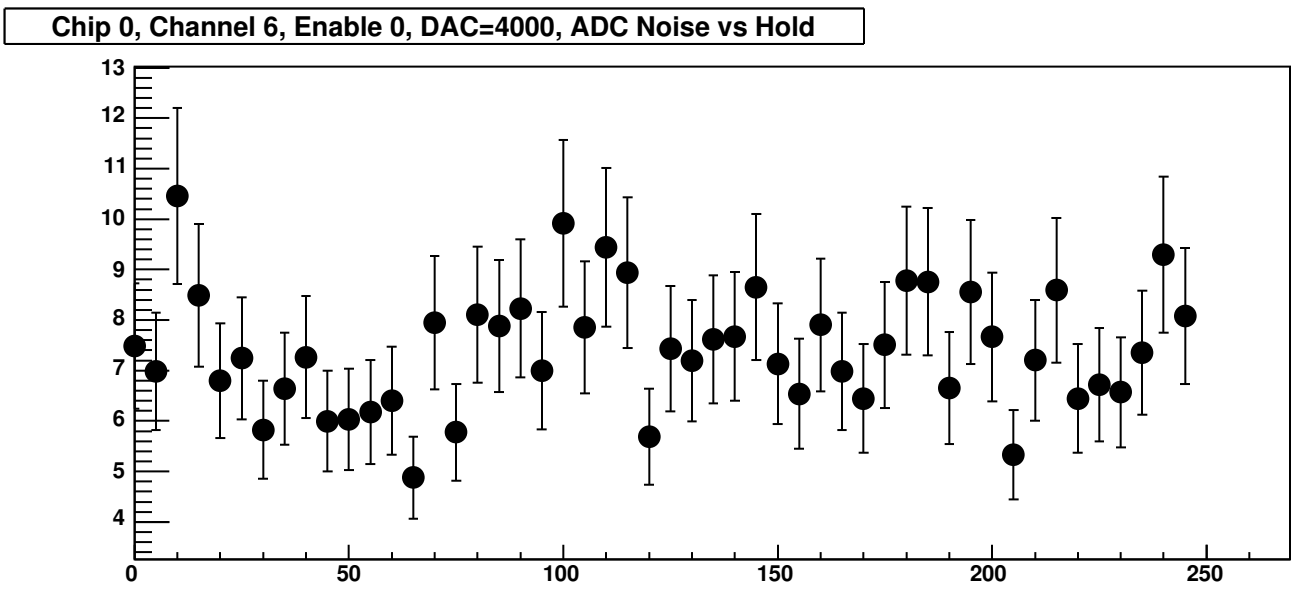


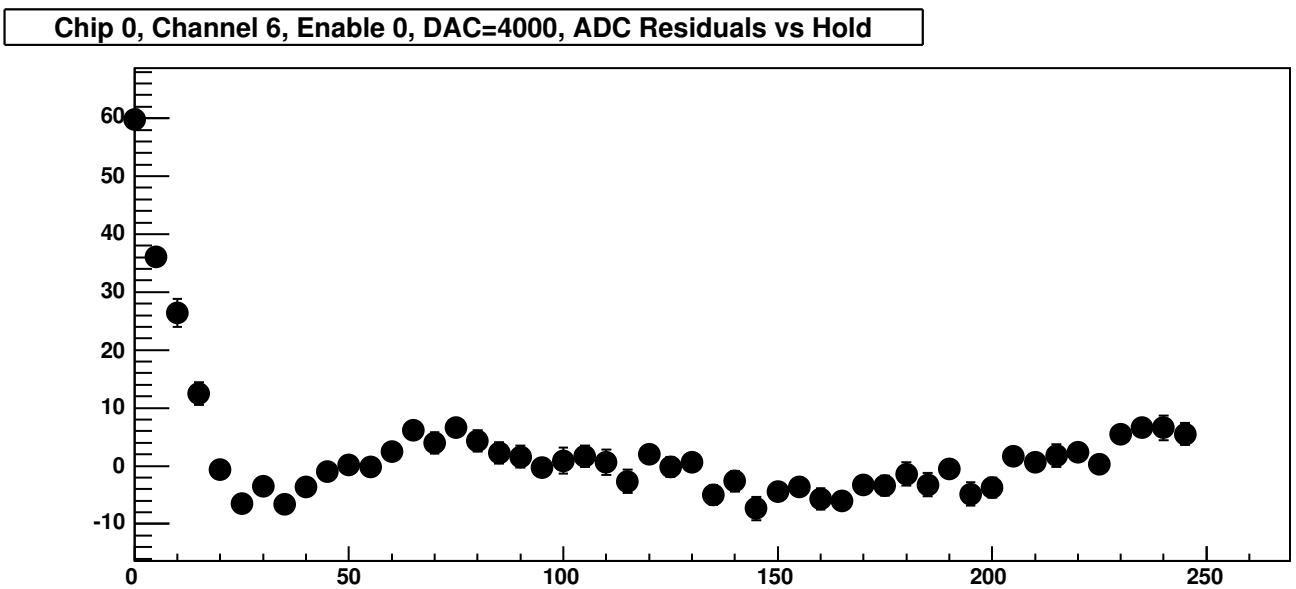

Chip 0, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 233.6 / 41
p0 413.5 ± 2.916
p1 2.045 ± 0.5181
p2 216.2 ± 3.006
p3 28.16 ± 0.5949
p4 -0.1909 ± 0.007738
620
600
580
560
540
520
500
480
0
50
100
150
200
250

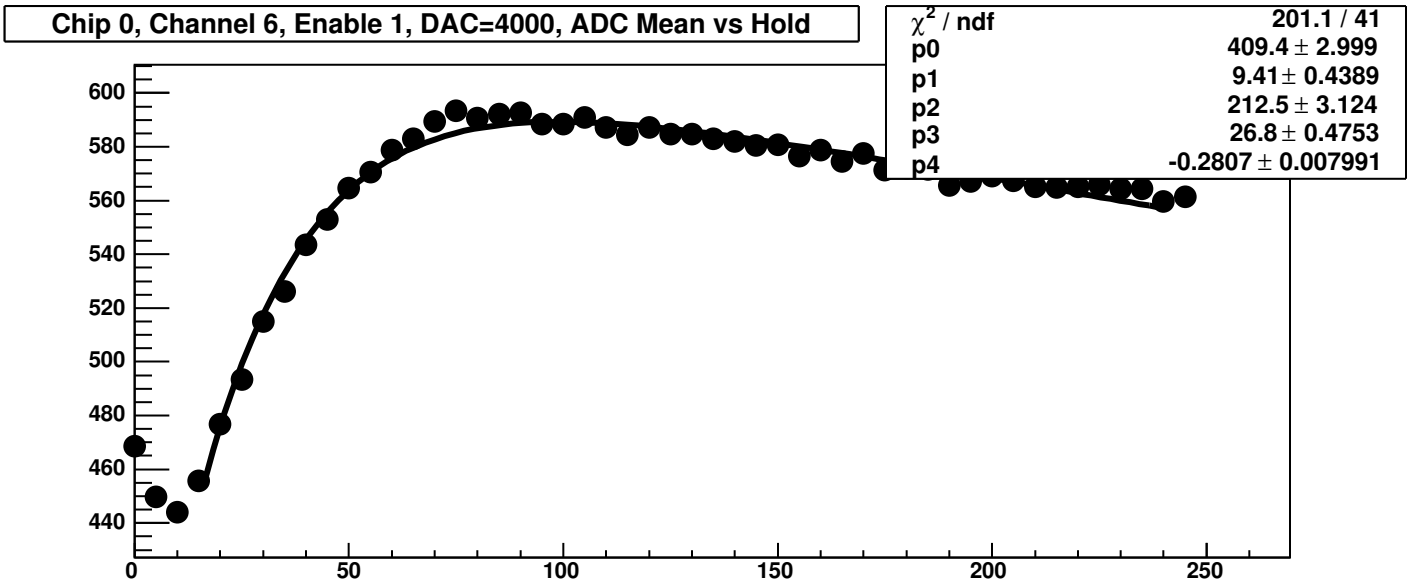

Chip 0, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

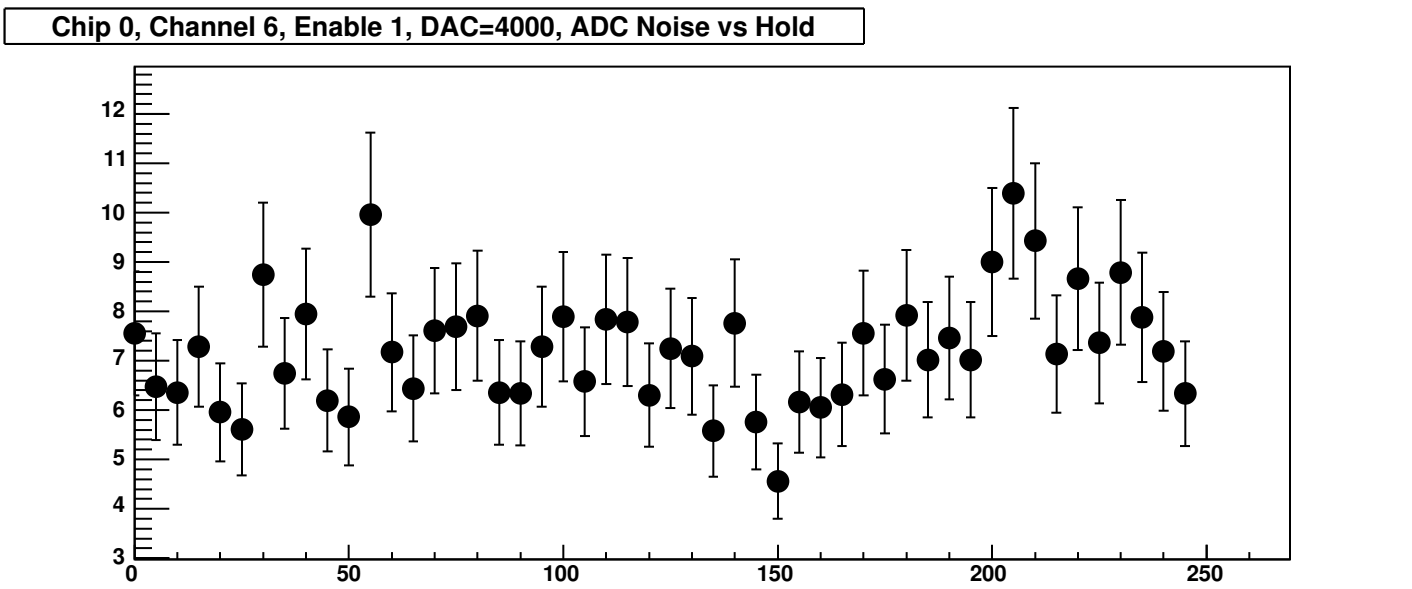

Chip 0, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold
80
60
40
20
0
0
50
100
150
200
250

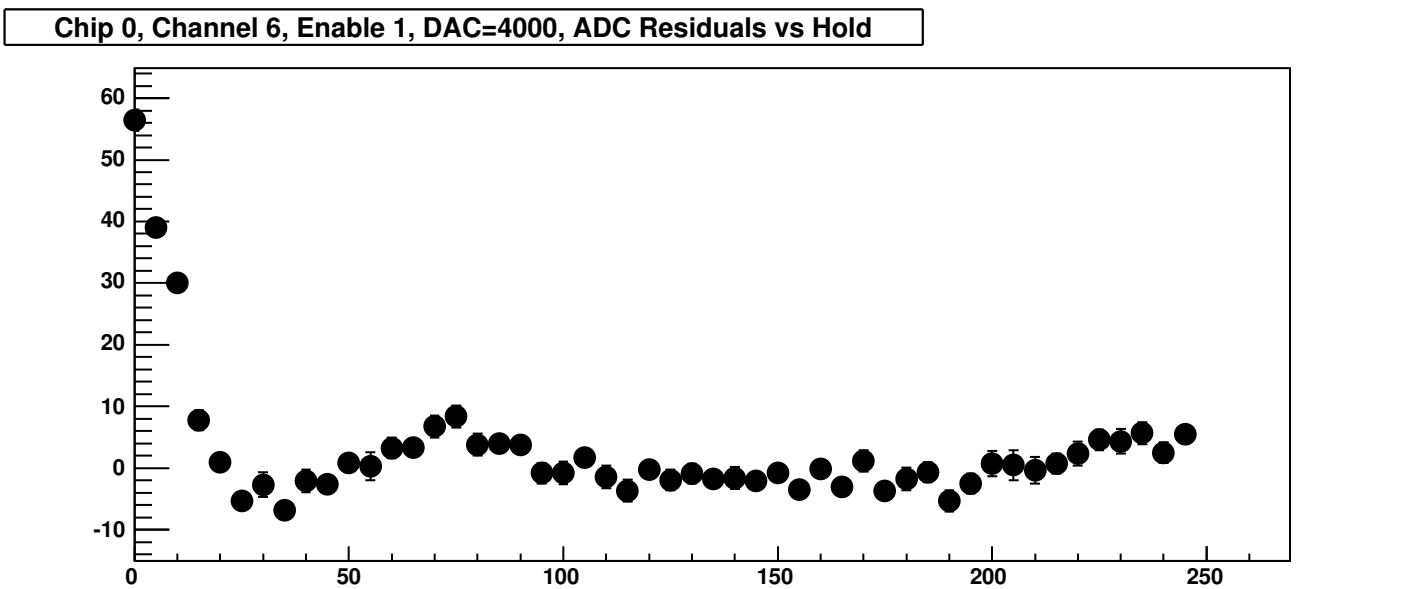

Chip 0, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 290.3 / 41
p0 405.5 ± 3.046
p1 10.93 ± 0.4265
p2 208.2 ± 3.167
p3 25.04 ± 0.4266
p4 -0.2809 ± 0.007464
600
580
560
540
520
500
480
460
440
420
0
50
100
150
200
250


Chip 0, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 0, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

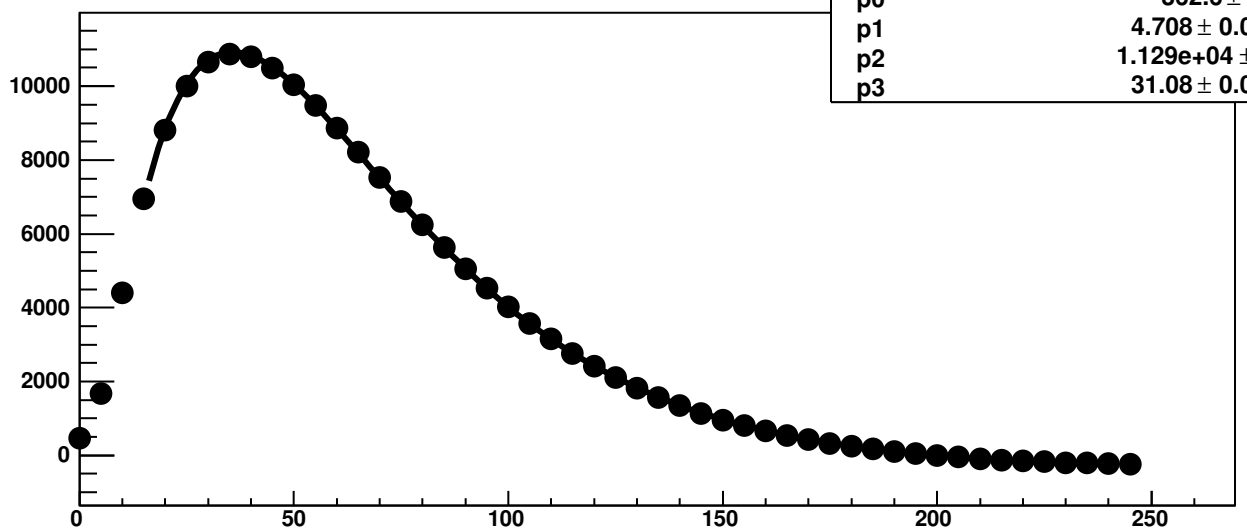

Chip 0, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 201.1 / 41
p0 409.4 ± 2.999
p1 9.41 ± 0.4389
p2 212.5 ± 3.124
p3 26.8 ± 0.4753
p4 -0.2807 ± 0.007991
600
580
560
540
520
500
480
460
440
0
50
100
150
200
250


Chip 0, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

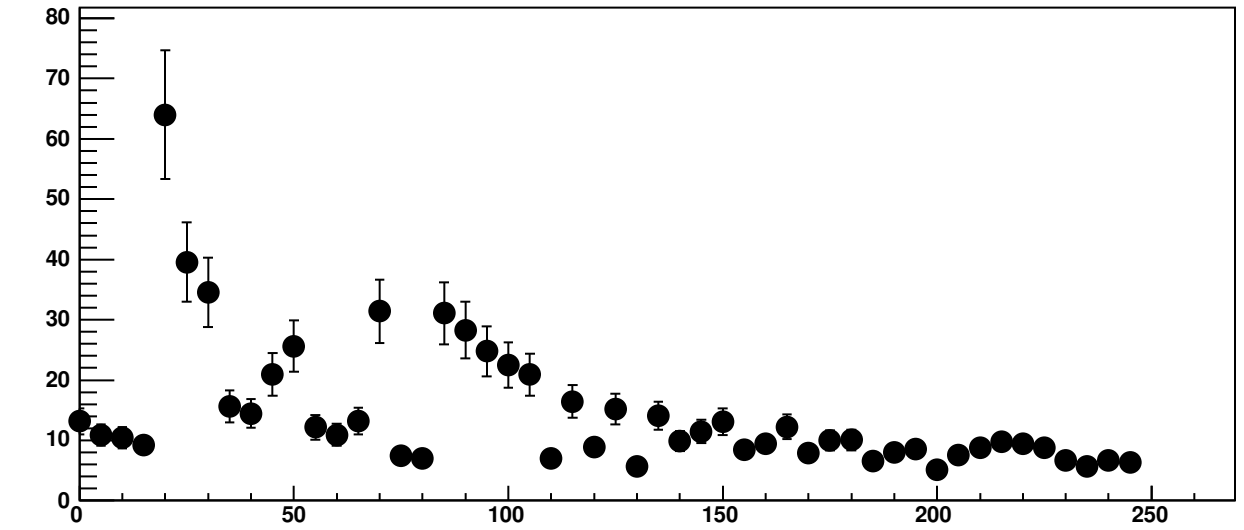

Chip 0, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

Chip 0, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold

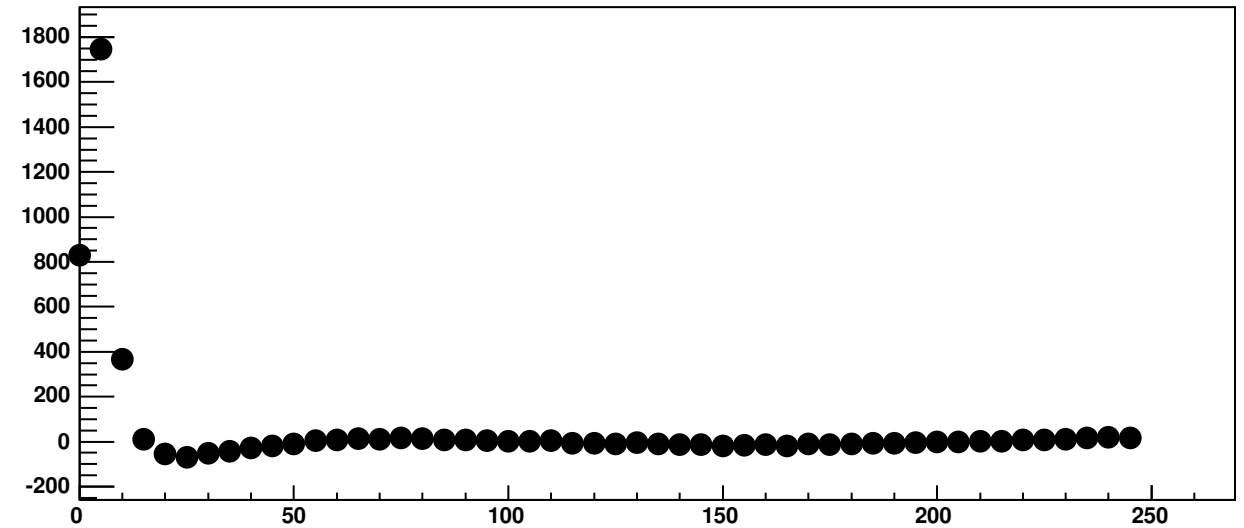
χ^2 / ndf	1346 / 42
p0	-362.6 ± 0.5902
p1	4.708 ± 0.006043
p2	1.129e+04 ± 1.427
p3	31.08 ± 0.005655



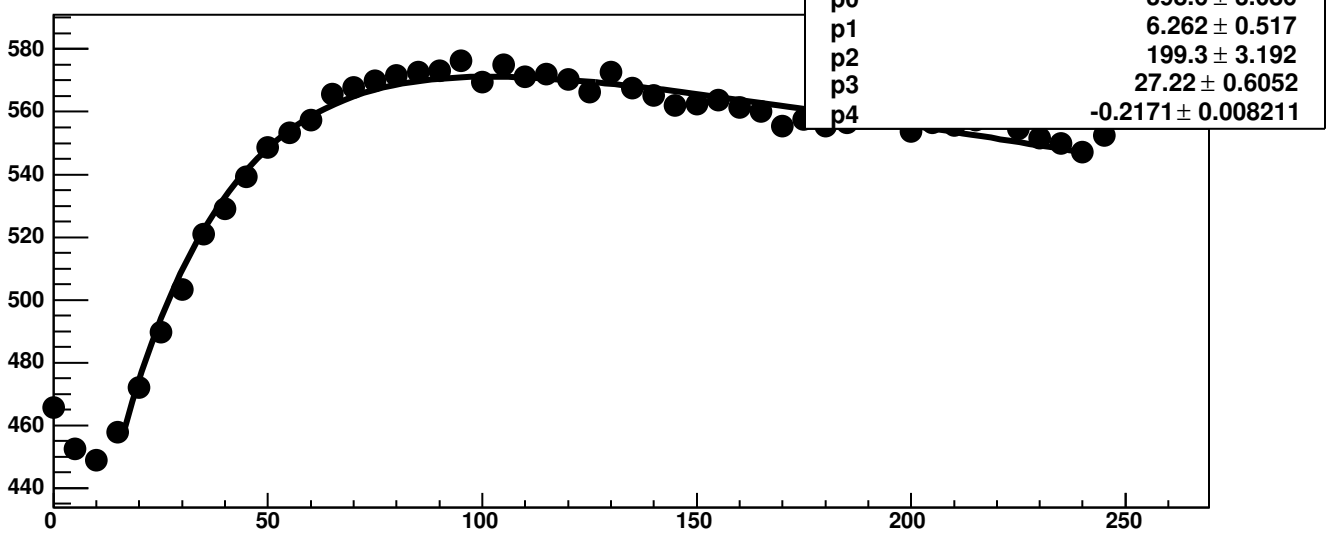
Chip 0, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold



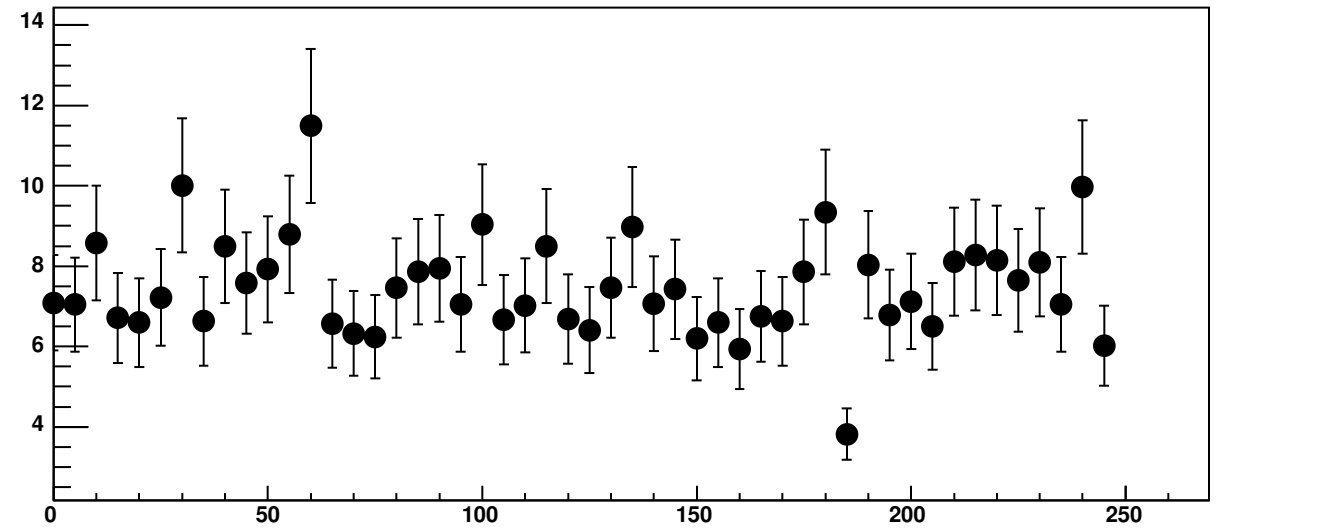
Chip 0, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold



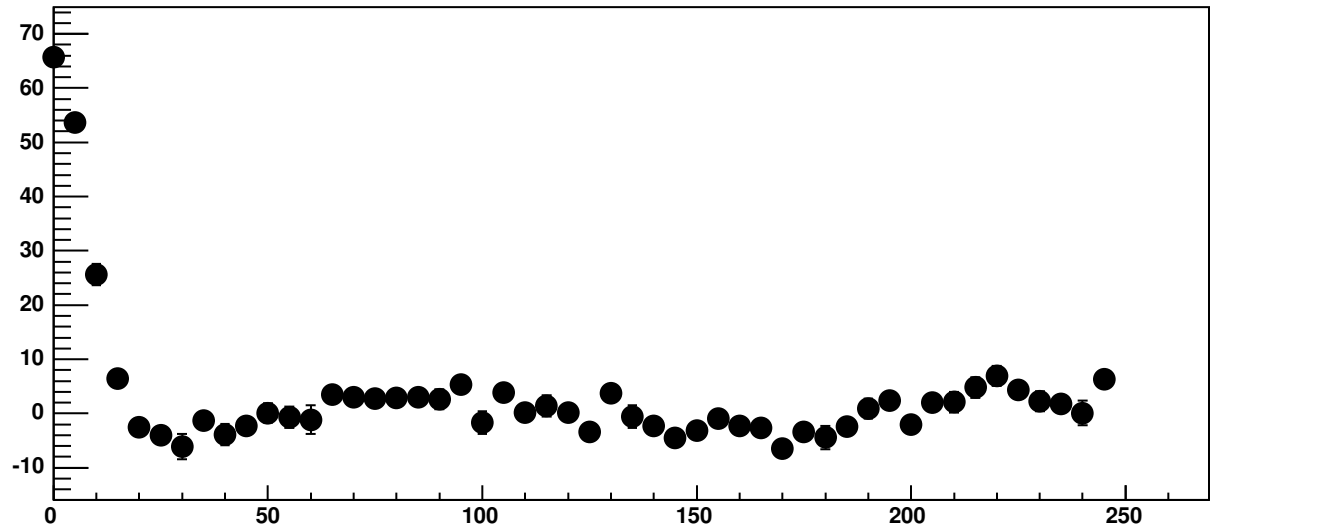
Chip 0, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold



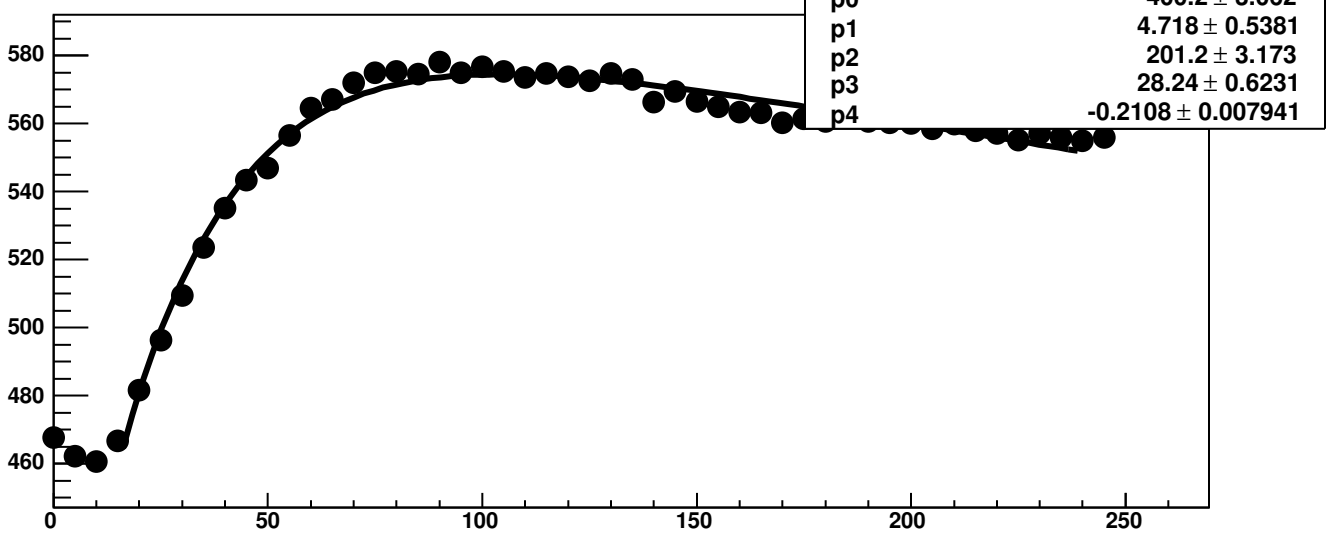
Chip 0, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



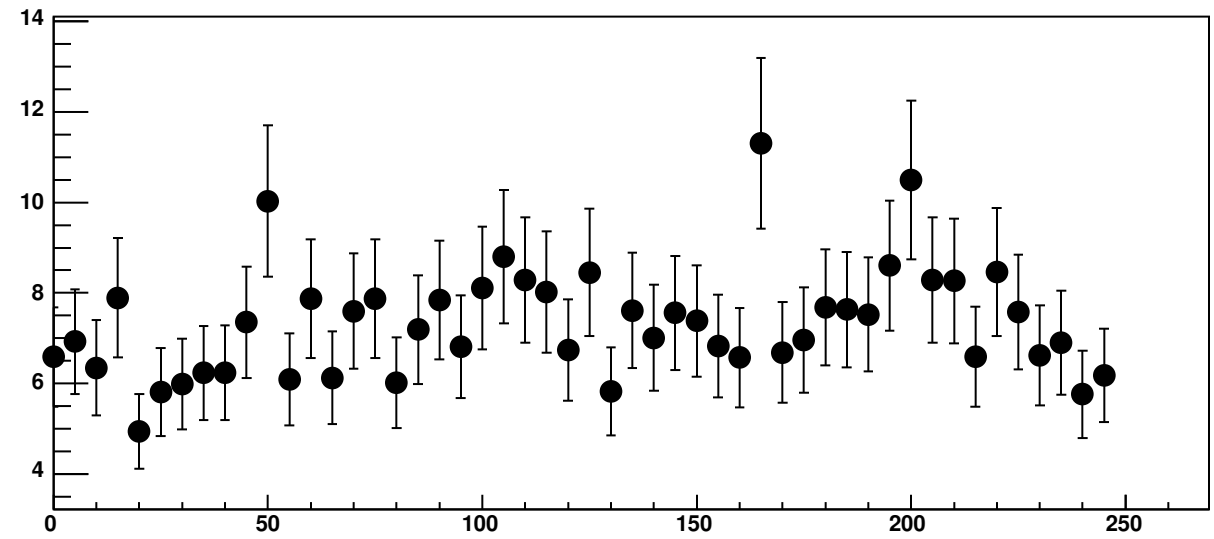
Chip 0, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold



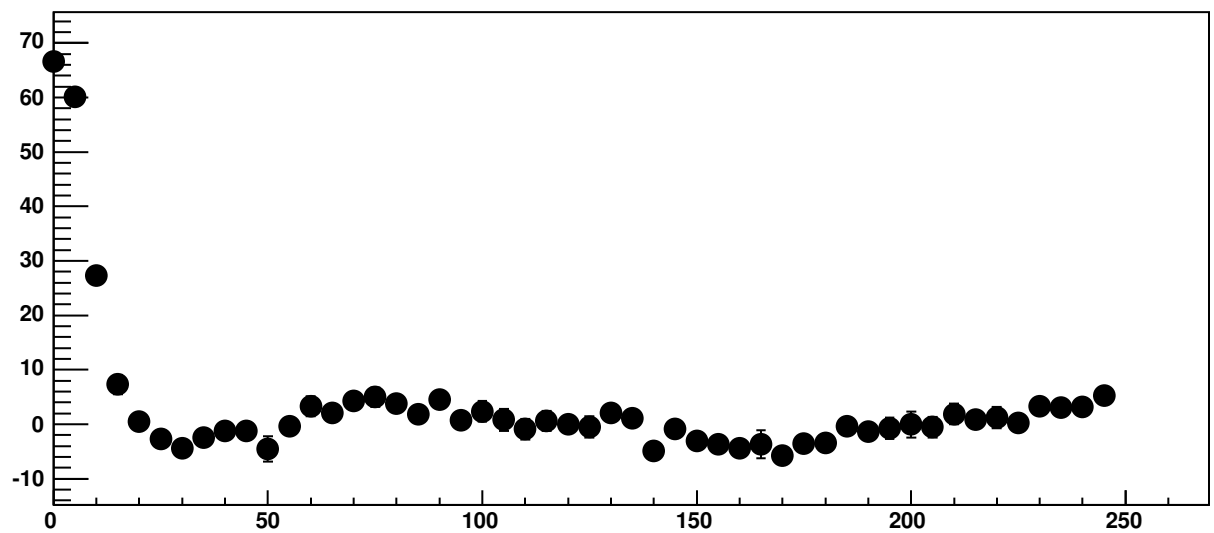
Chip 0, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

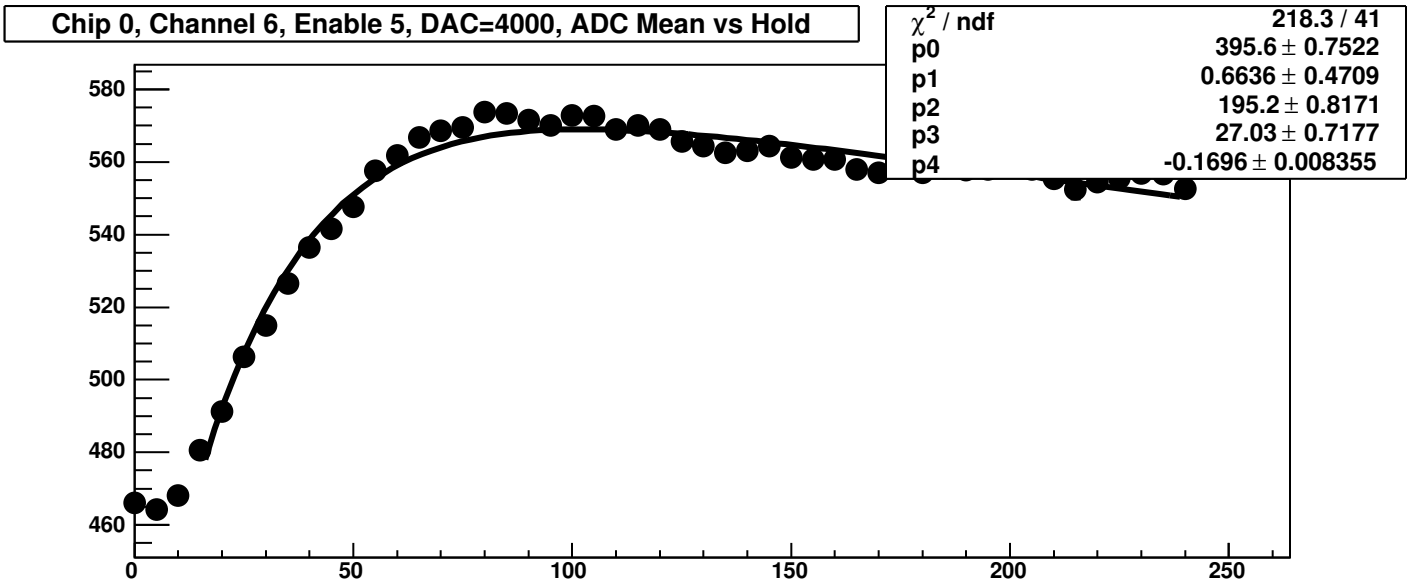


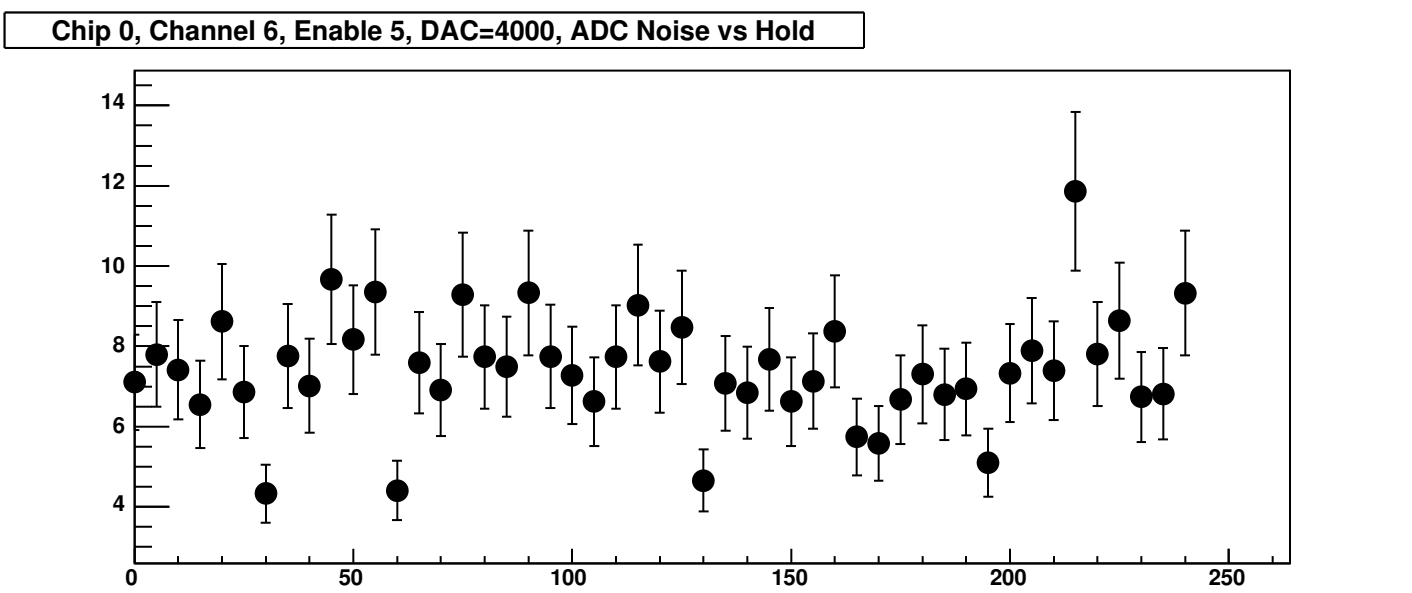
Chip 0, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold

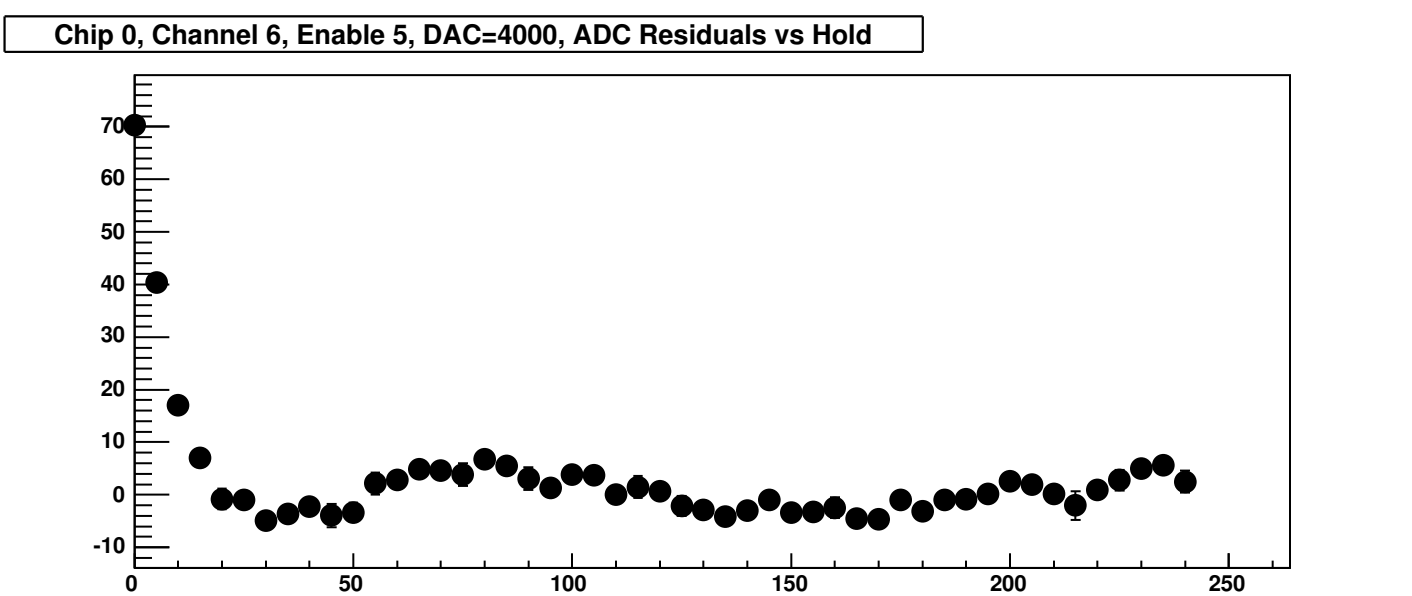


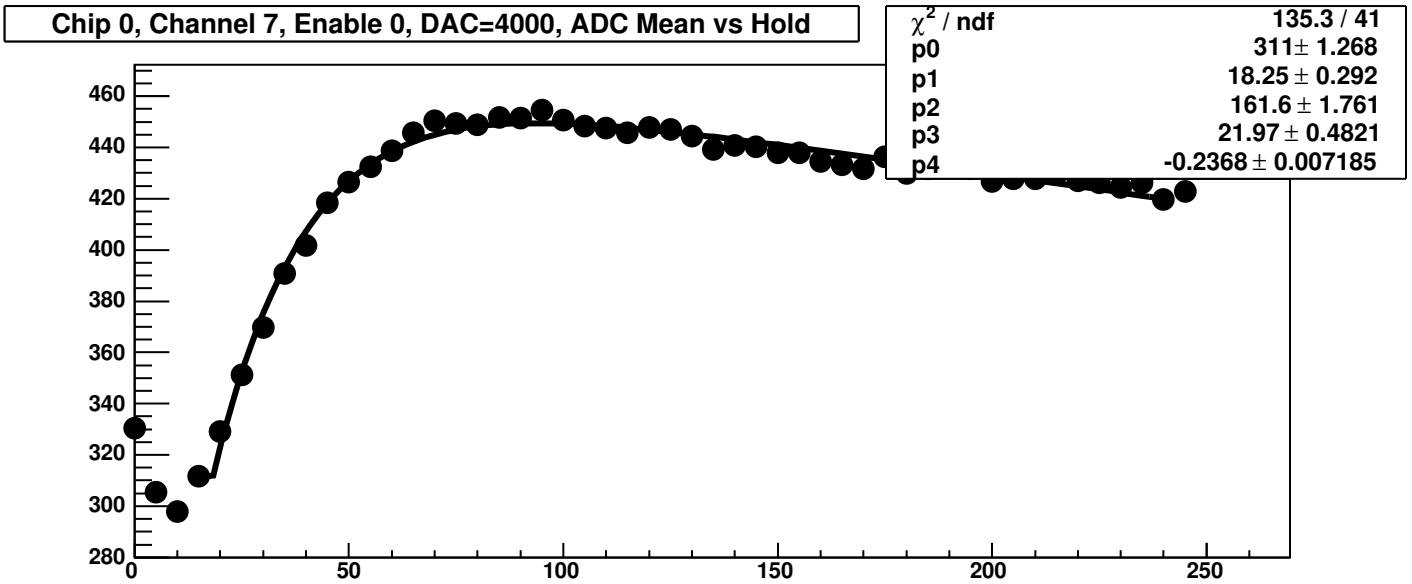
Chip 0, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

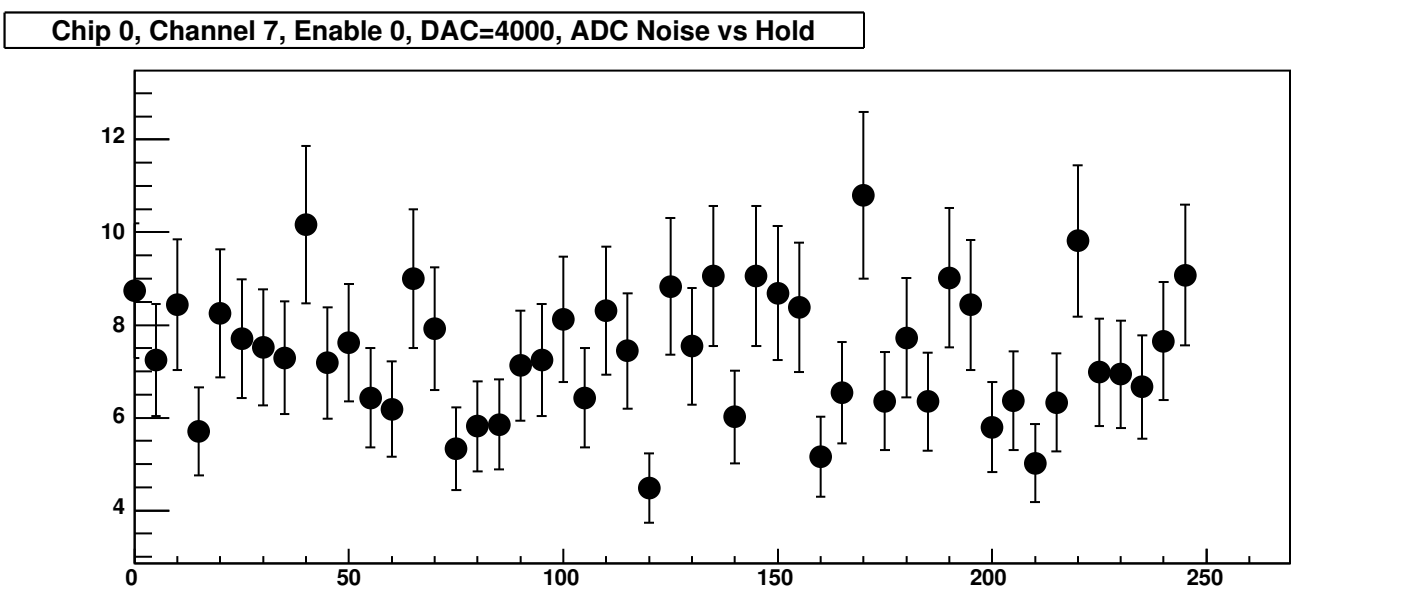


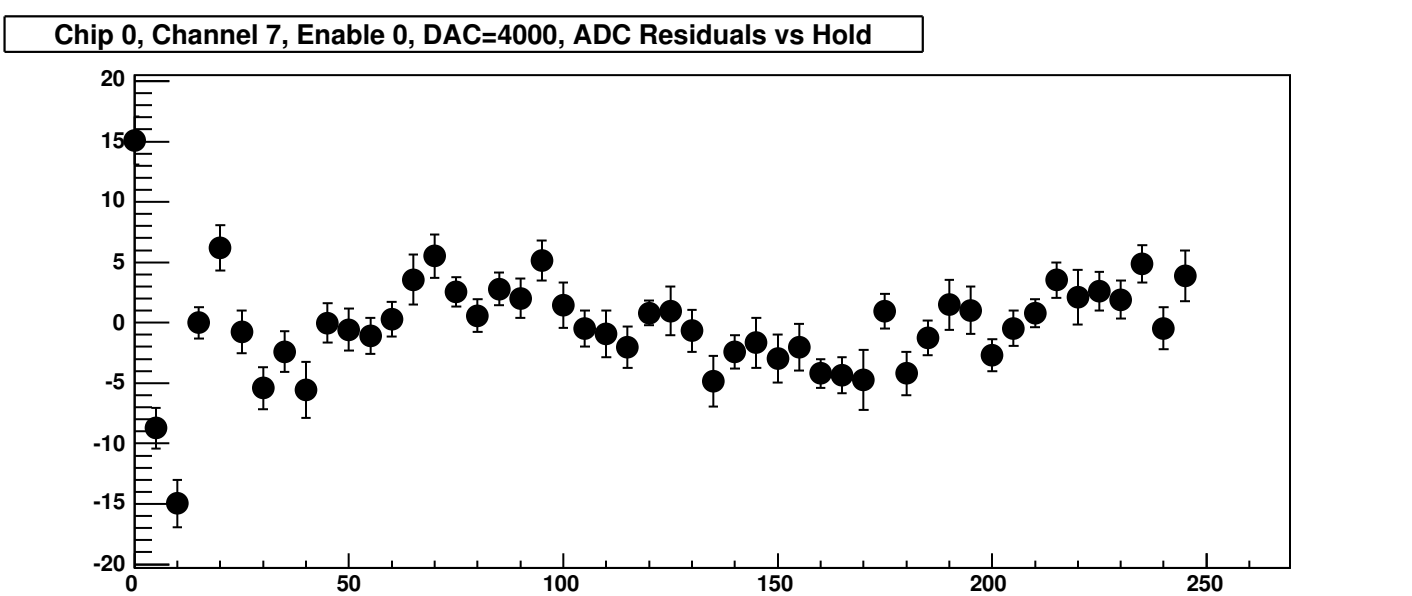

Chip 0, Channel 6, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 218.3 / 41
p0 395.6 ± 0.7522
p1 0.6636 ± 0.4709
p2 195.2 ± 0.8171
p3 27.03 ± 0.7177
p4 -0.1696 ± 0.008355
580
560
540
520
500
480
460
0
50
100
150
200
250

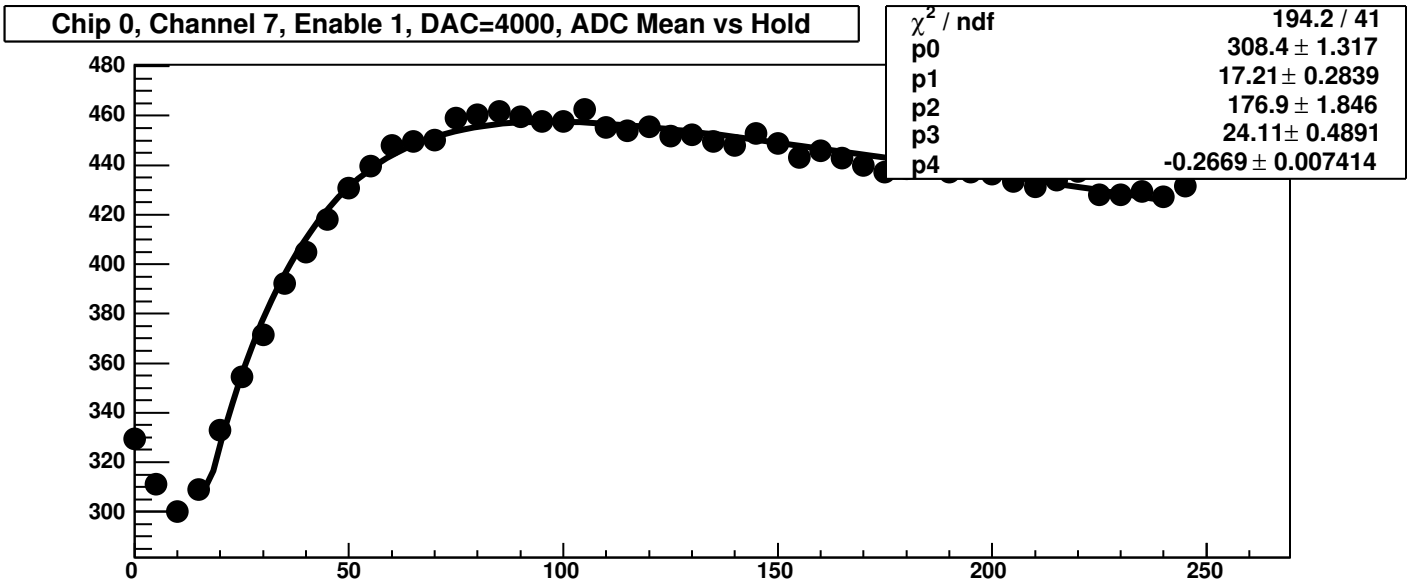

Chip 0, Channel 6, Enable 5, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

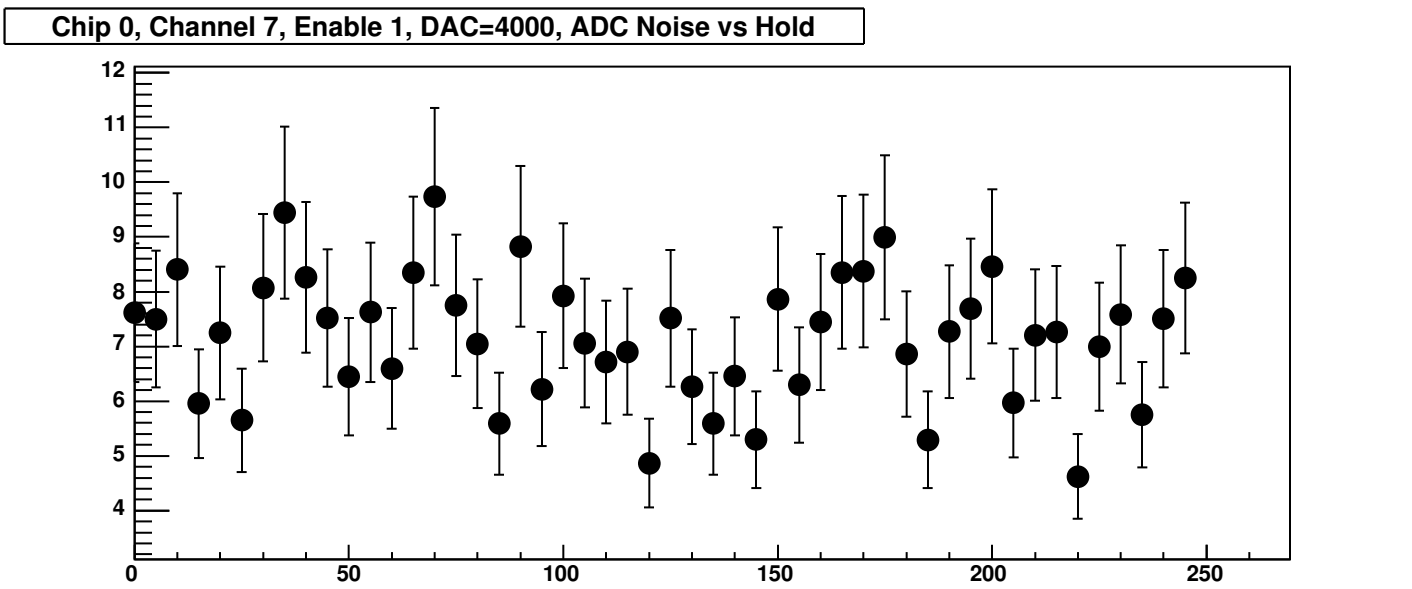

Chip 0, Channel 6, Enable 5, DAC=4000, ADC Residuals vs Hold
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

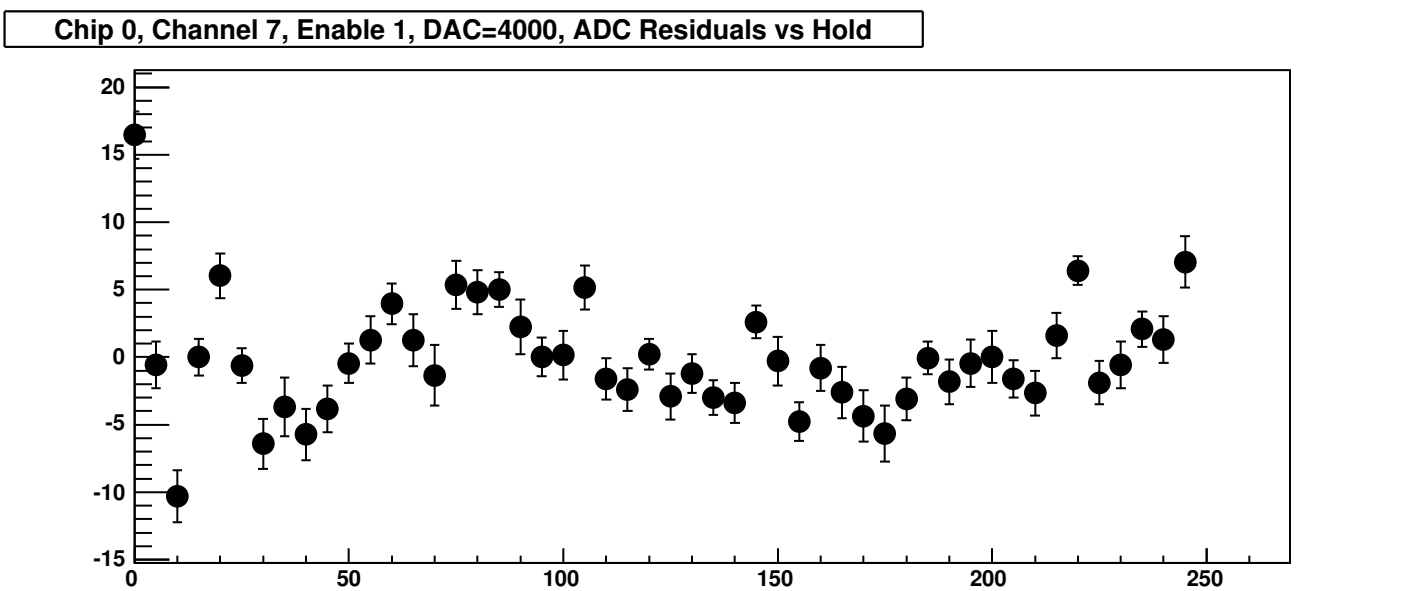

Chip 0, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 135.3 / 41
p0 311 ± 1.268
p1 18.25 ± 0.292
p2 161.6 ± 1.761
p3 21.97 ± 0.4821
p4 -0.2368 ± 0.007185
460
440
420
400
380
360
340
320
300
280
0
50
100
150
200
250

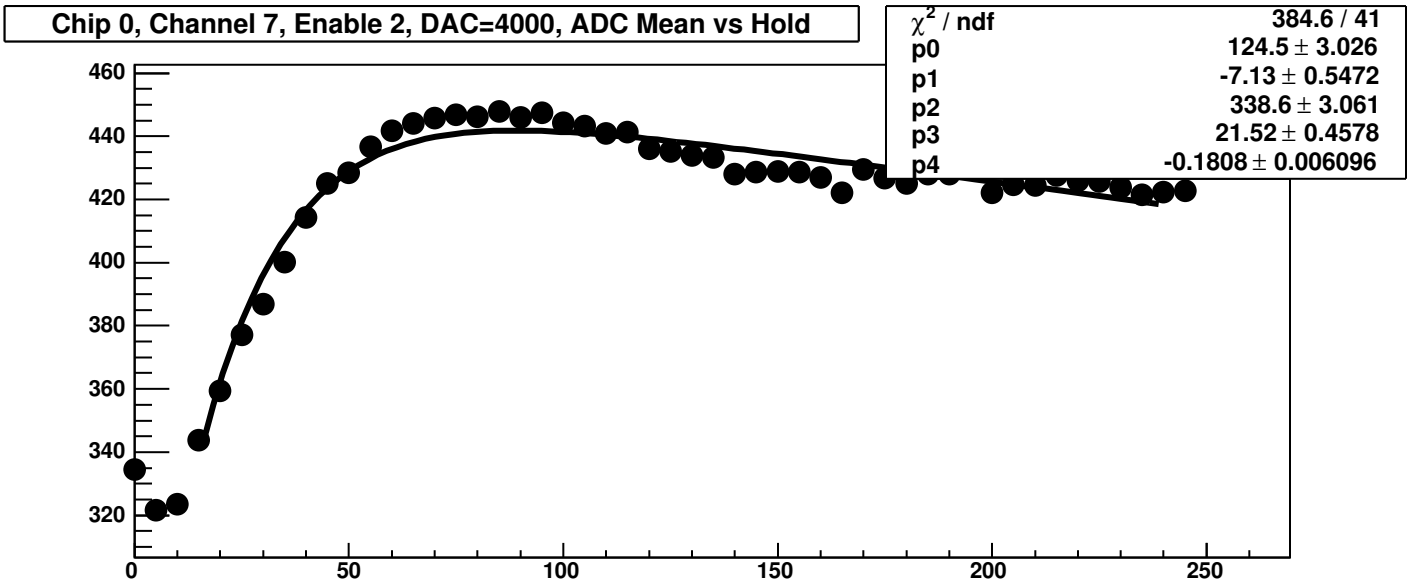

Chip 0, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

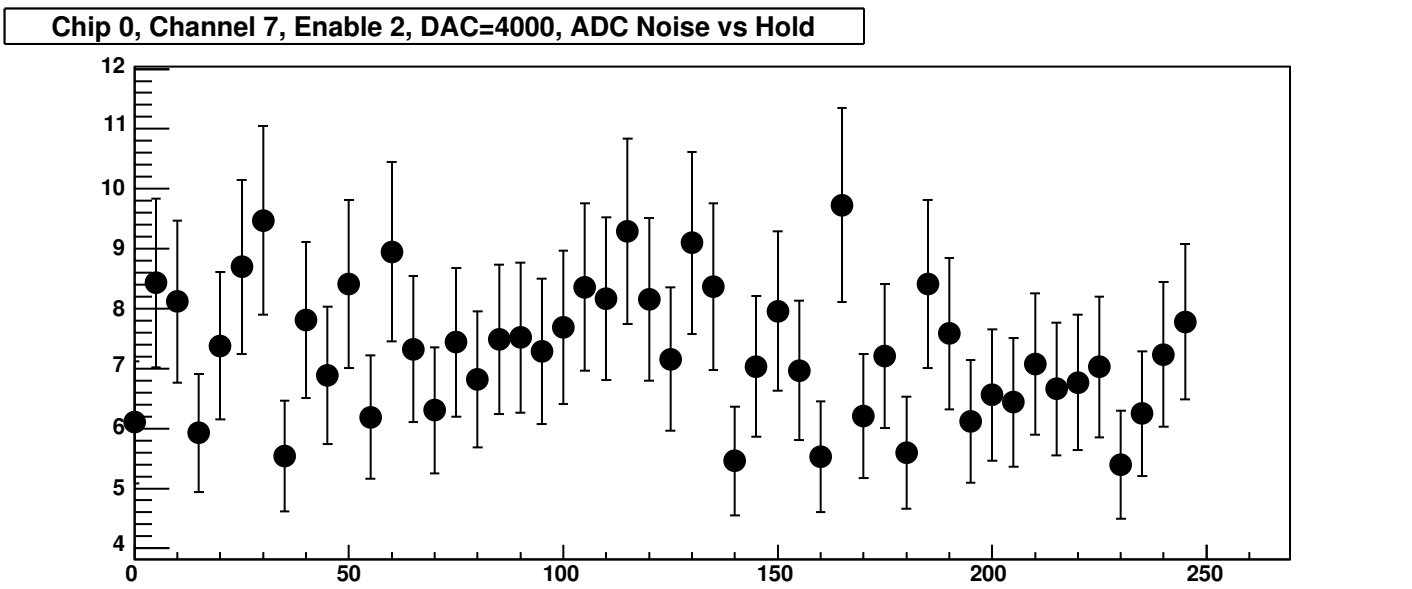

Chip 0, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
-15
-20
0
50
100
150
200
250

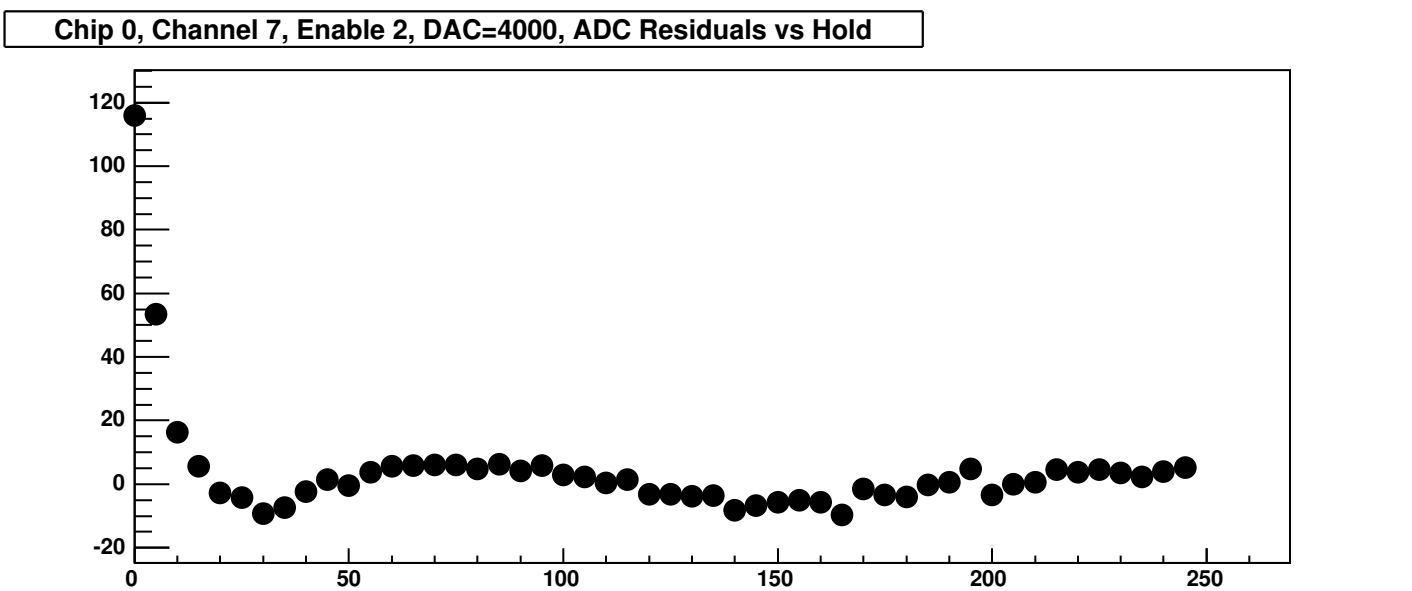

Chip 0, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 194.2 / 41
p0 308.4 ± 1.317
p1 17.21 ± 0.2839
p2 176.9 ± 1.846
p3 24.11 ± 0.4891
p4 -0.2669 ± 0.007414
480
460
440
420
400
380
360
340
320
300
0
50
100
150
200
250

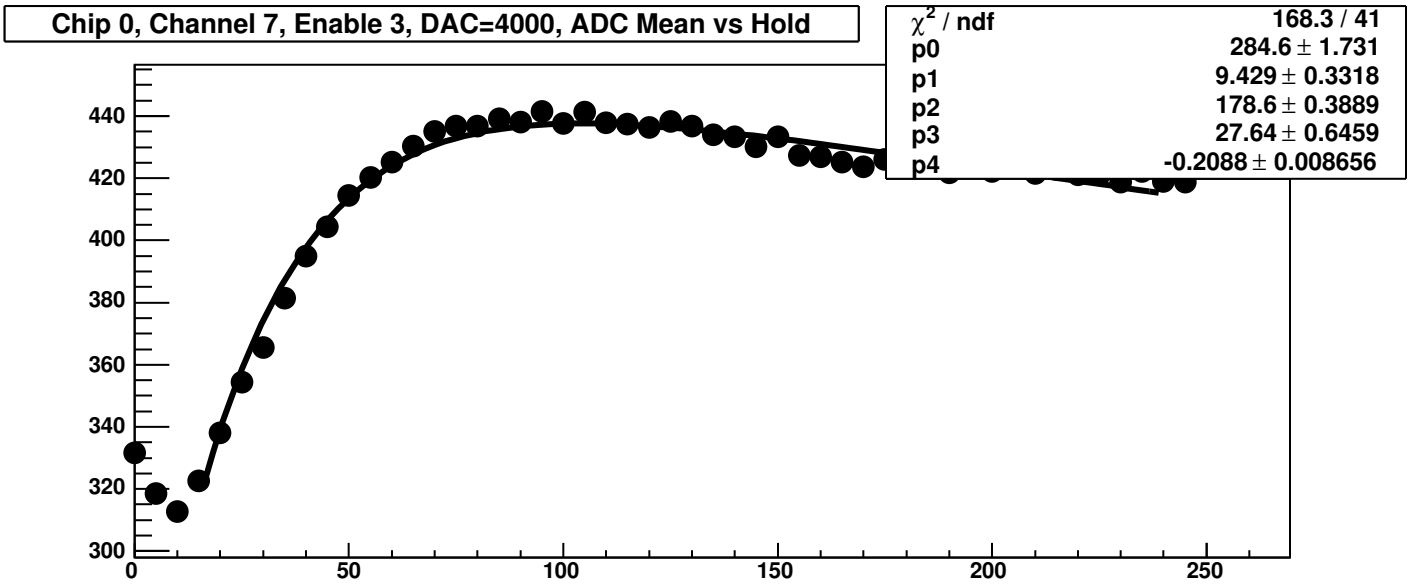

Chip 0, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

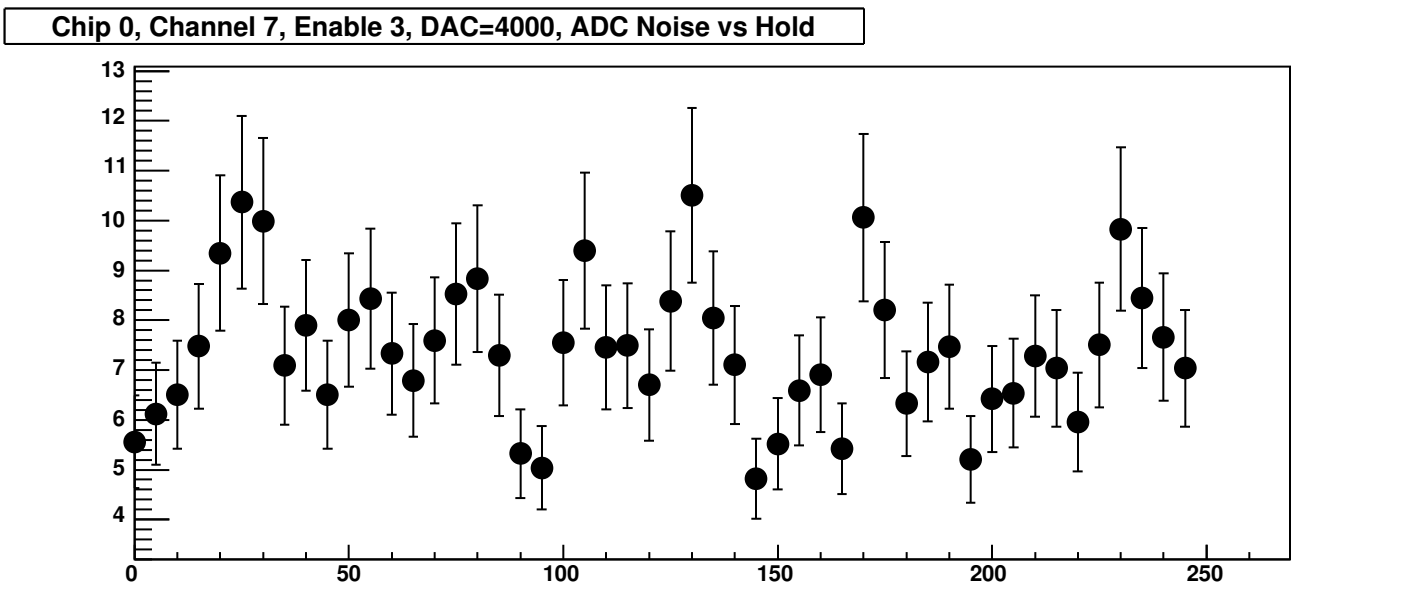

Chip 0, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

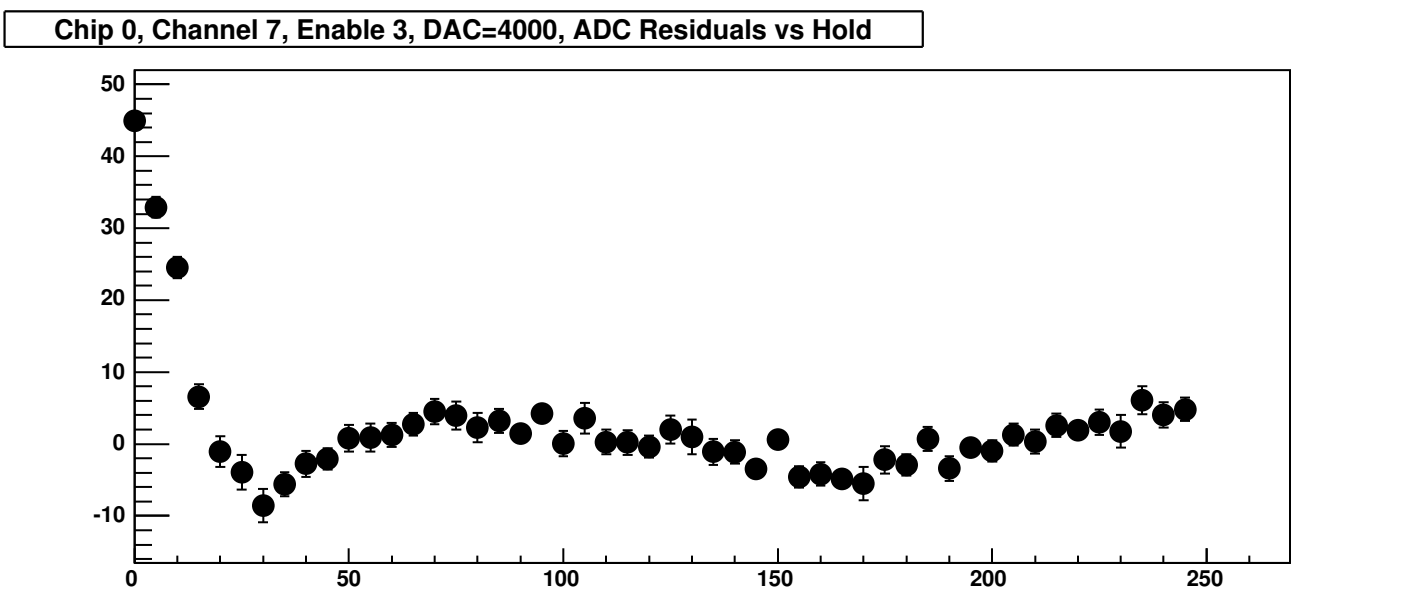

Chip 0, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 384.6 / 41
p0 124.5 ± 3.026
p1 -7.13 ± 0.5472
p2 338.6 ± 3.061
p3 21.52 ± 0.4578
p4 -0.1808 ± 0.006096
460
440
420
400
380
360
340
320
0
50
100
150
200
250

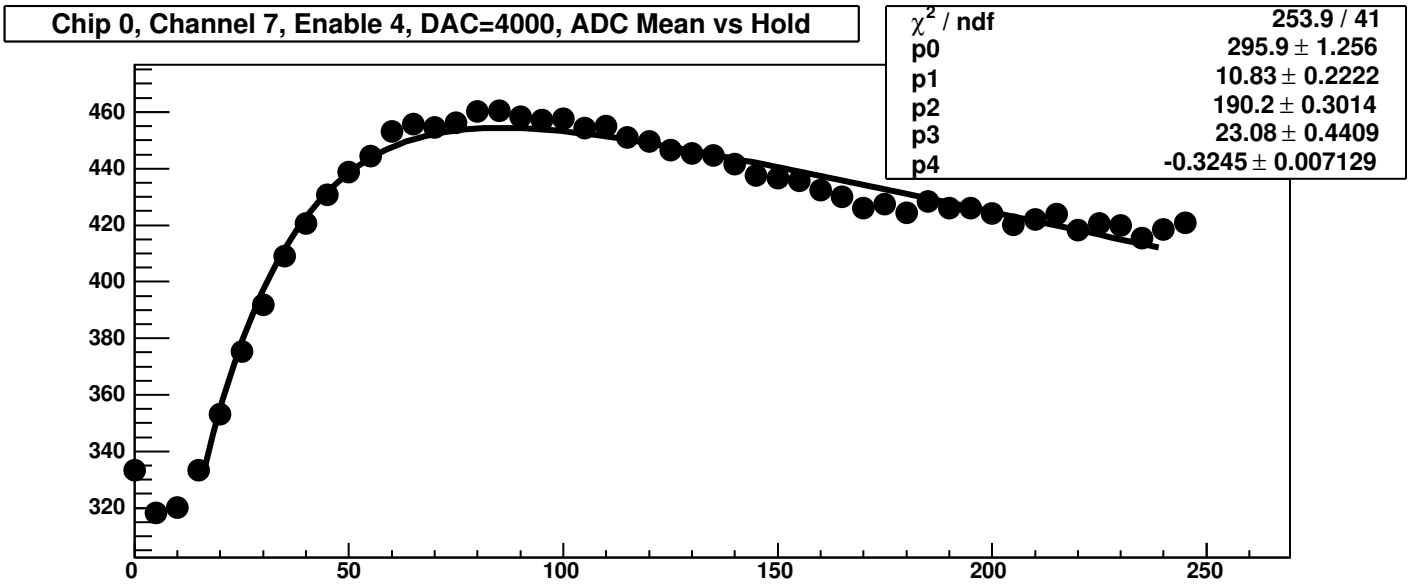

Chip 0, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

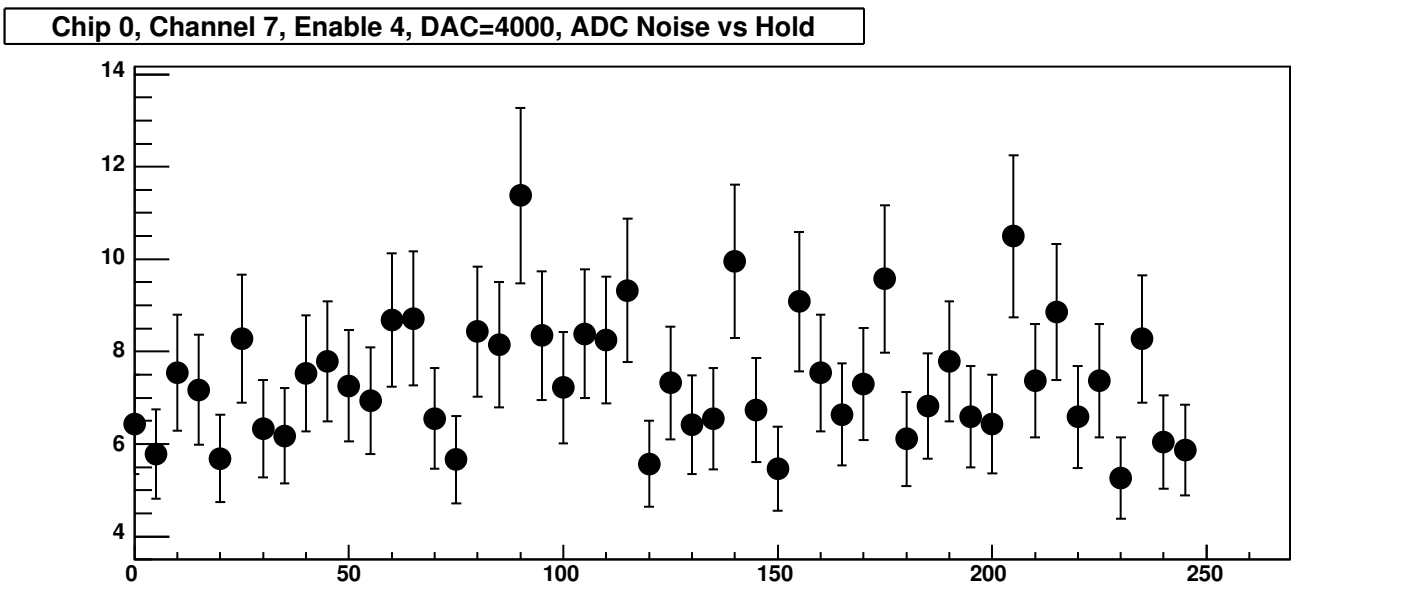

Chip 0, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold
120
100
80
60
40
20
0
-20
0
50
100
150
200
250

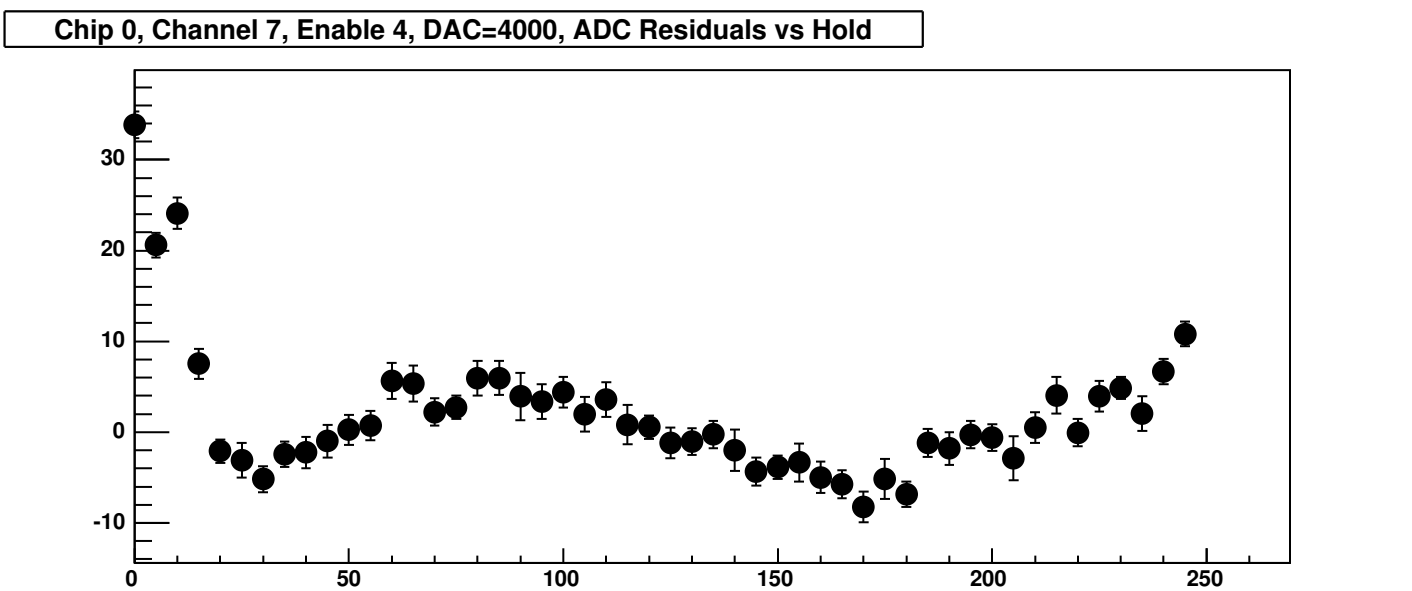

Chip 0, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 168.3 / 41
p0 284.6 ± 1.731
p1 9.429 ± 0.3318
p2 178.6 ± 0.3889
p3 27.64 ± 0.6459
p4 -0.2088 ± 0.008656
440
420
400
380
360
340
320
300
0
50
100
150
200
250

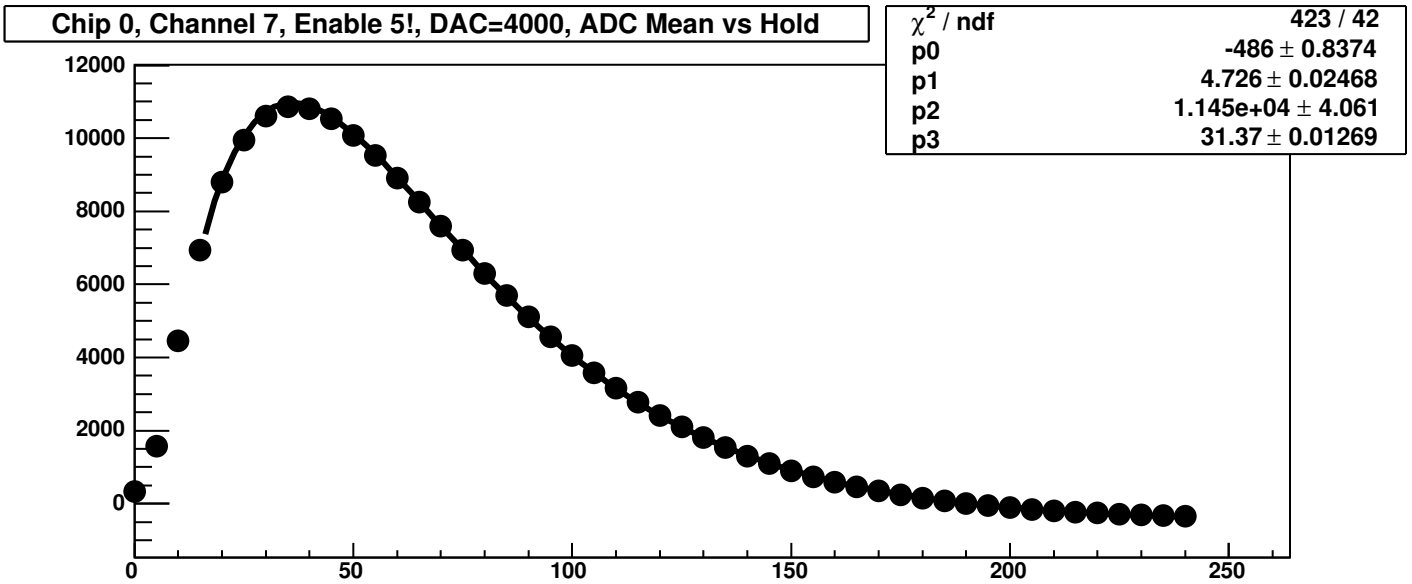

Chip 0, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

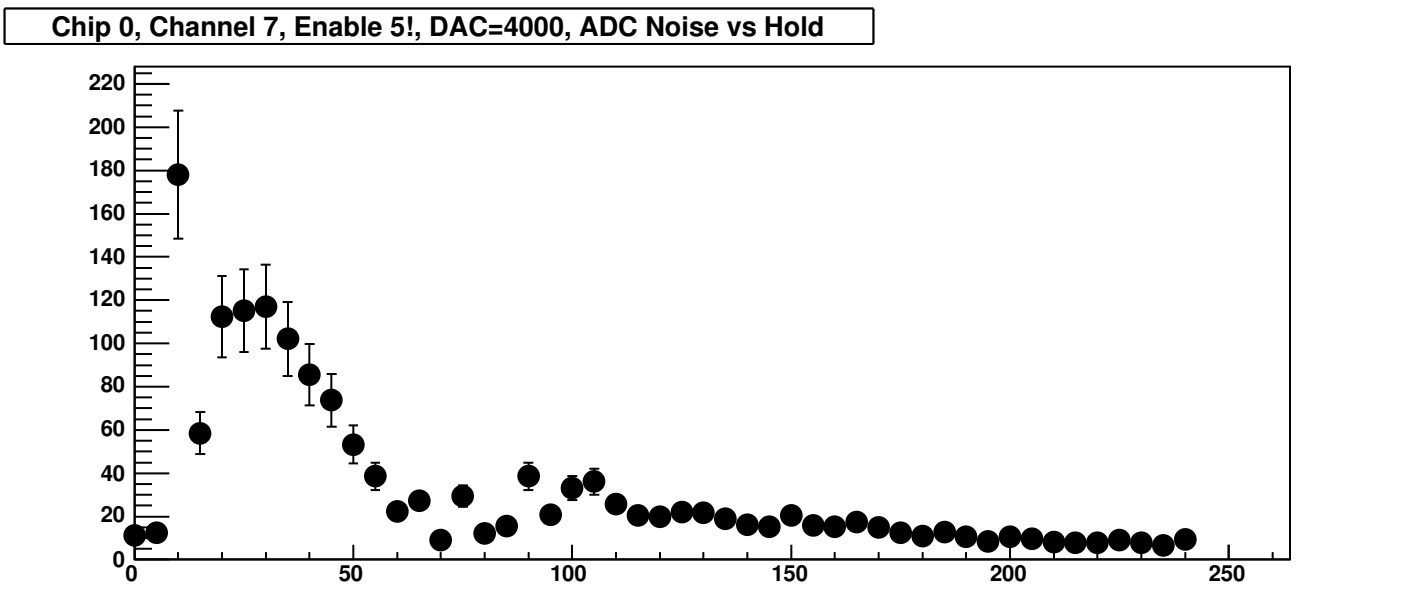

Chip 0, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250

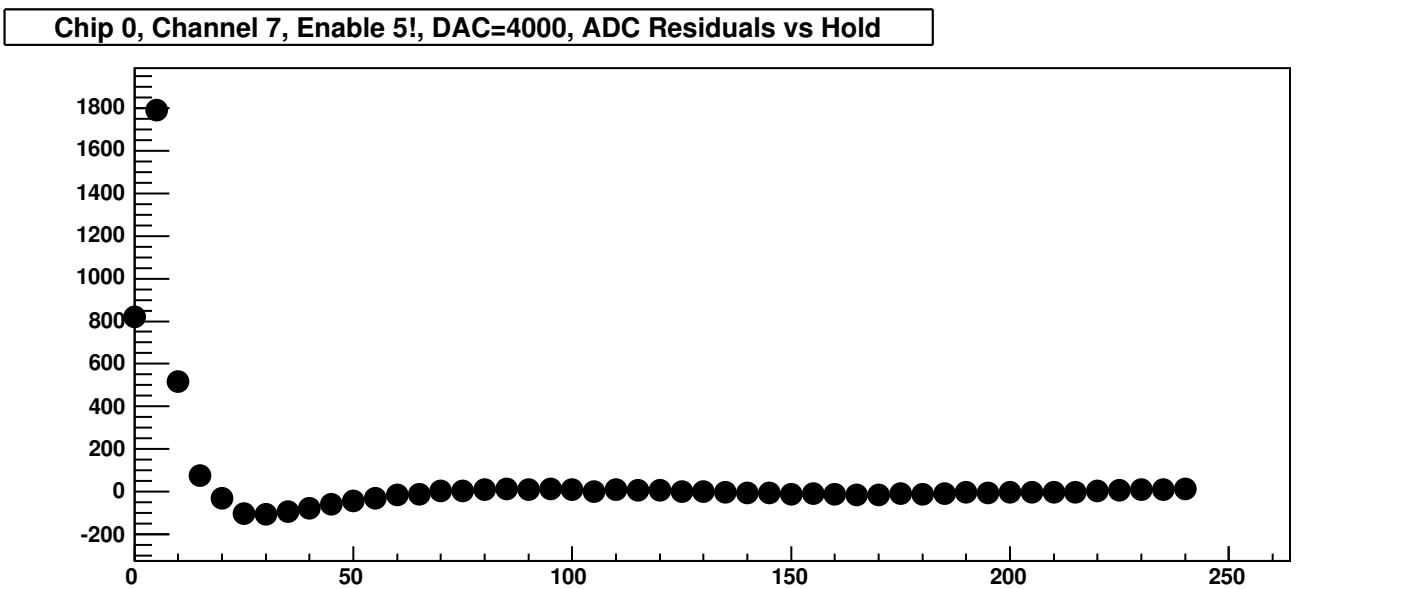

Chip 0, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 253.9 / 41
p0 295.9 ± 1.256
p1 10.83 ± 0.2222
p2 190.2 ± 0.3014
p3 23.08 ± 0.4409
p4 -0.3245 ± 0.007129
460
440
420
400
380
360
340
320
0
50
100
150
200
250


Chip 0, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

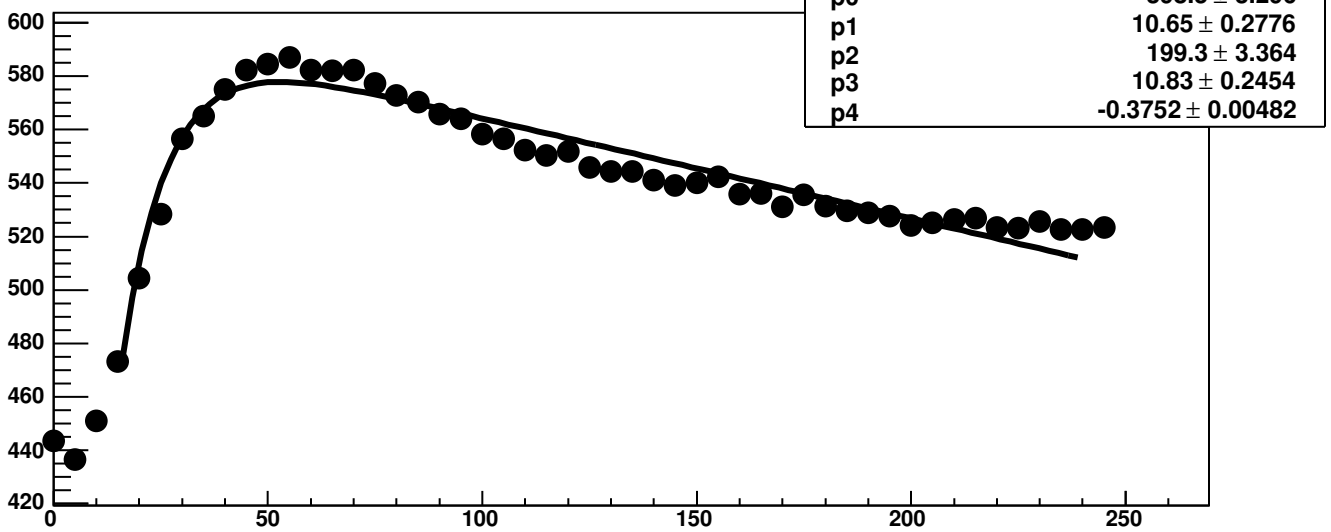

Chip 0, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
0
50
100
150
200
250


Chip 0, Channel 7, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 423 / 42
p0 -486 ± 0.8374
p1 4.726 ± 0.02468
p2 1.145e+04 ± 4.061
p3 31.37 ± 0.01269
12000
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

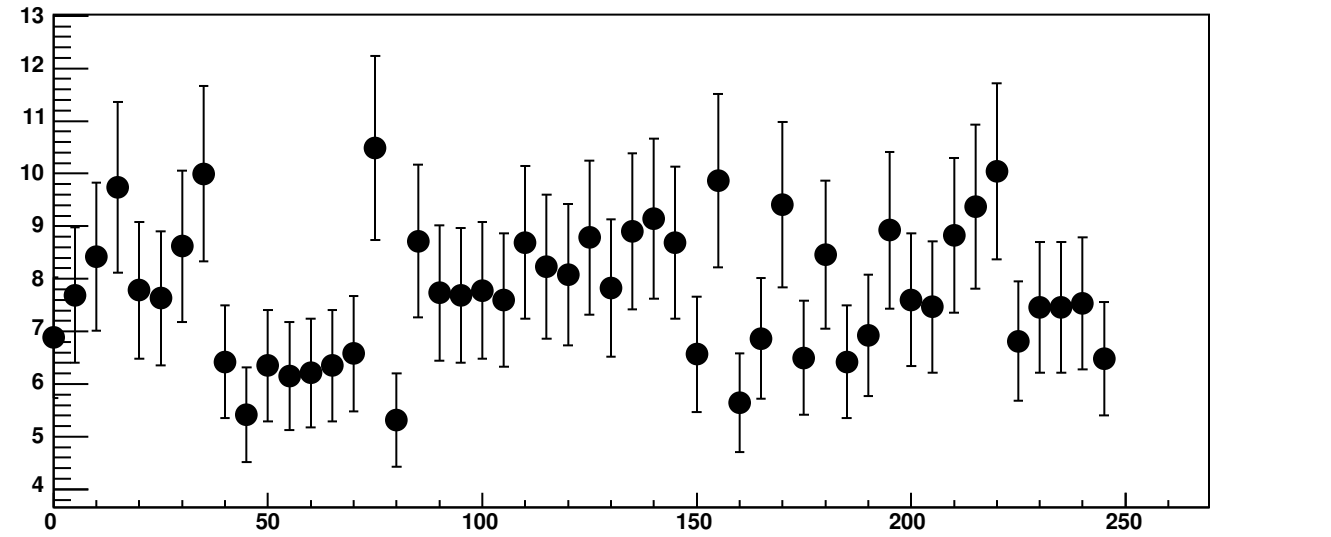

Chip 0, Channel 7, Enable 5!, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 7, Enable 5!, DAC=4000, ADC Residuals vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

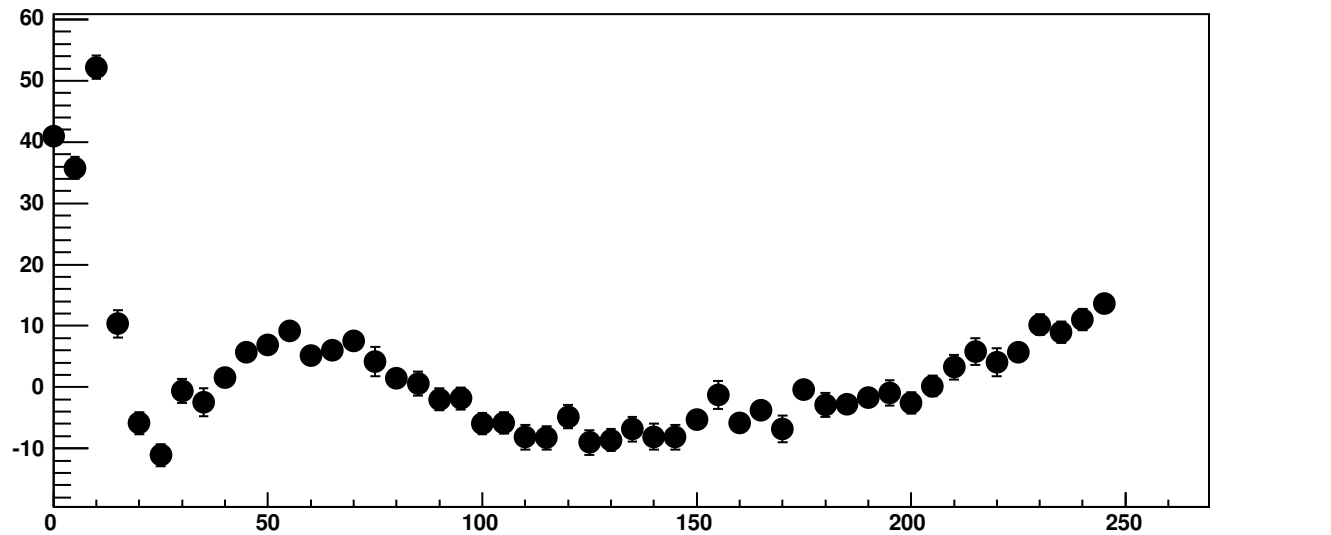
Chip 0, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold



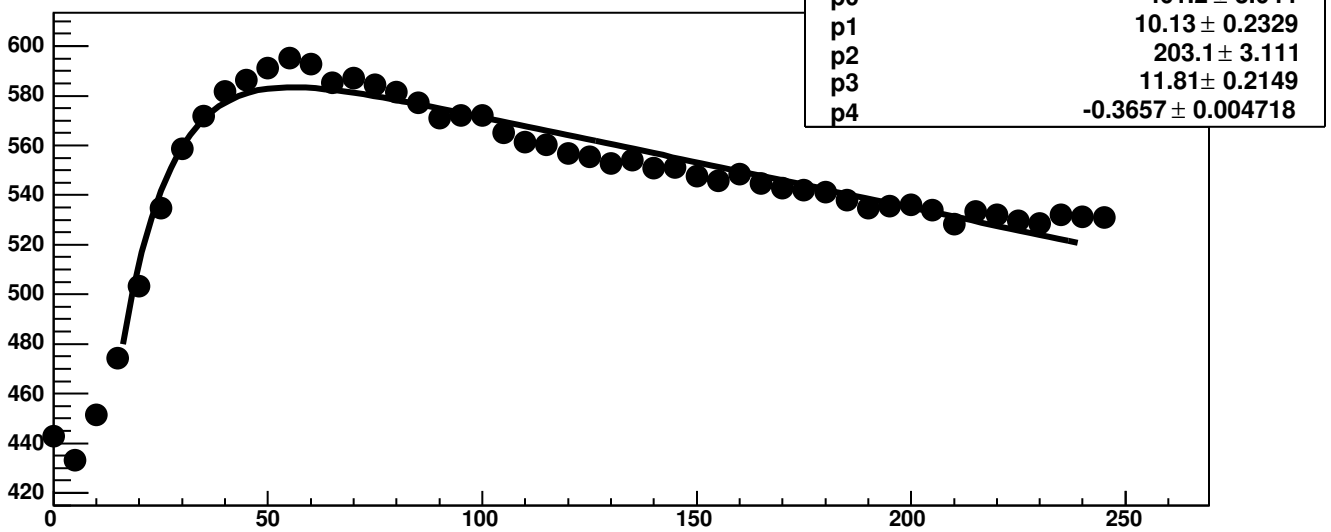
Chip 0, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



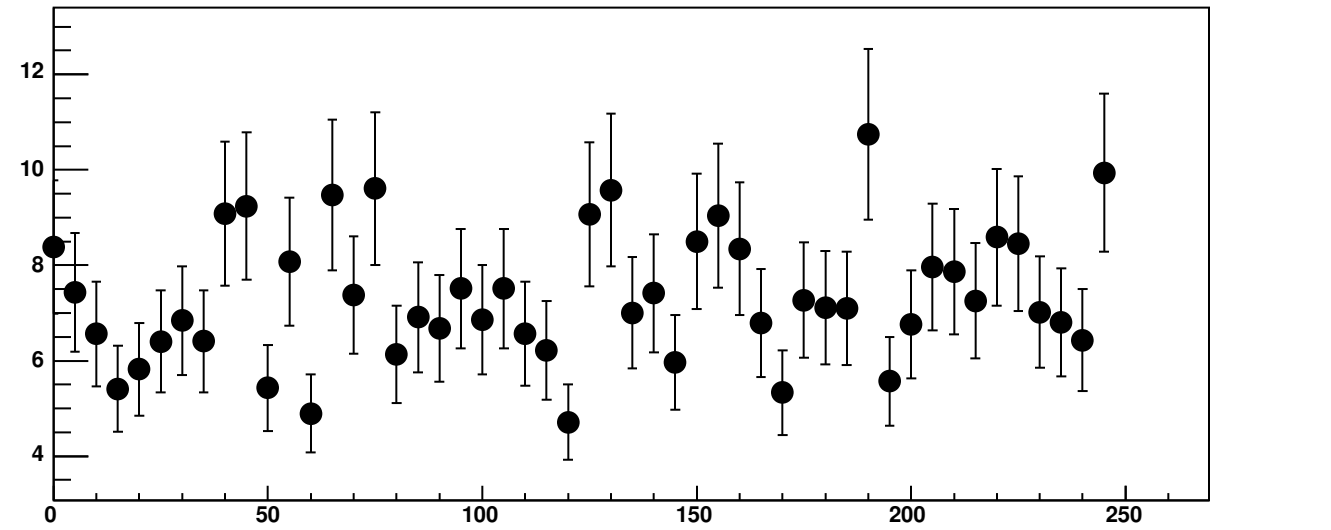
Chip 0, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold



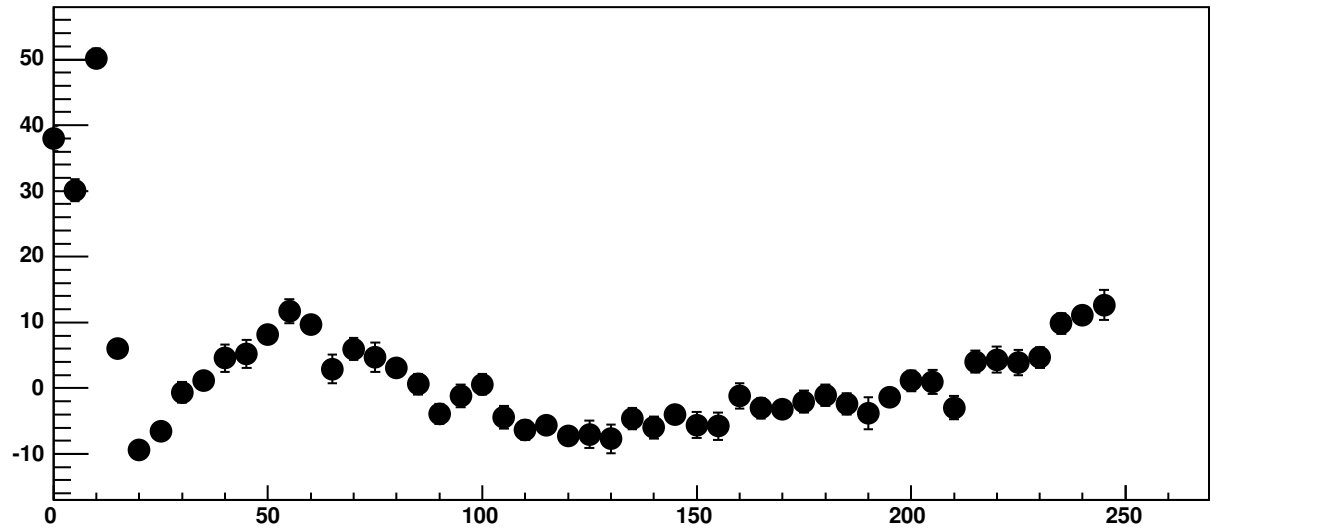
Chip 0, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

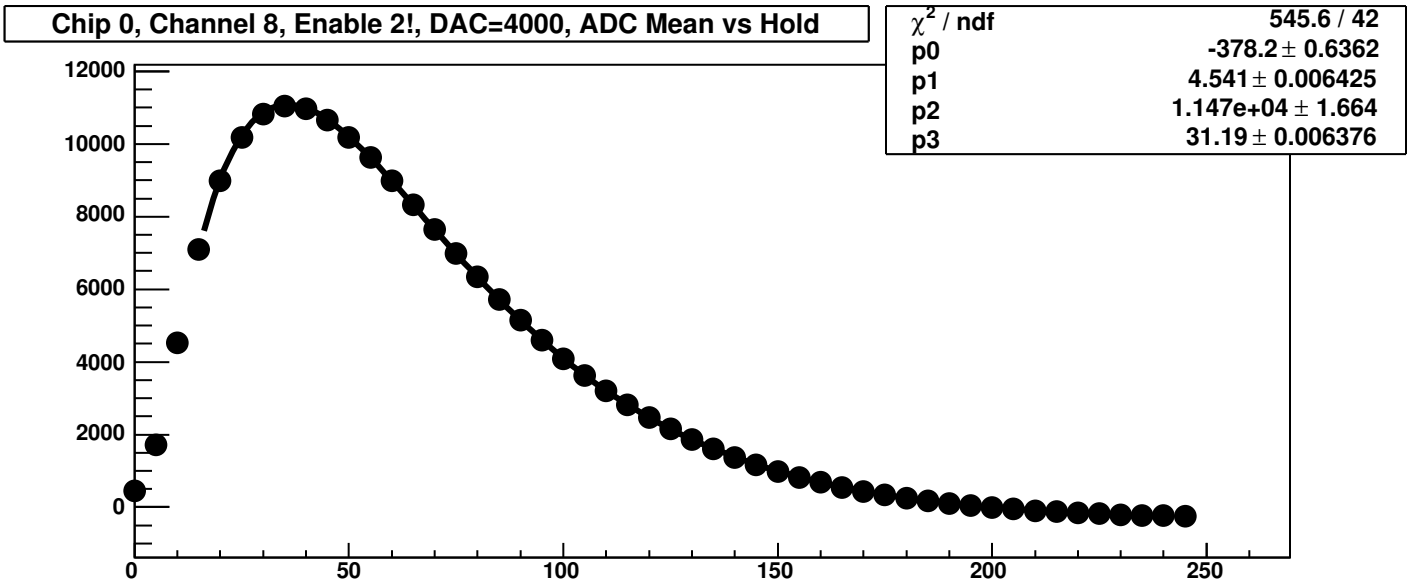


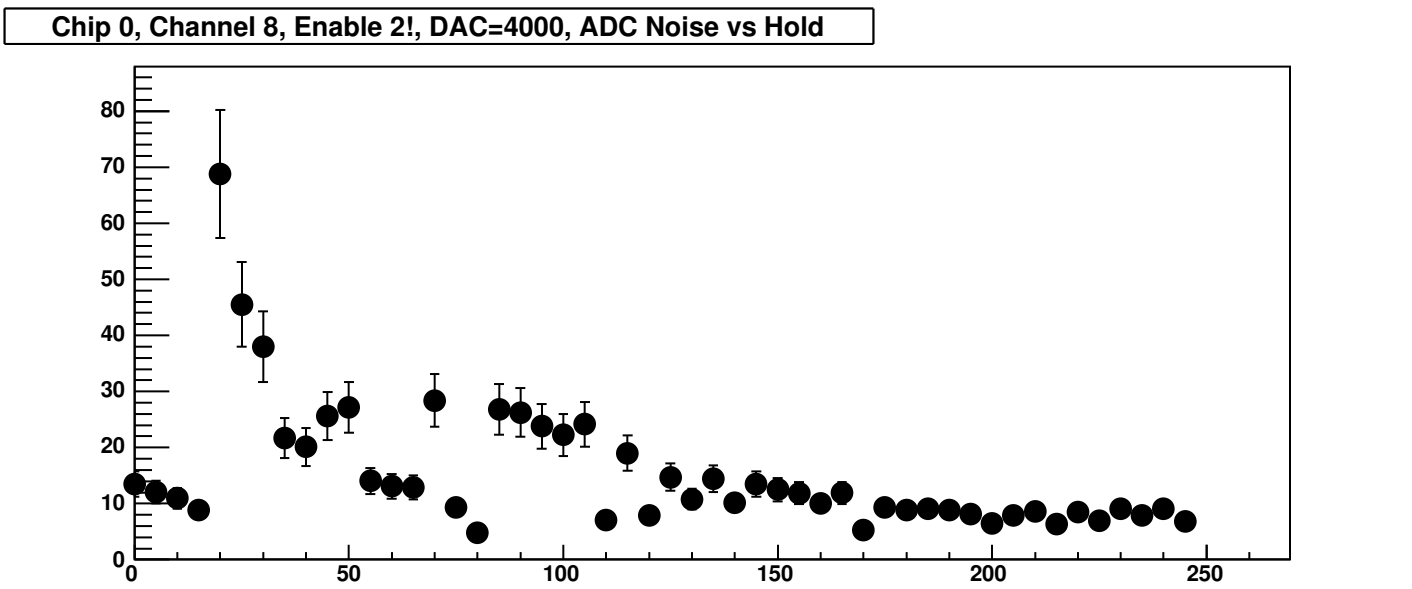
Chip 0, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold

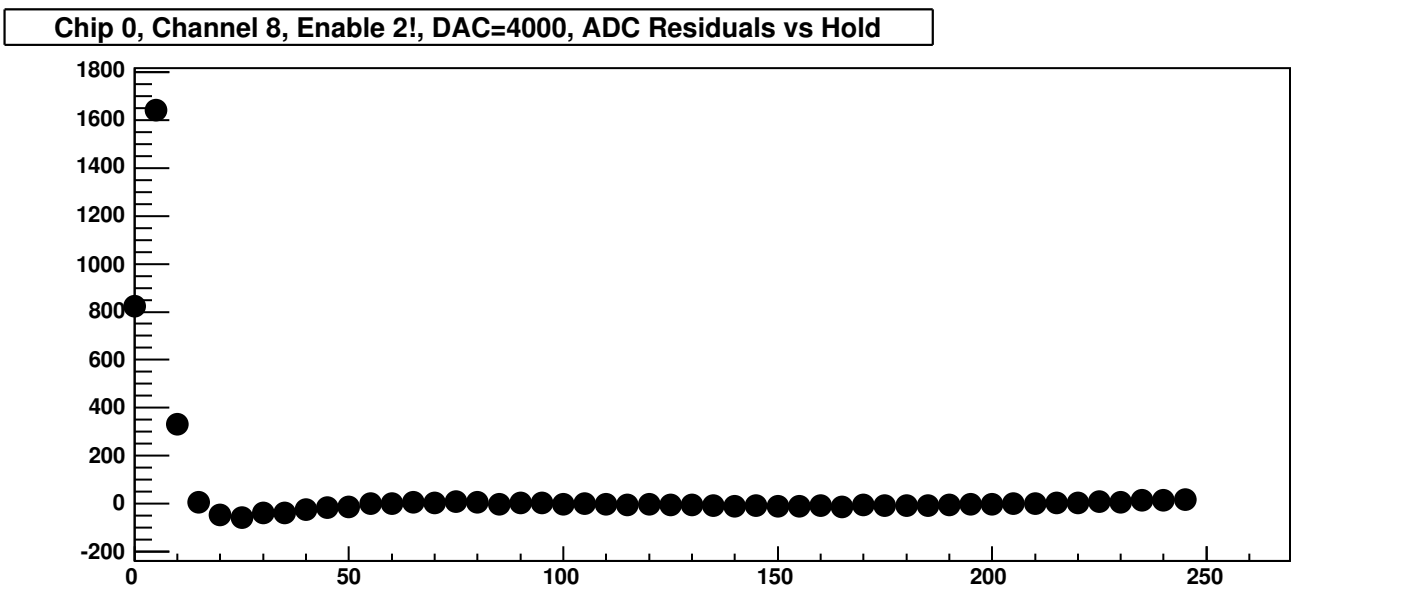


Chip 0, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

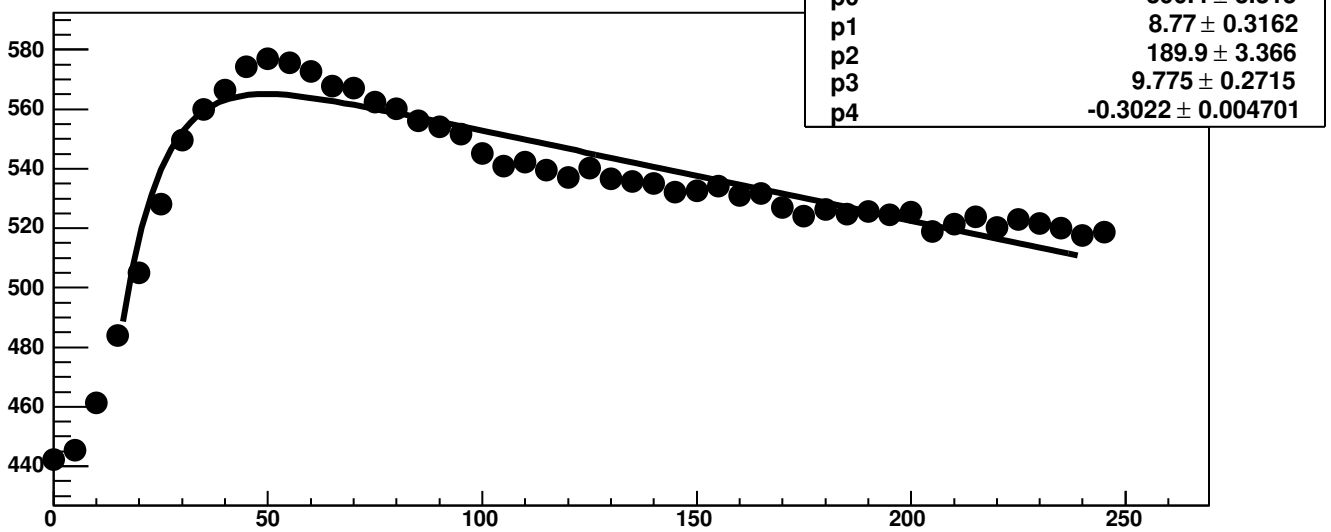



Chip 0, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 545.6 / 42
p0 -378.2 ± 0.6362
p1 4.541 ± 0.006425
p2 1.147e+04 ± 1.664
p3 31.19 ± 0.006376
12000
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

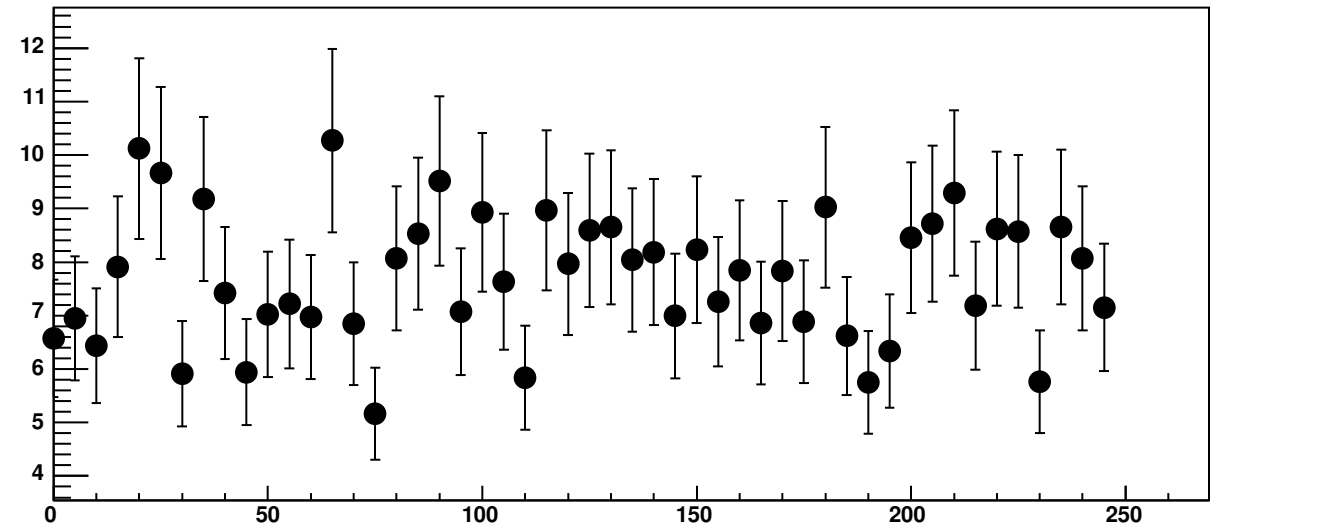

Chip 0, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250


Chip 0, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

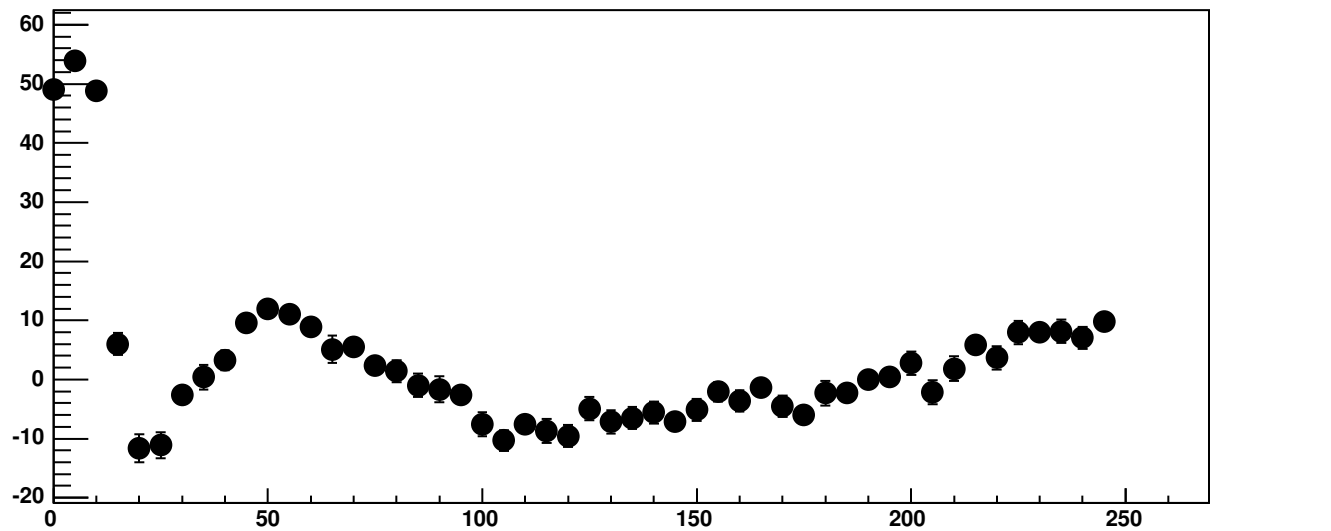
Chip 0, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold



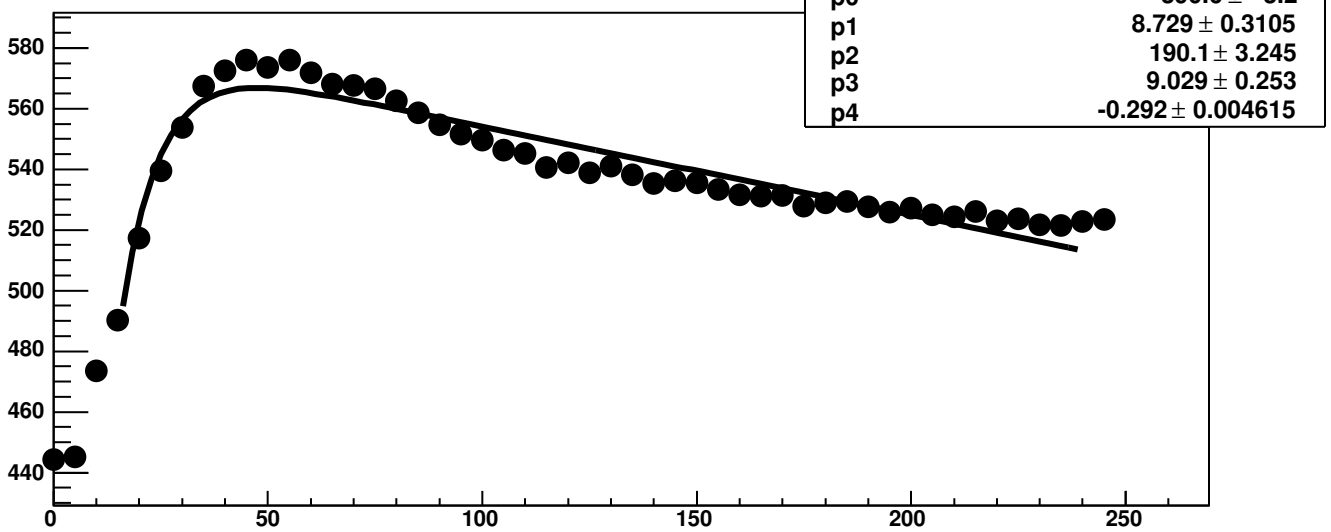
Chip 0, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



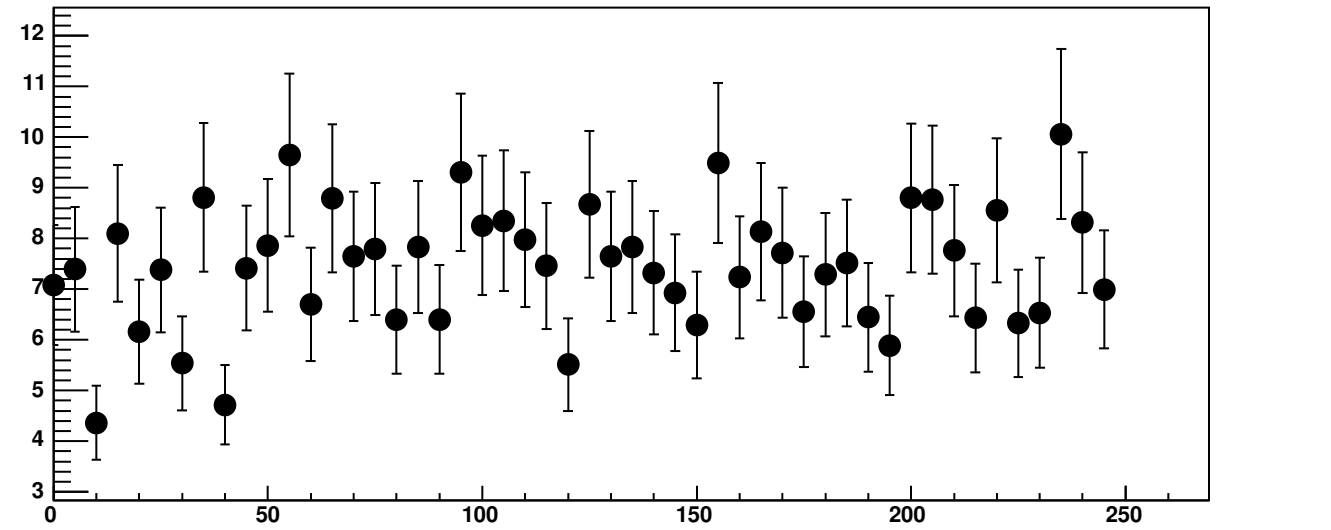
Chip 0, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold



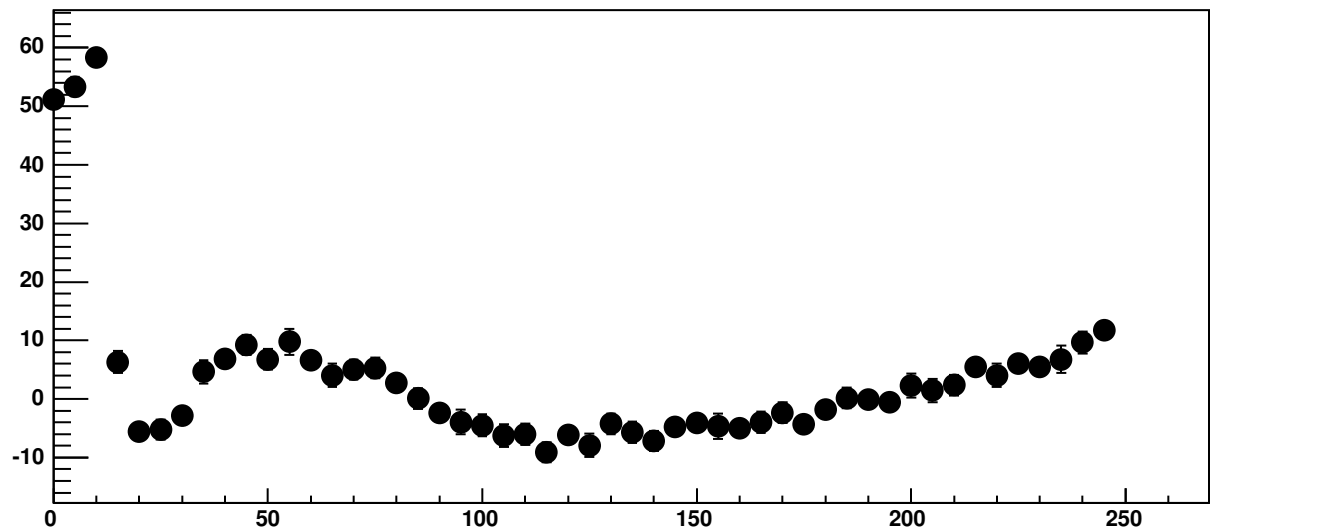
Chip 0, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold



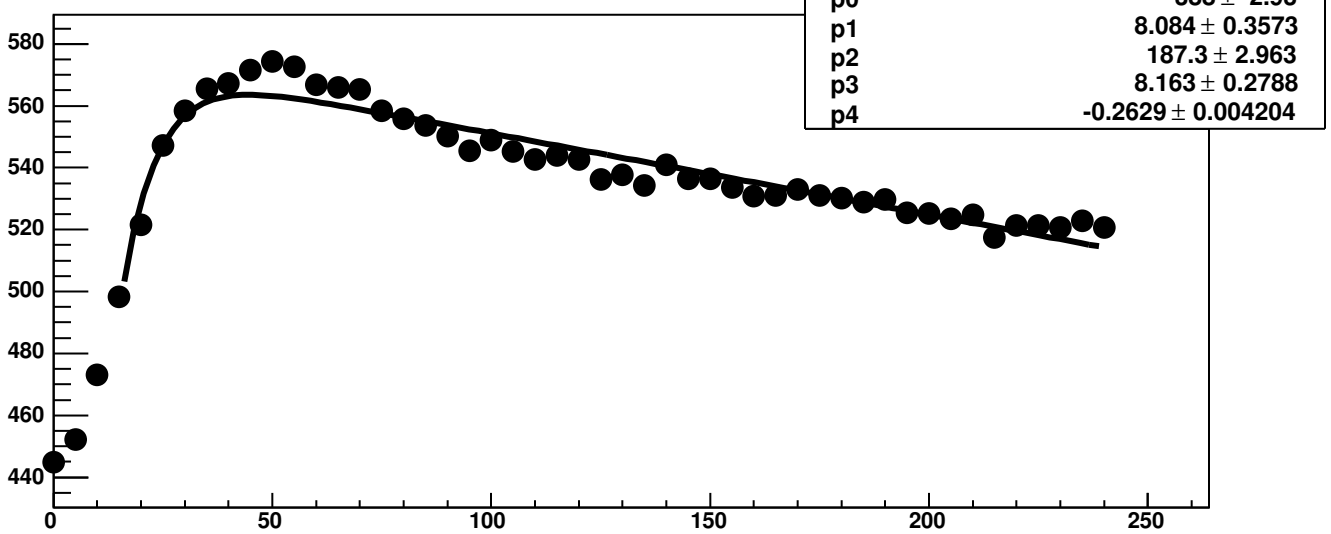
Chip 0, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



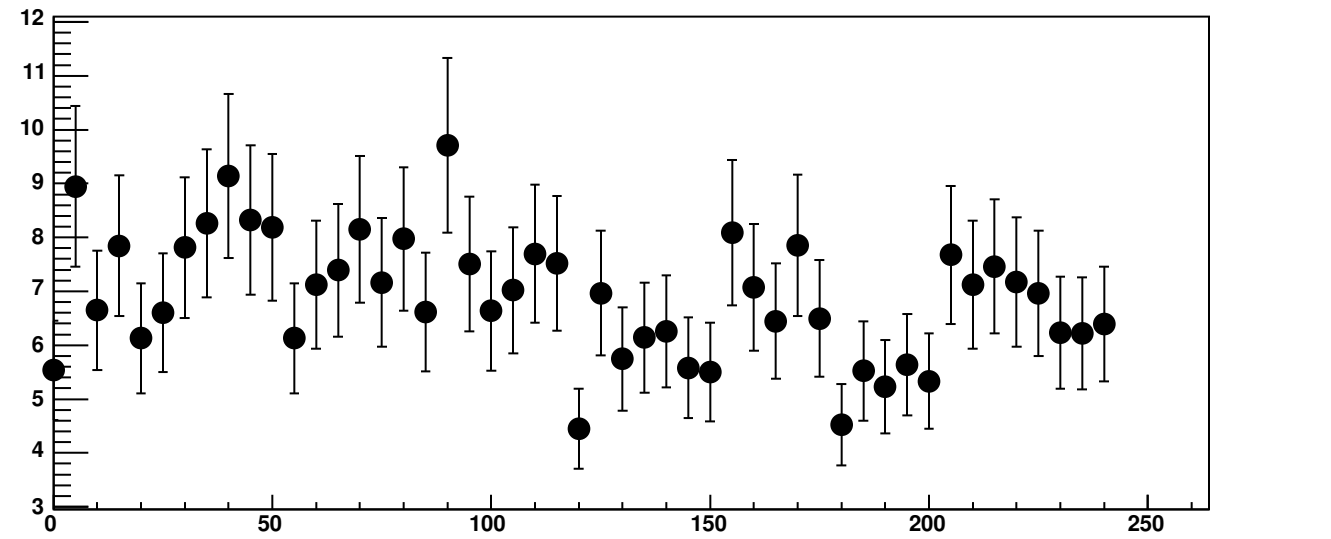
Chip 0, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold



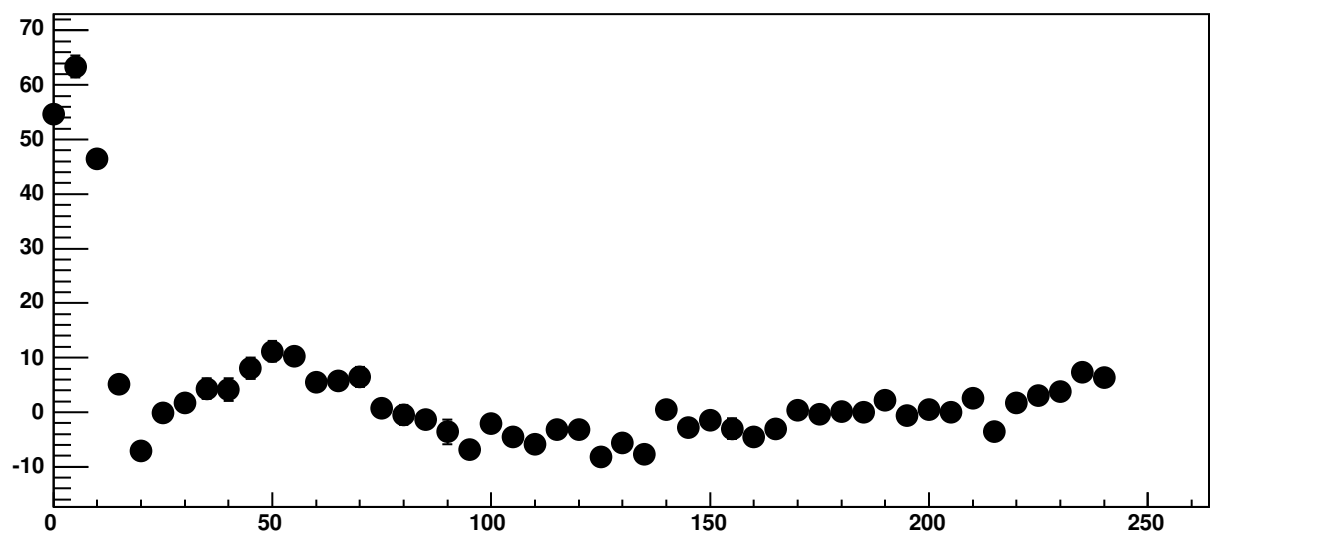
Chip 0, Channel 8, Enable 5, DAC=4000, ADC Mean vs Hold

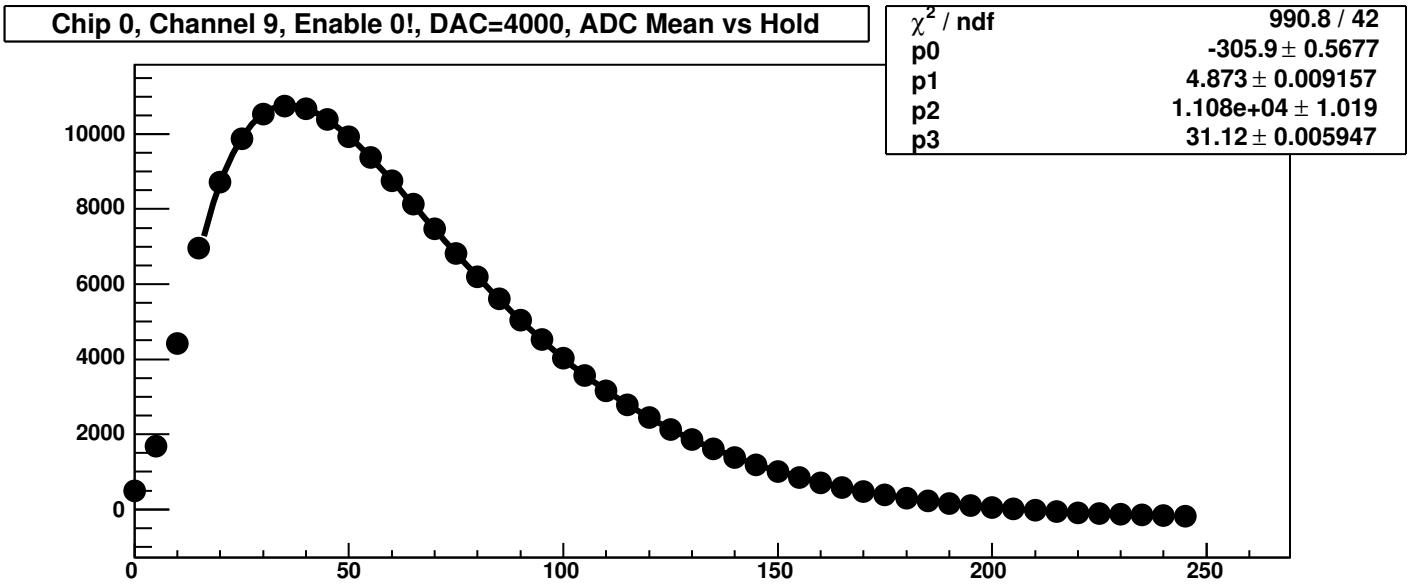


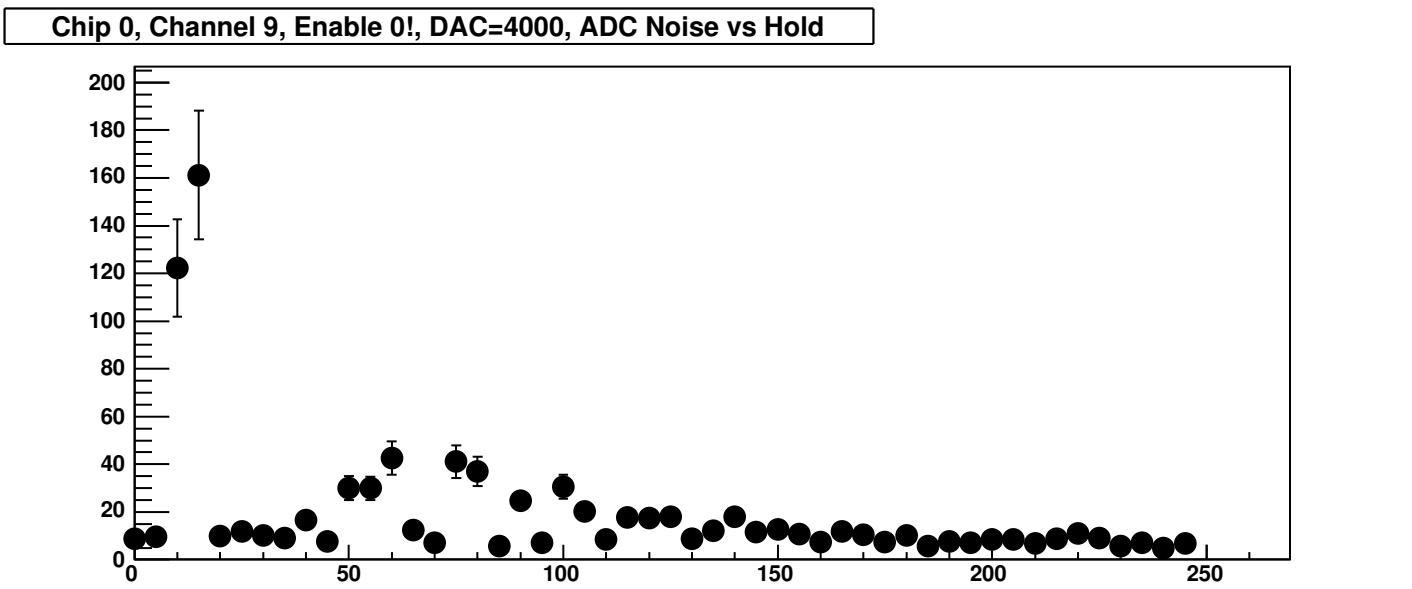
Chip 0, Channel 8, Enable 5, DAC=4000, ADC Noise vs Hold

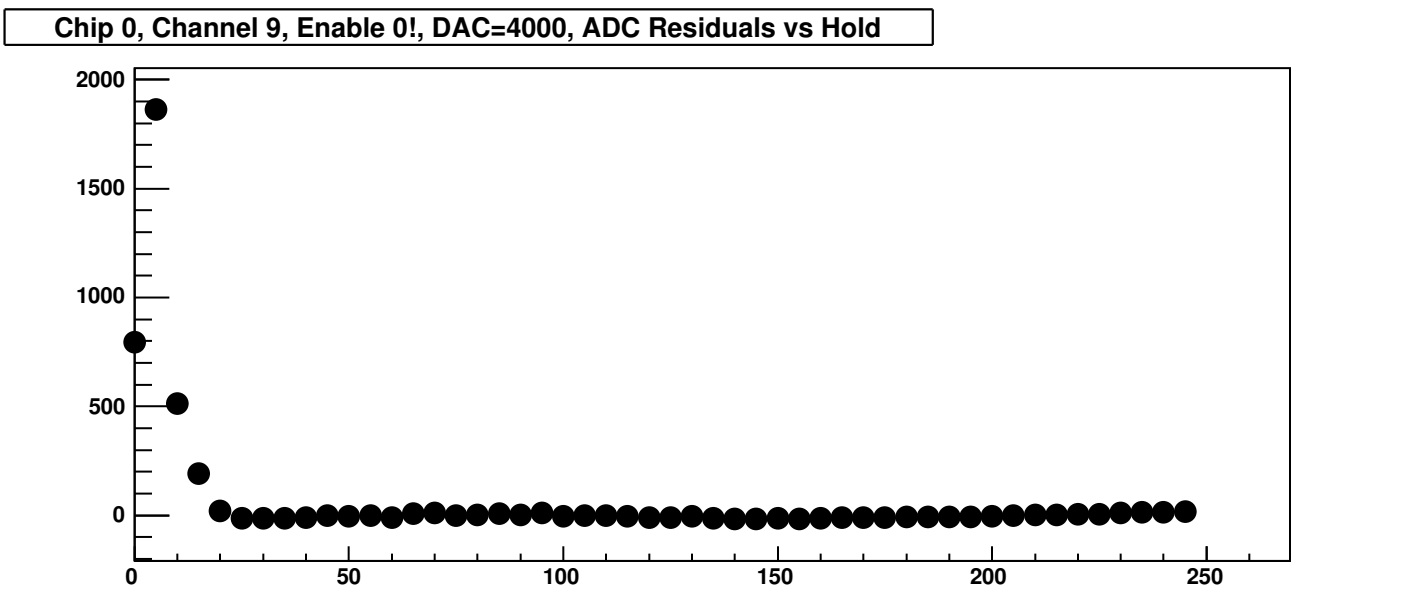


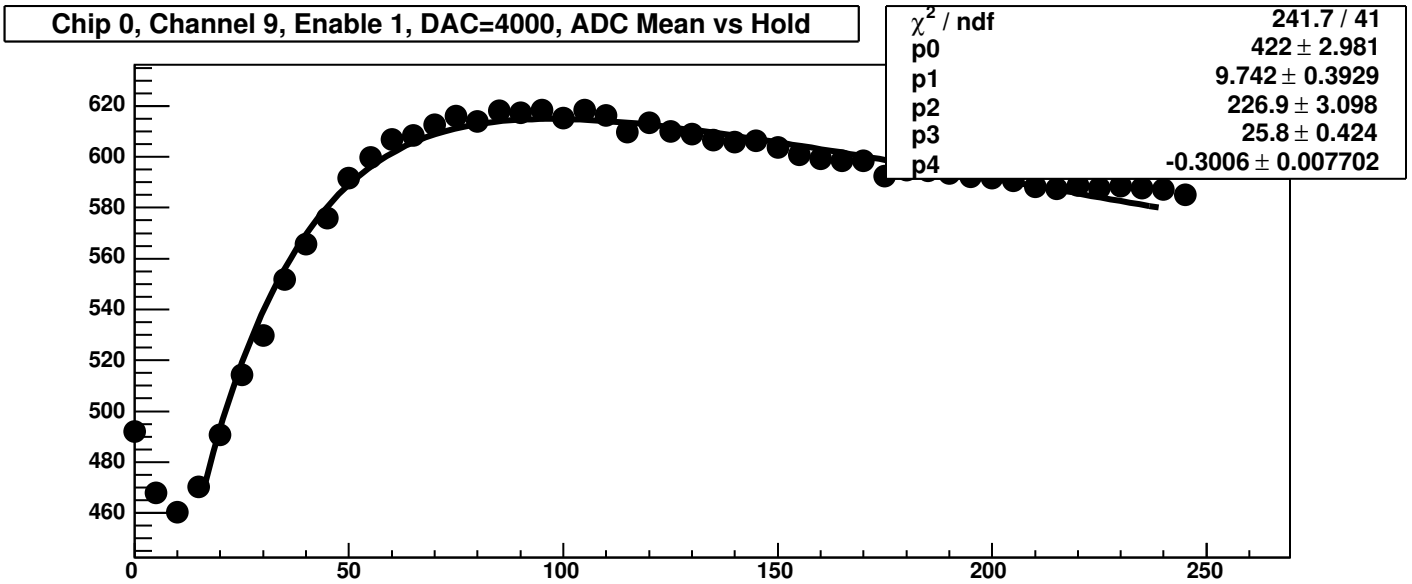
Chip 0, Channel 8, Enable 5, DAC=4000, ADC Residuals vs Hold

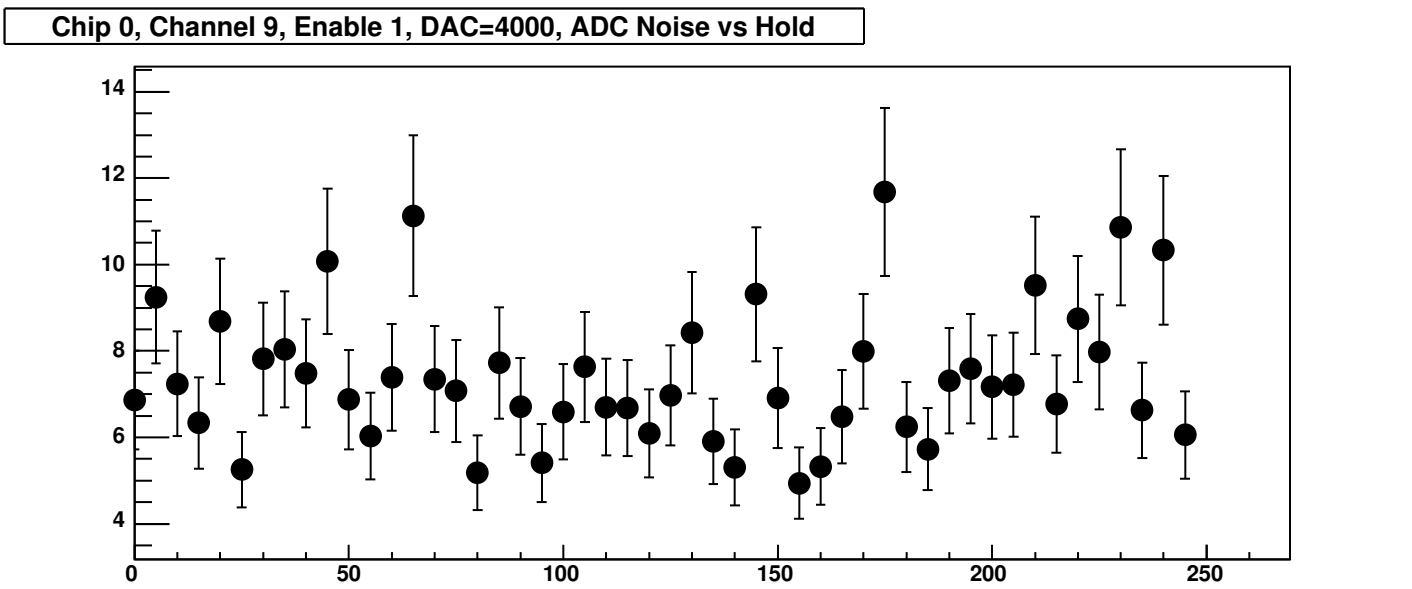


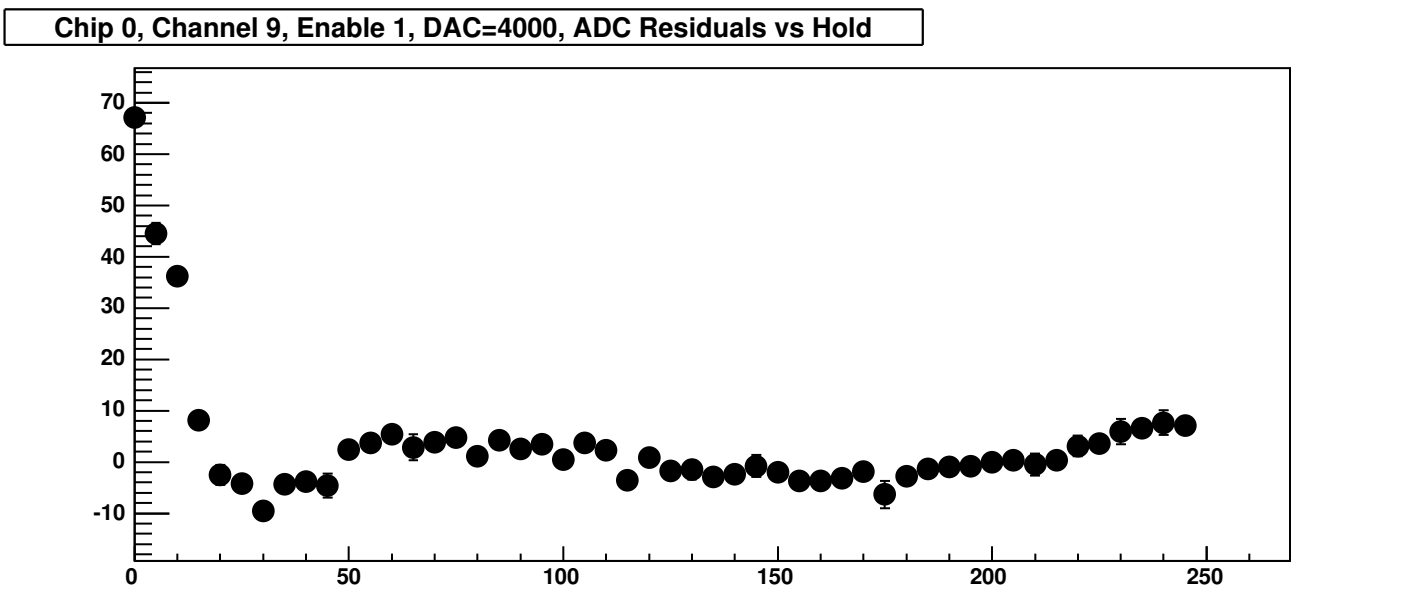

Chip 0, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 990.8 / 42
p0 -305.9 ± 0.5677
p1 4.873 ± 0.009157
p2 1.108e+04 ± 1.019
p3 31.12 ± 0.005947
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 0, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

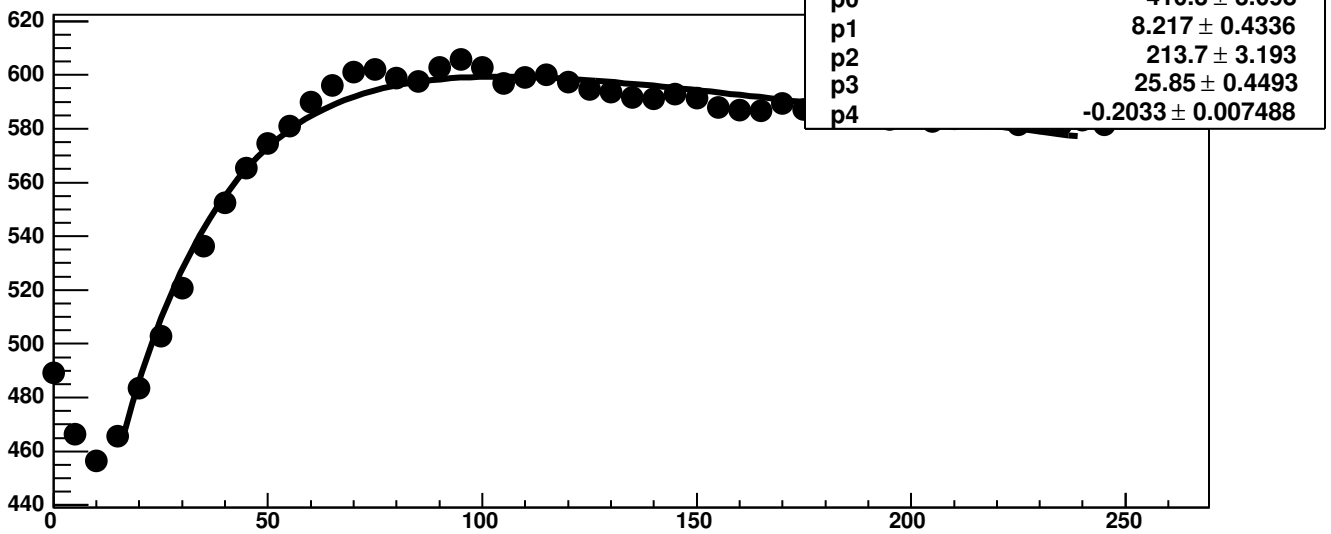

Chip 0, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 0, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 241.7 / 41
p0 422 ± 2.981
p1 9.742 ± 0.3929
p2 226.9 ± 3.098
p3 25.8 ± 0.424
p4 -0.3006 ± 0.007702
620
600
580
560
540
520
500
480
460
0
50
100
150
200
250

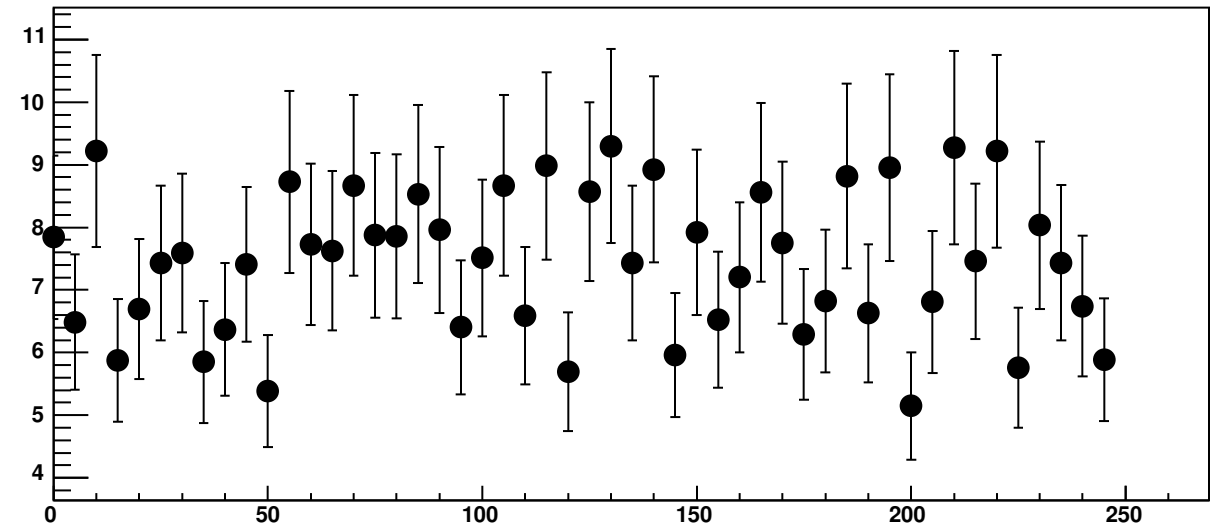

Chip 0, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 0, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

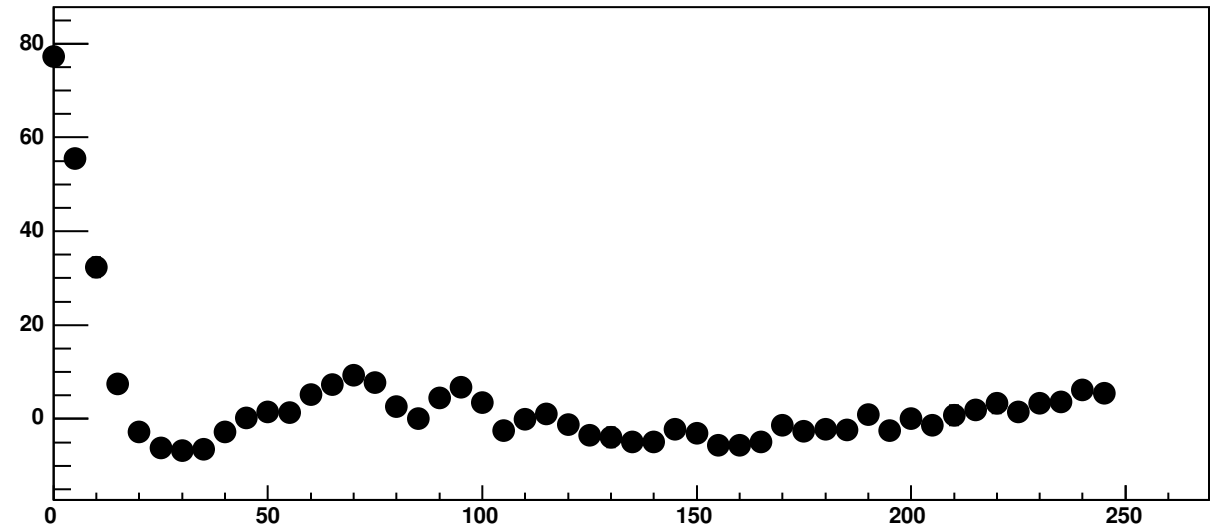
Chip 0, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold

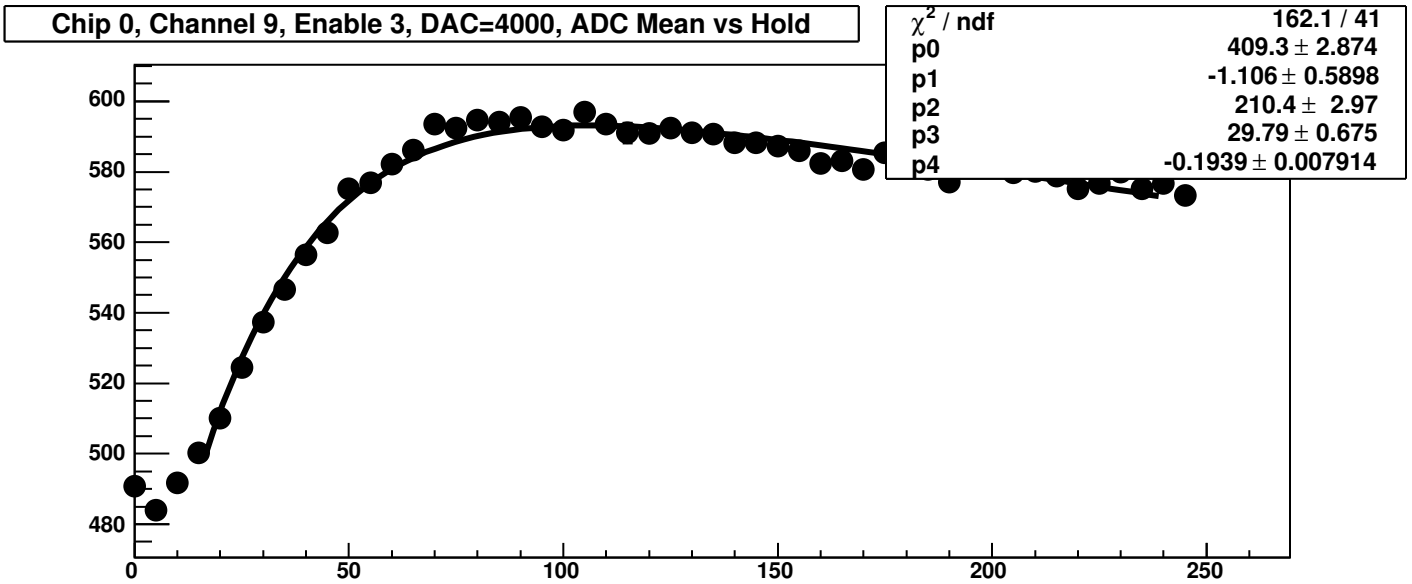


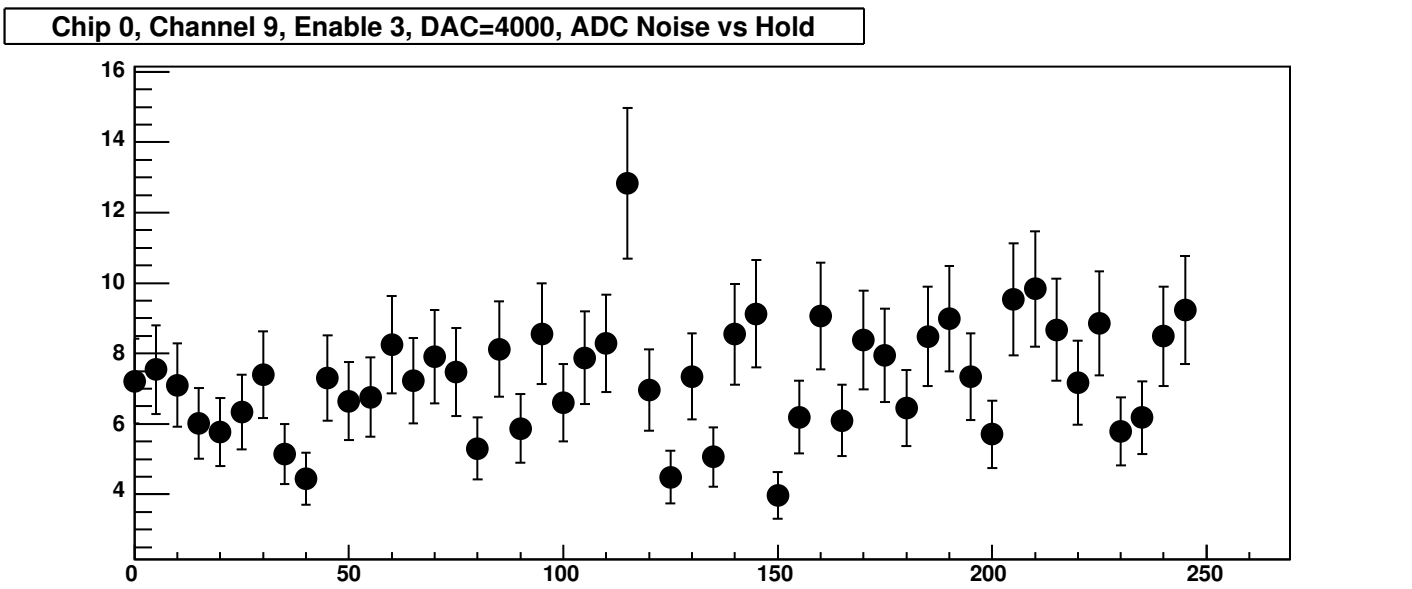
Chip 0, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold

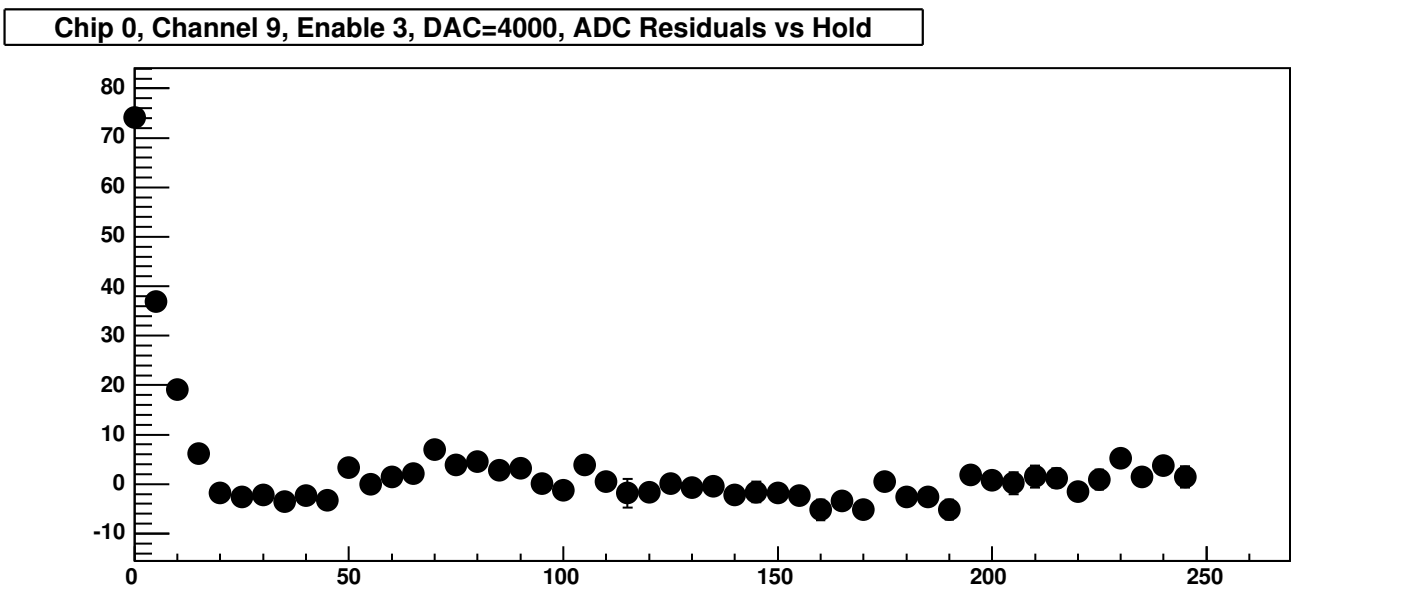


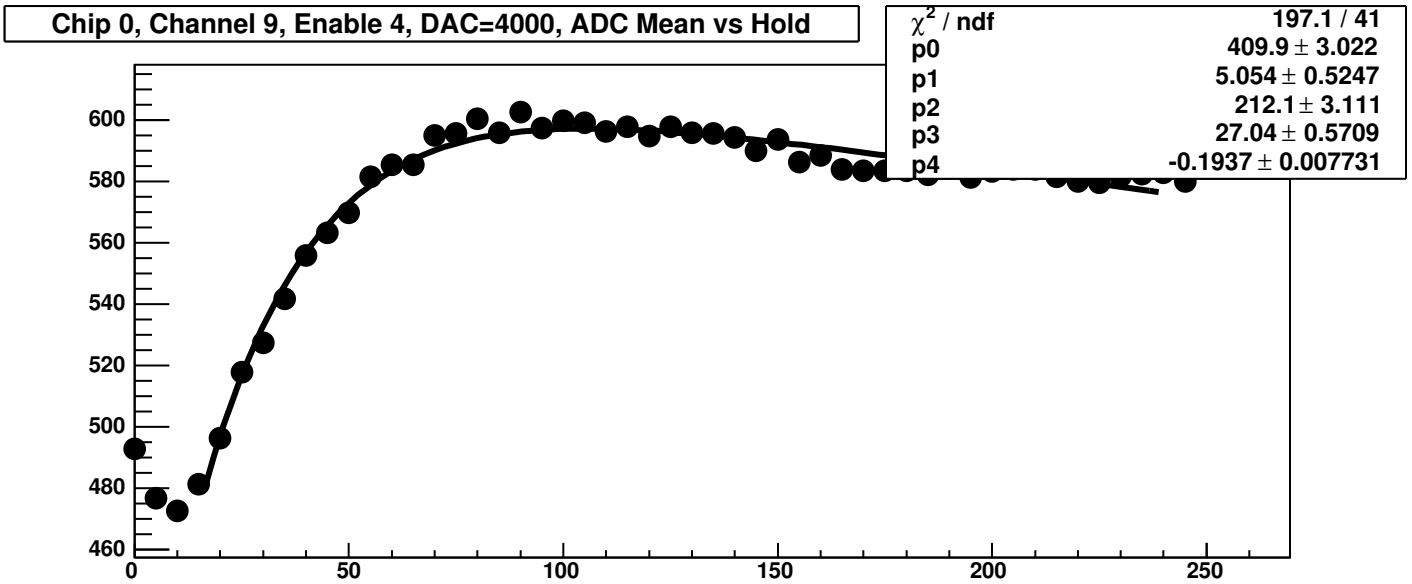
Chip 0, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold

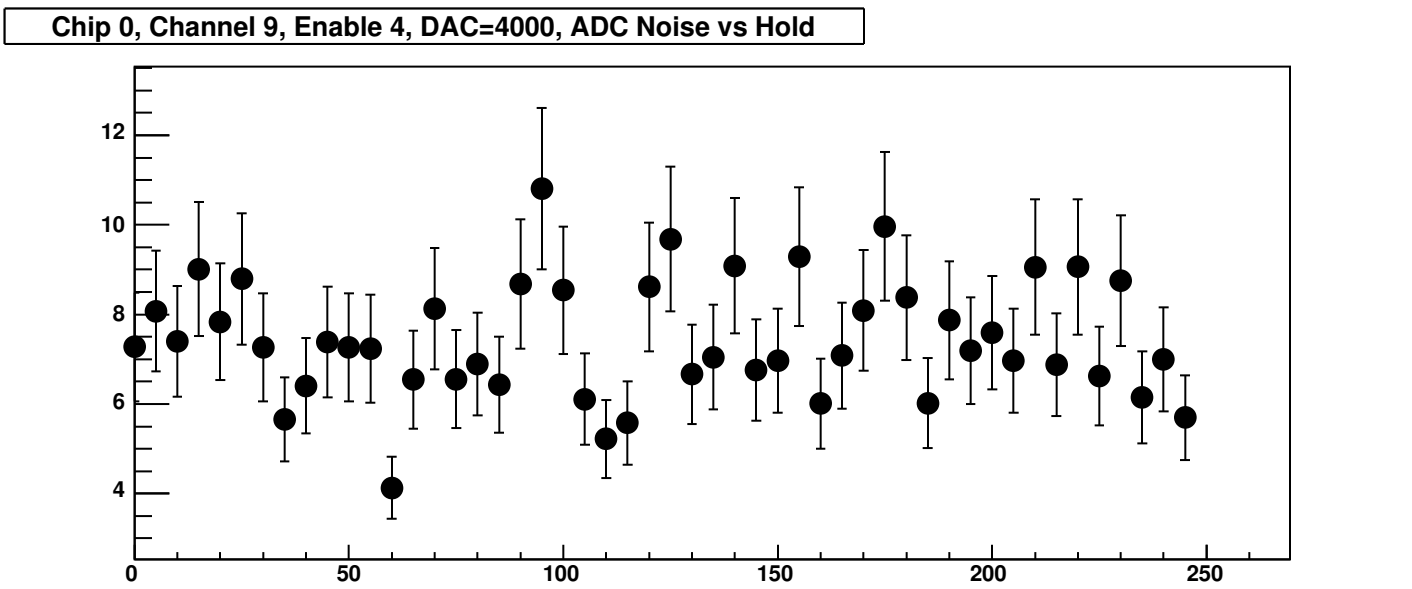


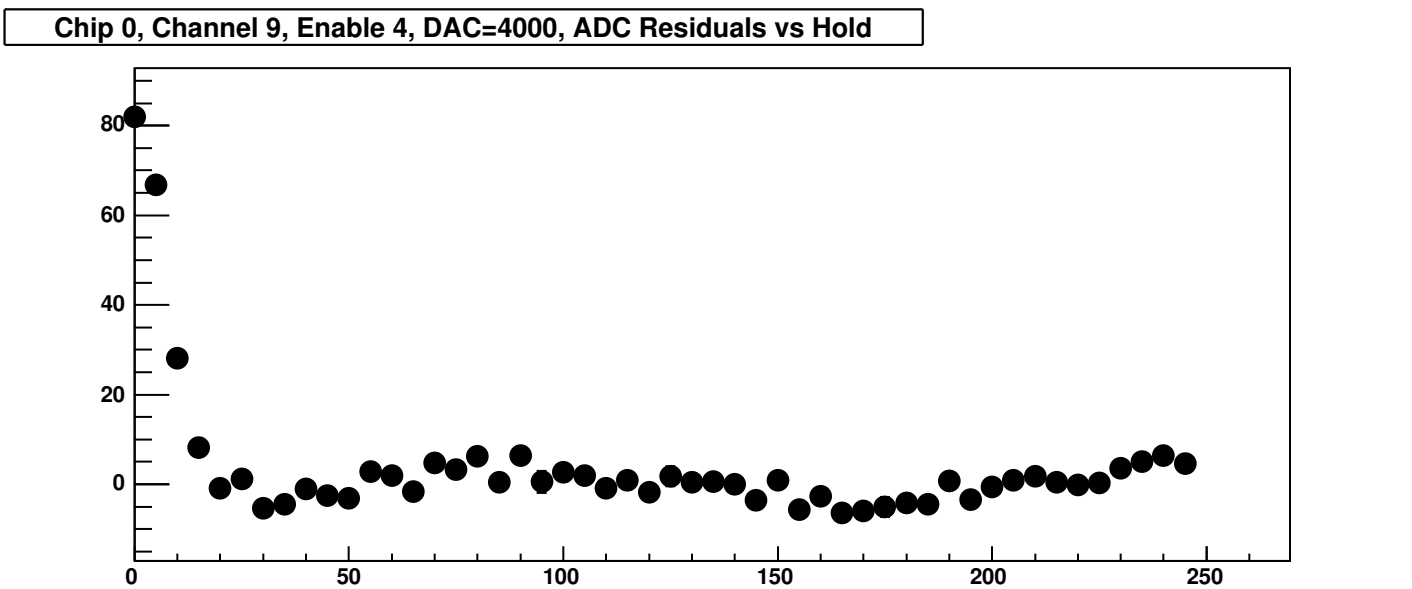

Chip 0, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 162.1 / 41
p0 409.3 ± 2.874
p1 -1.106 ± 0.5898
p2 210.4 ± 2.97
p3 29.79 ± 0.675
p4 -0.1939 ± 0.007914
600
580
560
540
520
500
480
0
50
100
150
200
250


Chip 0, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold
16
14
12
10
8
6
4
0
50
100
150
200
250

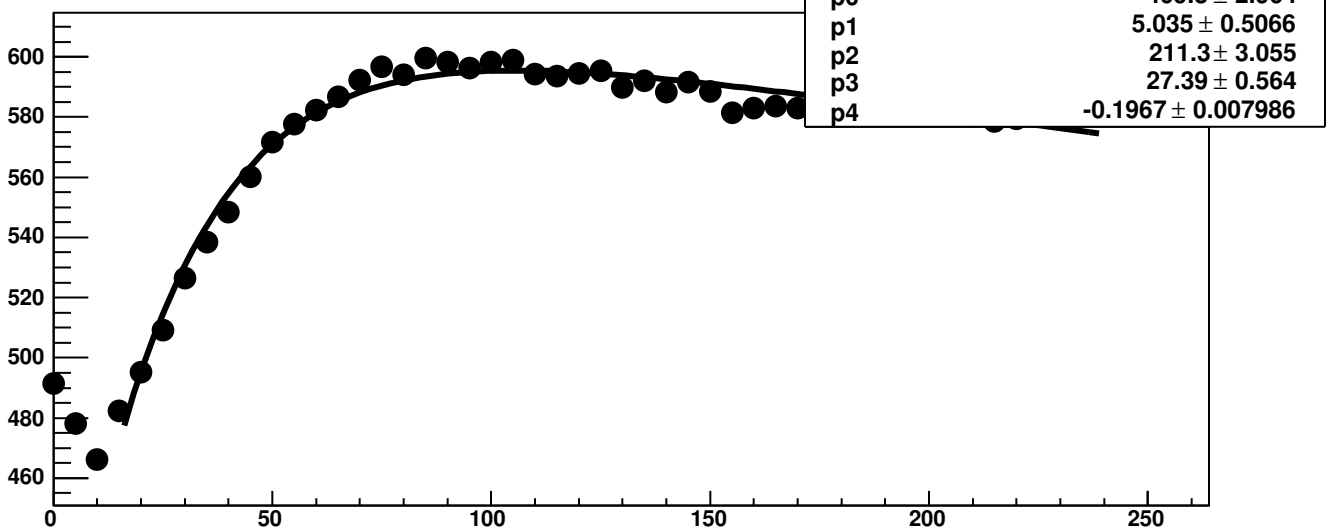

Chip 0, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold
80
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250


Chip 0, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 197.1 / 41
p0 409.9 ± 3.022
p1 5.054 ± 0.5247
p2 212.1 ± 3.111
p3 27.04 ± 0.5709
p4 -0.1937 ± 0.007731
600
580
560
540
520
500
480
460
0
50
100
150
200
250

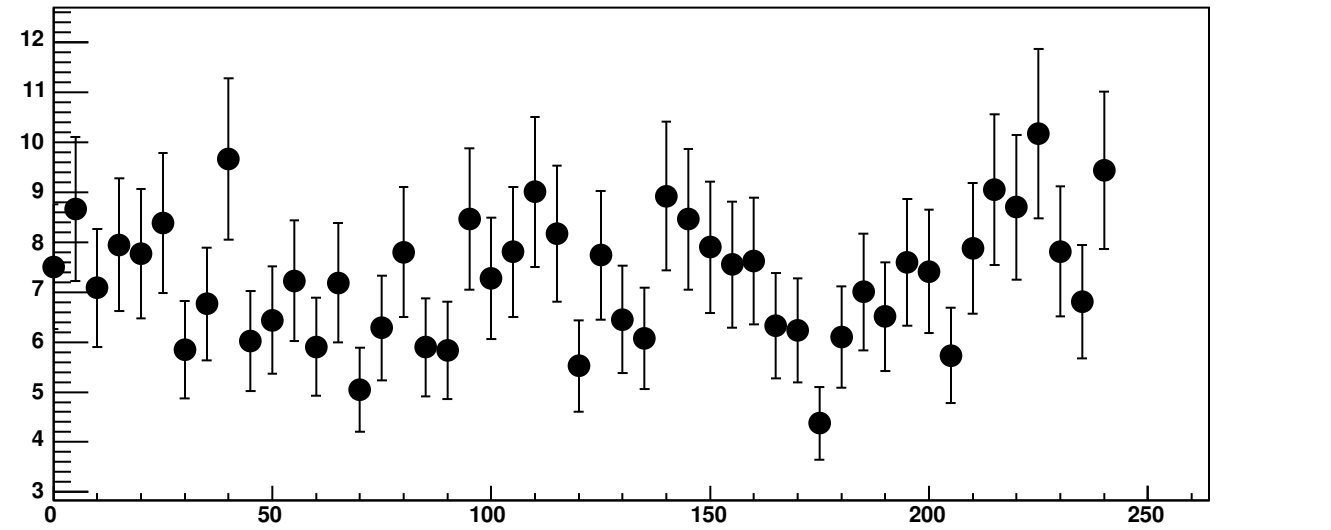

Chip 0, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 0, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold
80
60
40
20
0
0
50
100
150
200
250

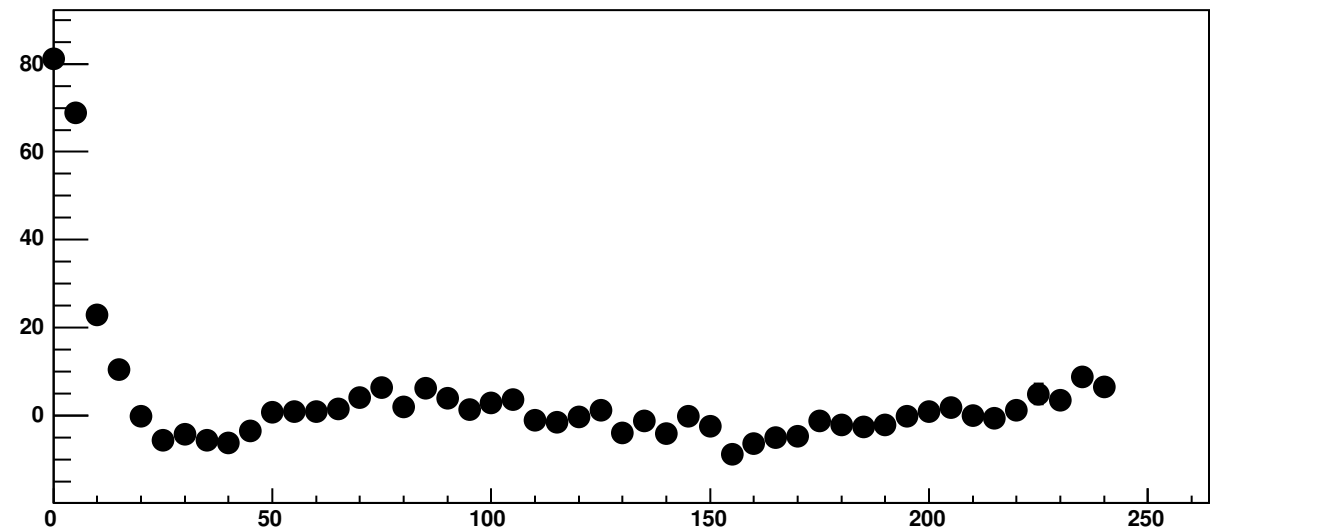
Chip 0, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold



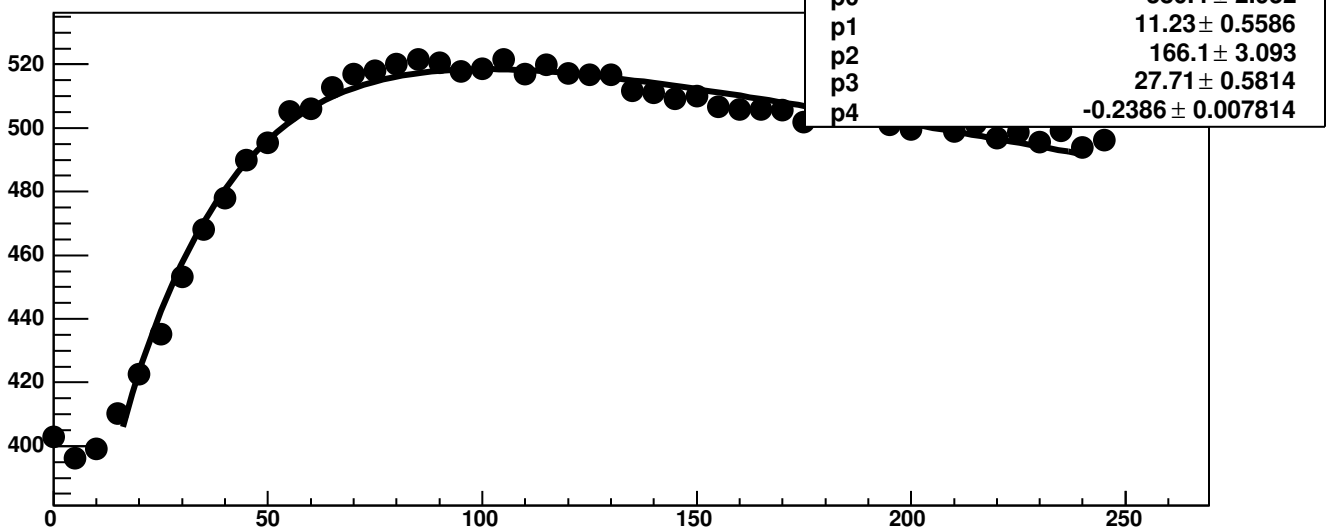
Chip 0, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



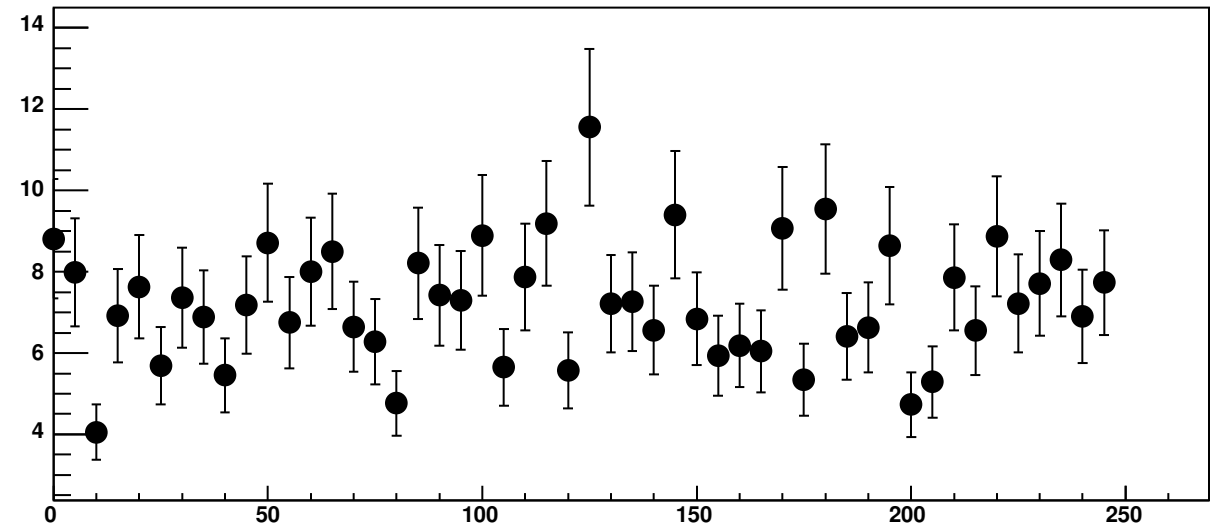
Chip 0, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold



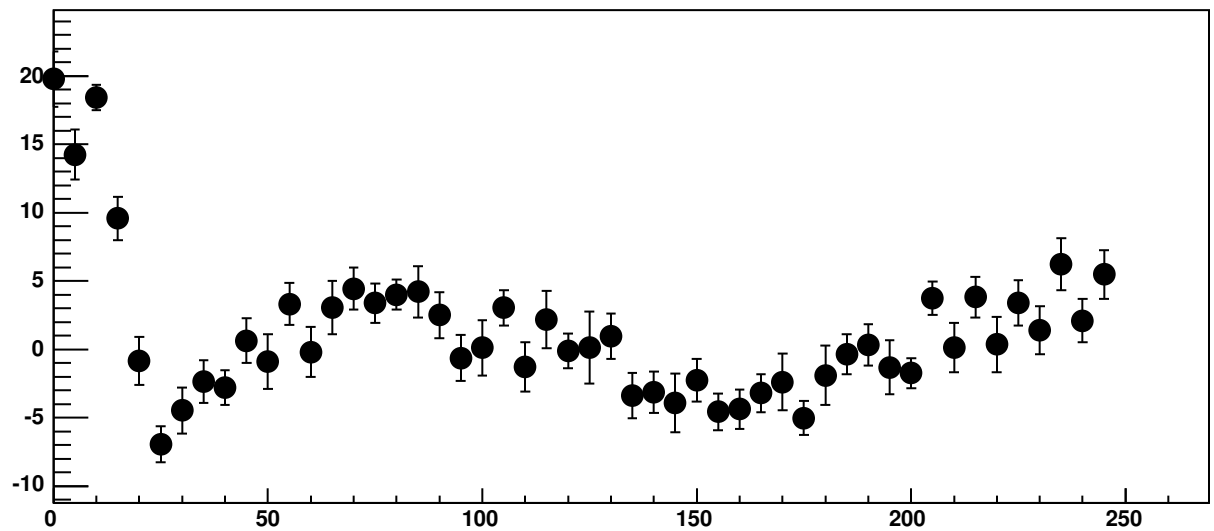
Chip 0, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold



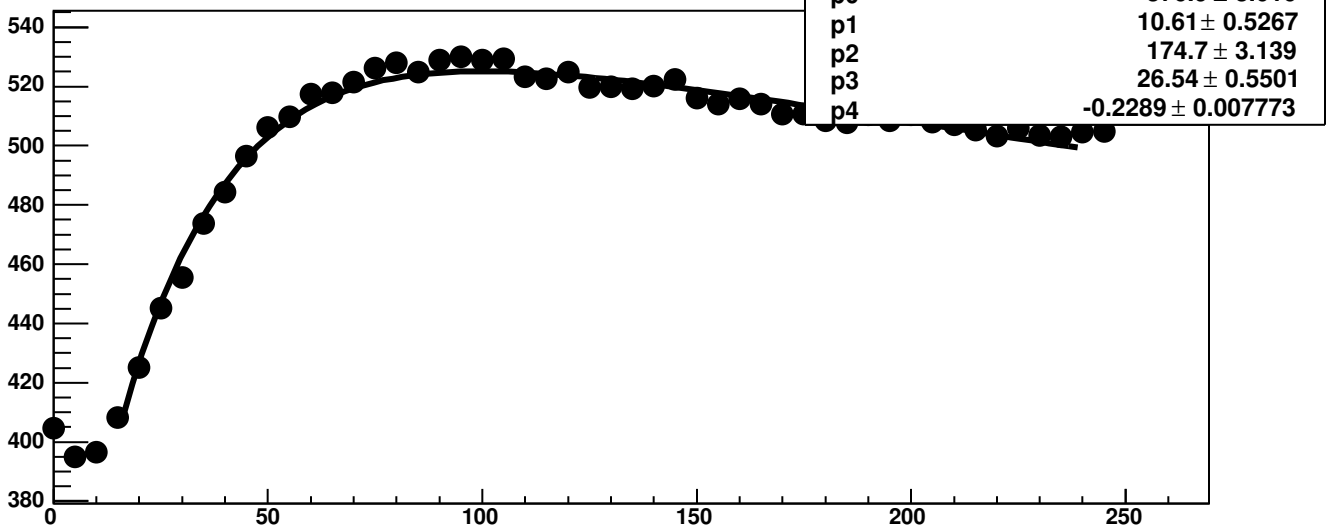
Chip 0, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold



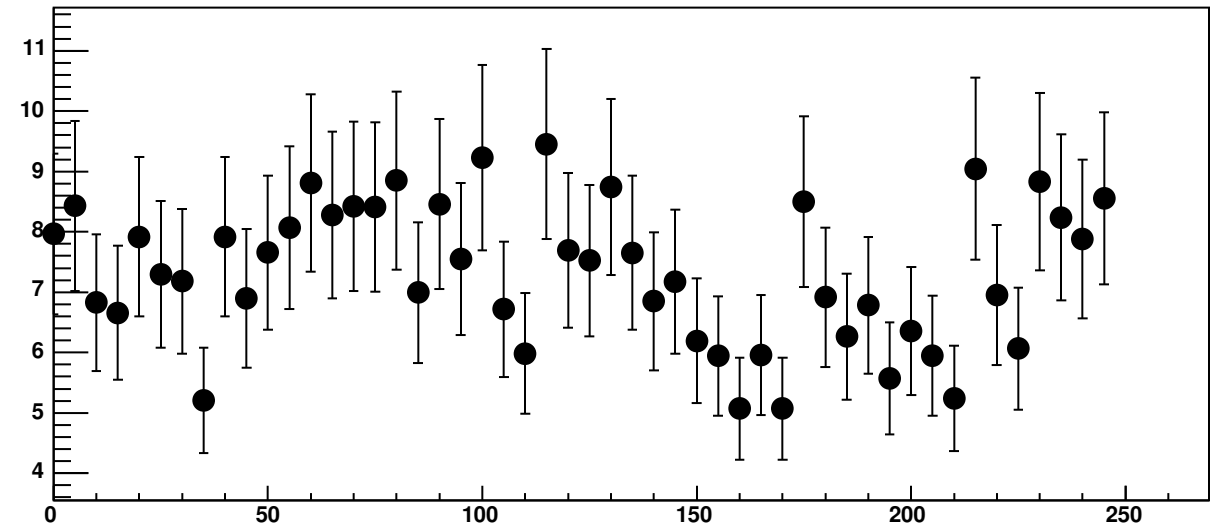
Chip 0, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold



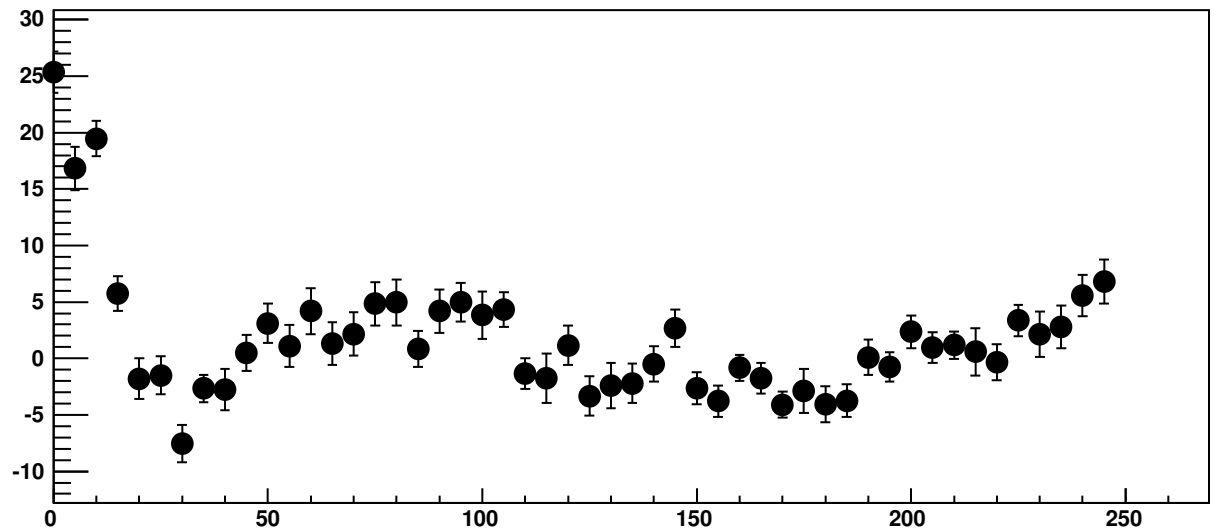
Chip 0, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold



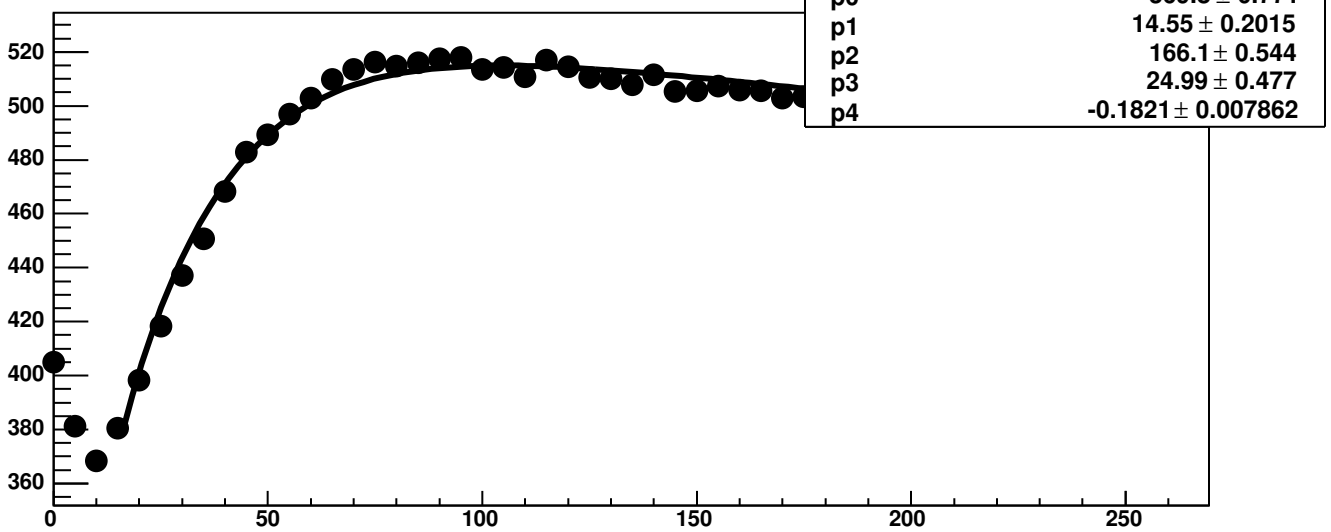
Chip 0, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



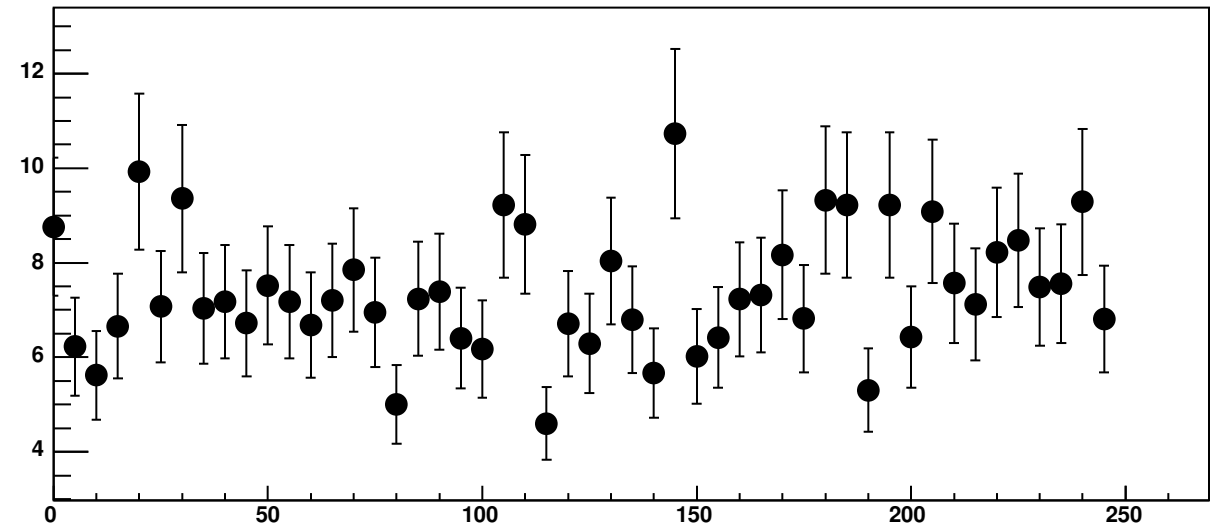
Chip 0, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold



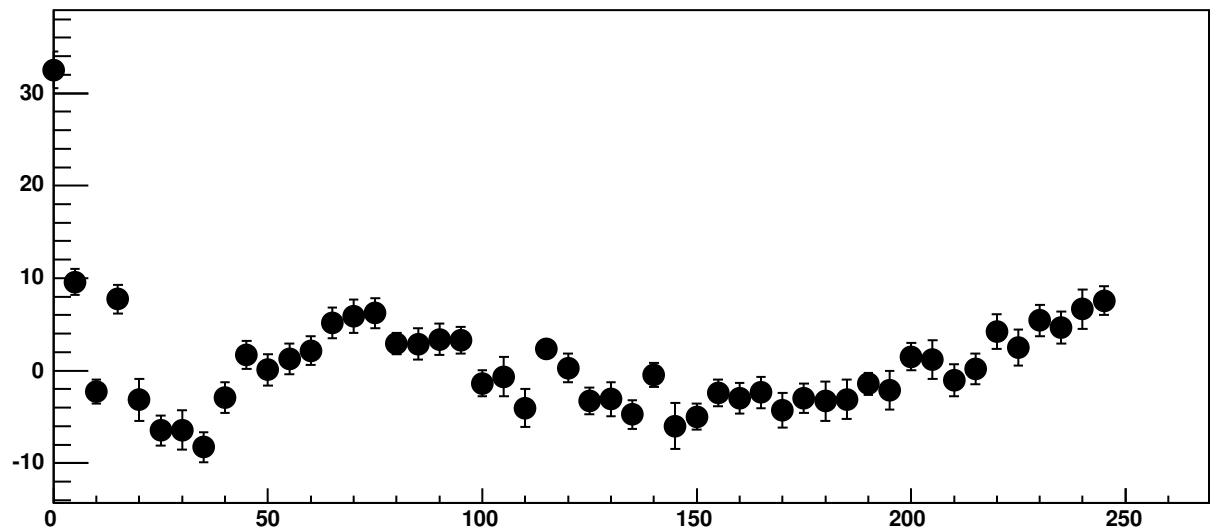
Chip 0, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

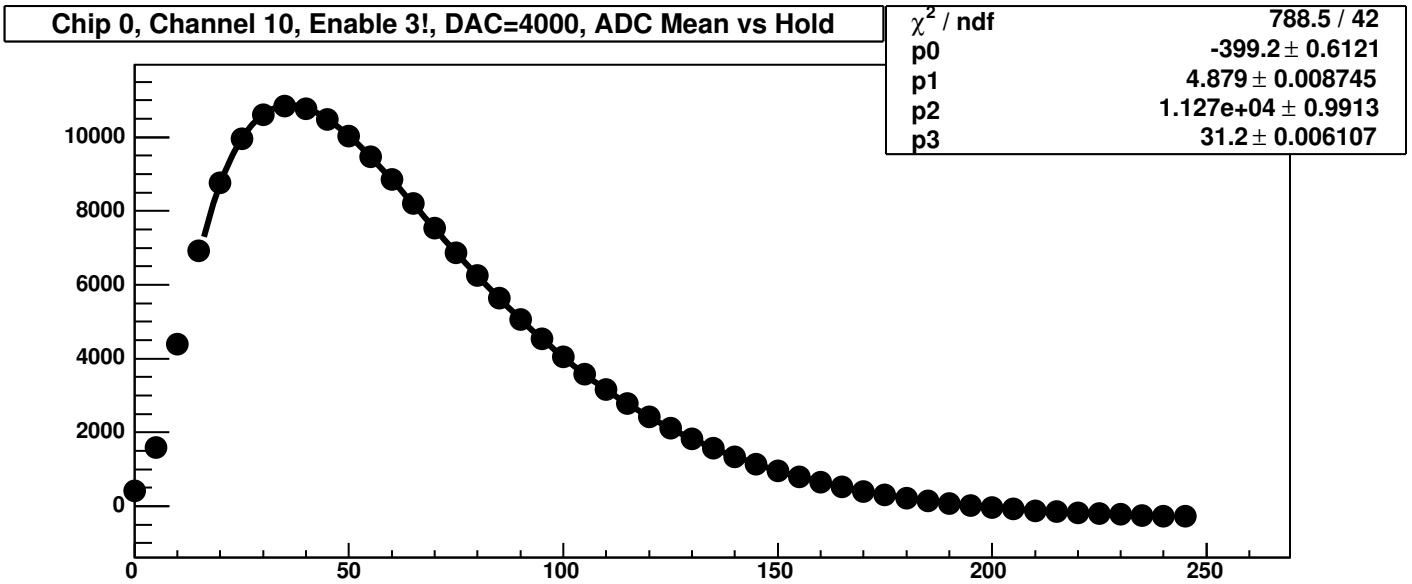


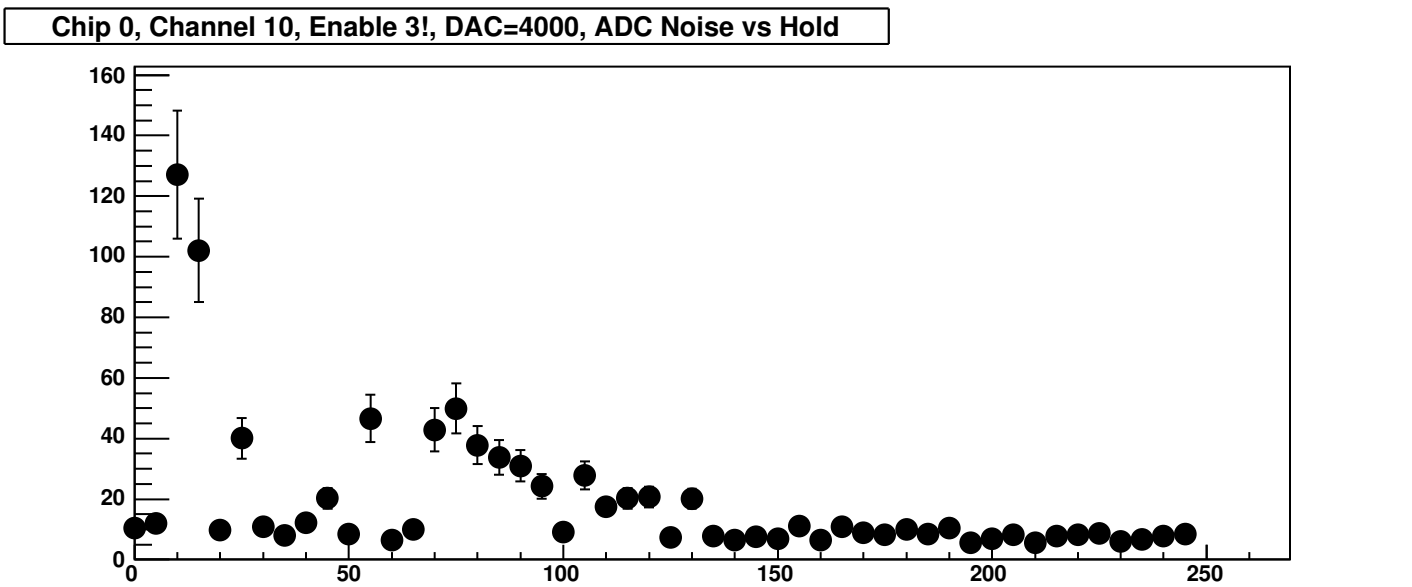
Chip 0, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold

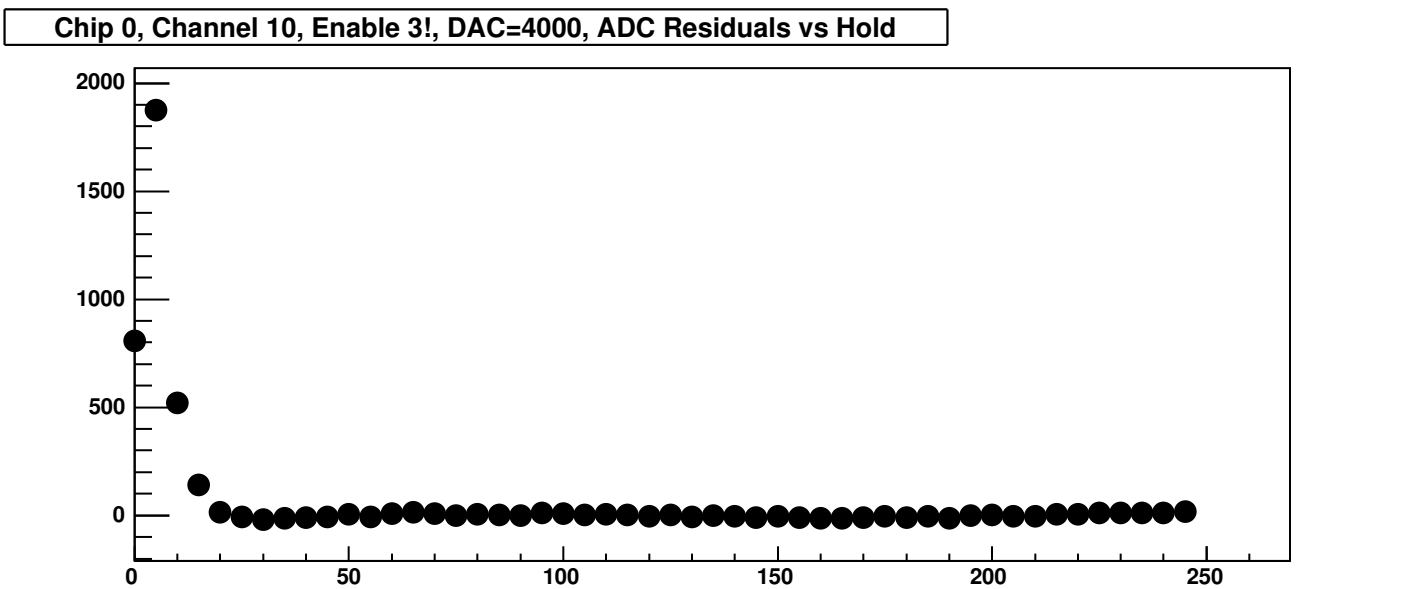


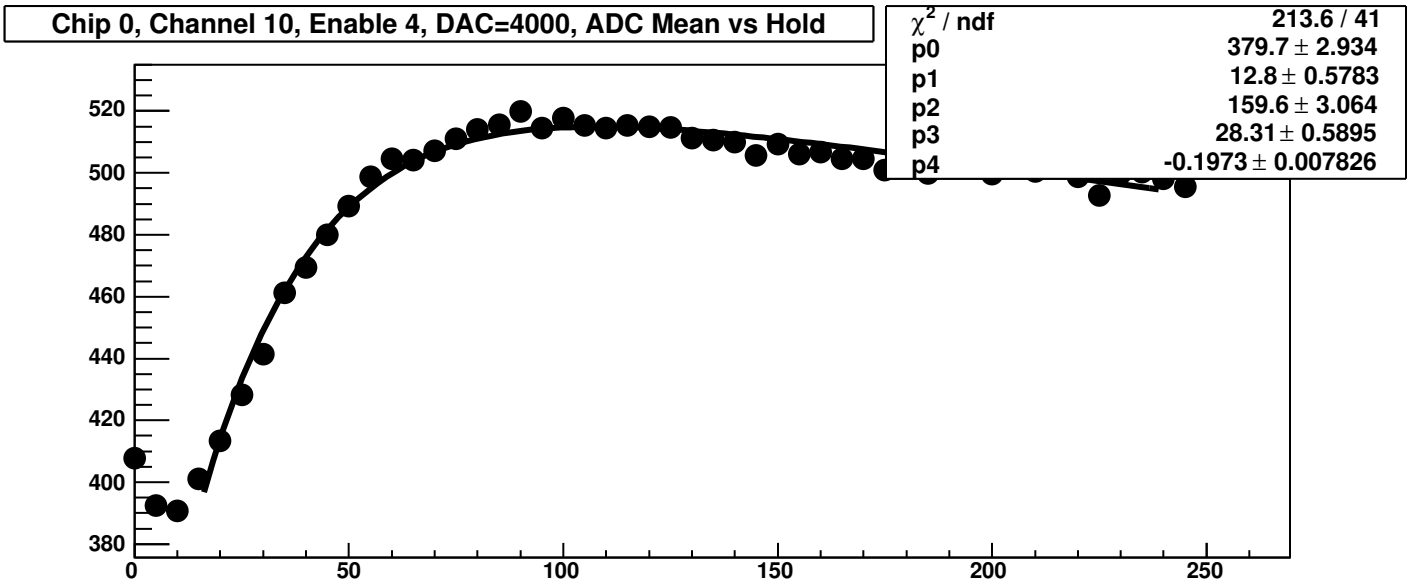
Chip 0, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

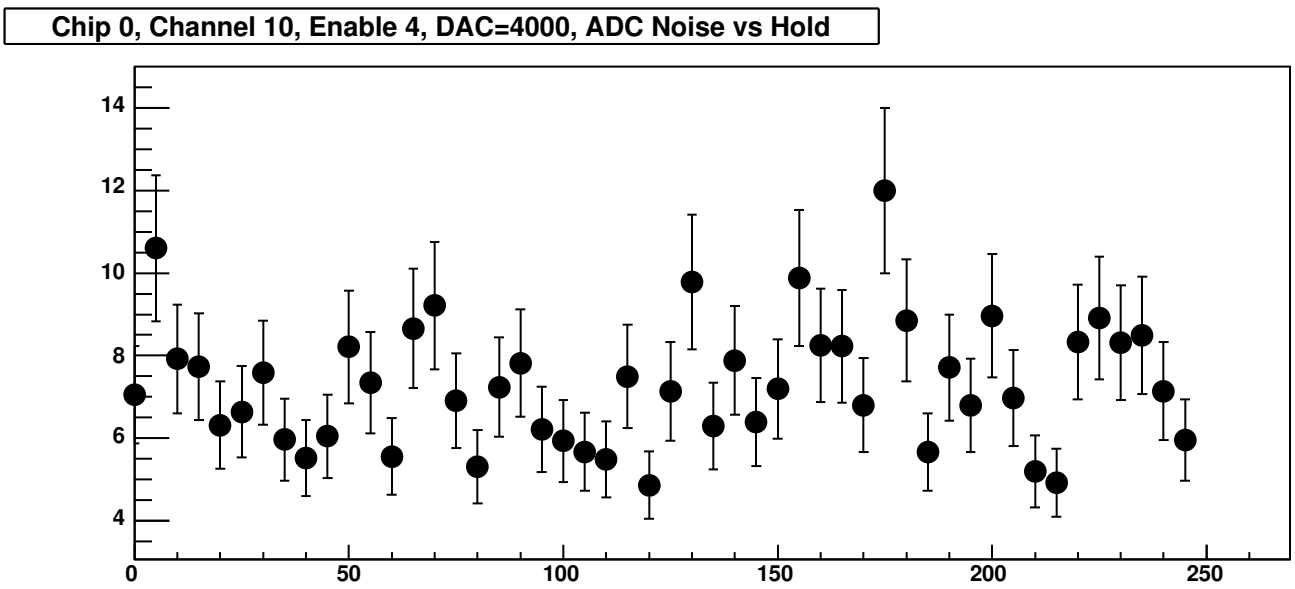


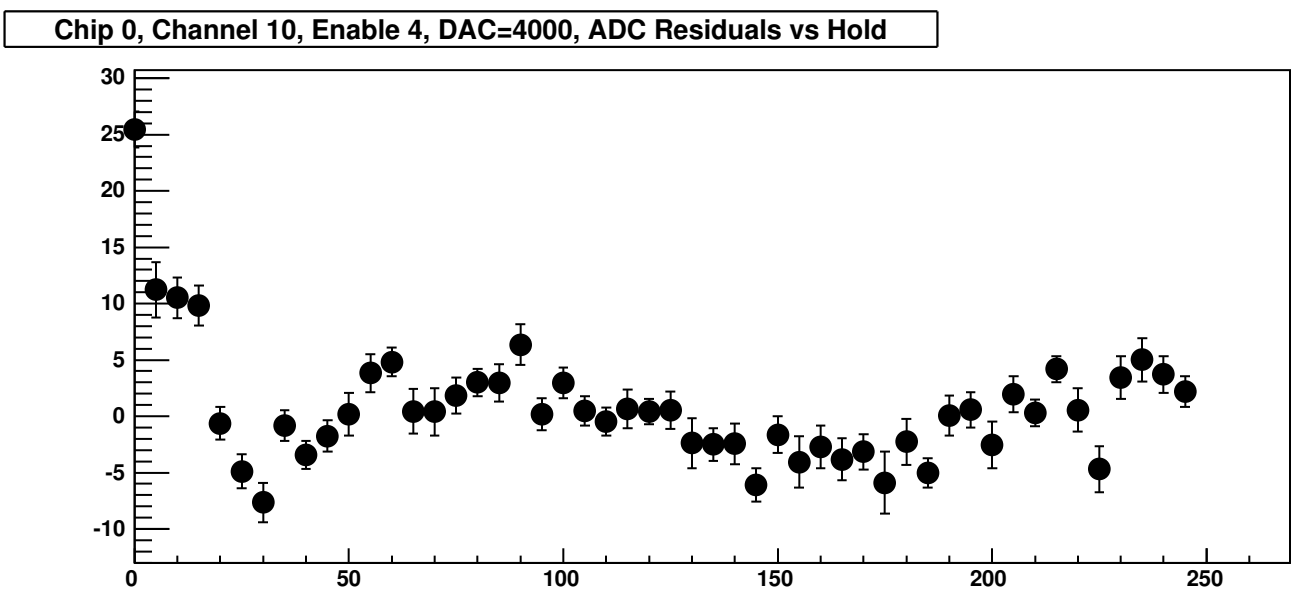

Chip 0, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 788.5 / 42
p0 -399.2 ± 0.6121
p1 4.879 ± 0.008745
p2 1.127e+04 ± 0.9913
p3 31.2 ± 0.006107
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 0, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

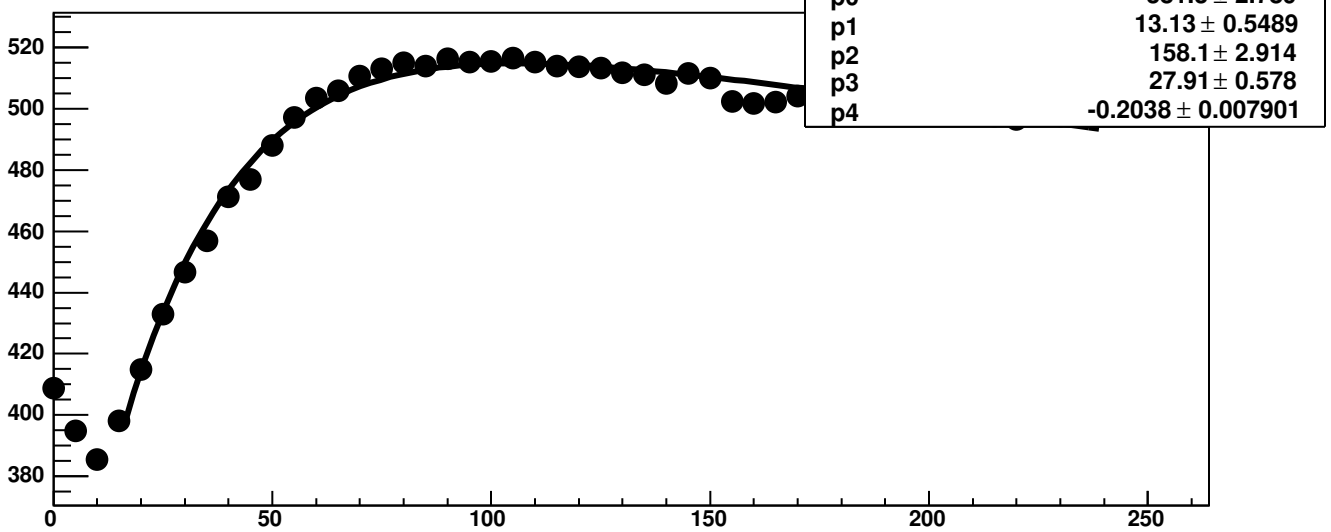

Chip 0, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 0, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 213.6 / 41
p0 379.7 ± 2.934
p1 12.8 ± 0.5783
p2 159.6 ± 3.064
p3 28.31 ± 0.5895
p4 -0.1973 ± 0.007826
520
500
480
460
440
420
400
380
0
50
100
150
200
250

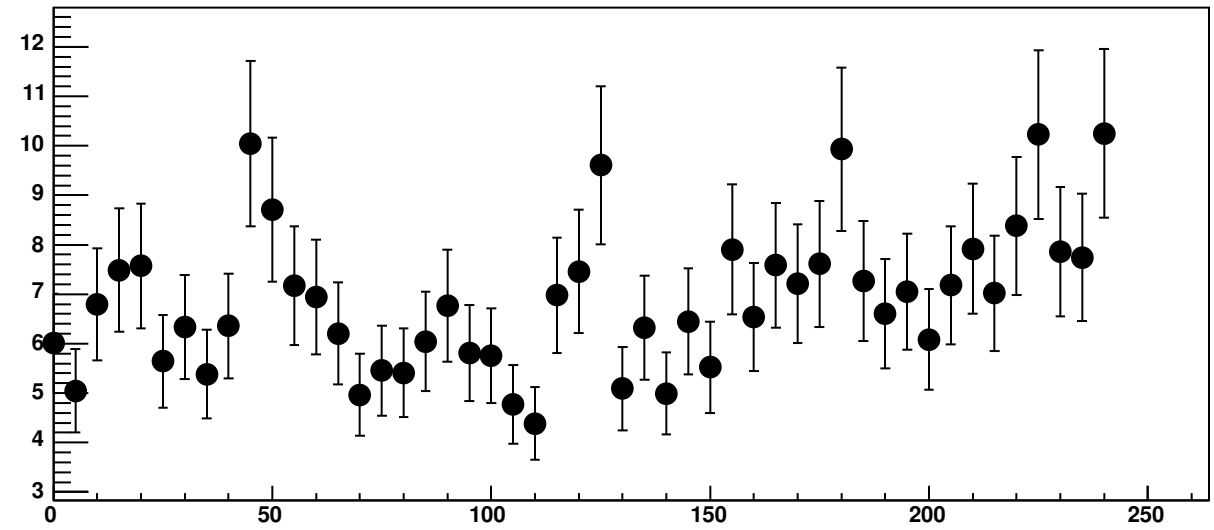

Chip 0, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 0, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
0
50
100
150
200
250

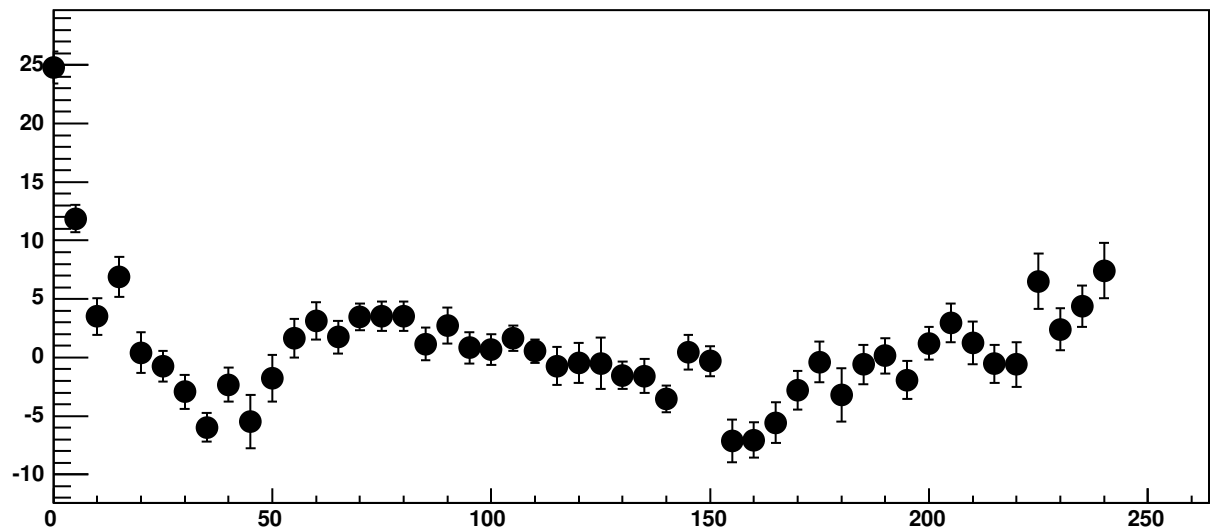
Chip 0, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold

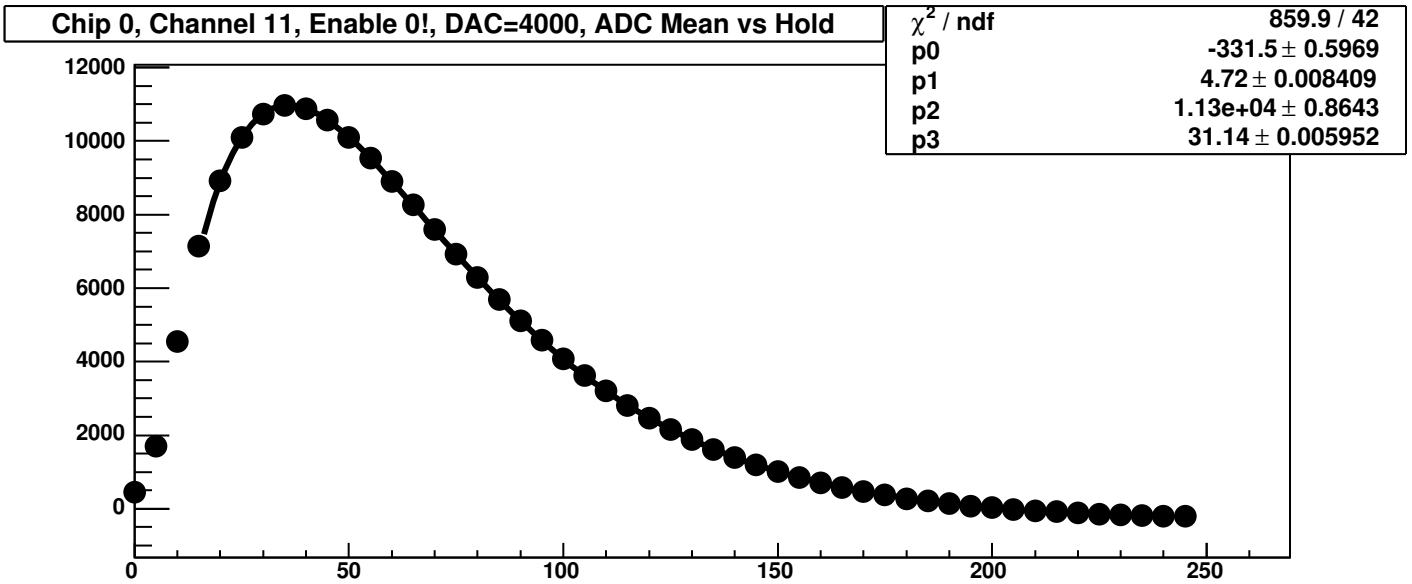


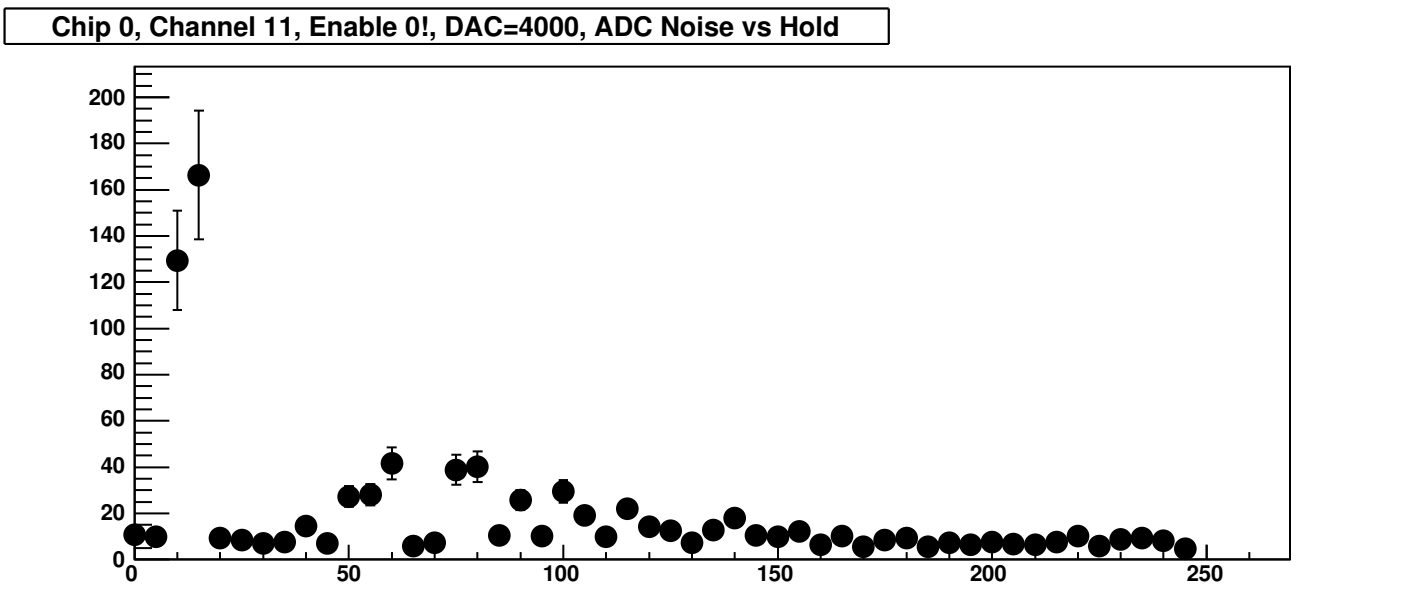
Chip 0, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold

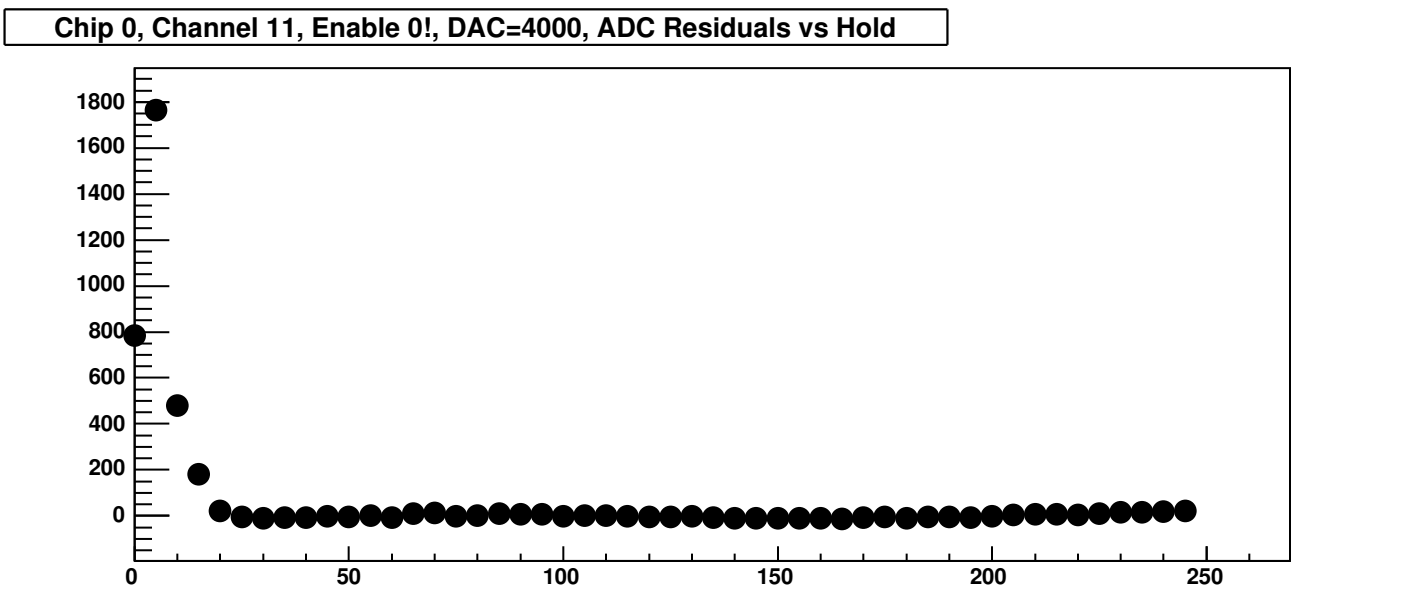


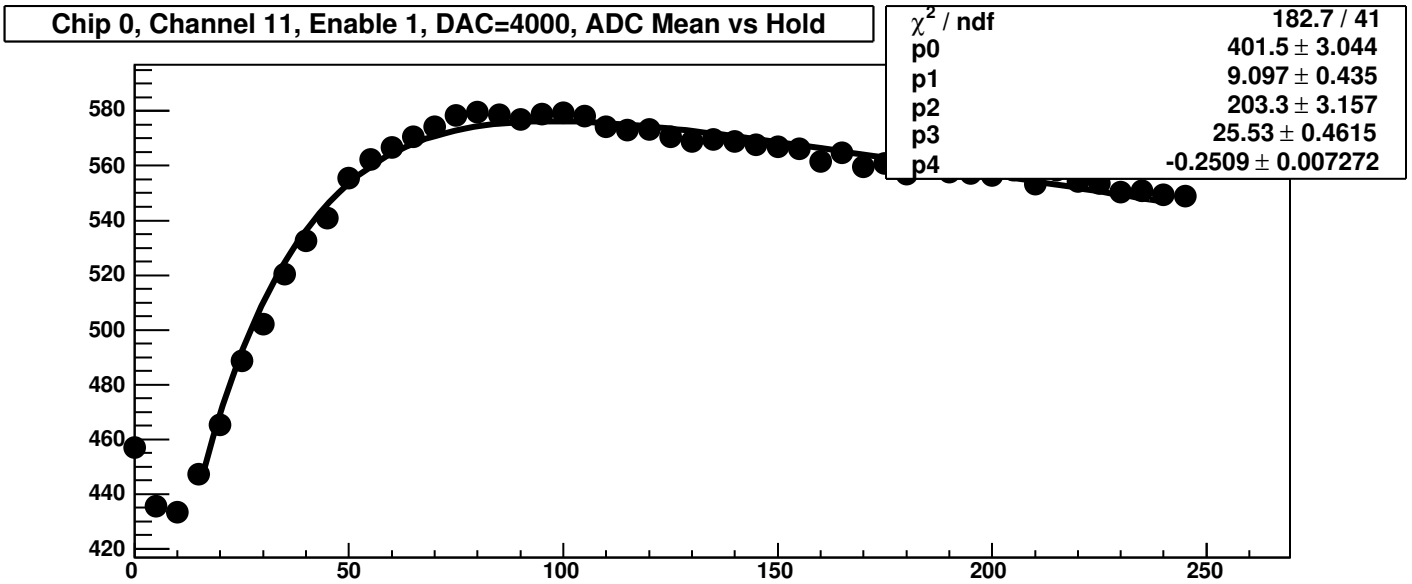
Chip 0, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

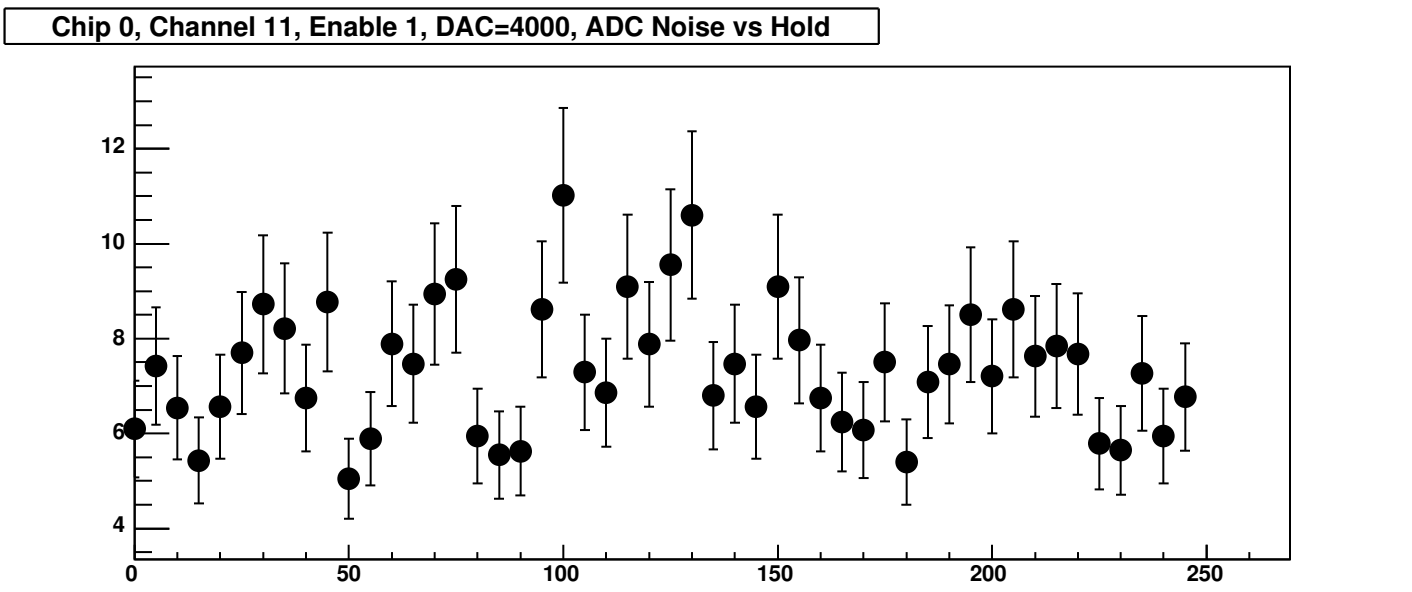


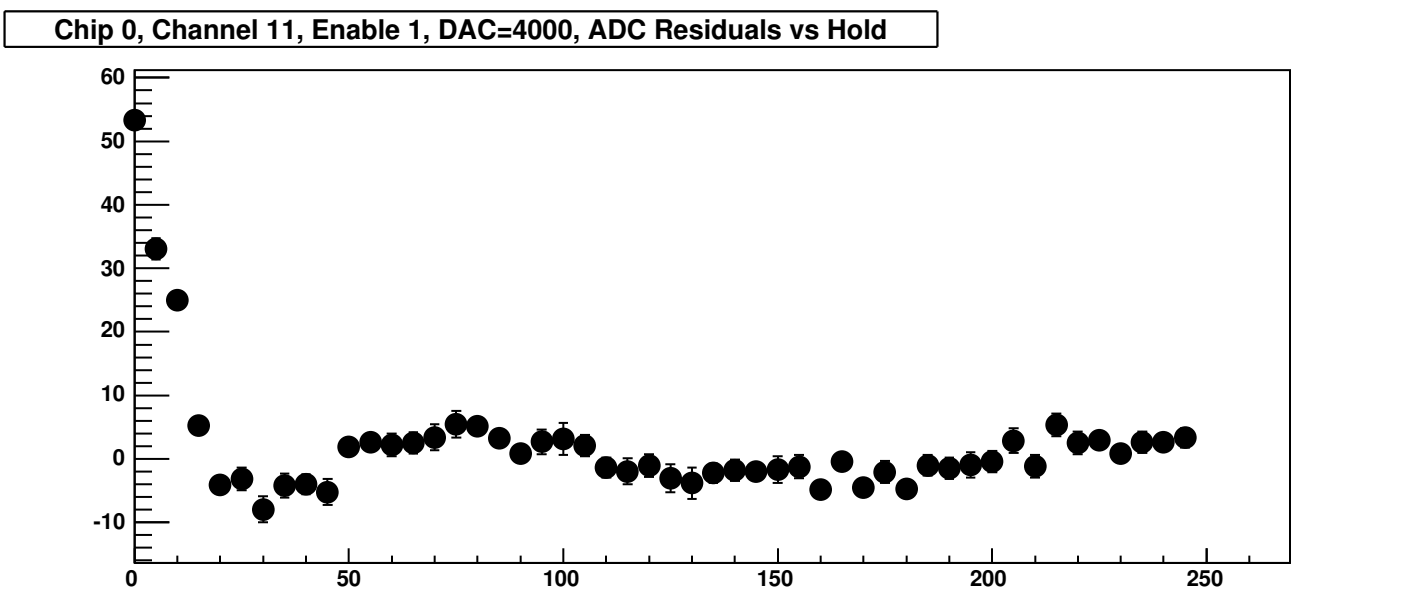

Chip 0, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 859.9 / 42
p0 -331.5 ± 0.5969
p1 4.72 ± 0.008409
p2 1.13e+04 ± 0.8643
p3 31.14 ± 0.005952
12000
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 0, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

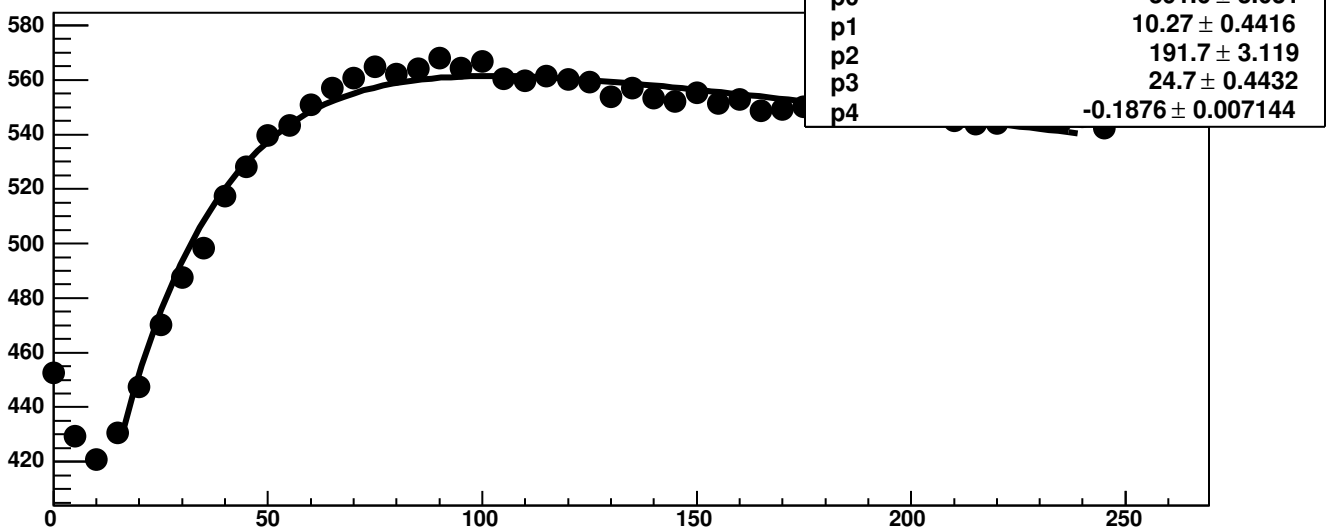

Chip 0, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 0, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 182.7 / 41
p0 401.5 ± 3.044
p1 9.097 ± 0.435
p2 203.3 ± 3.157
p3 25.53 ± 0.4615
p4 -0.2509 ± 0.007272
580
560
540
520
500
480
460
440
420
0
50
100
150
200
250

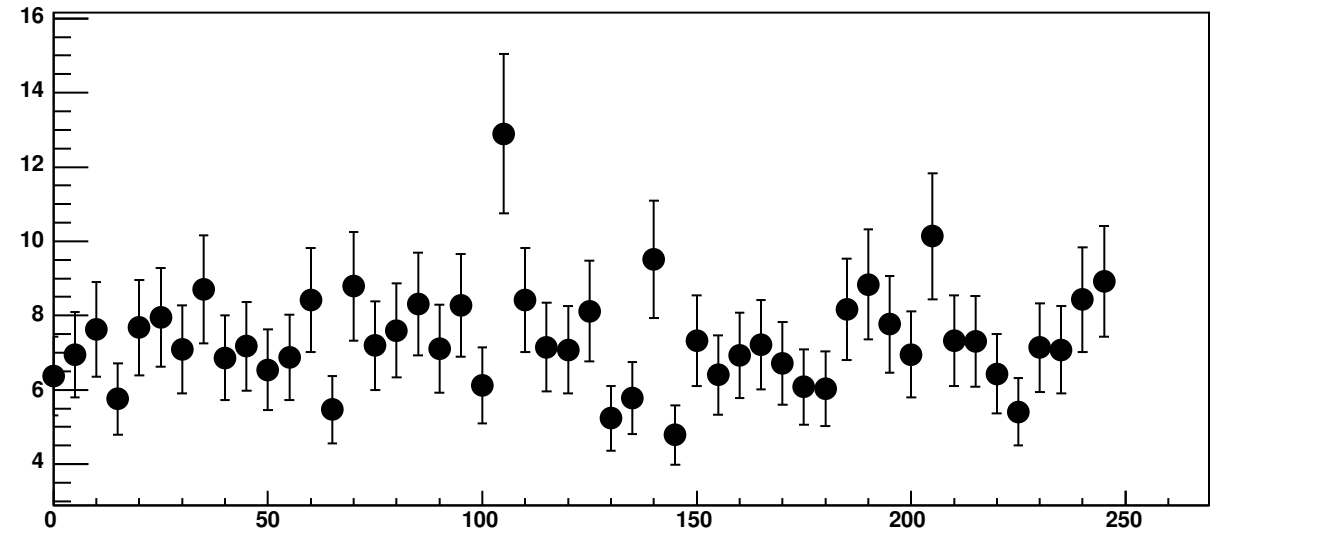

Chip 0, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 0, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

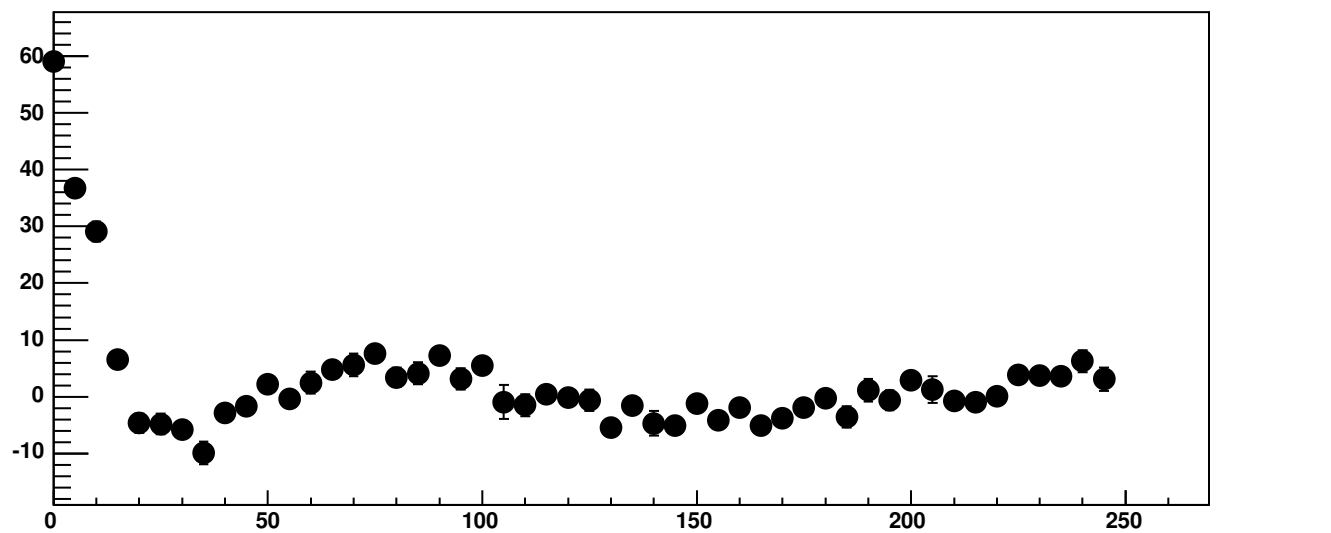
Chip 0, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

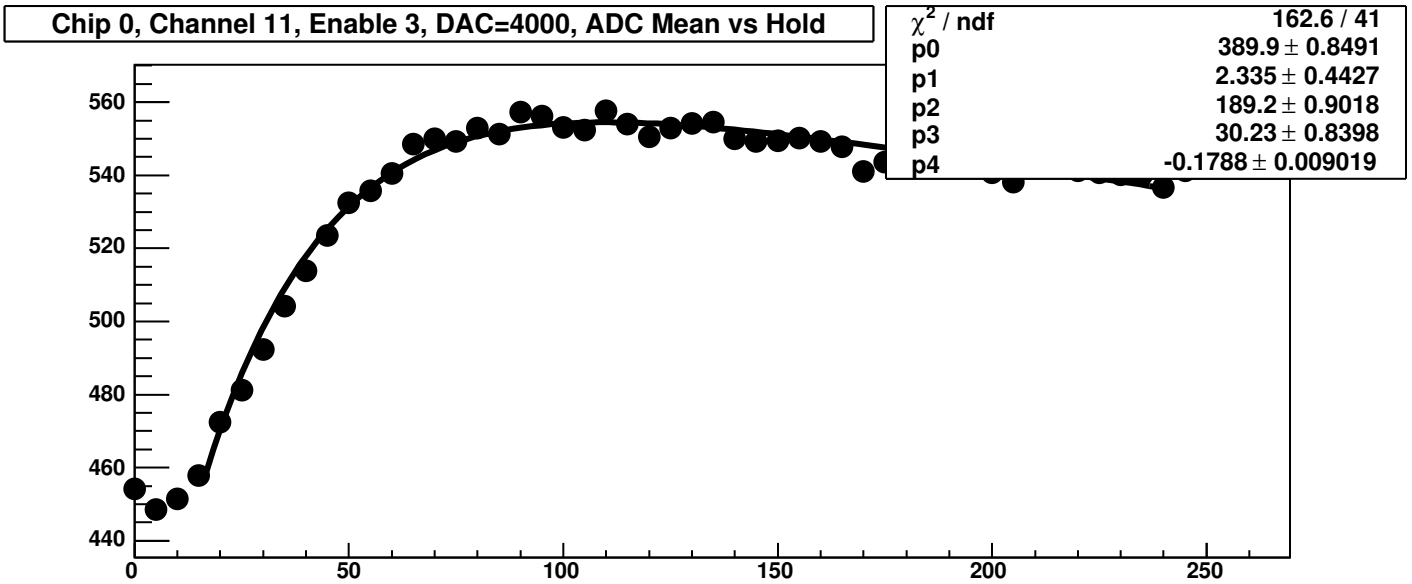


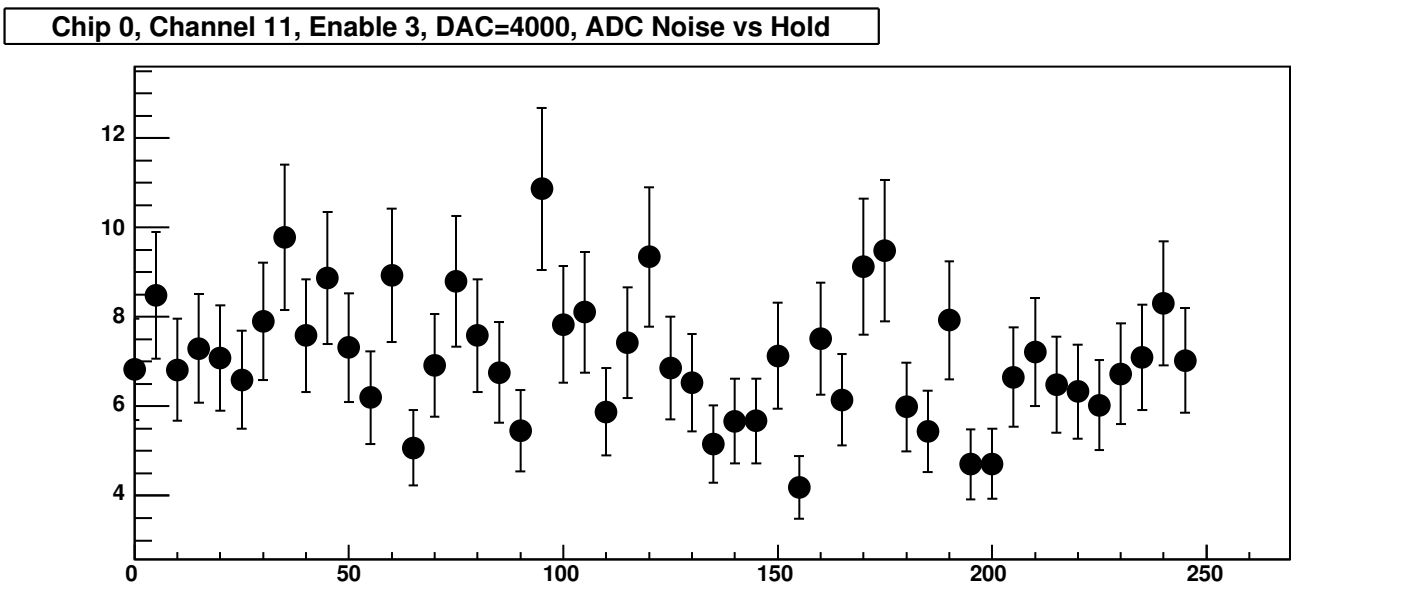
Chip 0, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold

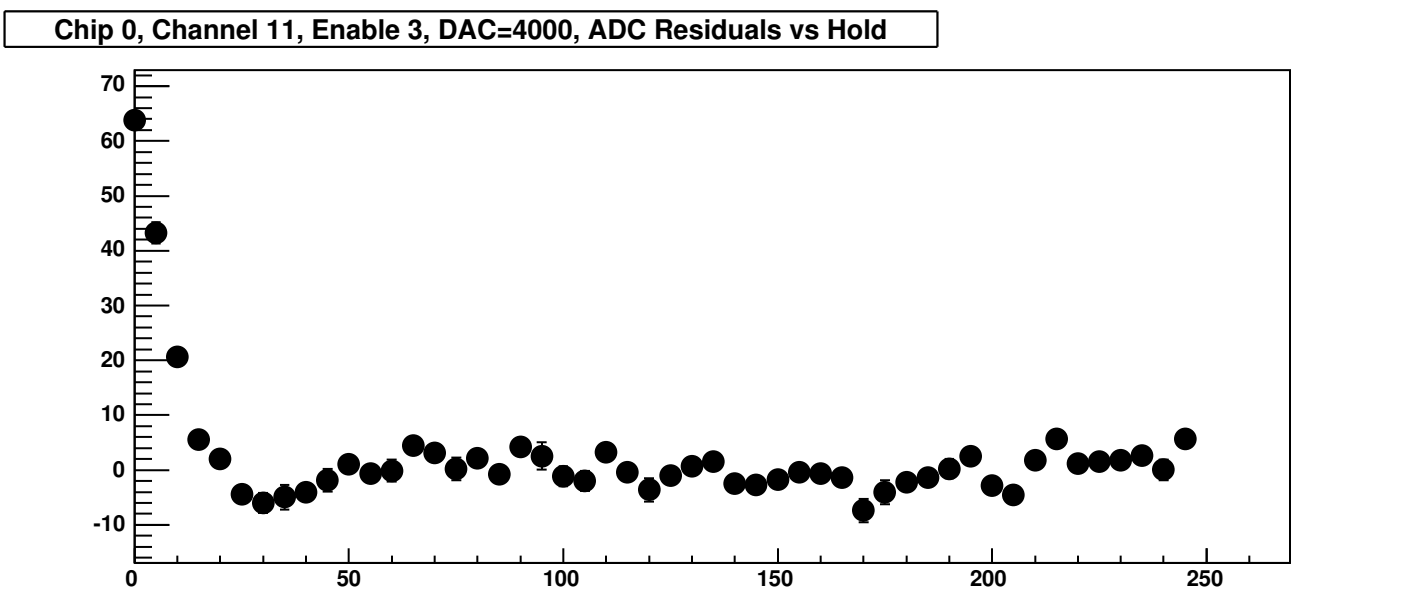


Chip 0, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

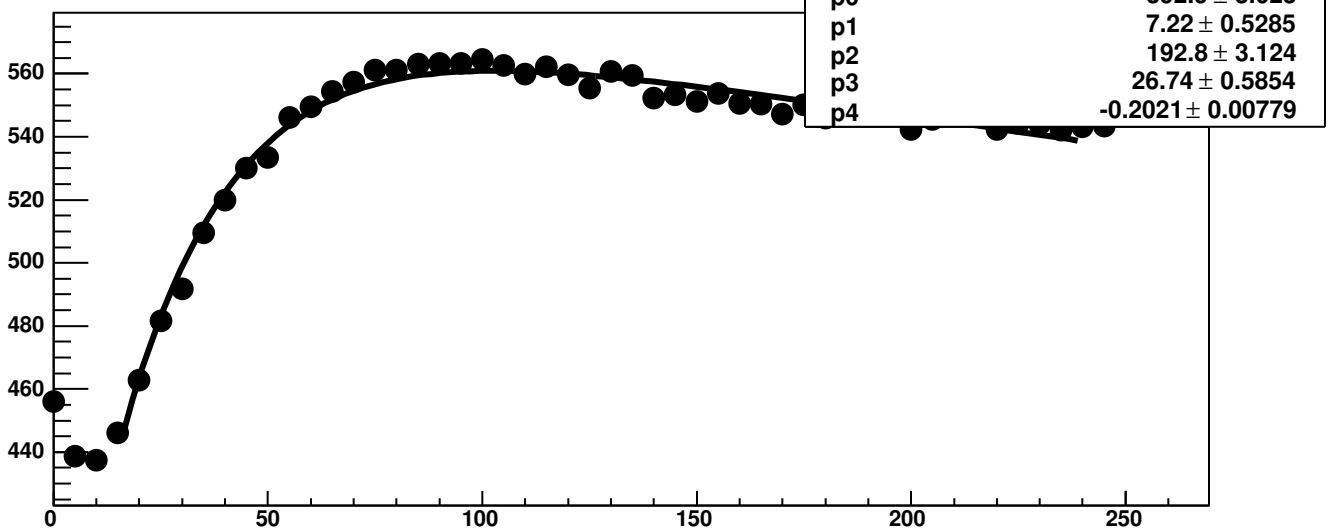



Chip 0, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 162.6 / 41
p0 389.9 ± 0.8491
p1 2.335 ± 0.4427
p2 189.2 ± 0.9018
p3 30.23 ± 0.8398
p4 -0.1788 ± 0.009019
560
540
520
500
480
460
440
0
50
100
150
200
250

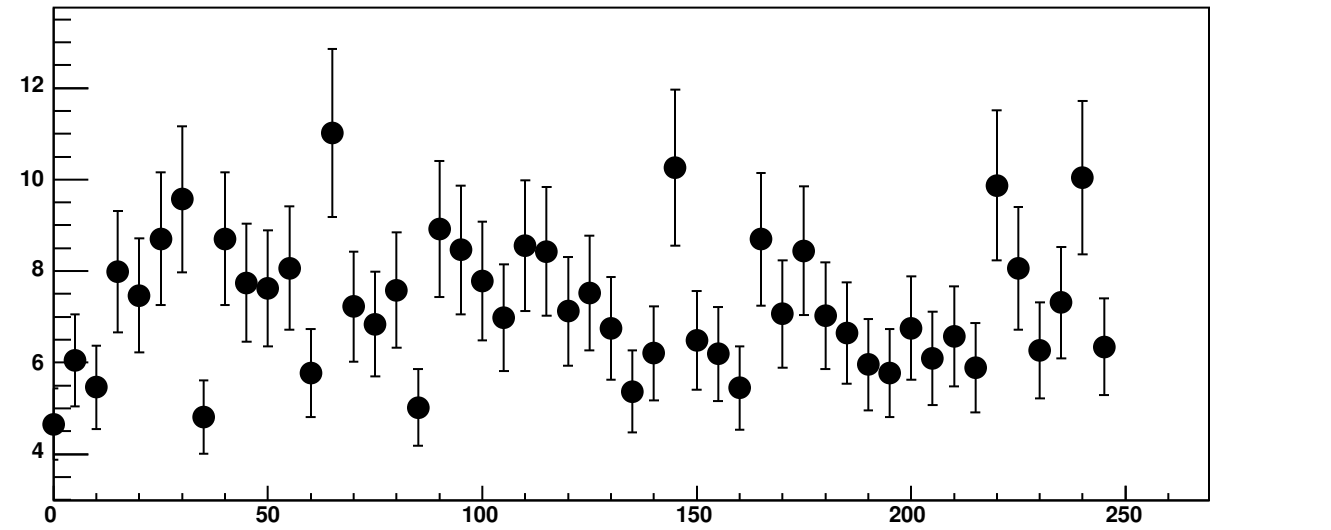

Chip 0, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 0, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

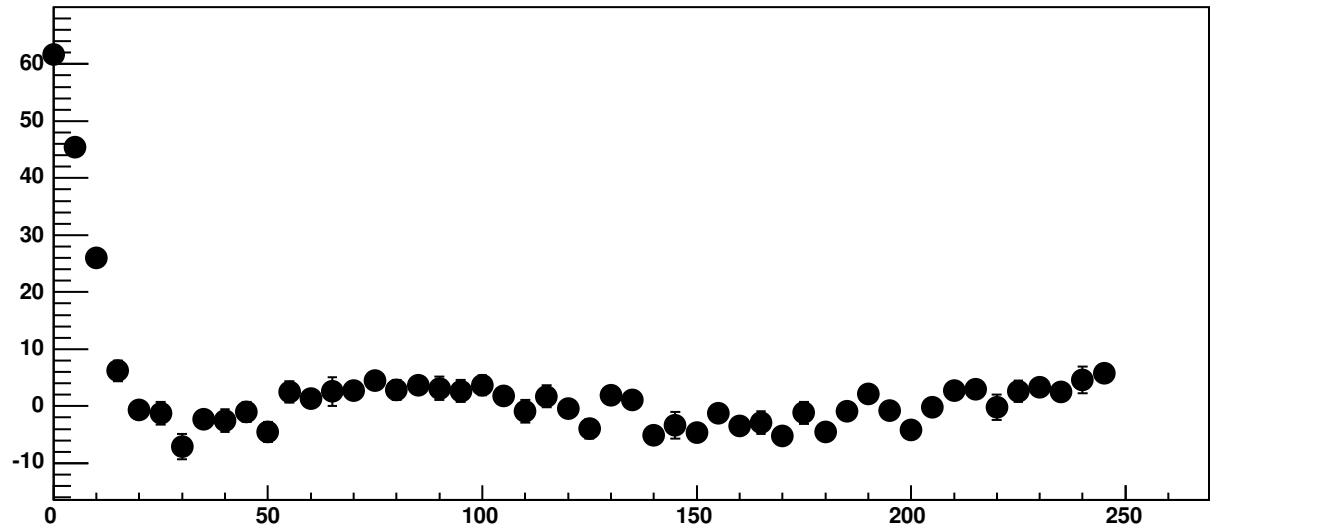
Chip 0, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

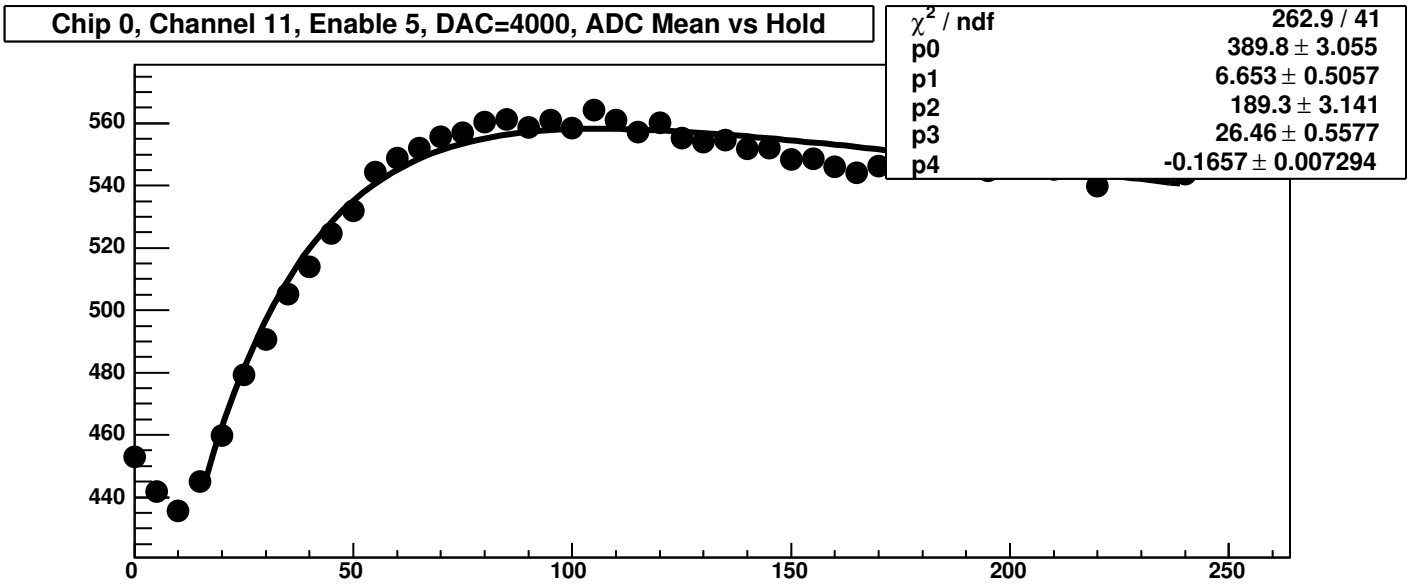


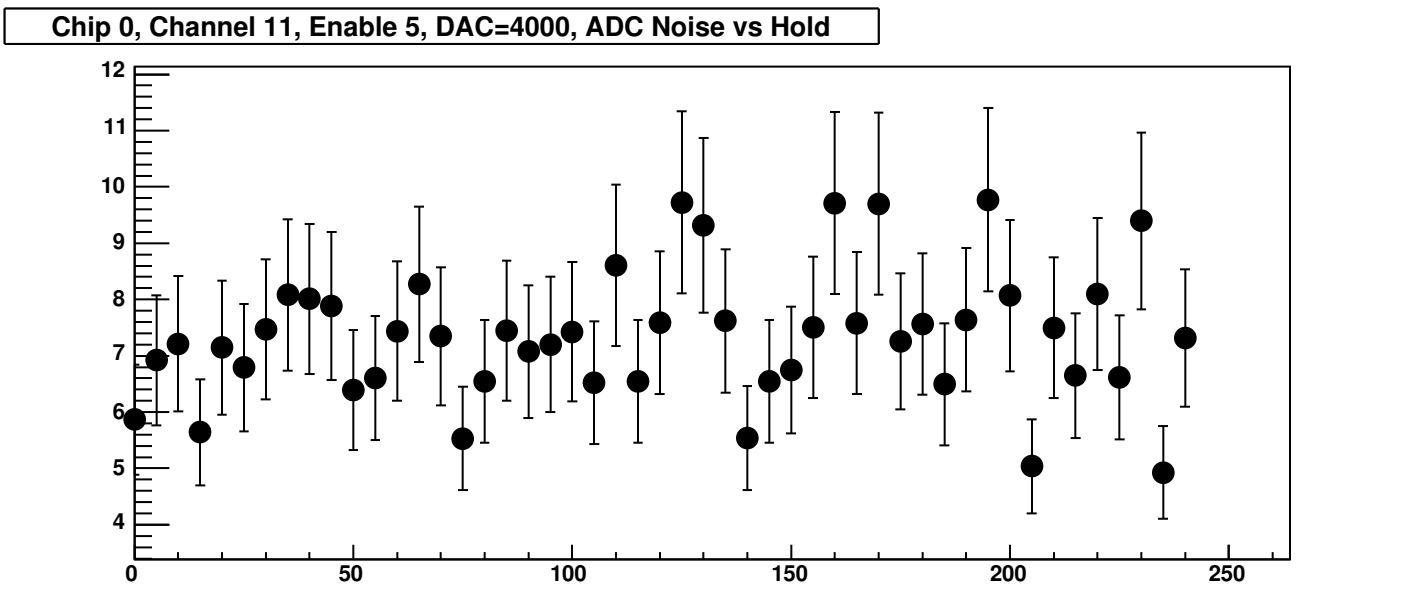
Chip 0, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold

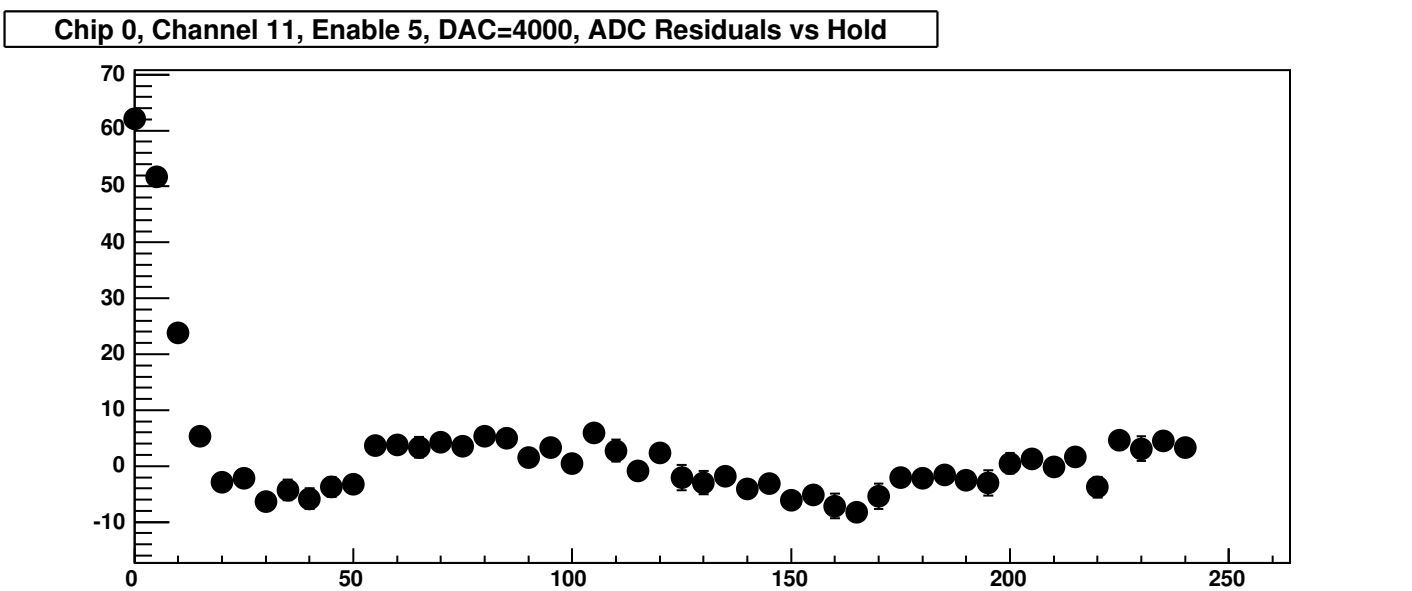


Chip 0, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

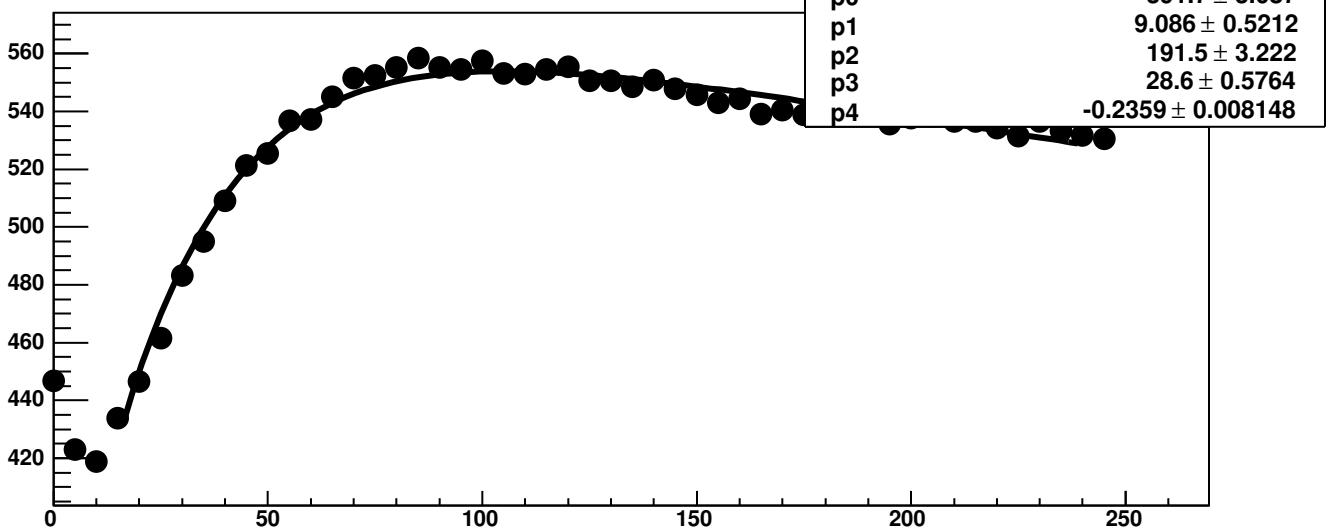



Chip 0, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 262.9 / 41
p0 389.8 ± 3.055
p1 6.653 ± 0.5057
p2 189.3 ± 3.141
p3 26.46 ± 0.5577
p4 -0.1657 ± 0.007294
560
540
520
500
480
460
440
0
50
100
150
200
250

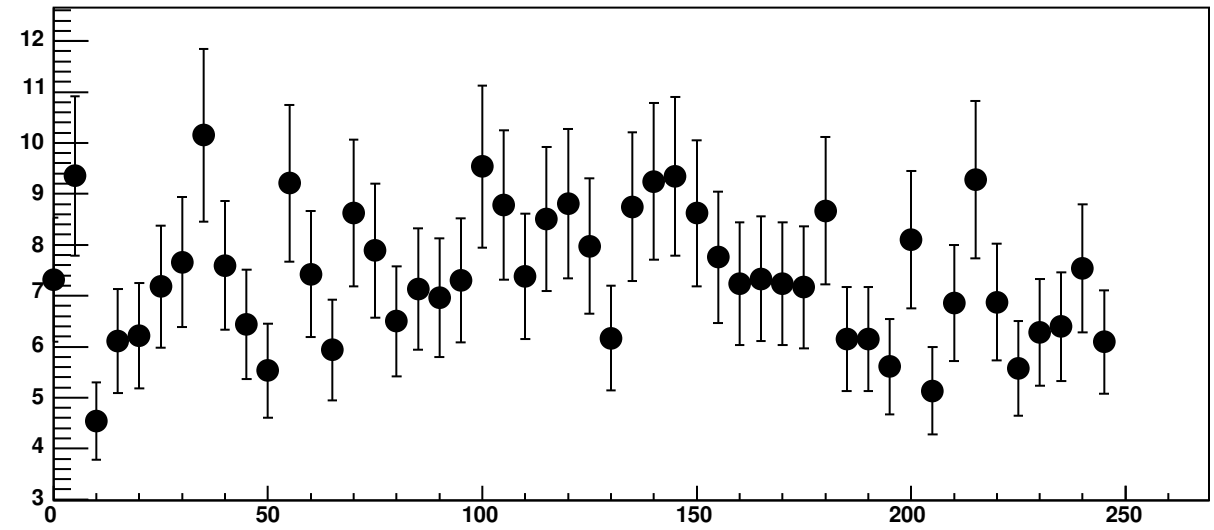

Chip 0, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 0, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

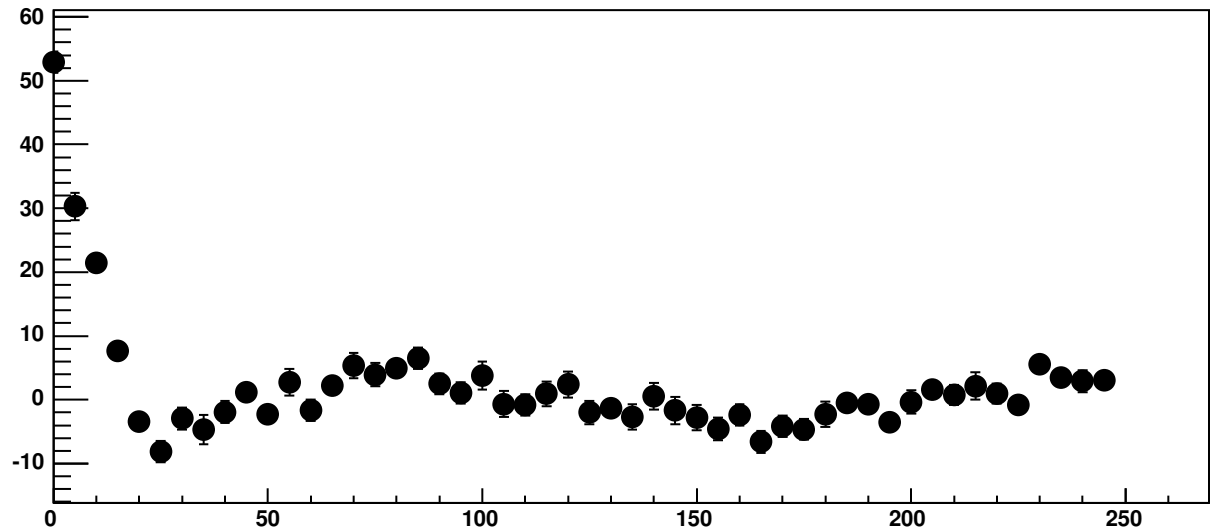
Chip 0, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

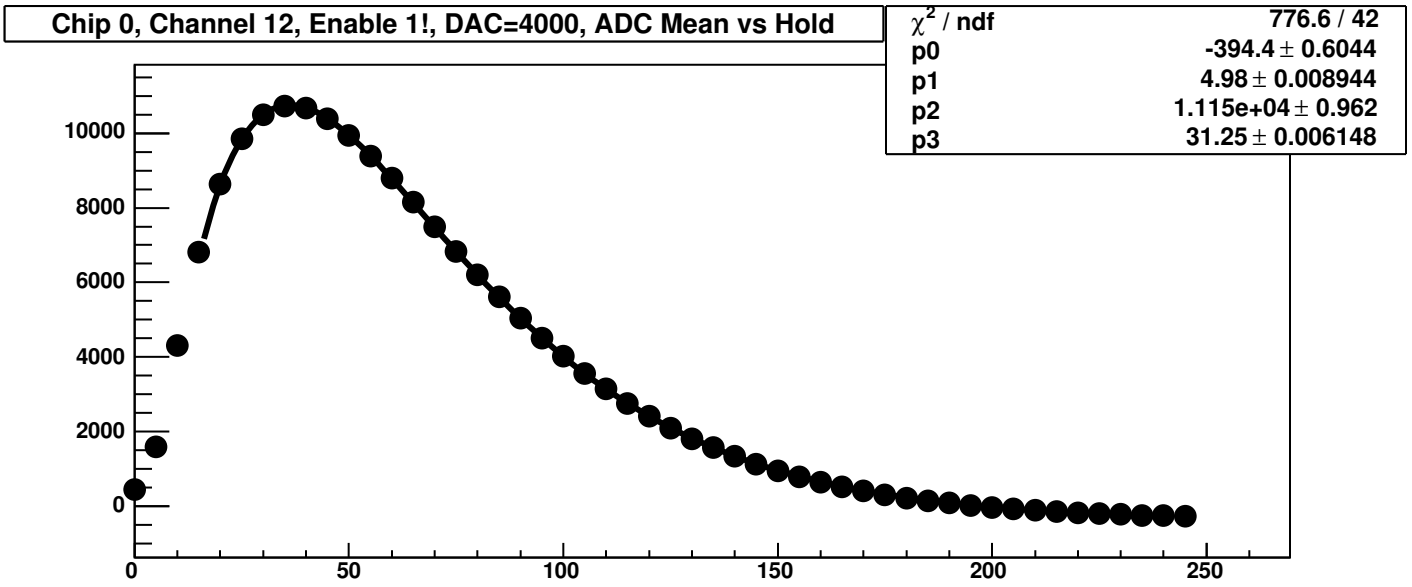


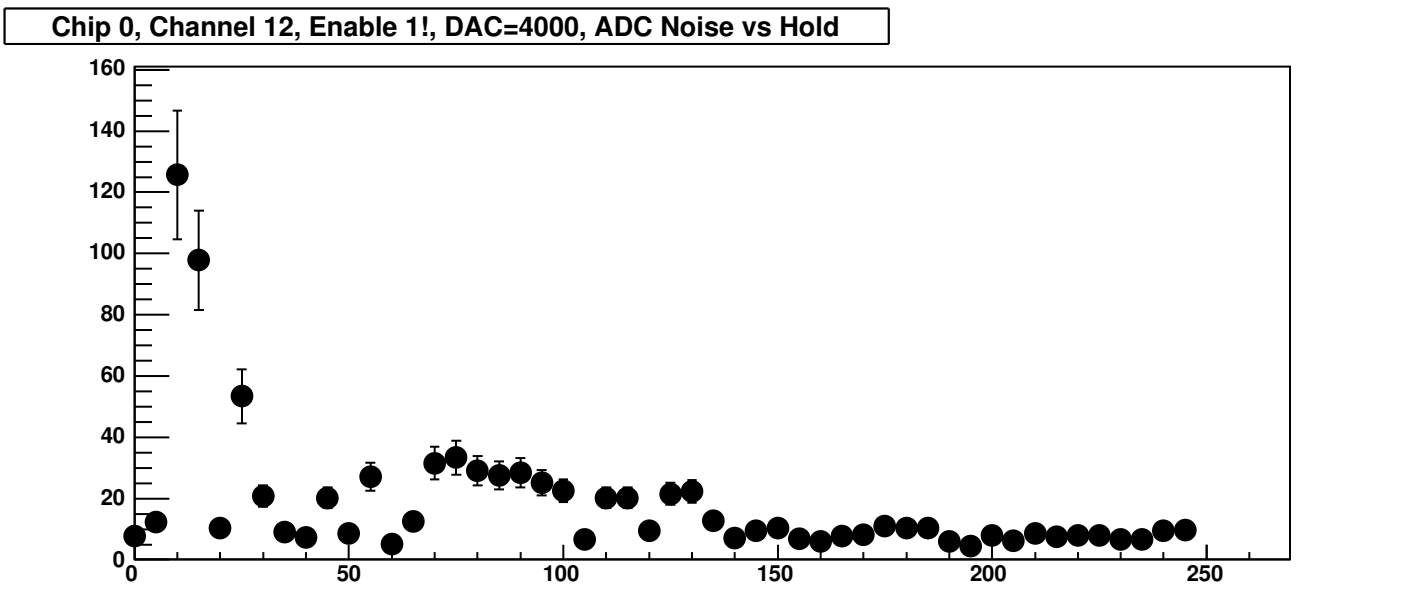
Chip 0, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold

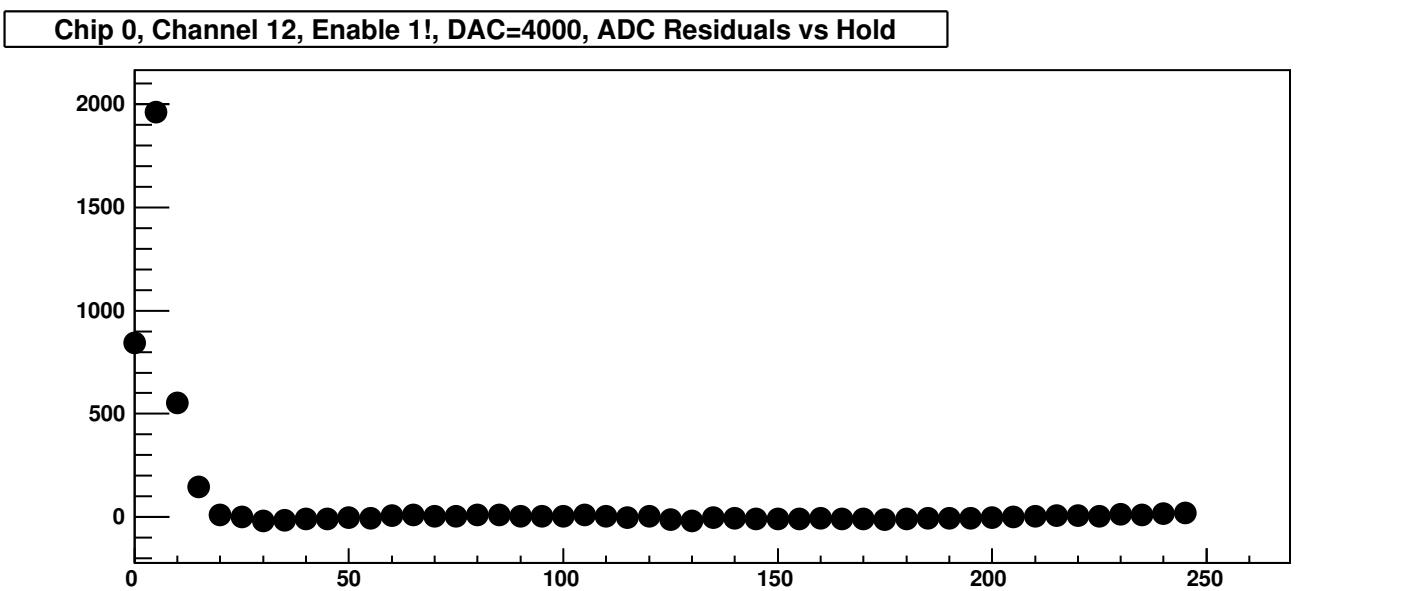


Chip 0, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

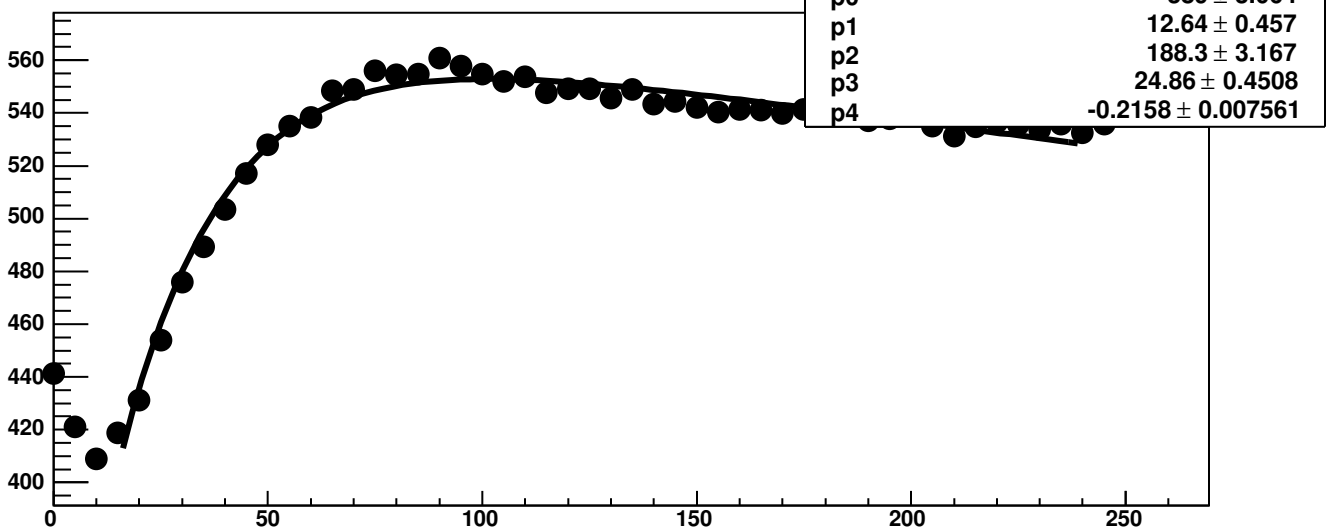



Chip 0, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 776.6 / 42
p0 -394.4 ± 0.6044
p1 4.98 ± 0.008944
p2 1.115e+04 ± 0.962
p3 31.25 ± 0.006148
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

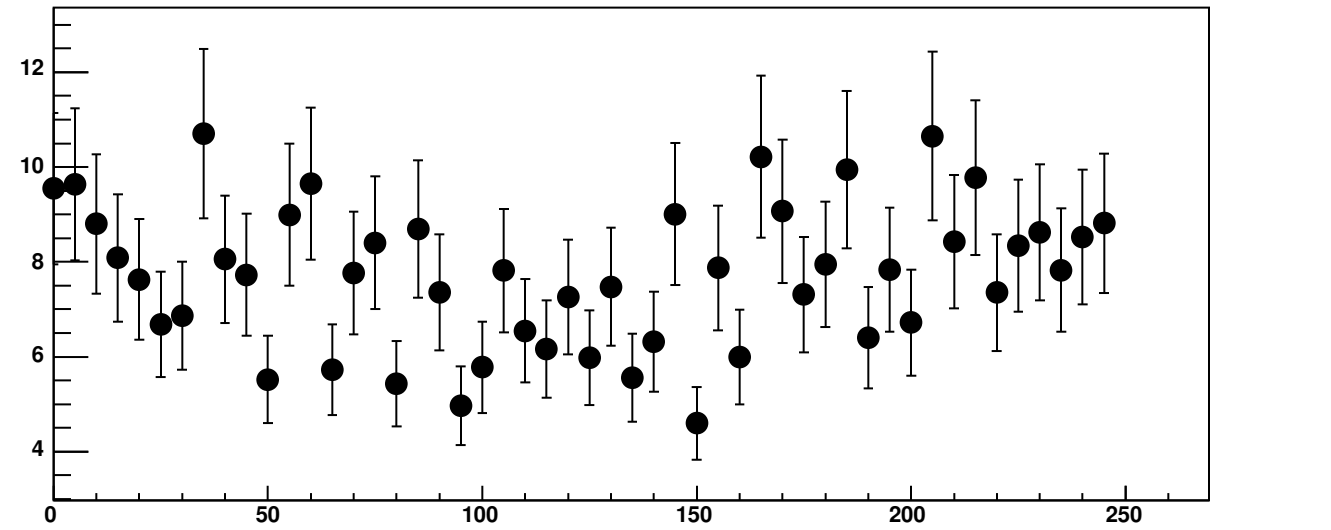

Chip 0, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
0
50
100
150
200
250

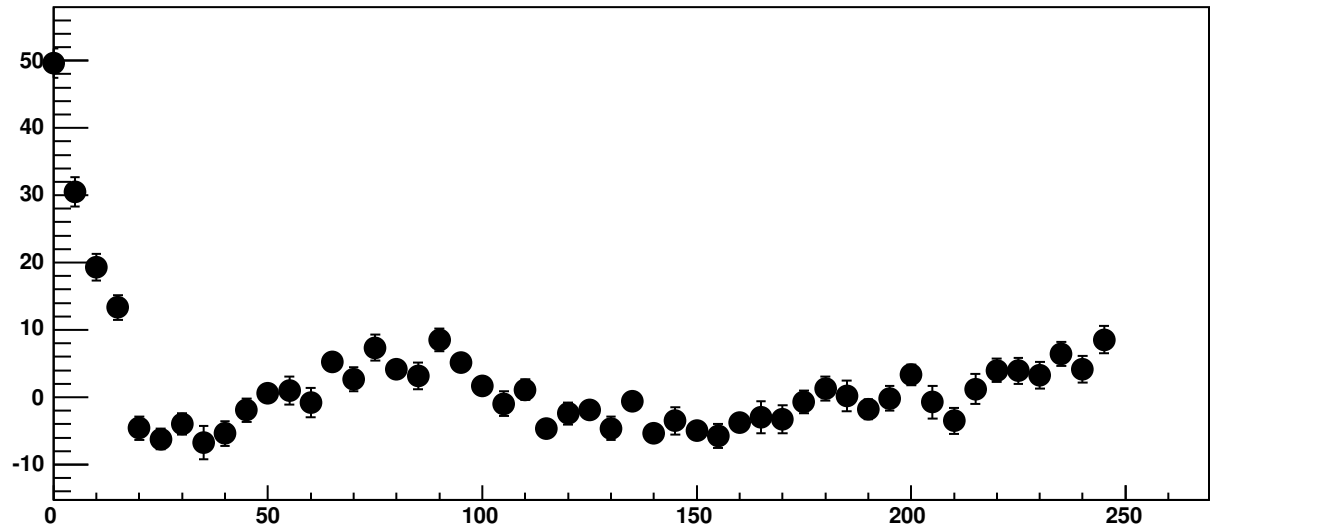
Chip 0, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold



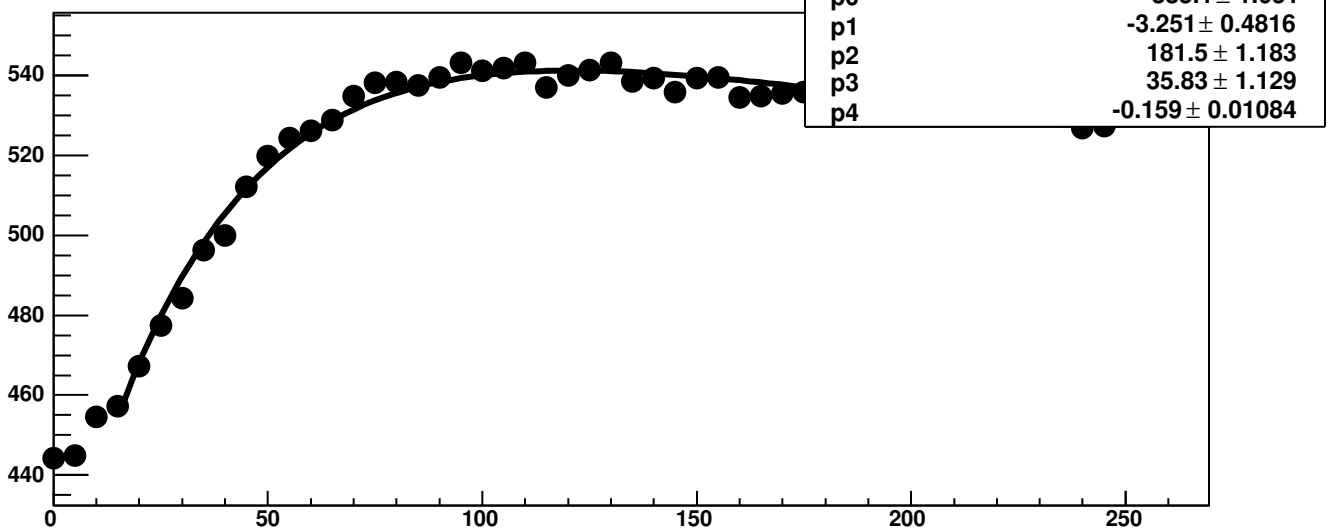
Chip 0, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



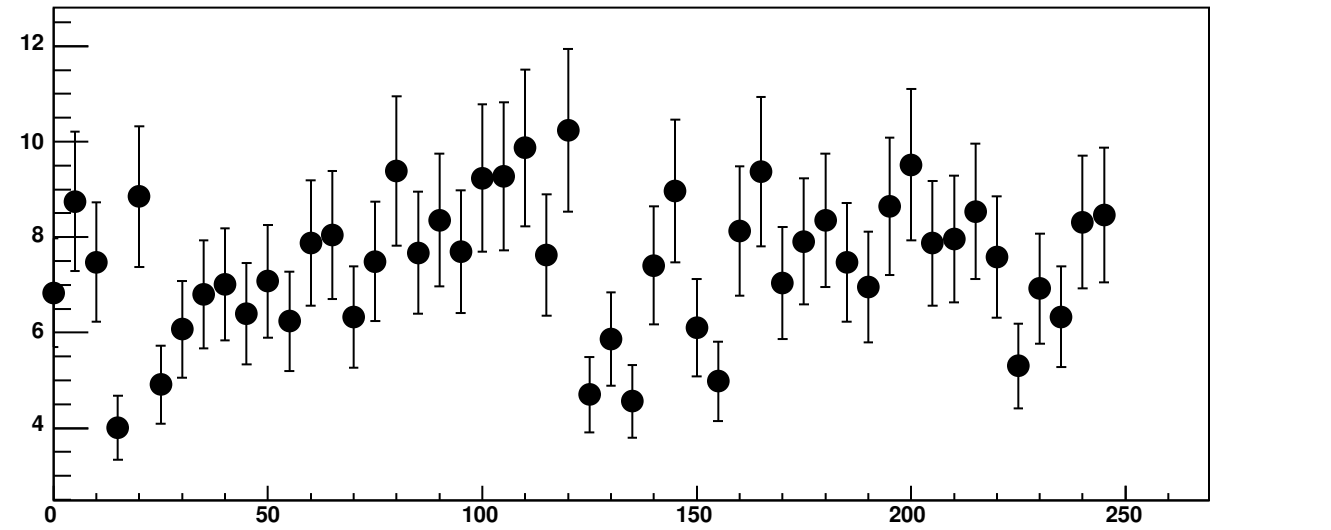
Chip 0, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold



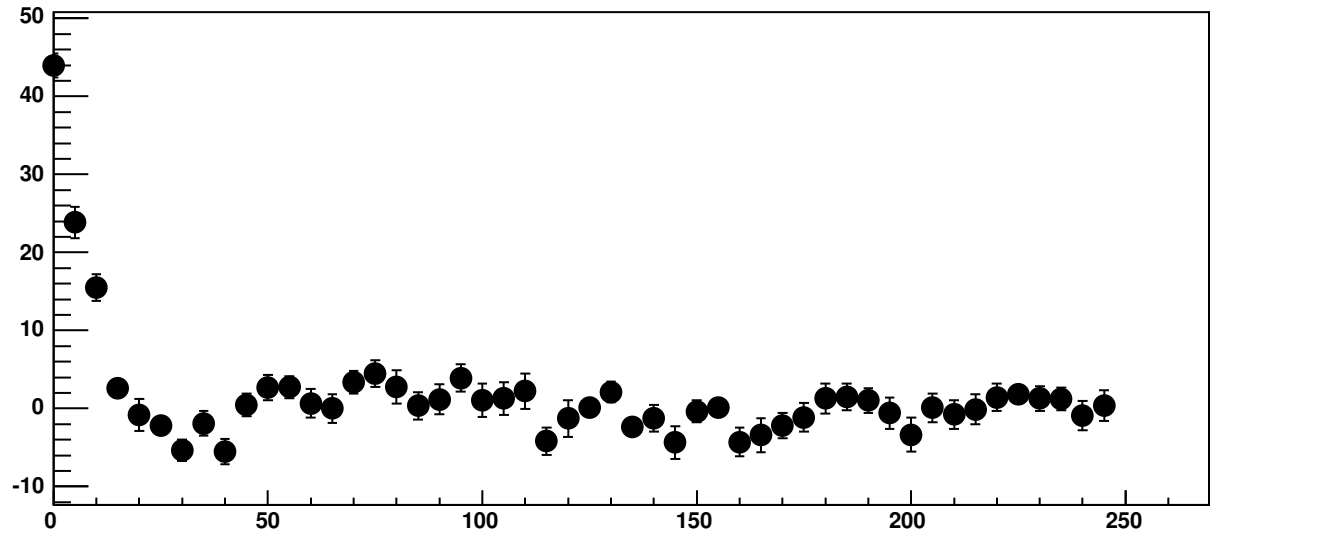
Chip 0, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold



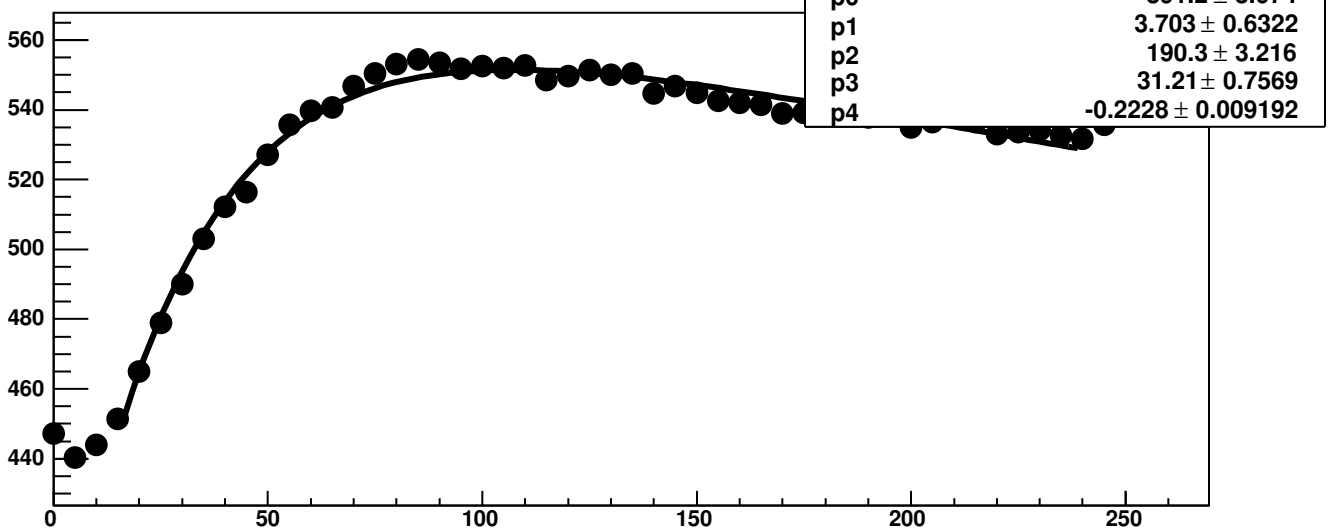
Chip 0, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold



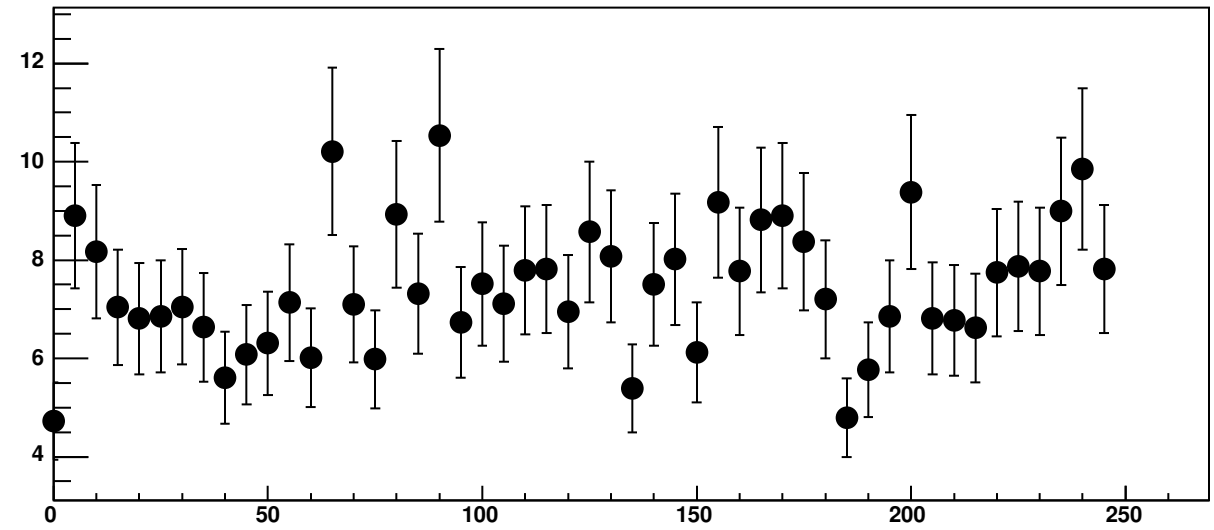
Chip 0, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold



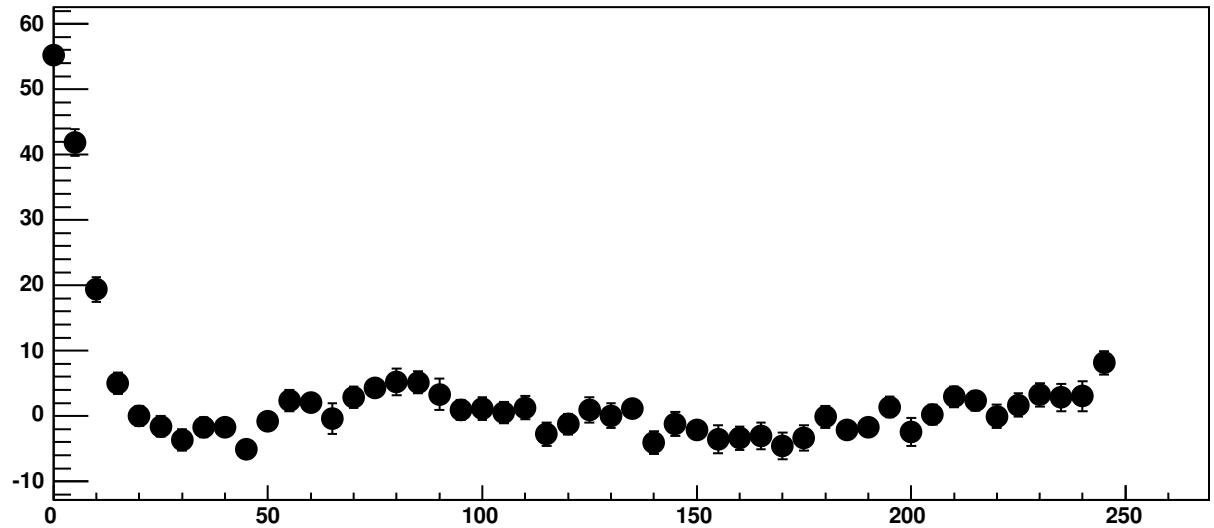
Chip 0, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold

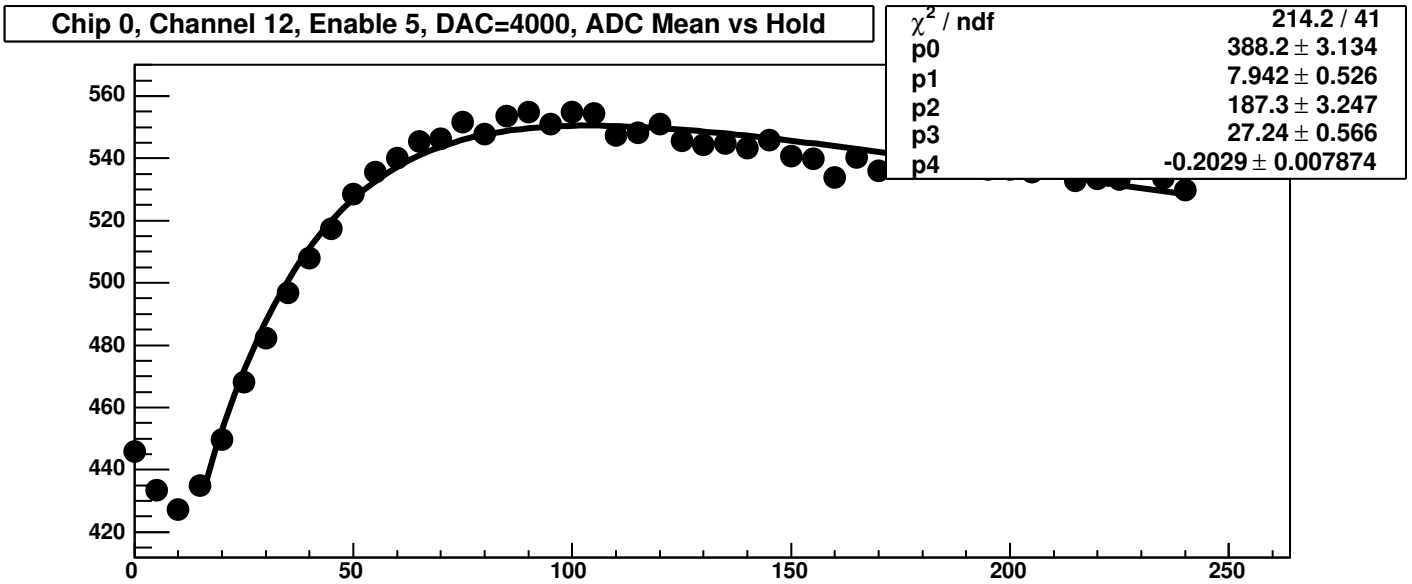


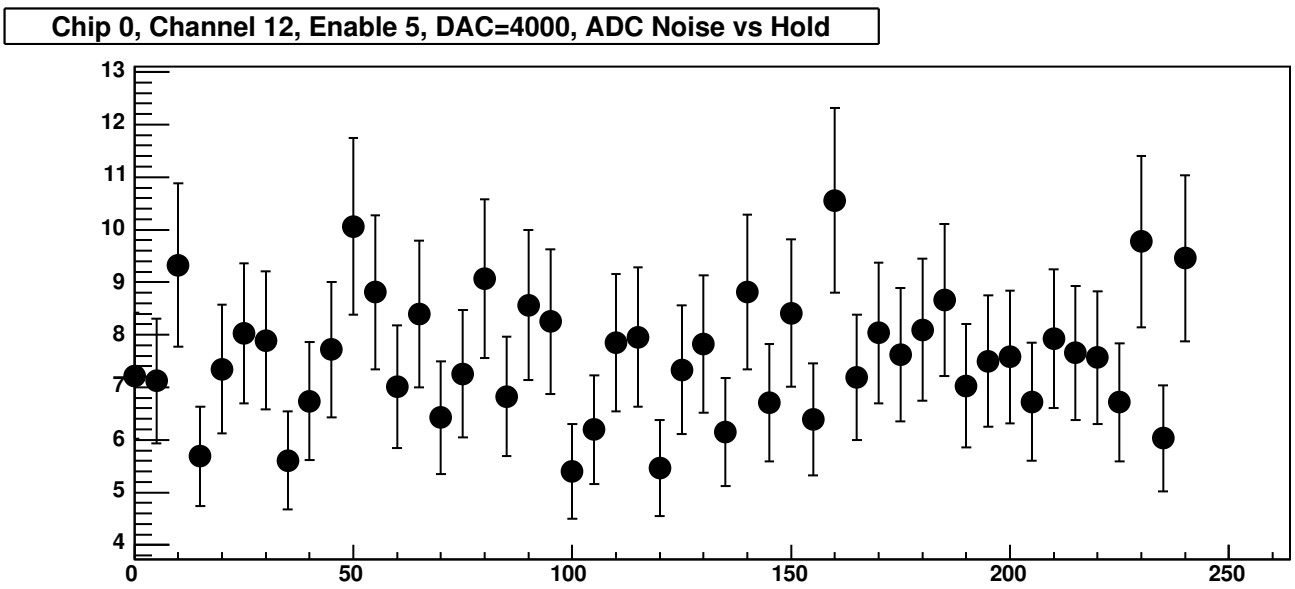
Chip 0, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold

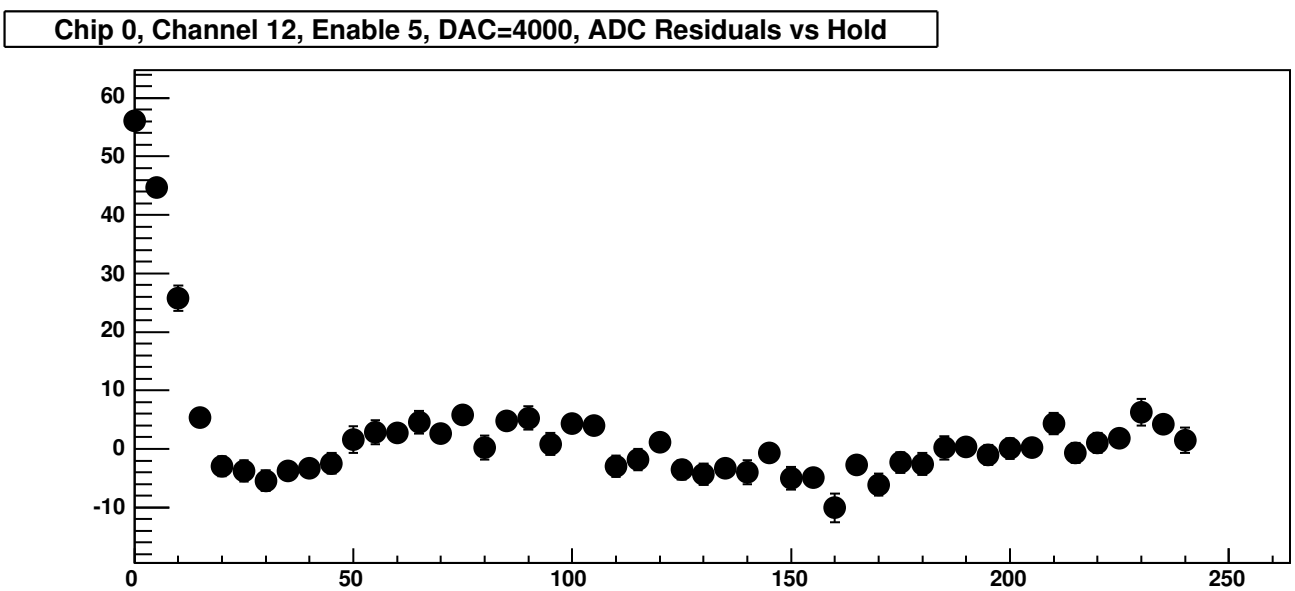


Chip 0, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold

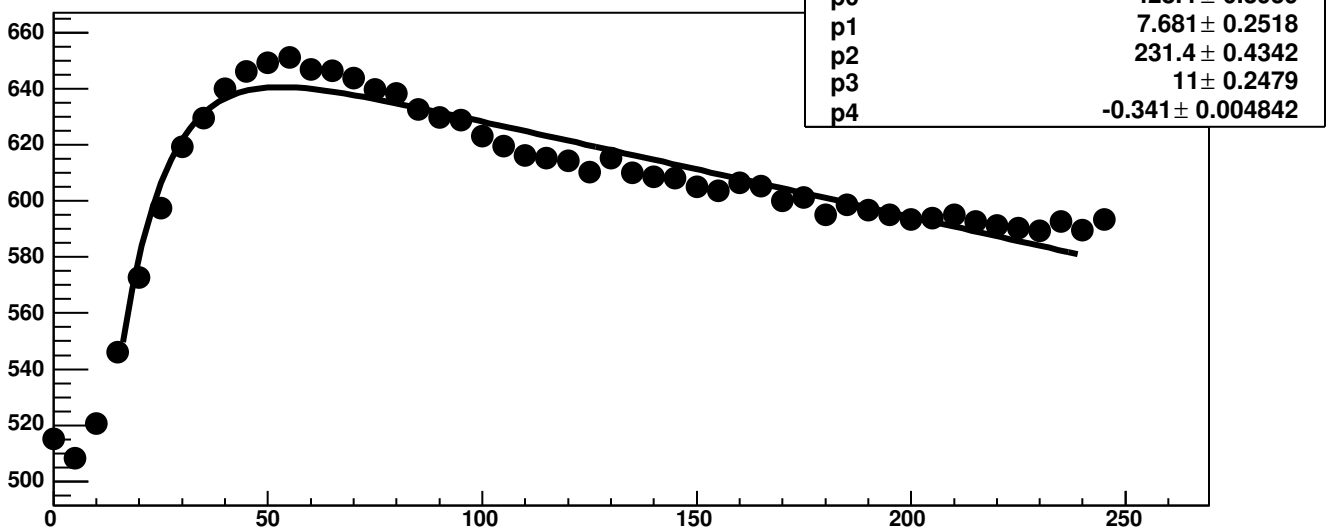



Chip 0, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 214.2 / 41
p0 388.2 ± 3.134
p1 7.942 ± 0.526
p2 187.3 ± 3.247
p3 27.24 ± 0.566
p4 -0.2029 ± 0.007874
560
540
520
500
480
460
440
420
0
50
100
150
200
250

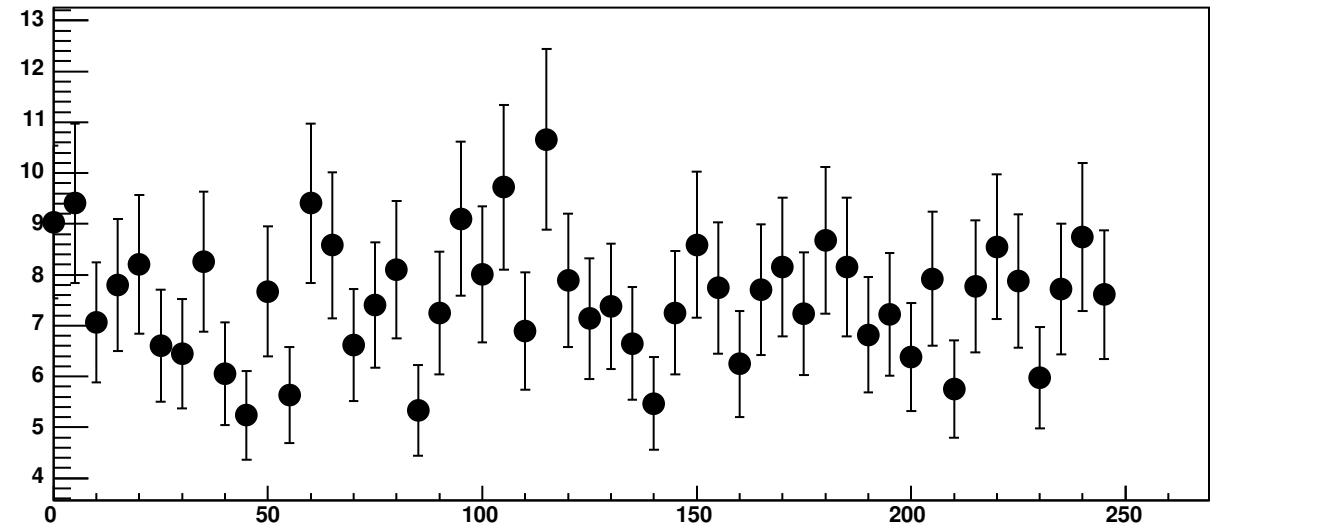

Chip 0, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 0, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

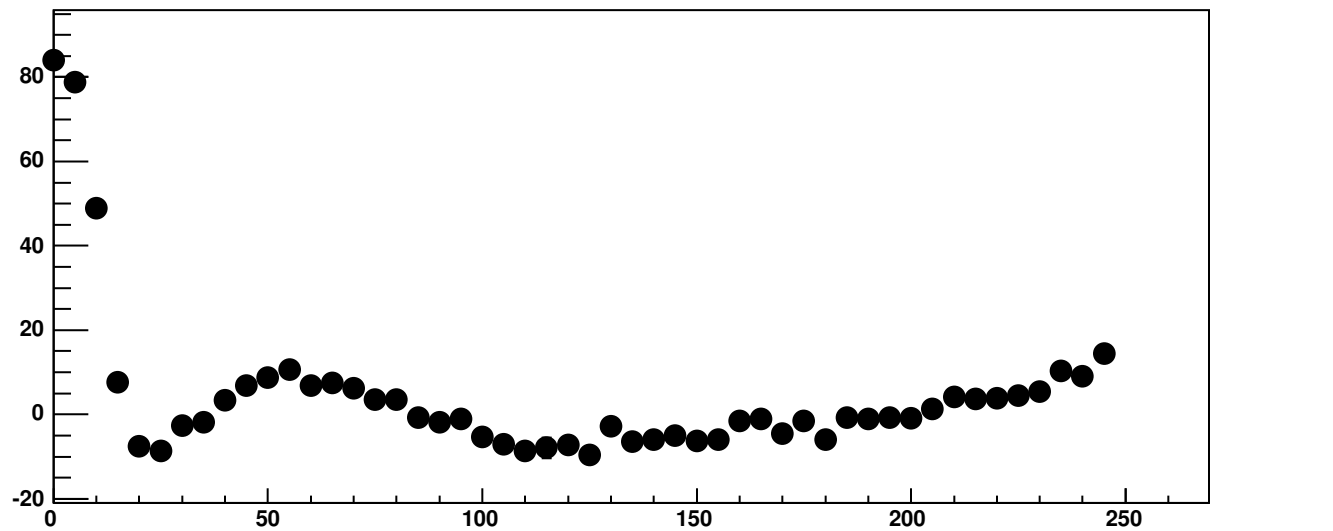
Chip 0, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold



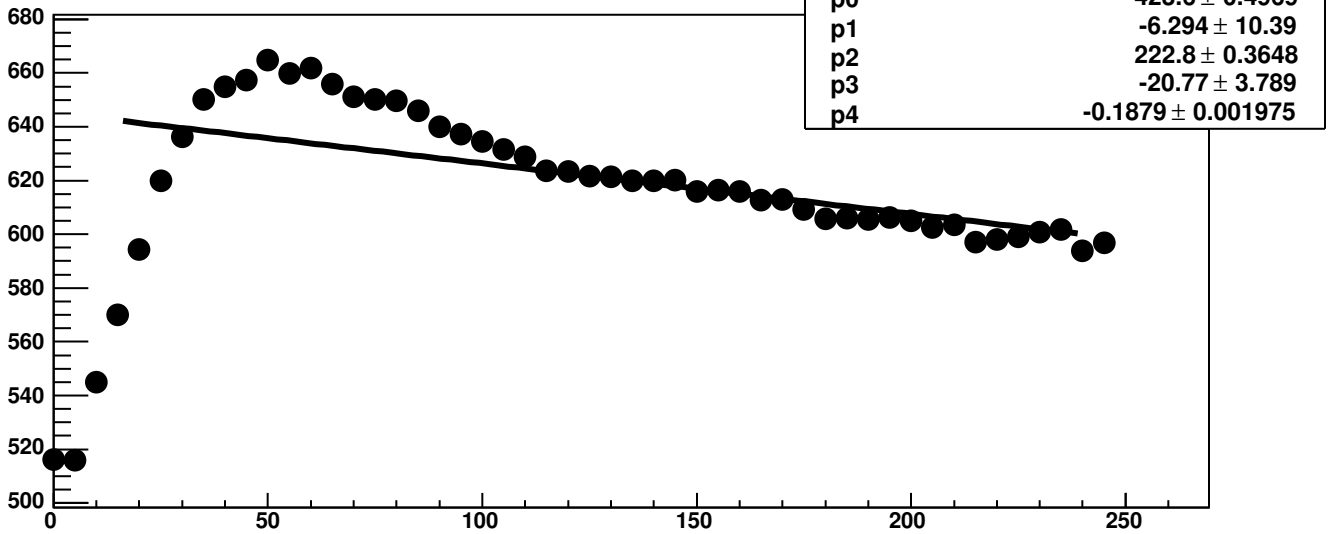
Chip 0, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



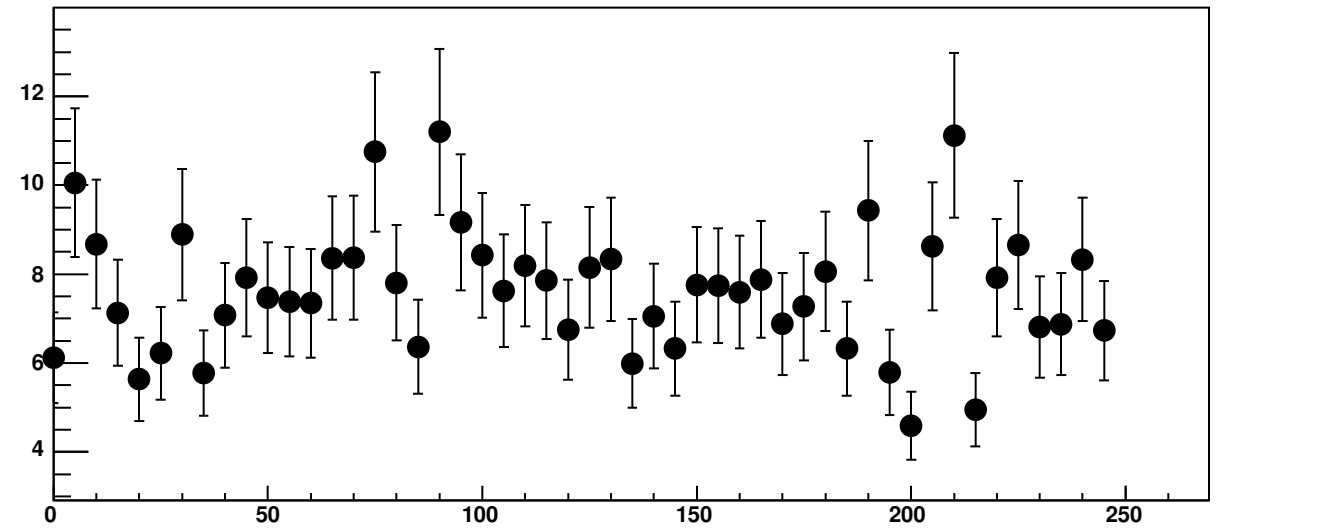
Chip 0, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold



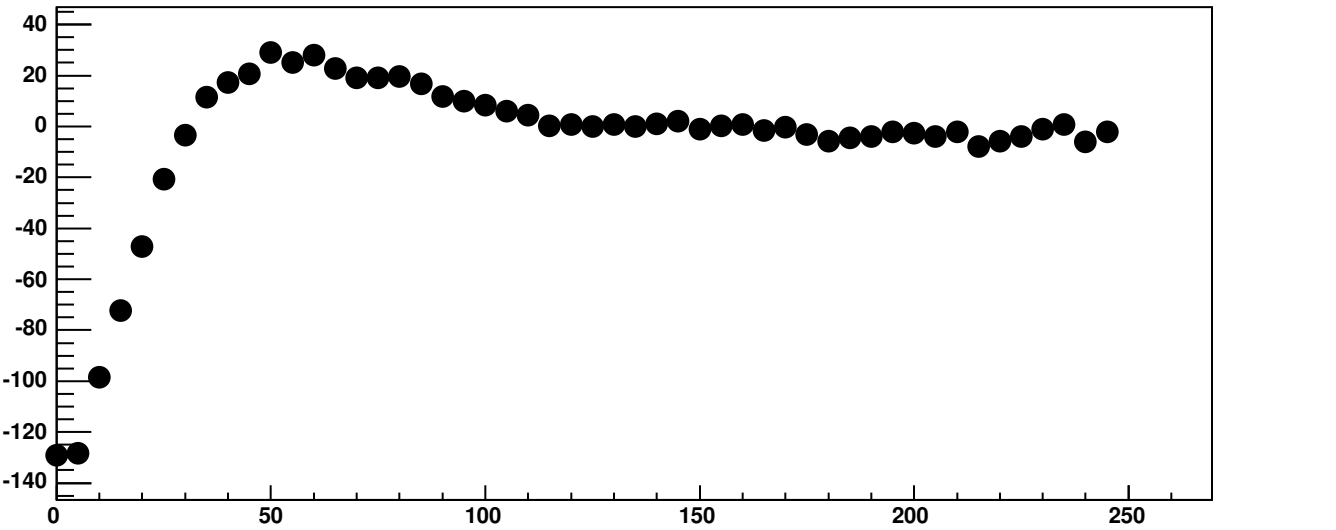
Chip 0, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold



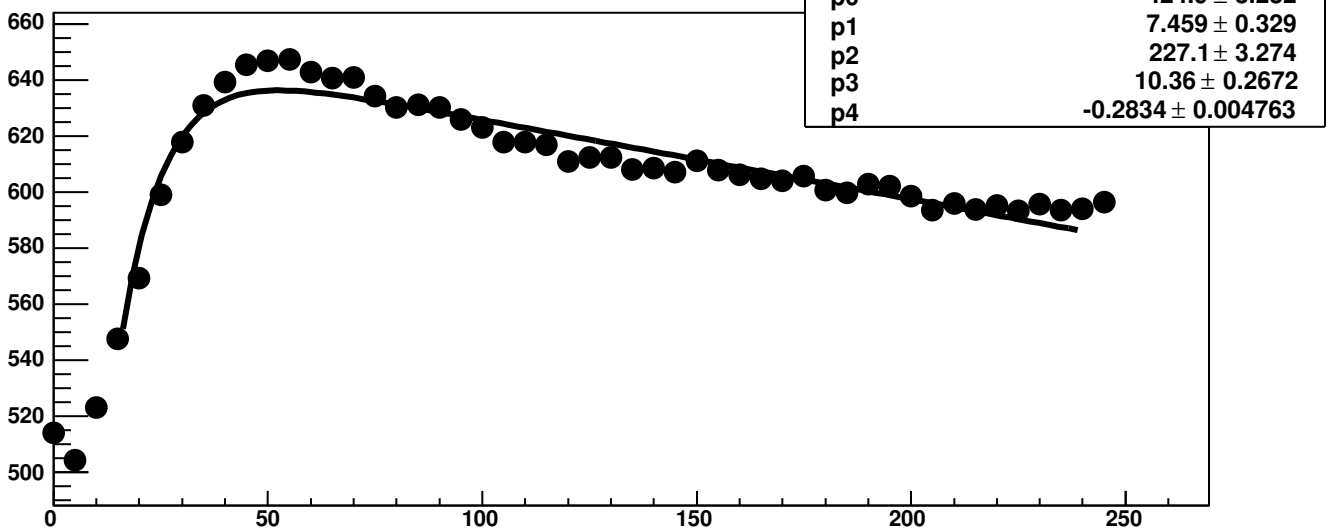
Chip 0, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold



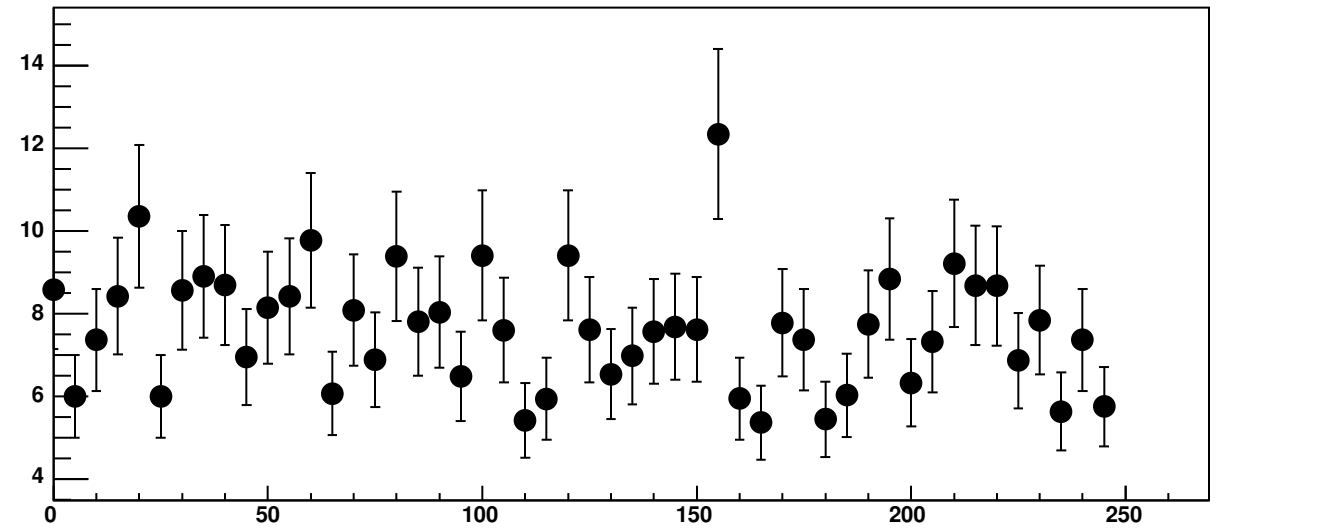
Chip 0, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold



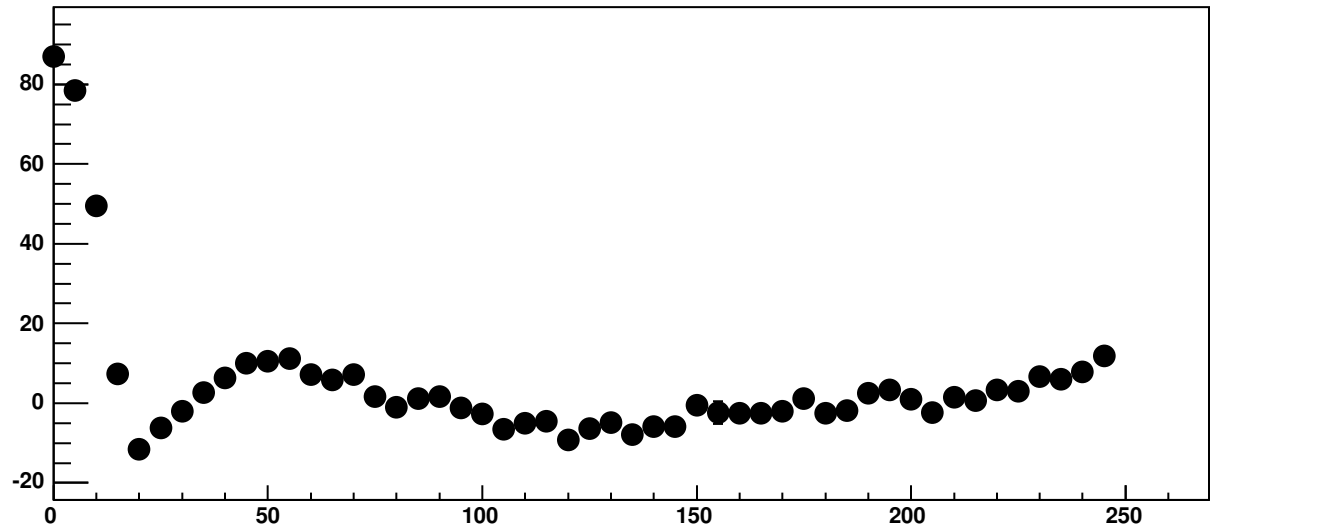
Chip 0, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold



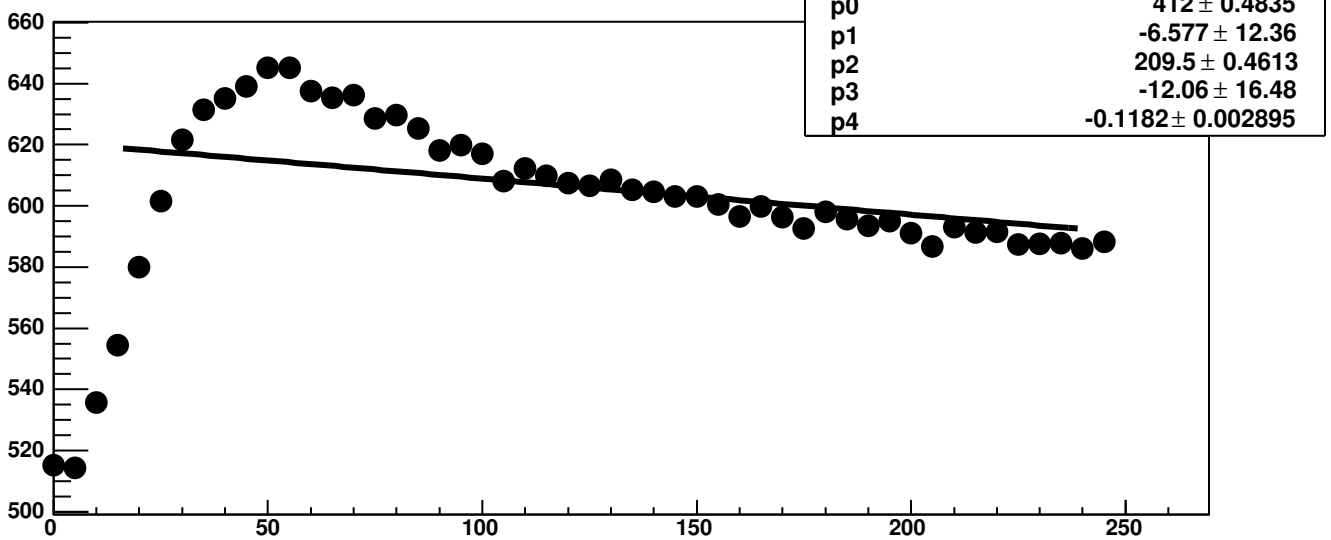
Chip 0, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



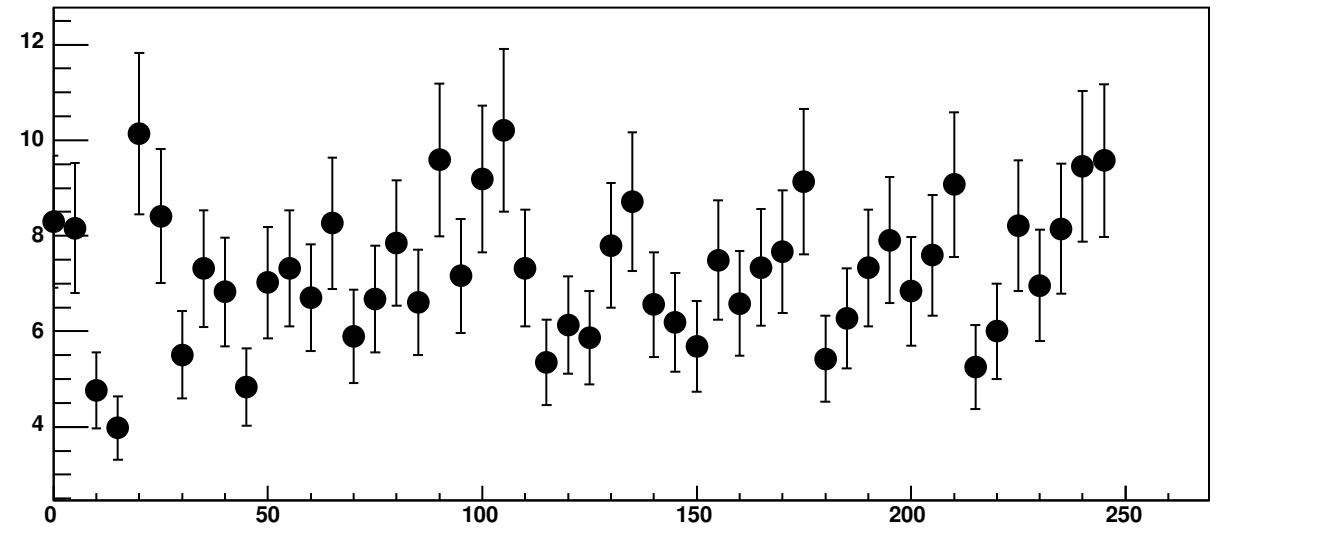
Chip 0, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold



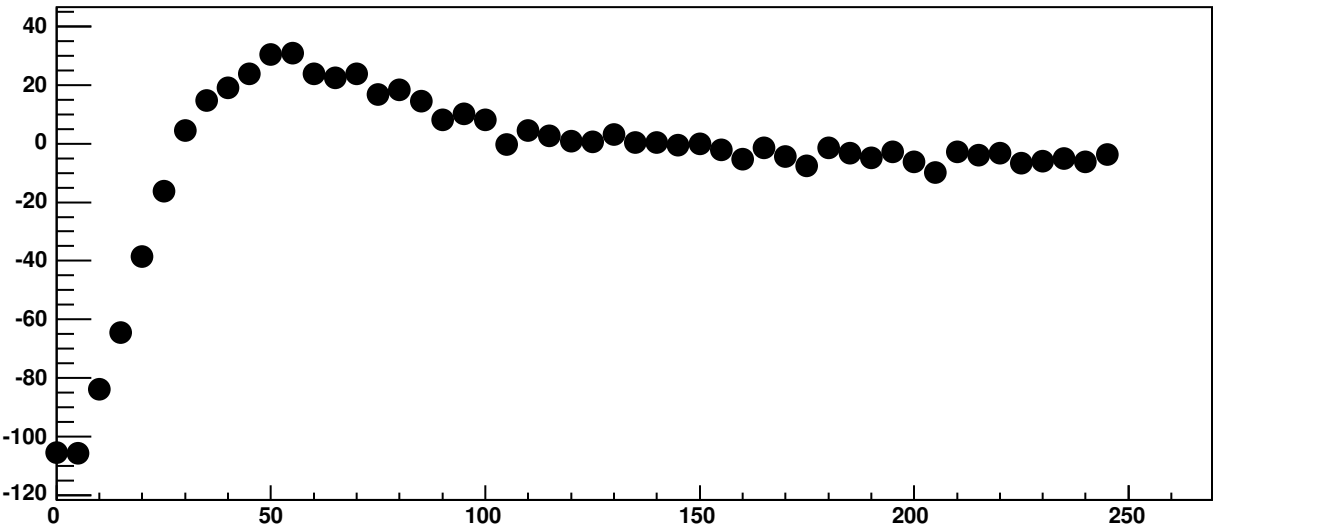
Chip 0, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

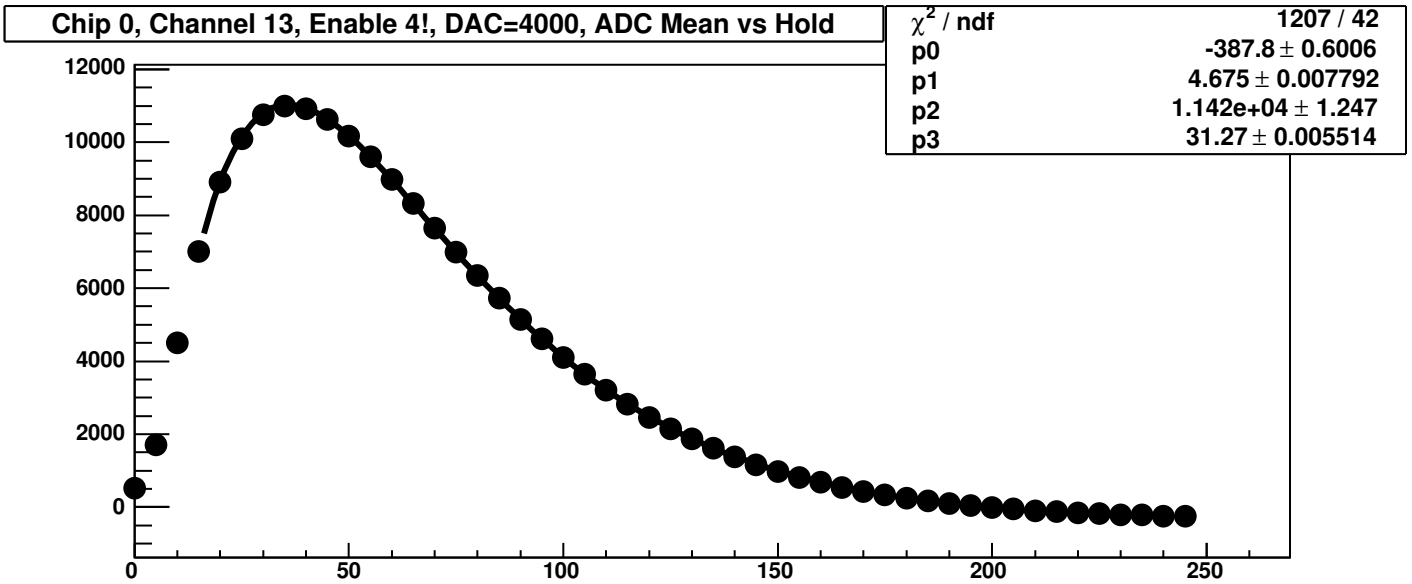


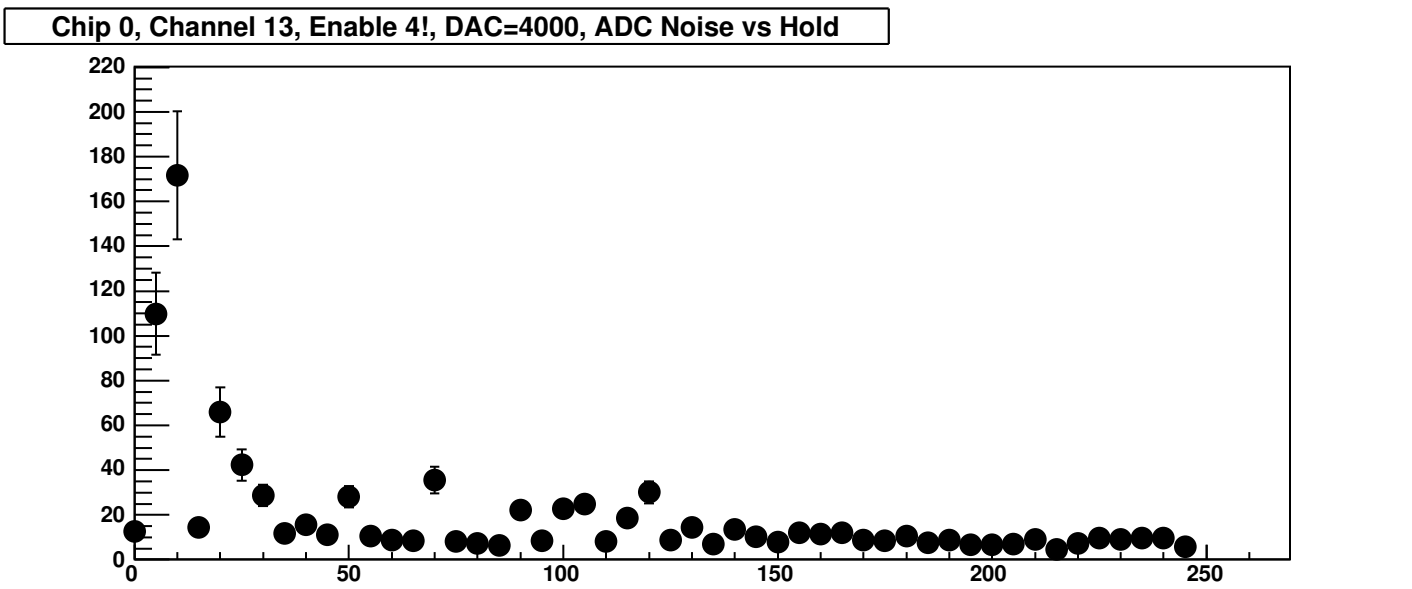
Chip 0, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold

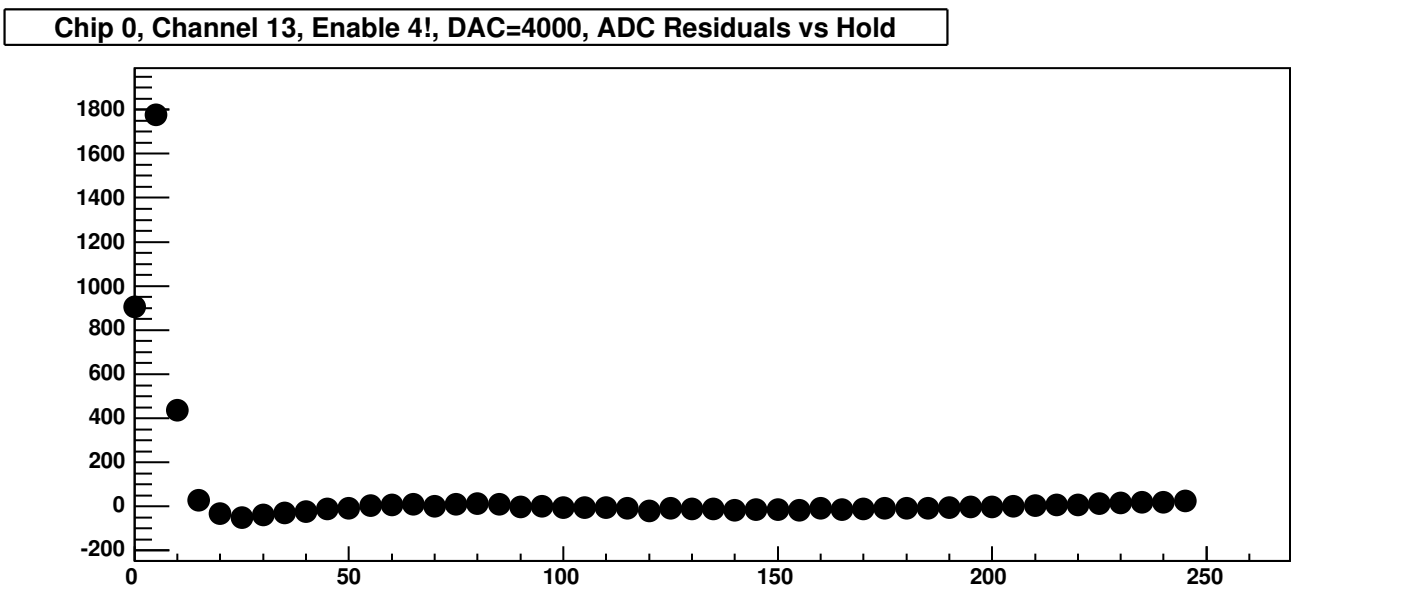


Chip 0, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

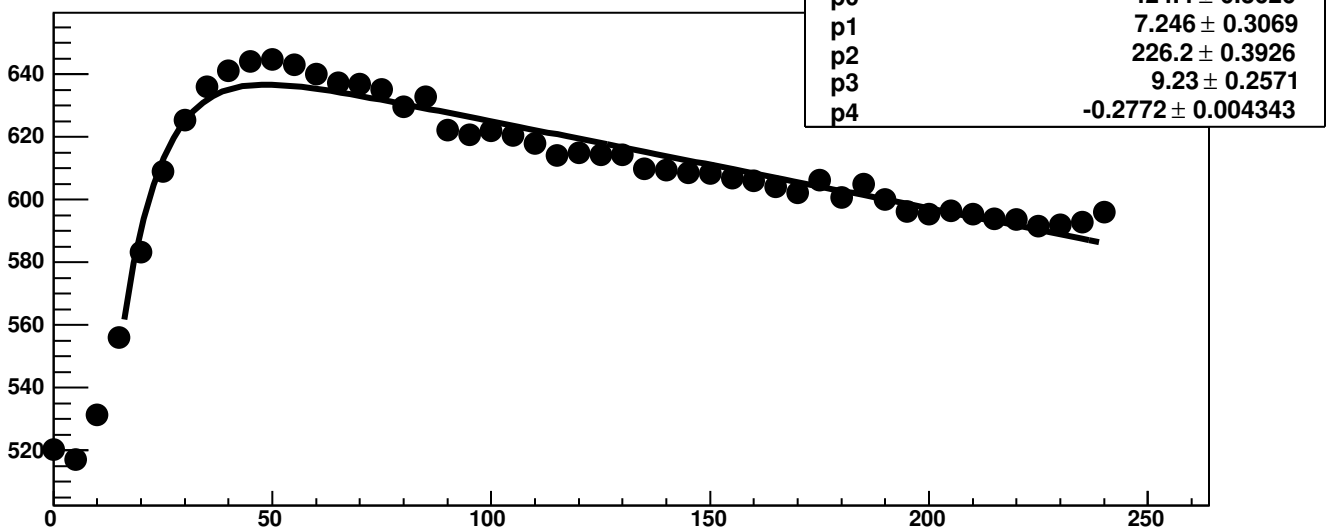



Chip 0, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1207 / 42
p0 -387.8 ± 0.6006
p1 4.675 ± 0.007792
p2 1.142e+04 ± 1.247
p3 31.27 ± 0.005514
12000
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

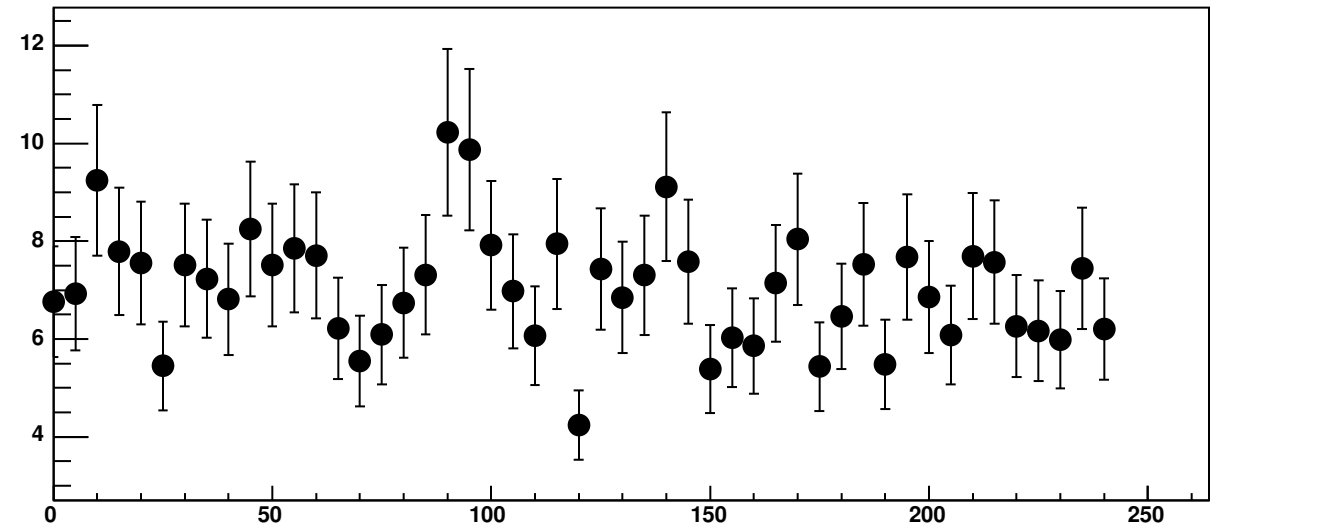

Chip 0, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

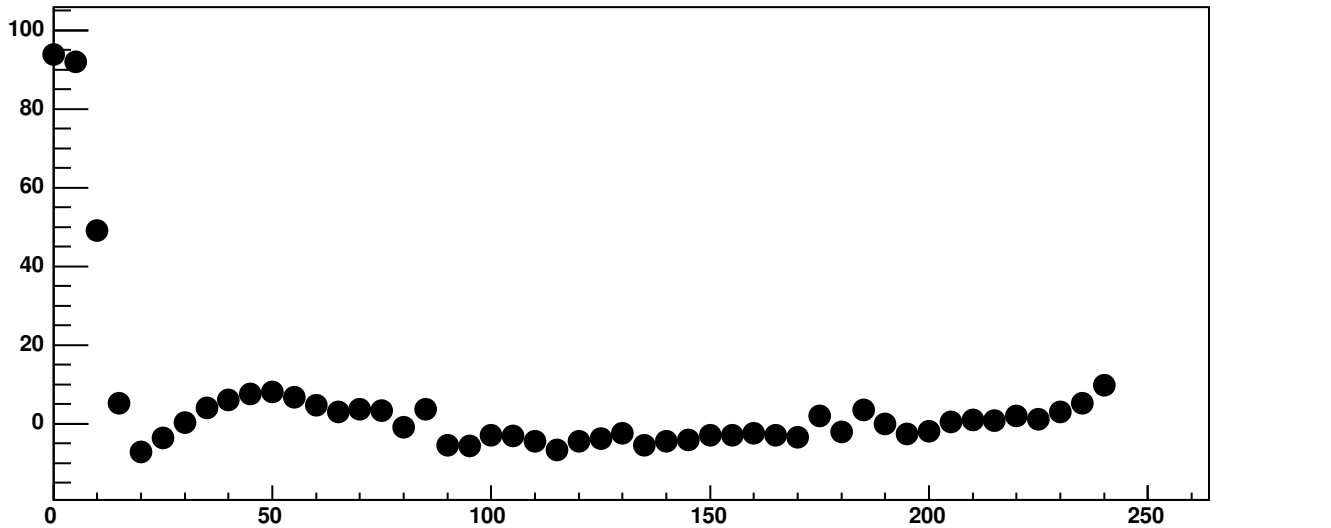
Chip 0, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

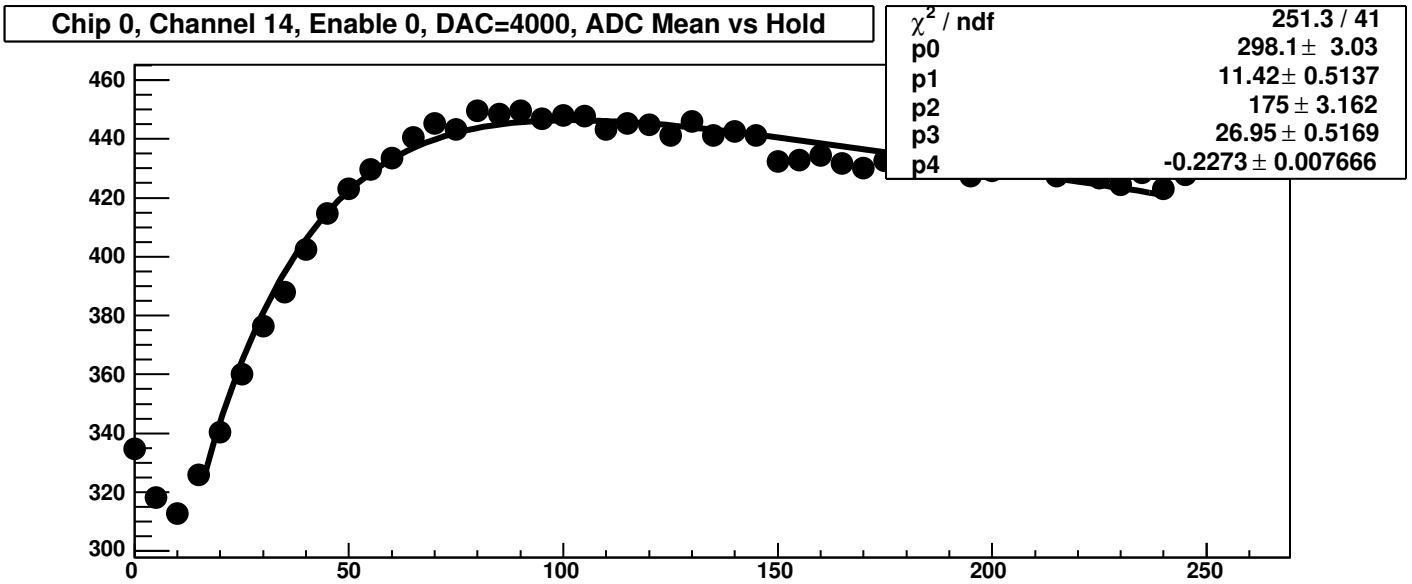


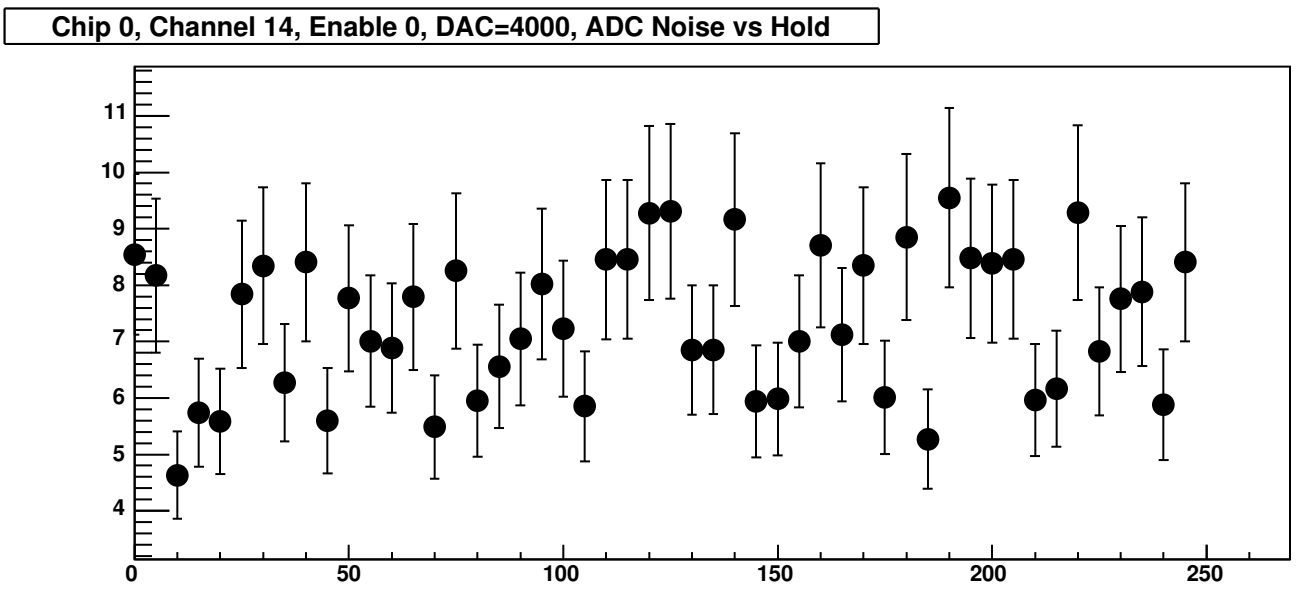
Chip 0, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold

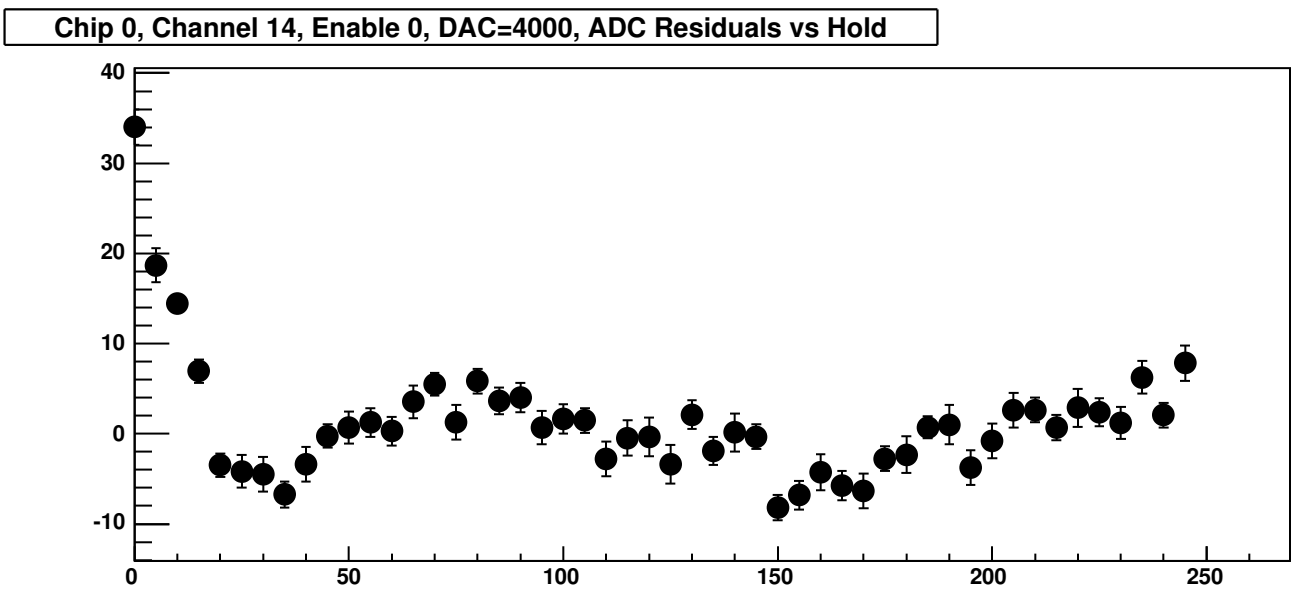


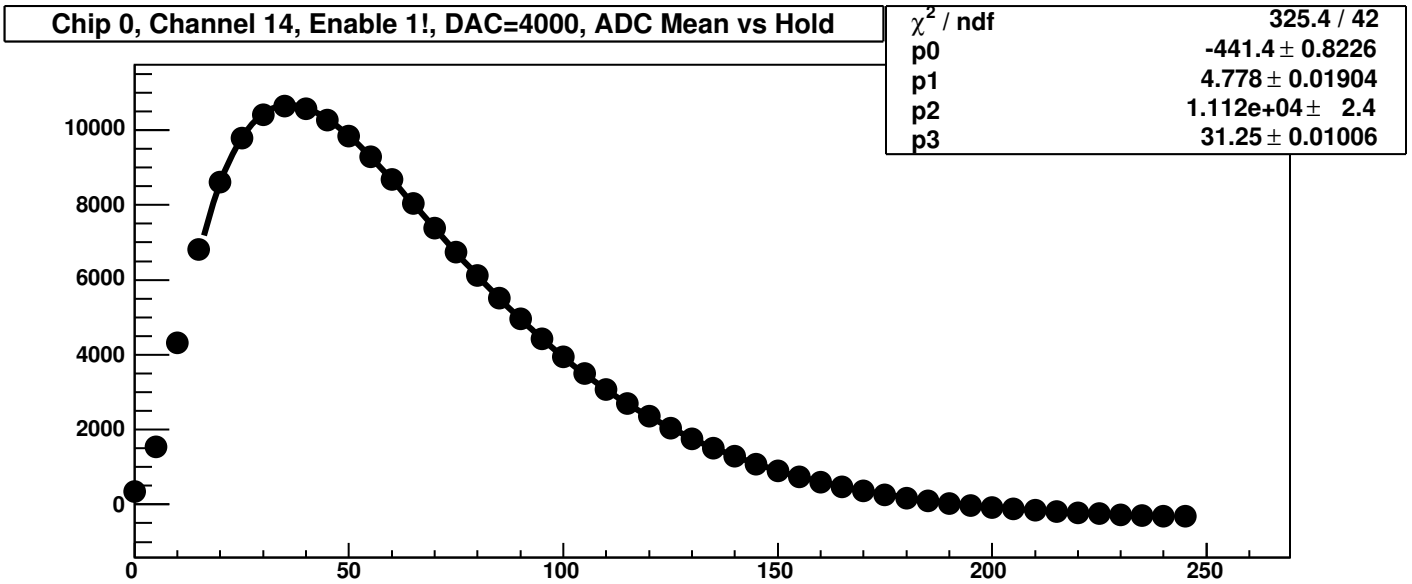
Chip 0, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold

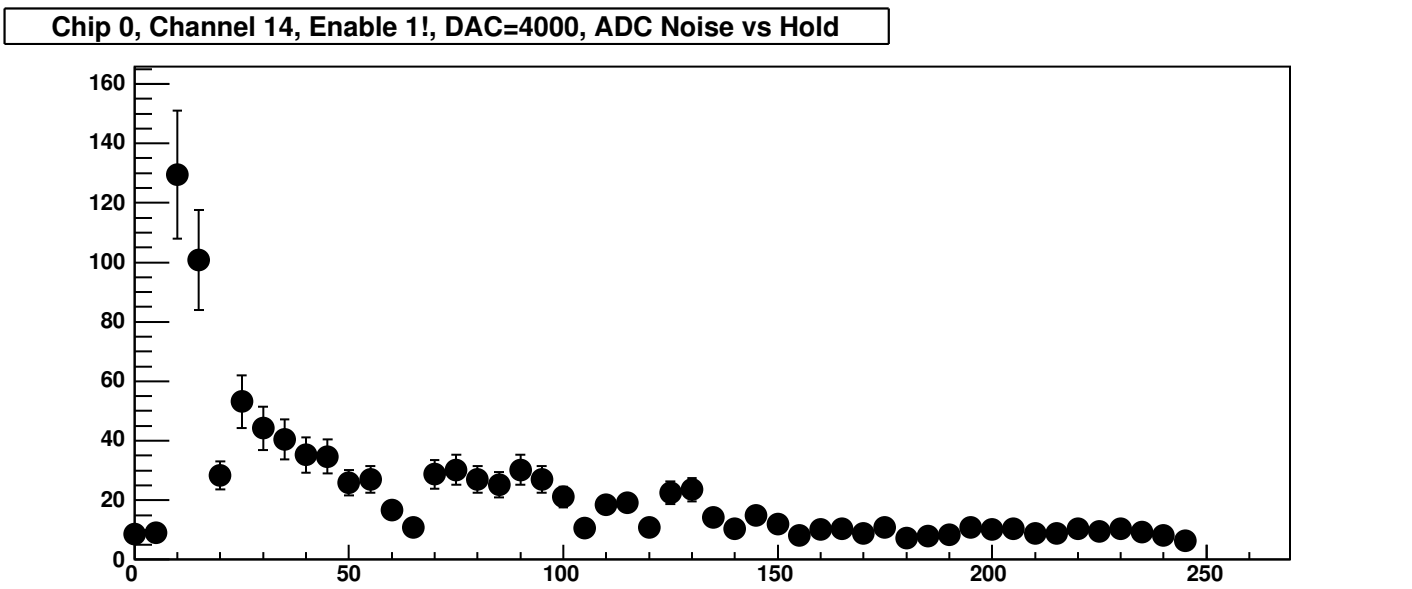


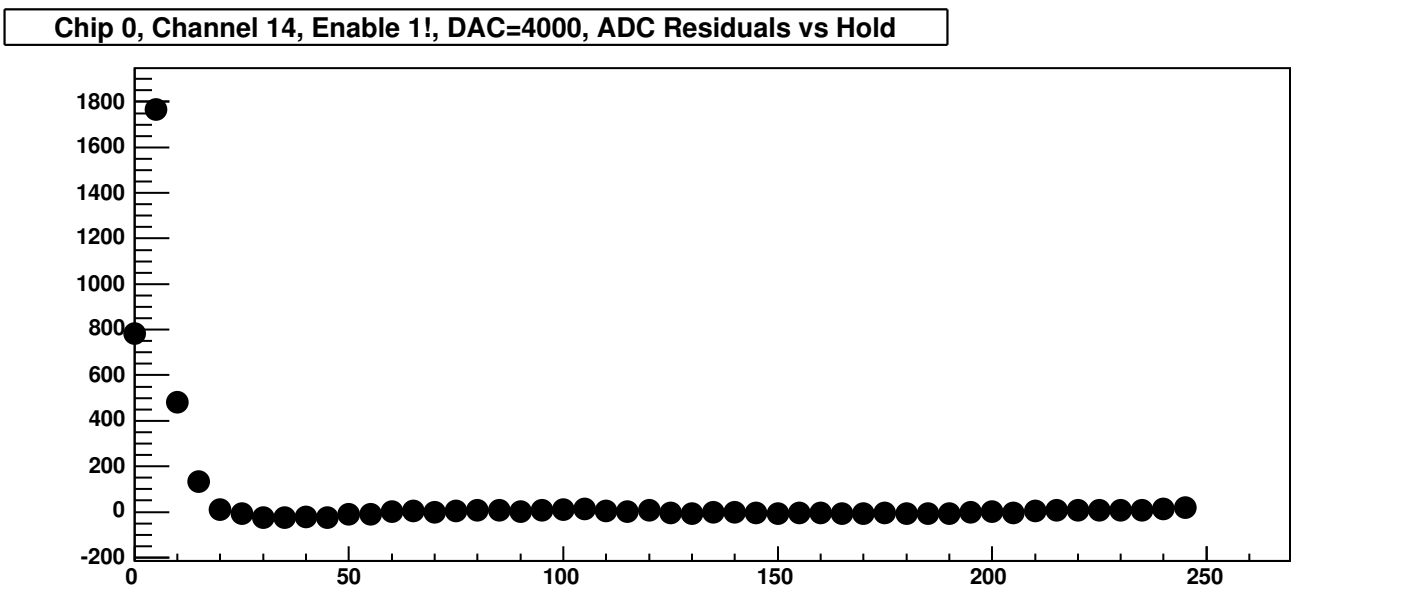

Chip 0, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 251.3 / 41
p0 298.1 ± 3.03
p1 11.42 ± 0.5137
p2 175 ± 3.162
p3 26.95 ± 0.5169
p4 -0.2273 ± 0.007666
460
440
420
400
380
360
340
320
300
0
50
100
150
200
250


Chip 0, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250

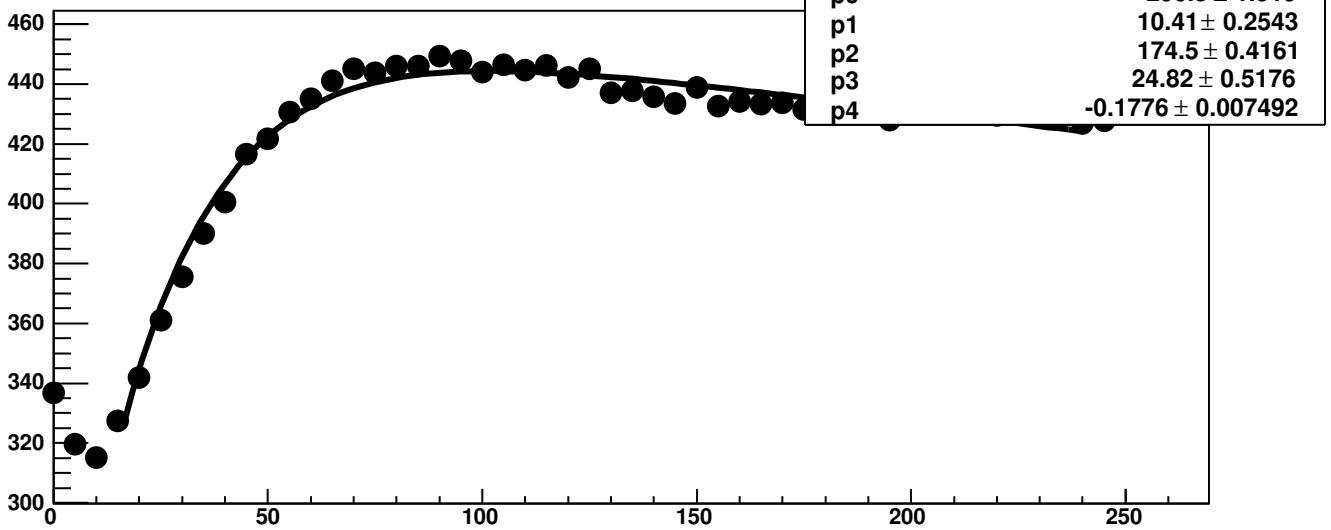

Chip 0, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250


Chip 0, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 325.4 / 42
p0 -441.4 ± 0.8226
p1 4.778 ± 0.01904
p2 1.112e+04 ± 2.4
p3 31.25 ± 0.01006
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

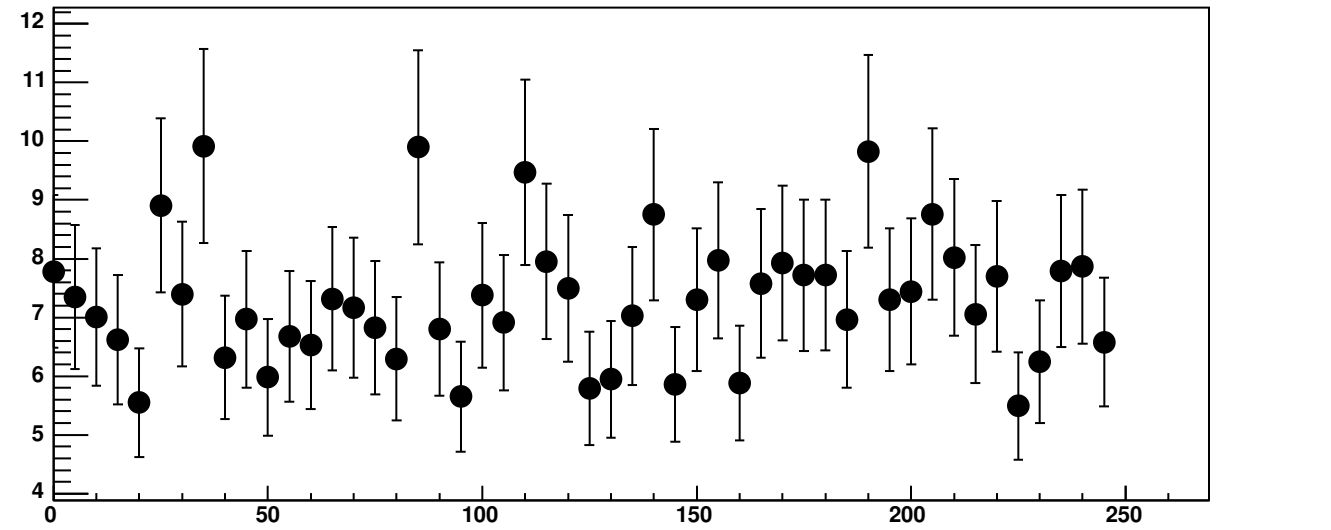

Chip 0, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

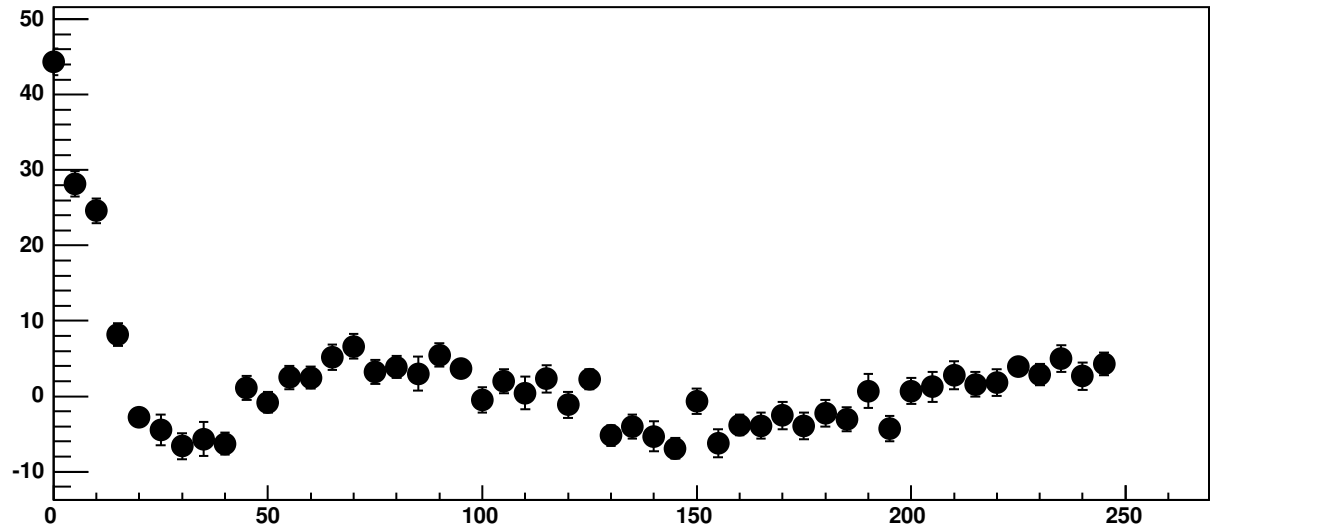
Chip 0, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold



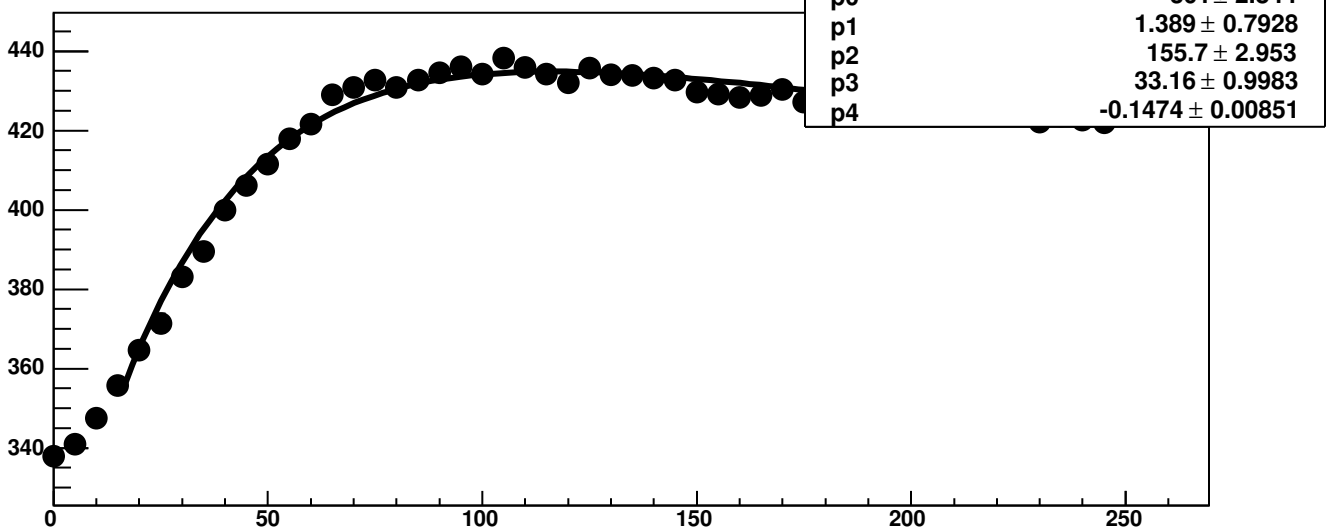
Chip 0, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



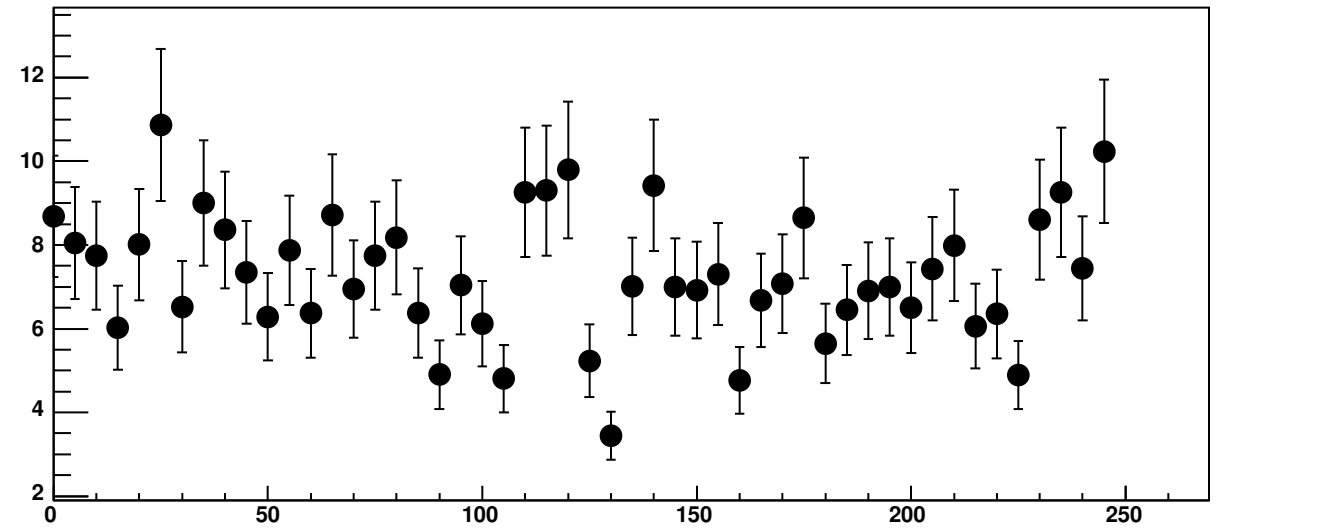
Chip 0, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold



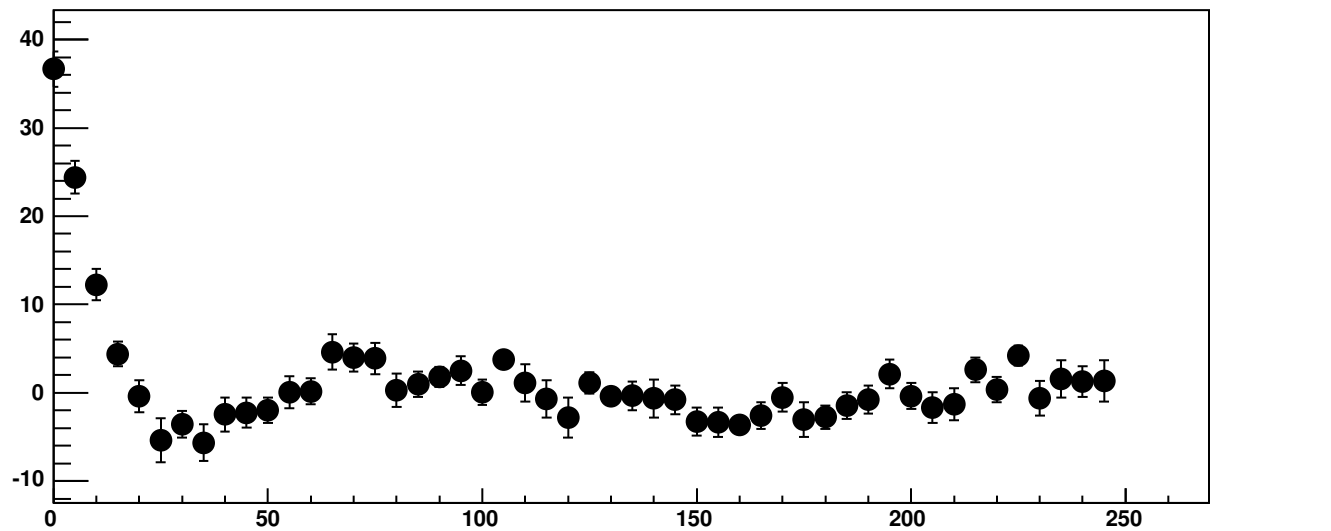
Chip 0, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

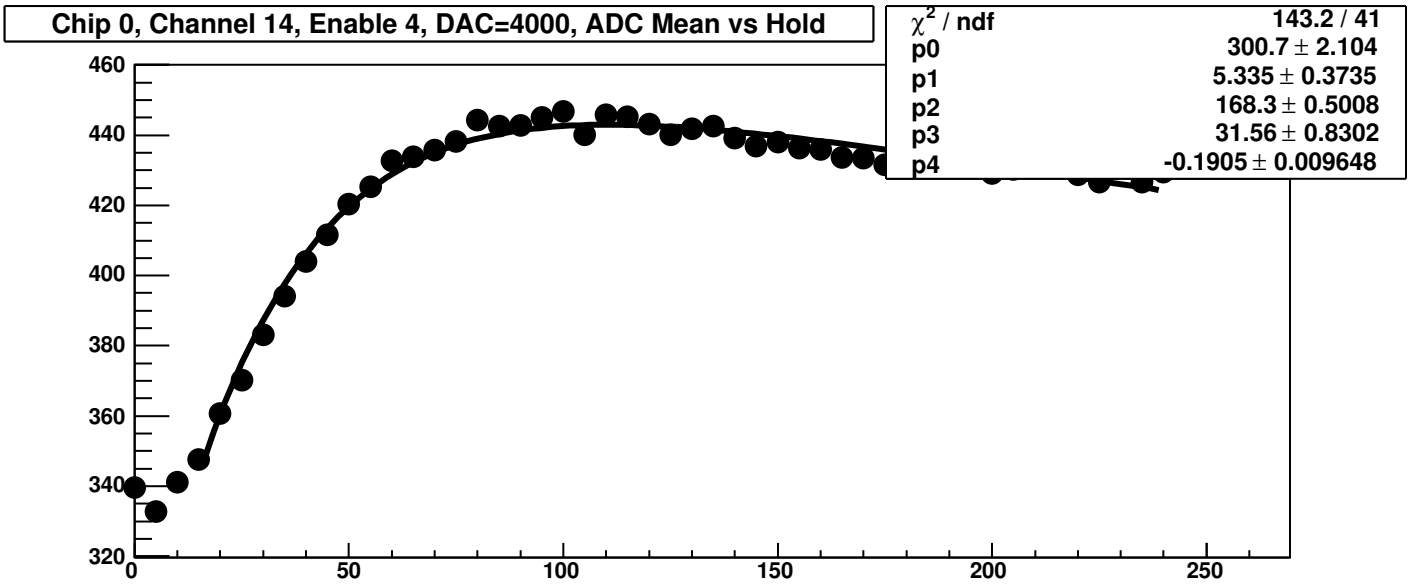


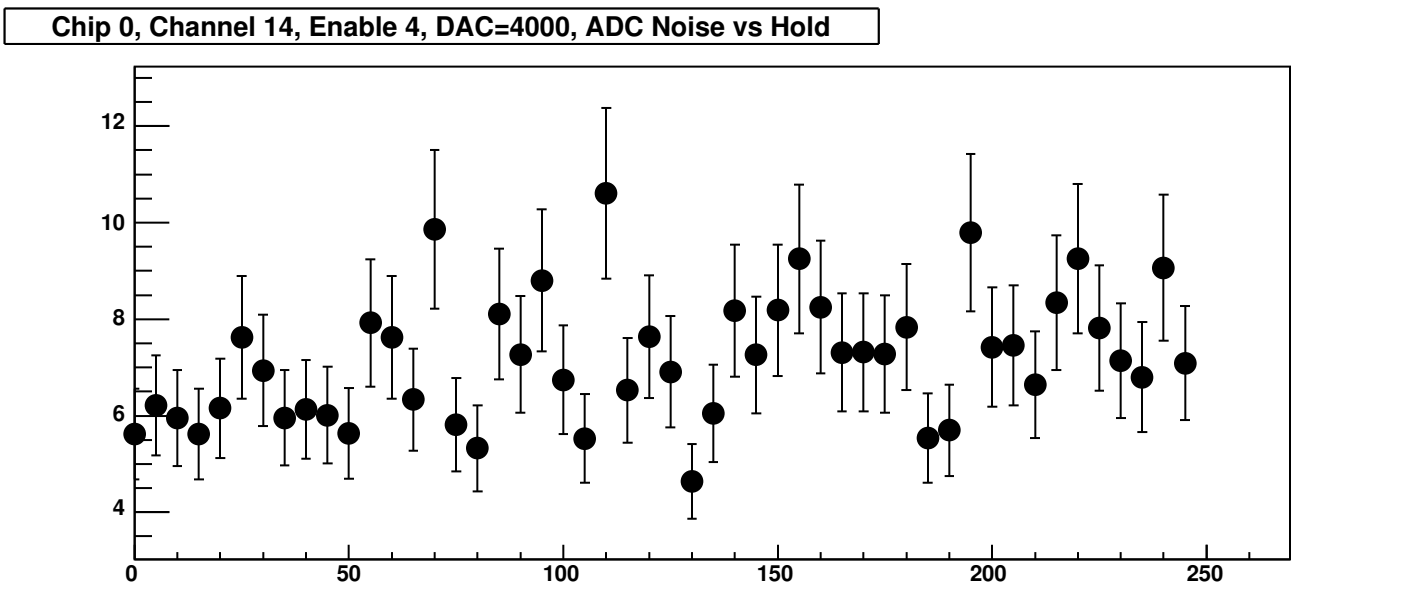
Chip 0, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold

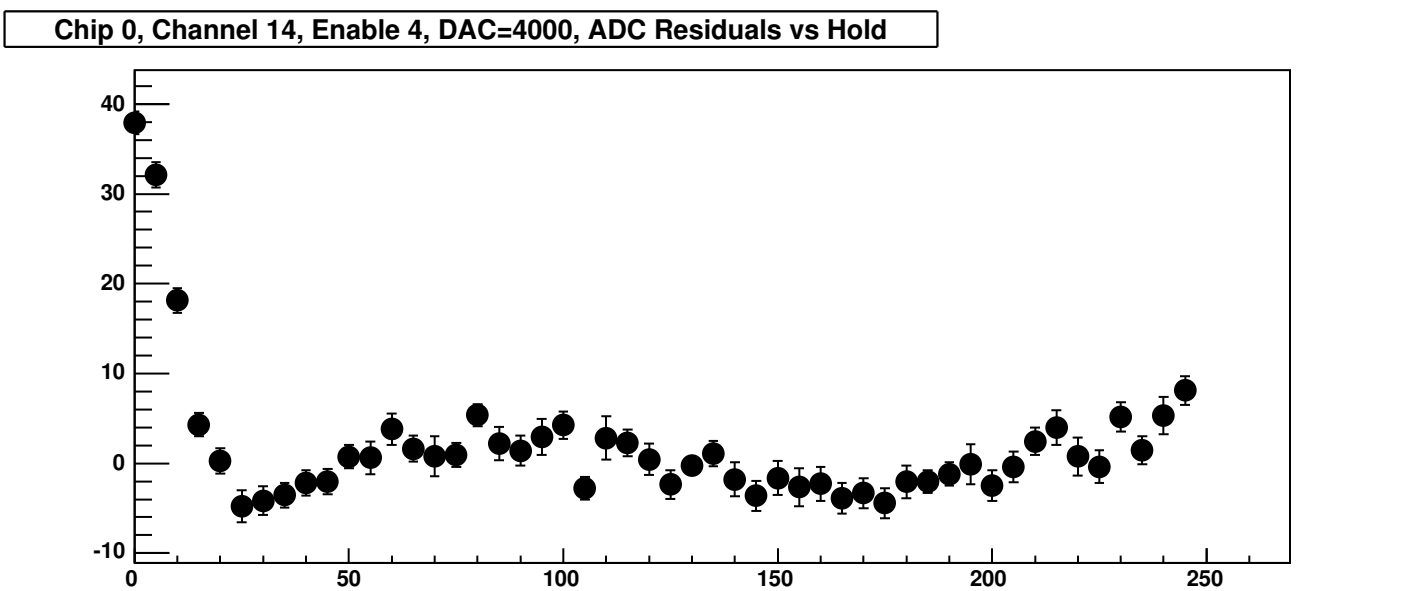


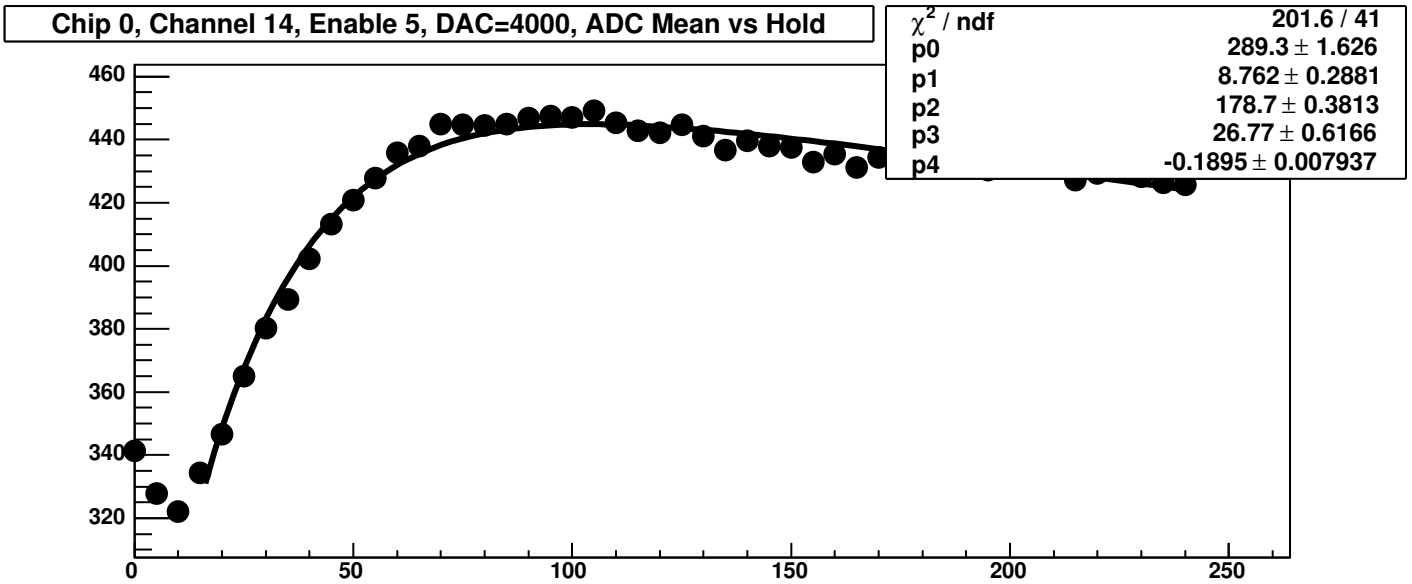
Chip 0, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

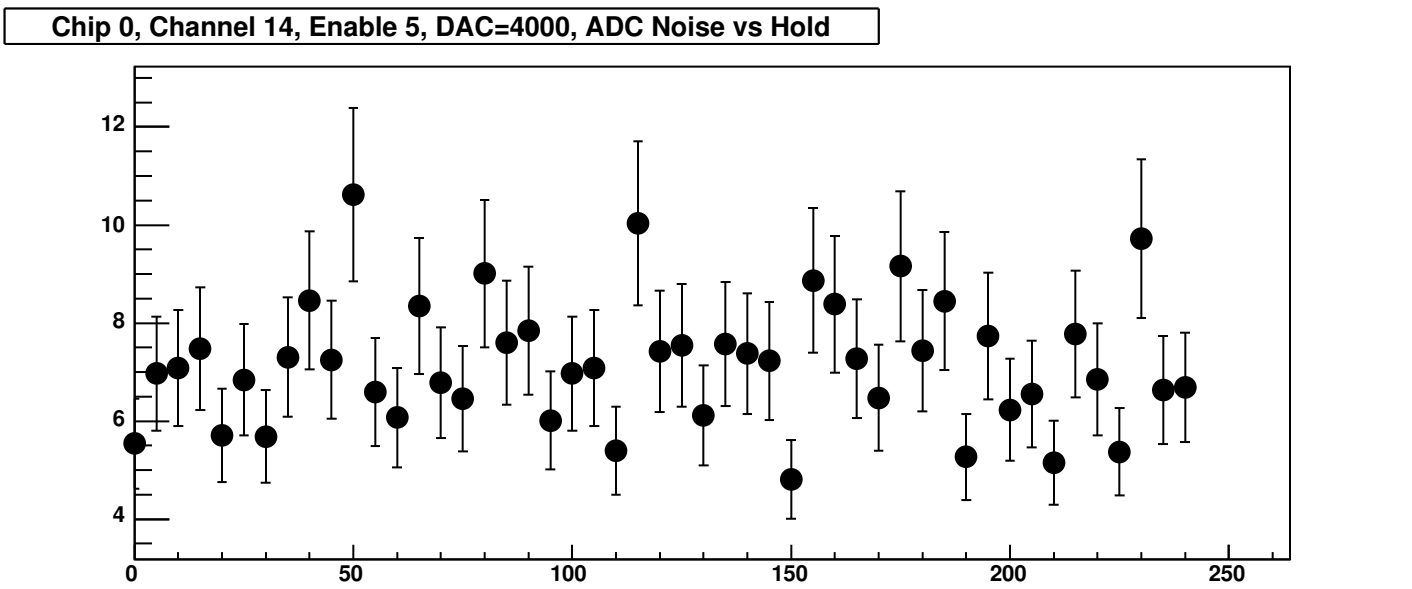


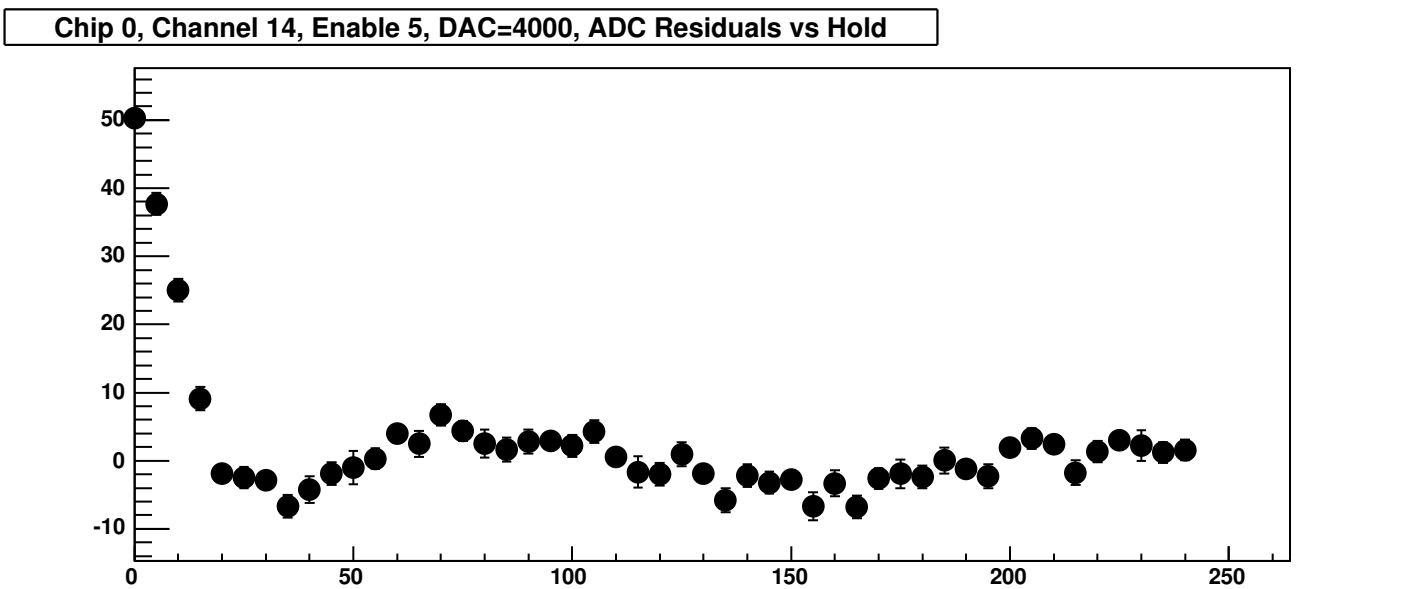

Chip 0, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 143.2 / 41
p0 300.7 ± 2.104
p1 5.335 ± 0.3735
p2 168.3 ± 0.5008
p3 31.56 ± 0.8302
p4 -0.1905 ± 0.009648
460
440
420
400
380
360
340
320
0
50
100
150
200
250

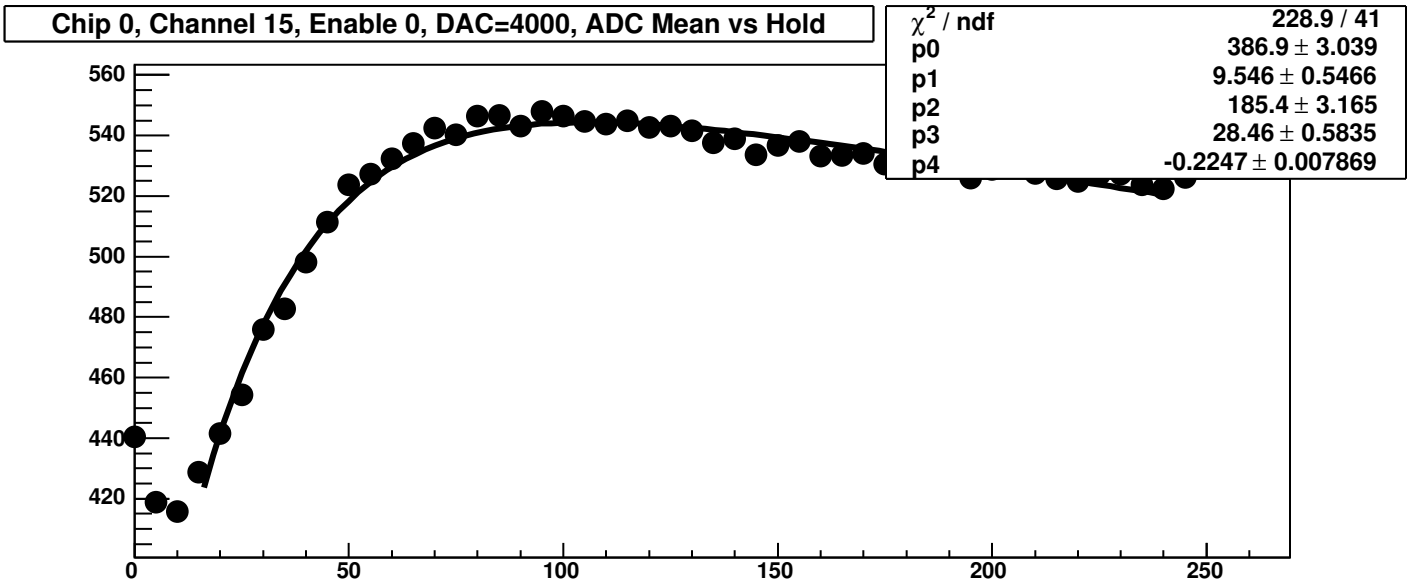

Chip 0, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

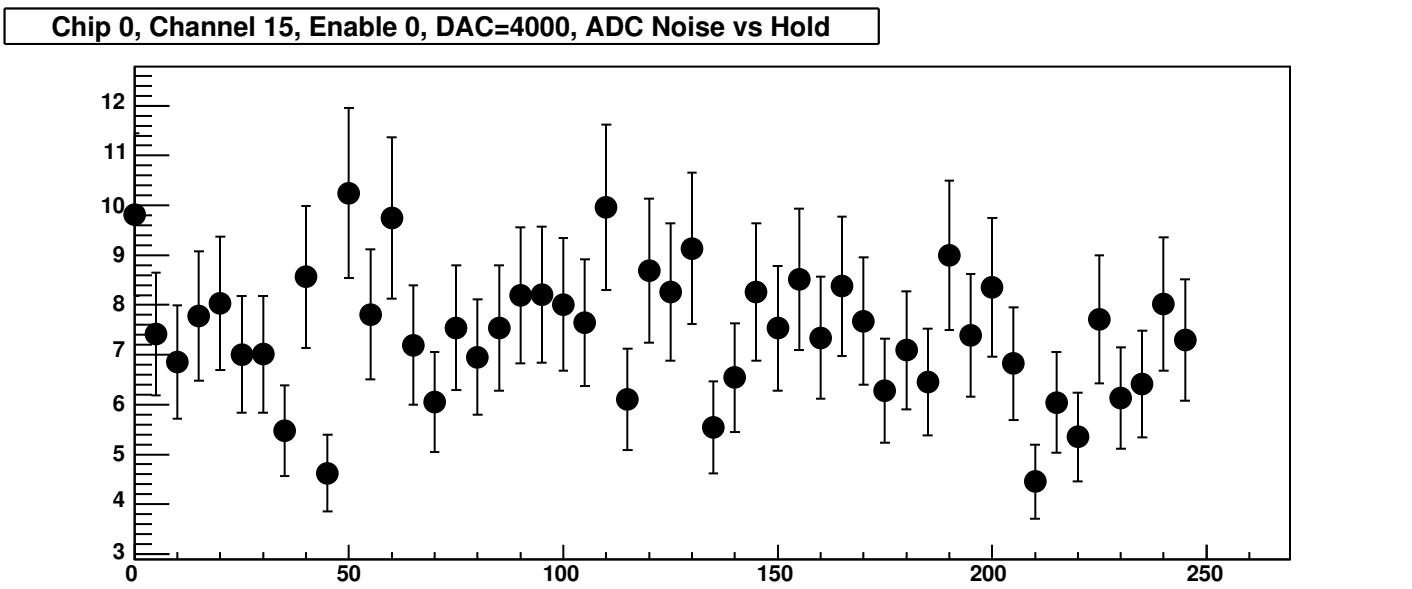

Chip 0, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

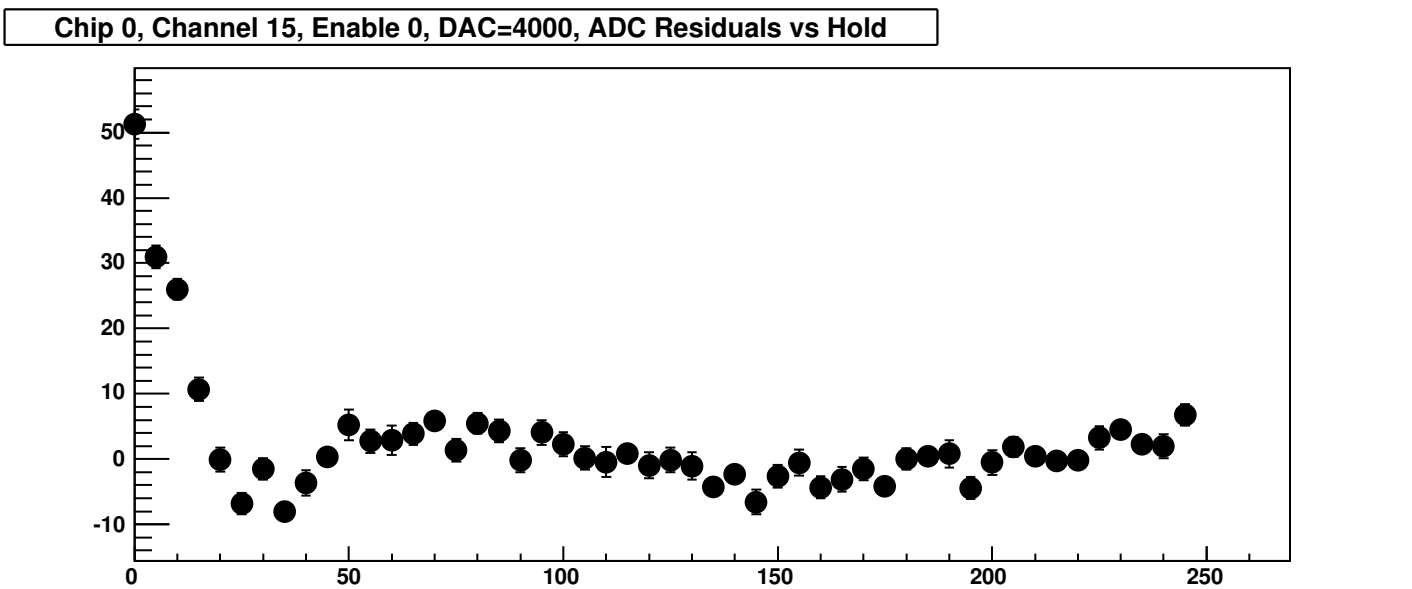

Chip 0, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 201.6 / 41
p0 289.3 ± 1.626
p1 8.762 ± 0.2881
p2 178.7 ± 0.3813
p3 26.77 ± 0.6166
p4 -0.1895 ± 0.007937
460
440
420
400
380
360
340
320
0
50
100
150
200
250


Chip 0, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

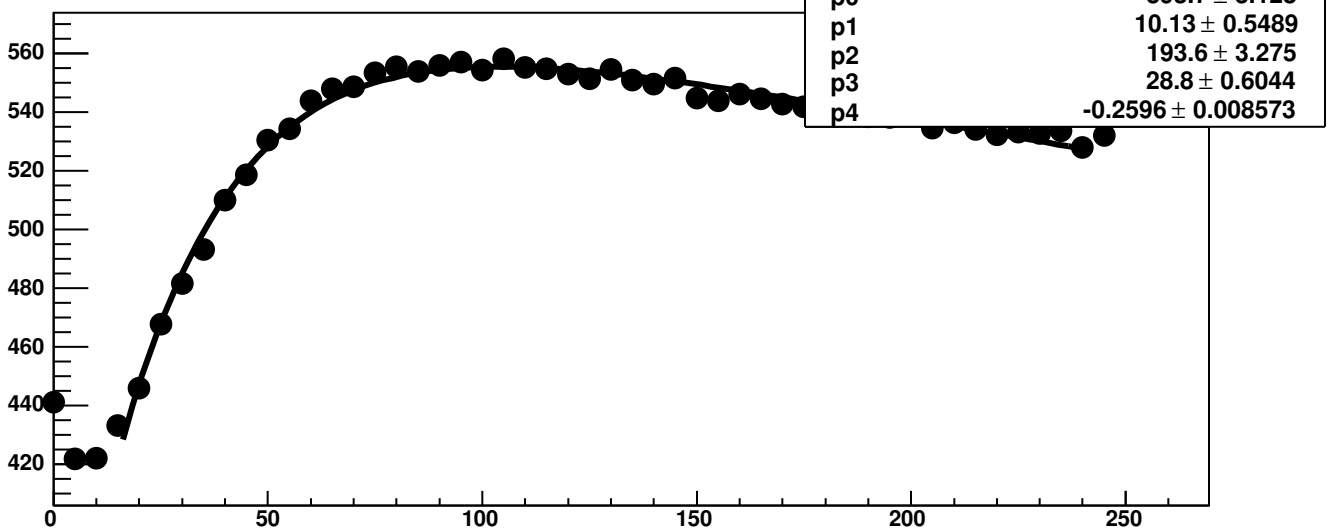

Chip 0, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250


Chip 0, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 228.9 / 41
p0 386.9 ± 3.039
p1 9.546 ± 0.5466
p2 185.4 ± 3.165
p3 28.46 ± 0.5835
p4 -0.2247 ± 0.007869
560
540
520
500
480
460
440
420
0
50
100
150
200
250

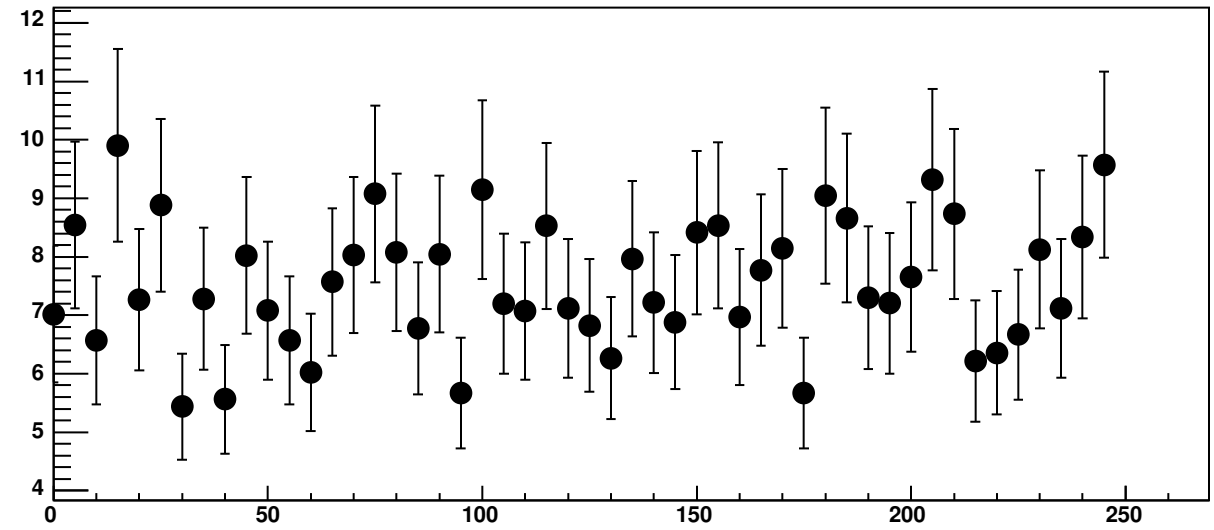

Chip 0, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 0, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250

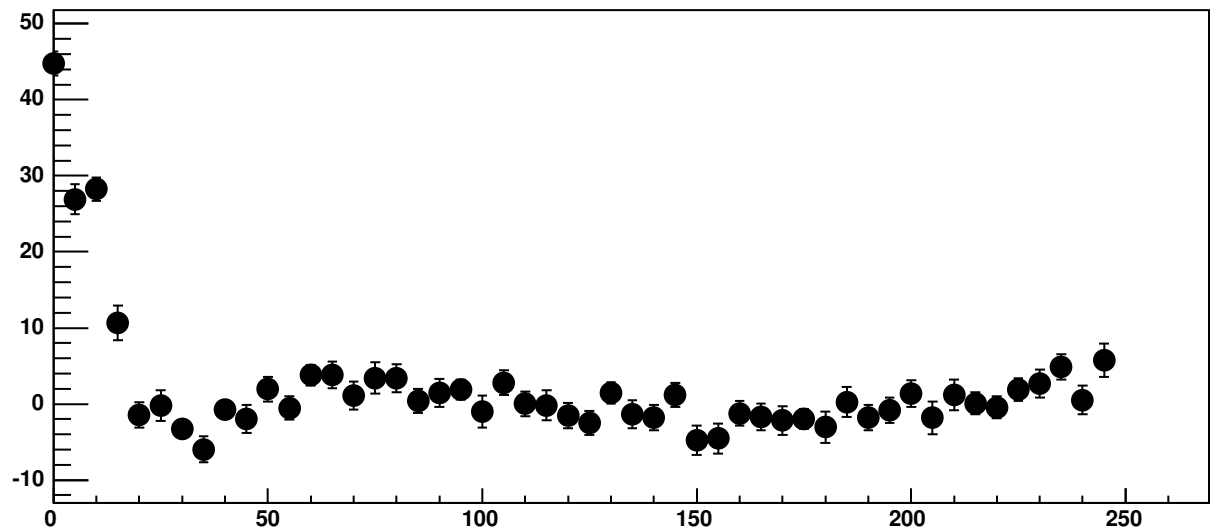
Chip 0, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold

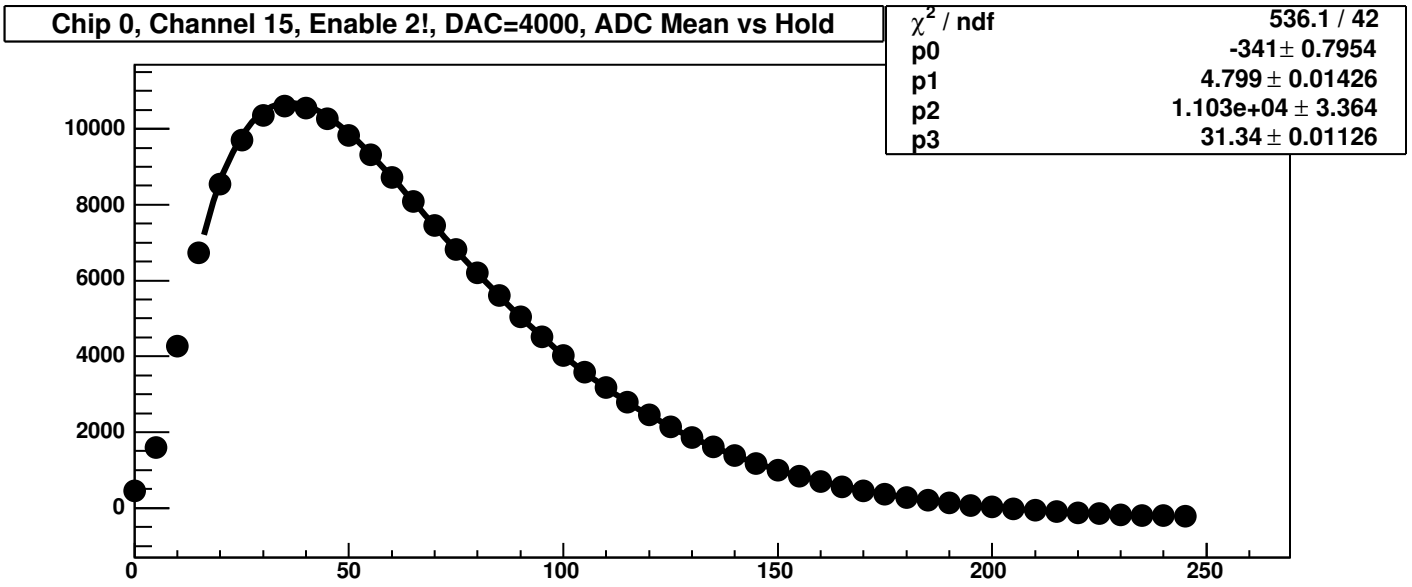


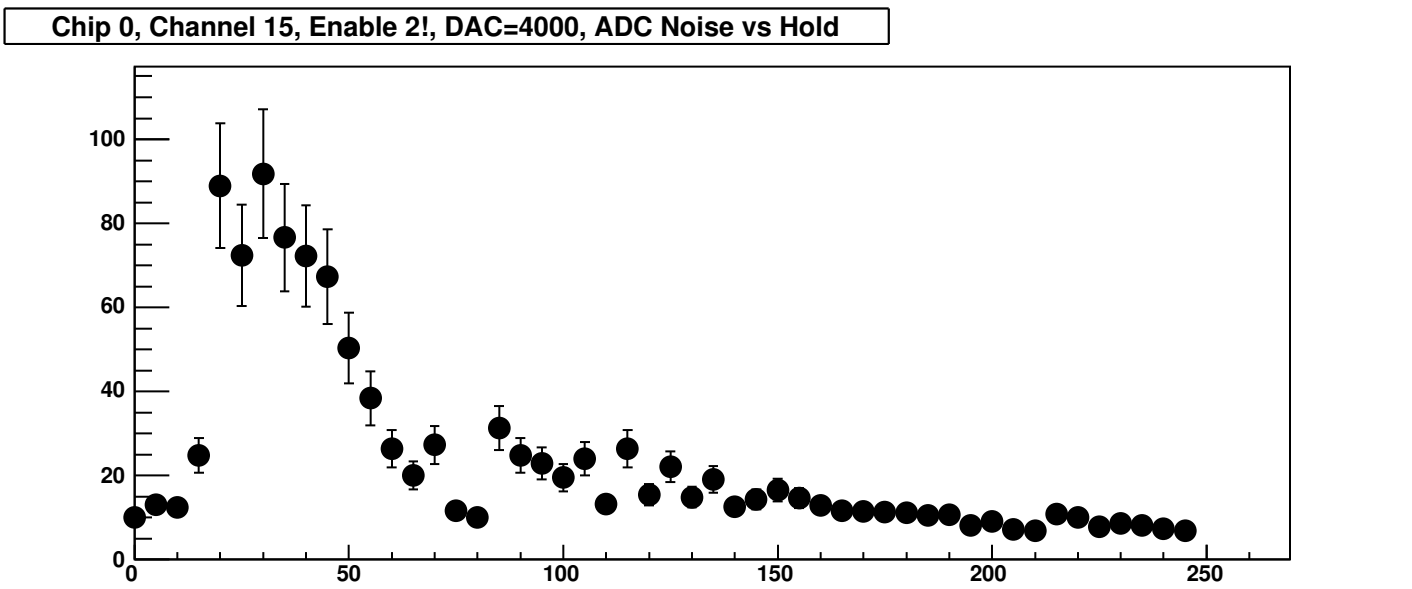
Chip 0, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold

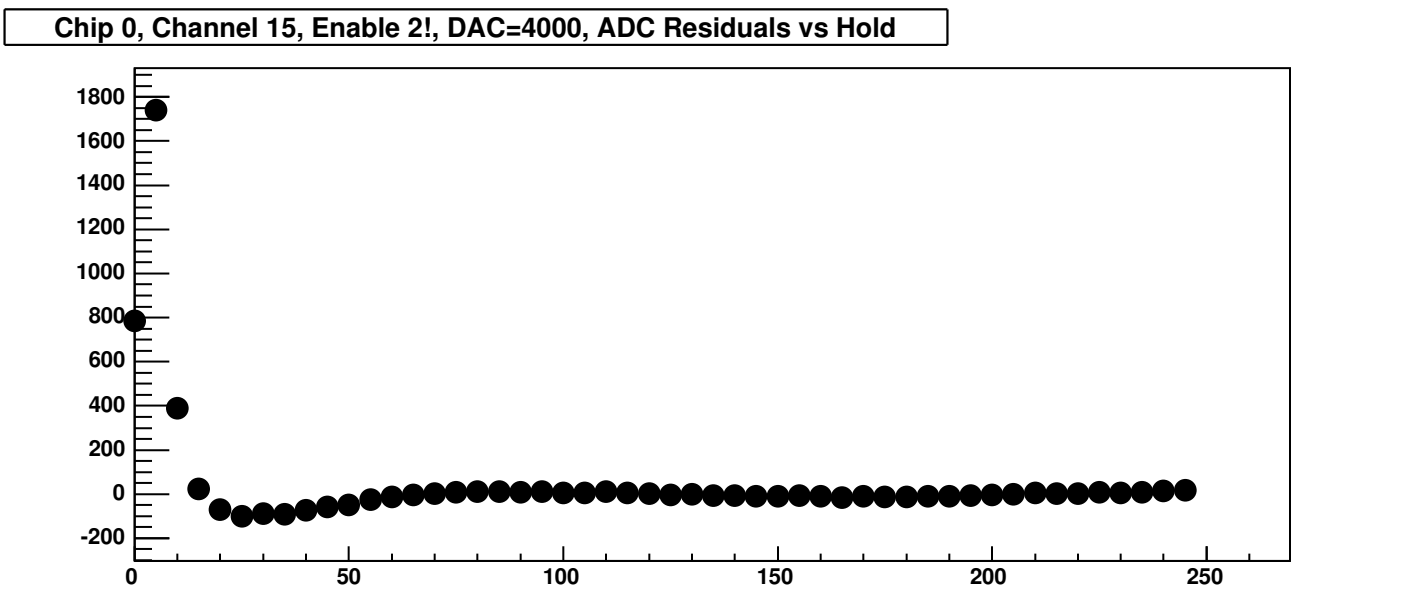


Chip 0, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

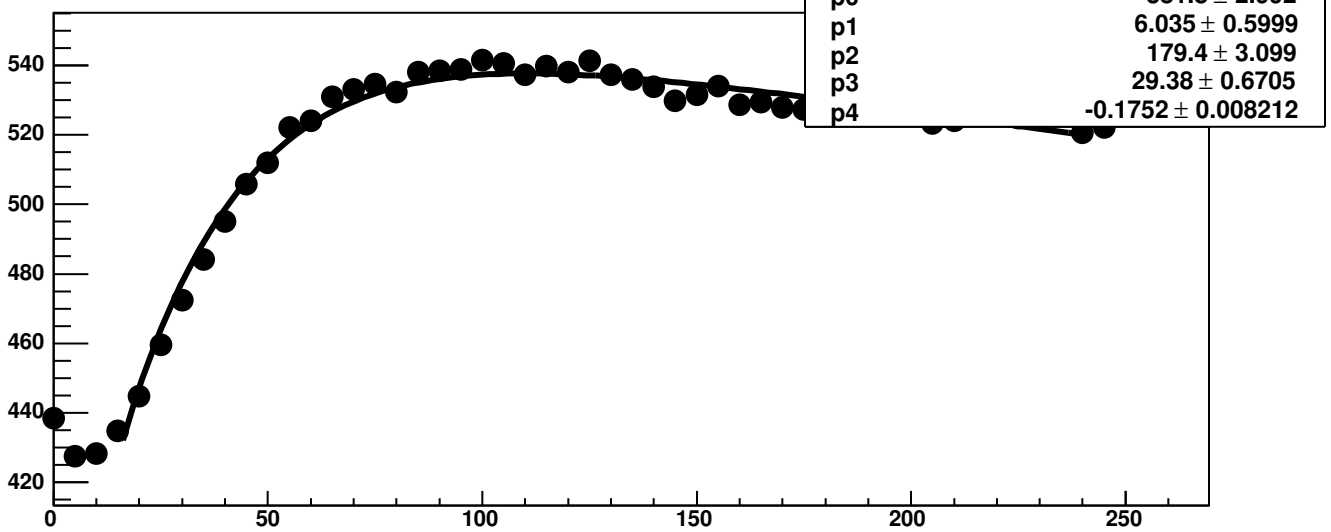



Chip 0, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 536.1 / 42
p0 -341 ± 0.7954
p1 4.799 ± 0.01426
p2 1.103e+04 ± 3.364
p3 31.34 ± 0.01126
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

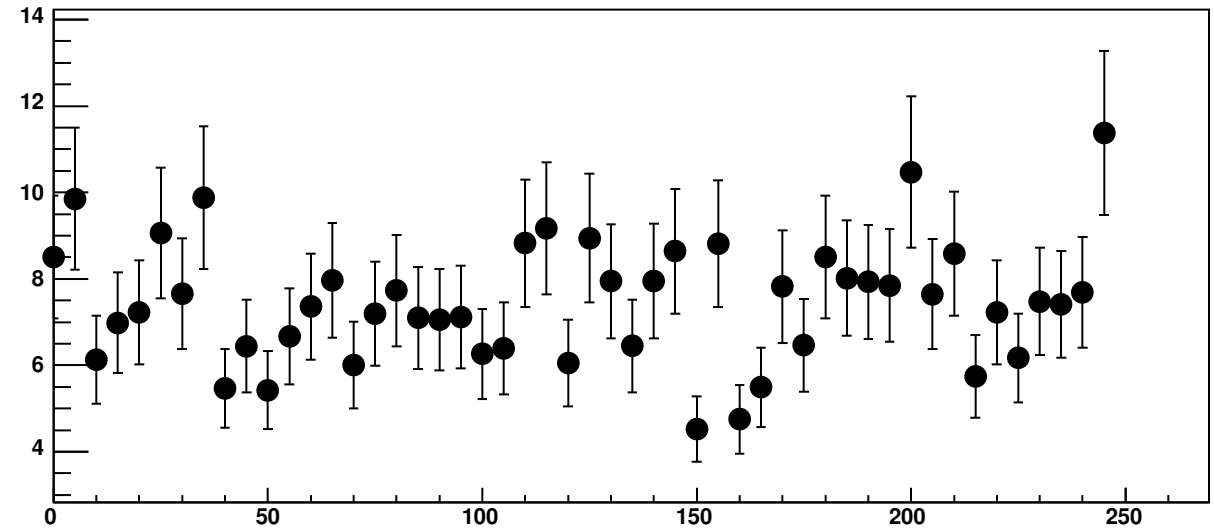

Chip 0, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

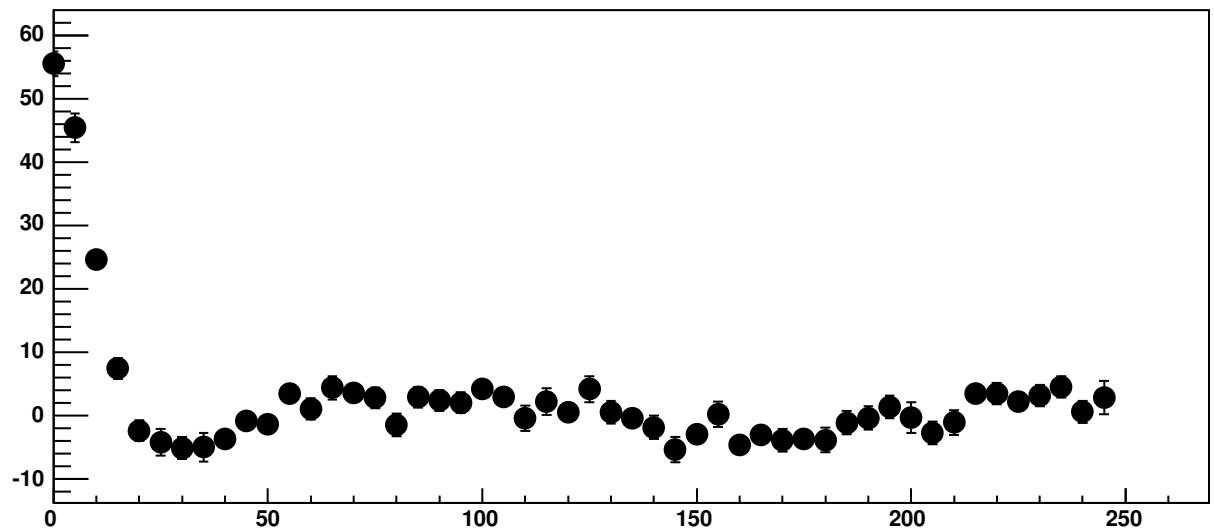
Chip 0, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

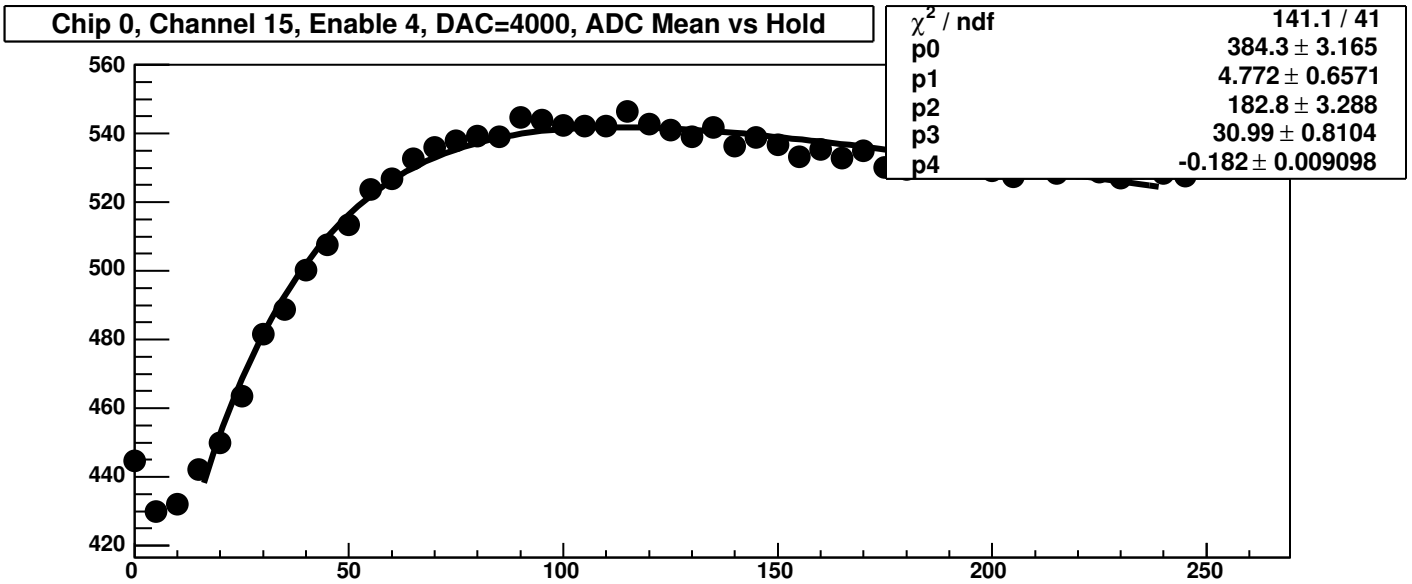


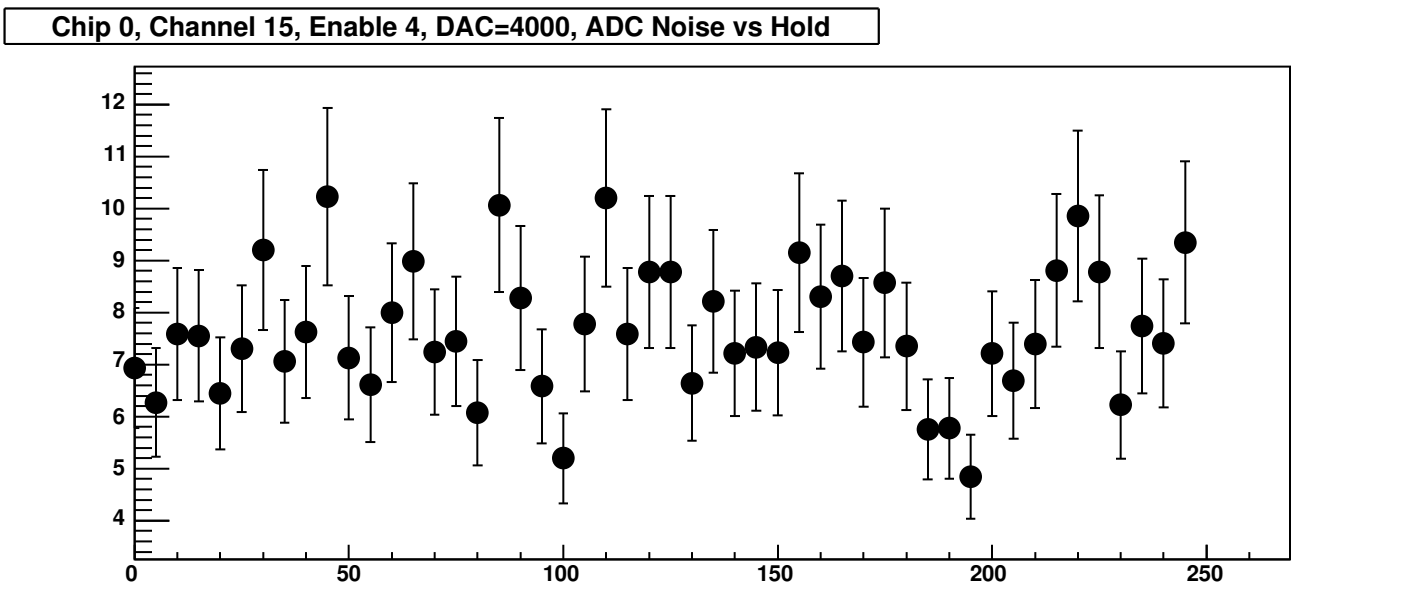
Chip 0, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold

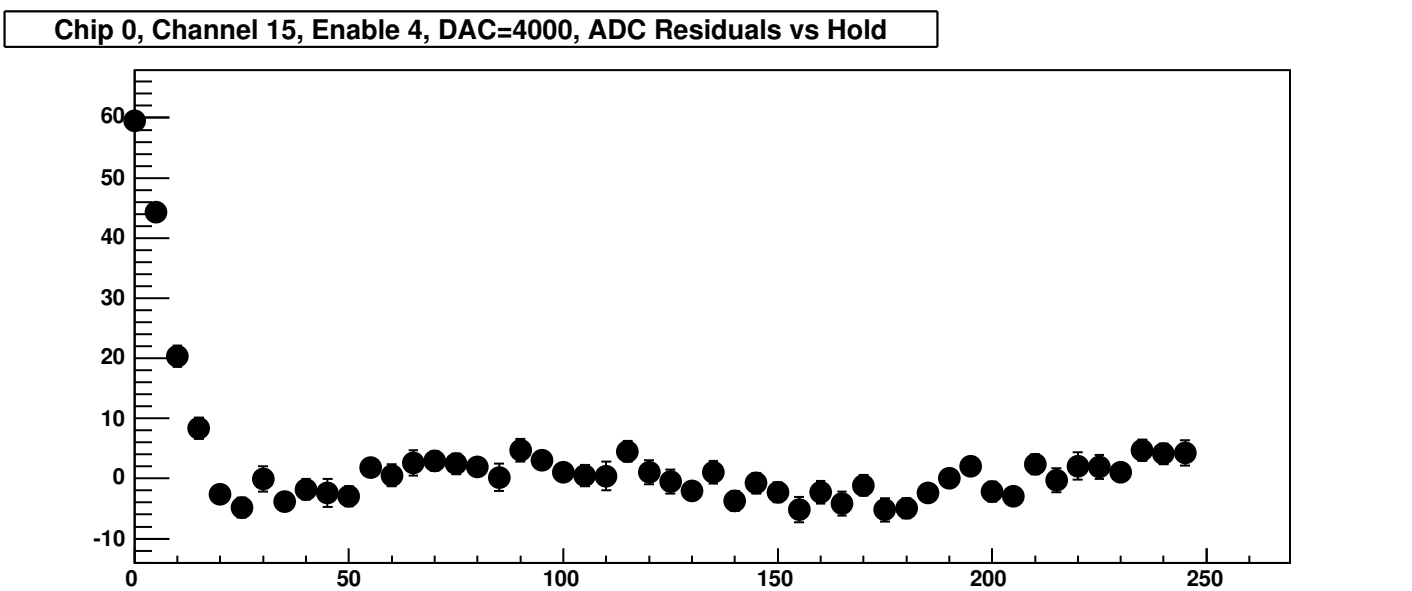


Chip 0, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold

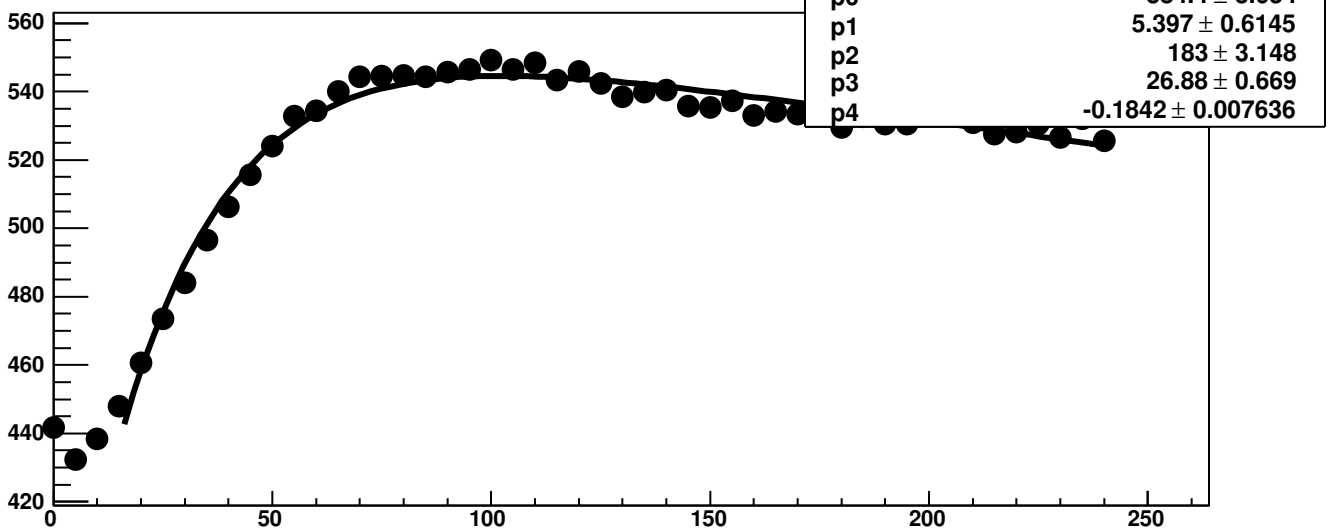



Chip 0, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 141.1 / 41
p0 384.3 ± 3.165
p1 4.772 ± 0.6571
p2 182.8 ± 3.288
p3 30.99 ± 0.8104
p4 -0.182 ± 0.009098
560
540
520
500
480
460
440
420
0
50
100
150
200
250

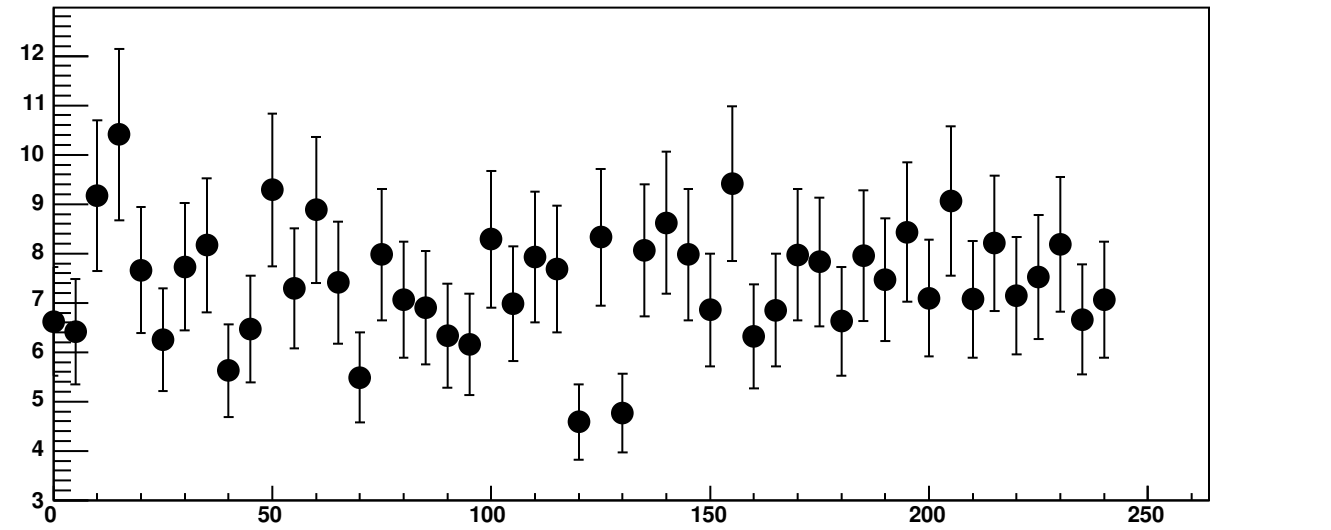

Chip 0, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 0, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

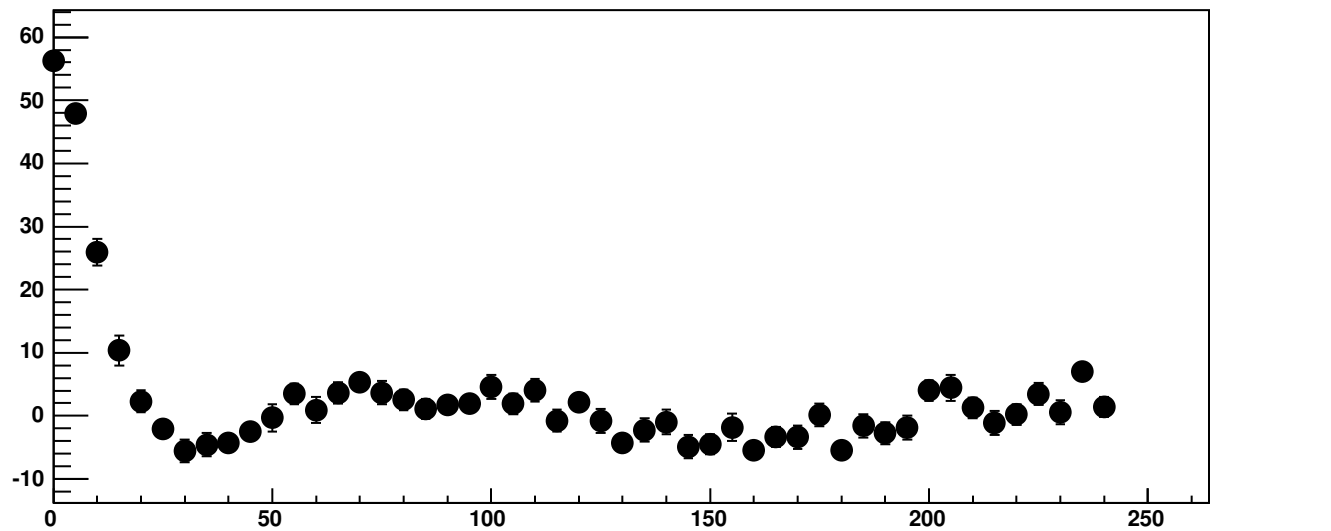
Chip 0, Channel 15, Enable 5, DAC=4000, ADC Mean vs Hold

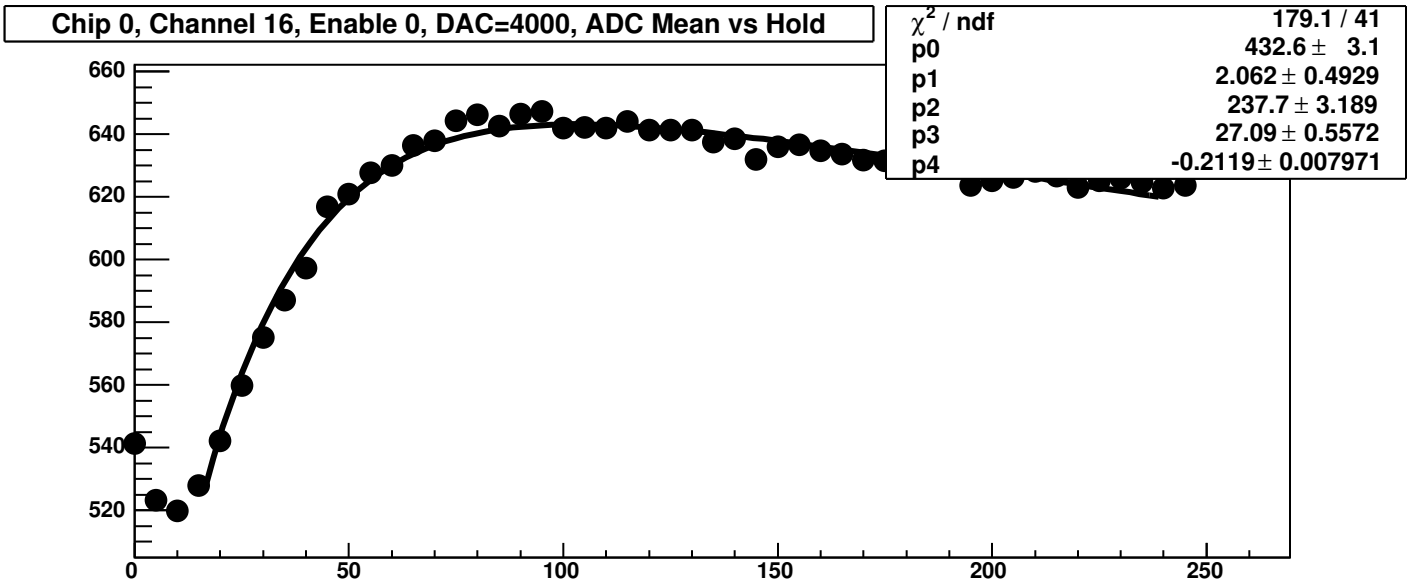


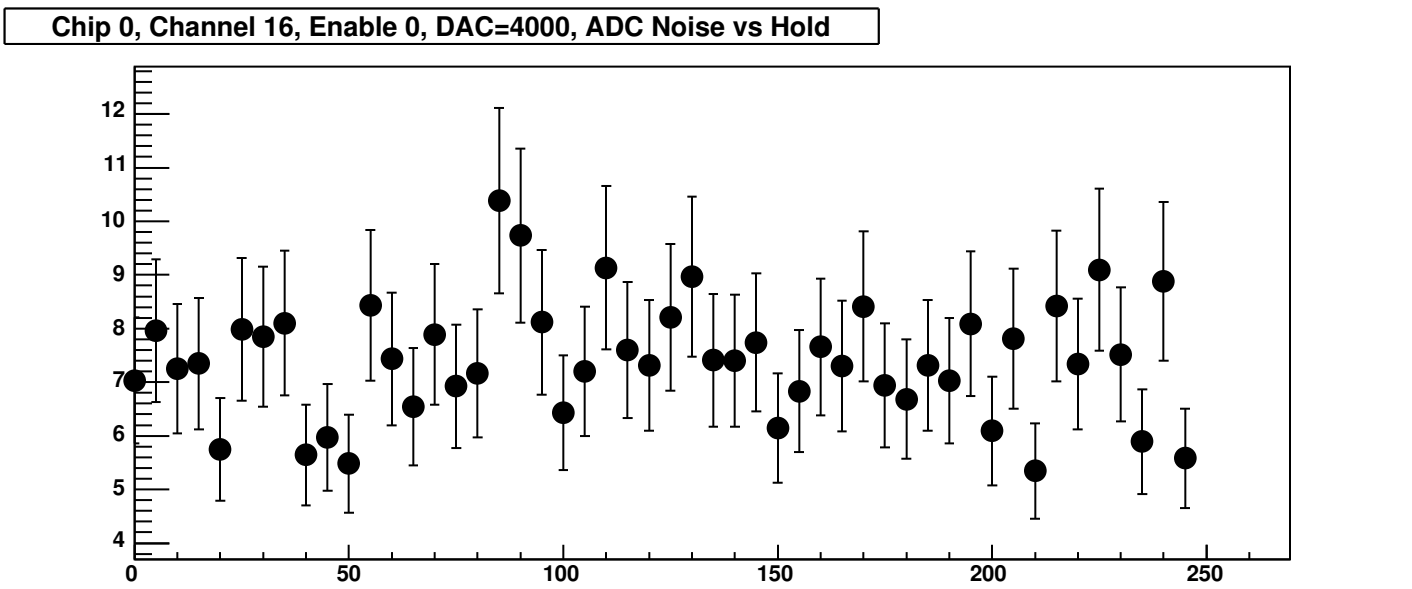
Chip 0, Channel 15, Enable 5, DAC=4000, ADC Noise vs Hold

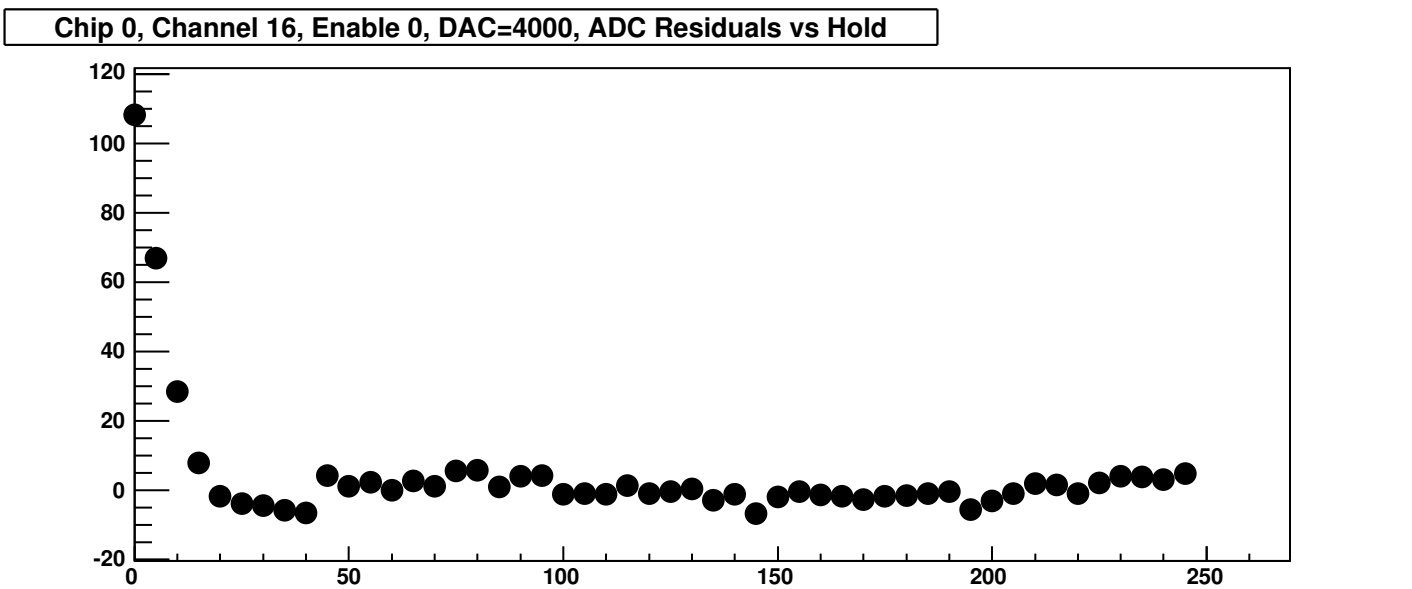


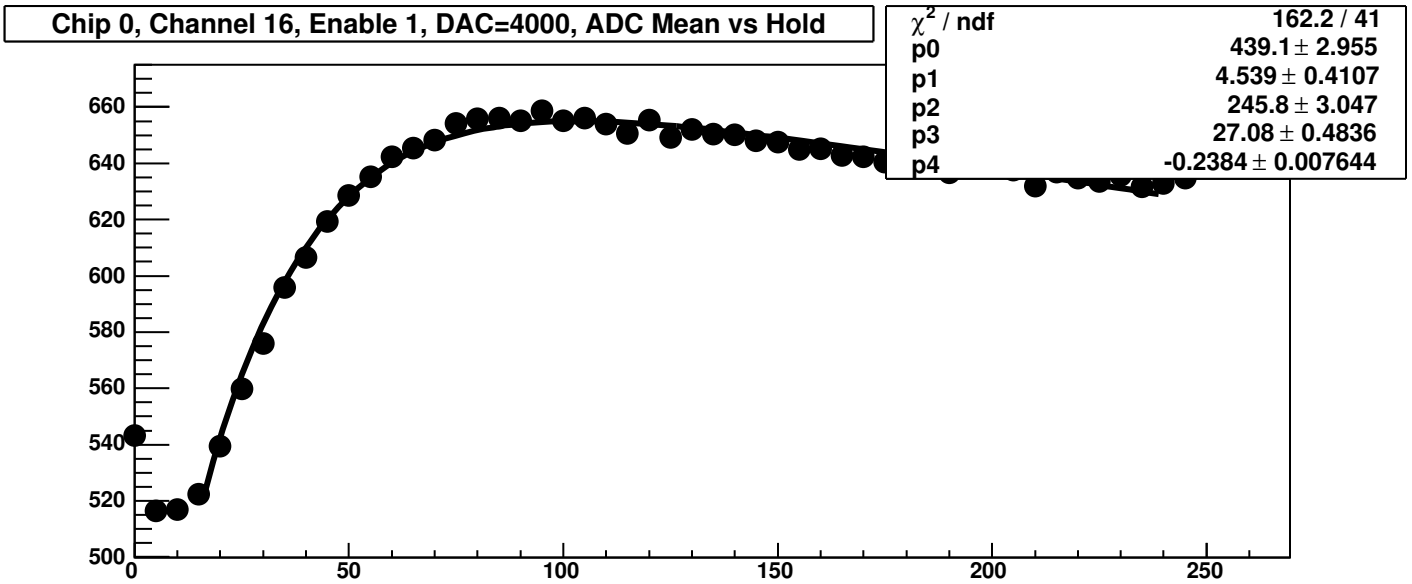
Chip 0, Channel 15, Enable 5, DAC=4000, ADC Residuals vs Hold

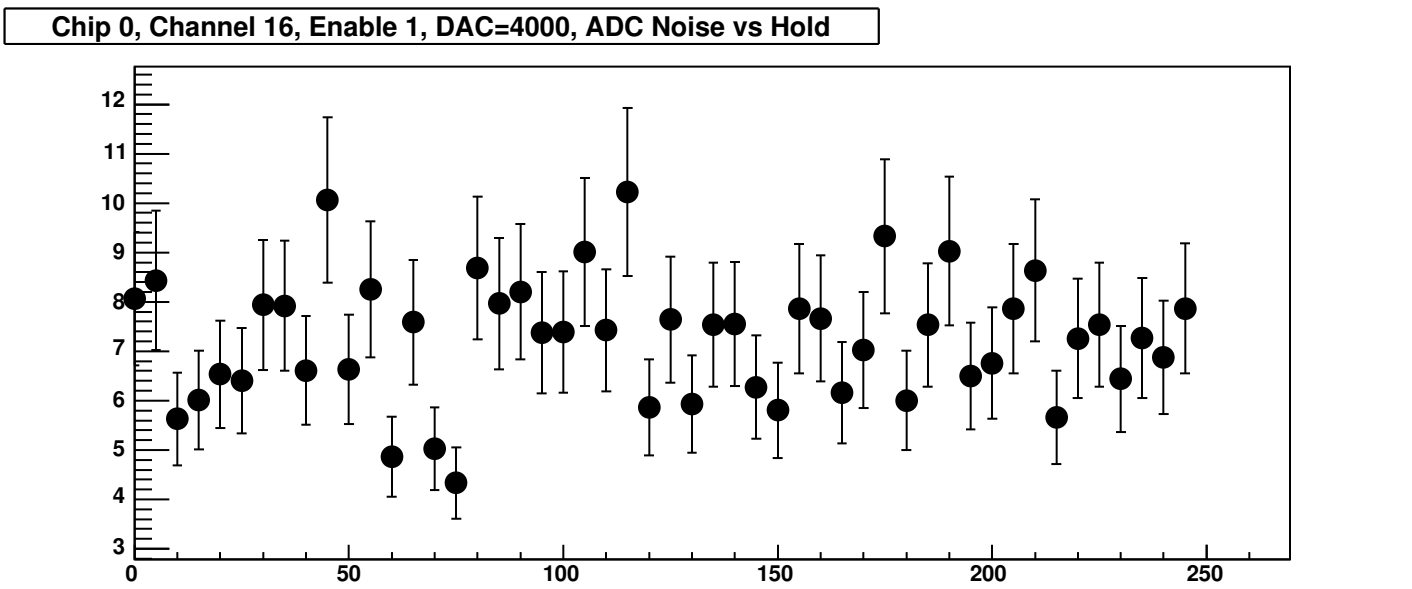


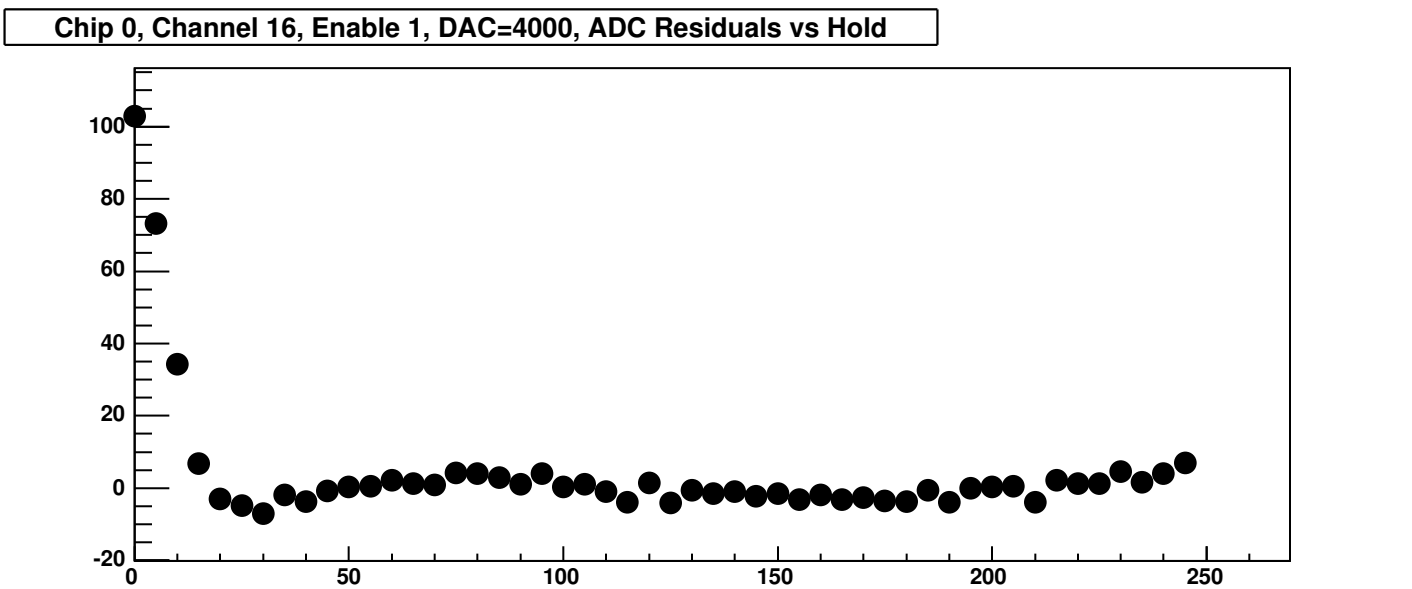

Chip 0, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 179.1 / 41
p0 432.6 ± 3.1
p1 2.062 ± 0.4929
p2 237.7 ± 3.189
p3 27.09 ± 0.5572
p4 -0.2119 ± 0.007971
660
640
620
600
580
560
540
520
0
50
100
150
200
250


Chip 0, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

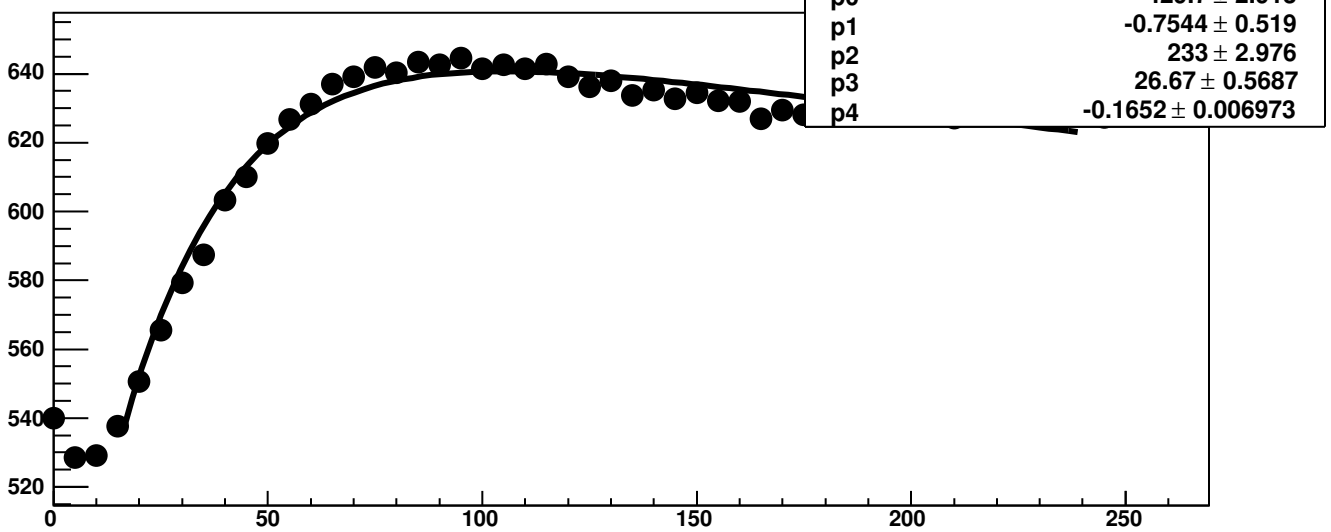

Chip 0, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold
120
100
80
60
40
20
0
-20
0
50
100
150
200
250


Chip 0, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 162.2 / 41
p0 439.1 ± 2.955
p1 4.539 ± 0.4107
p2 245.8 ± 3.047
p3 27.08 ± 0.4836
p4 -0.2384 ± 0.007644
660
640
620
600
580
560
540
520
500
0
50
100
150
200
250

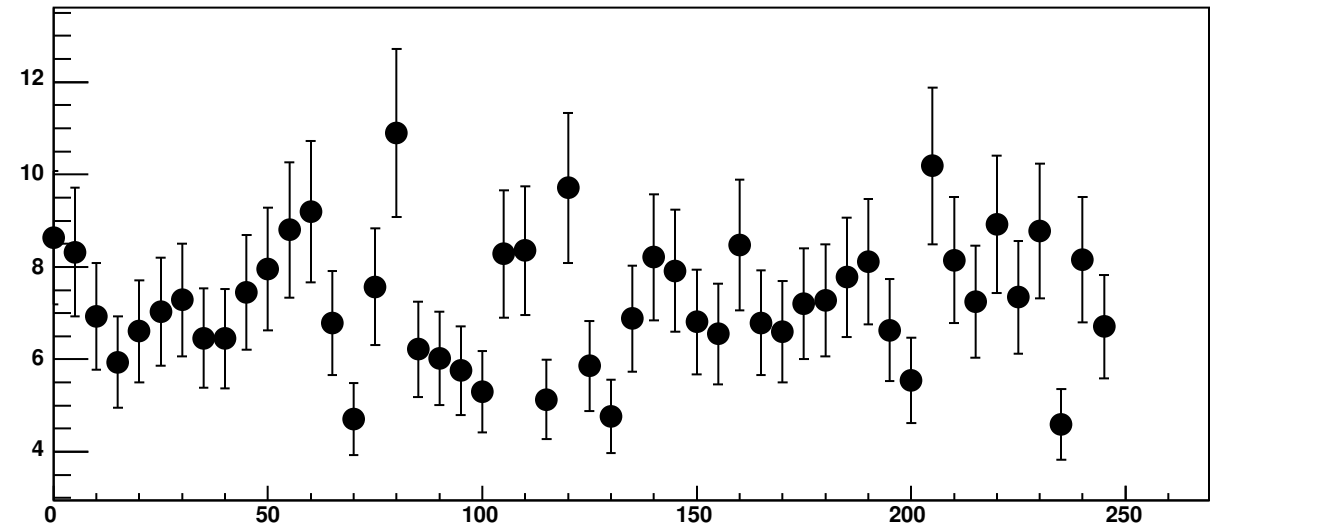

Chip 0, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 0, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold
100
80
60
40
20
0
-20
0
50
100
150
200
250

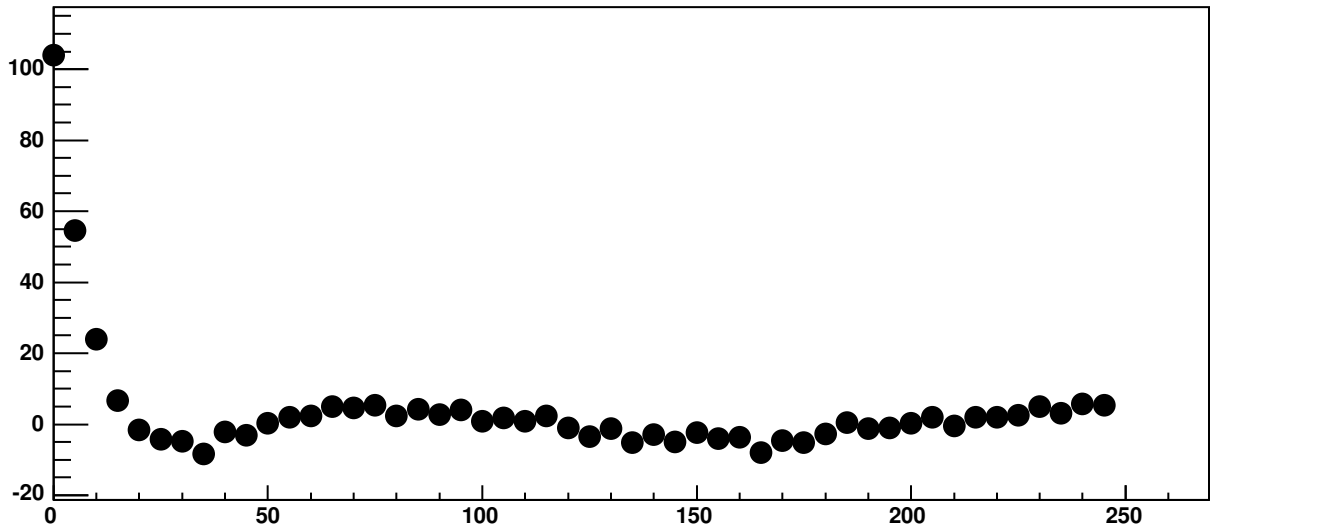
Chip 0, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold



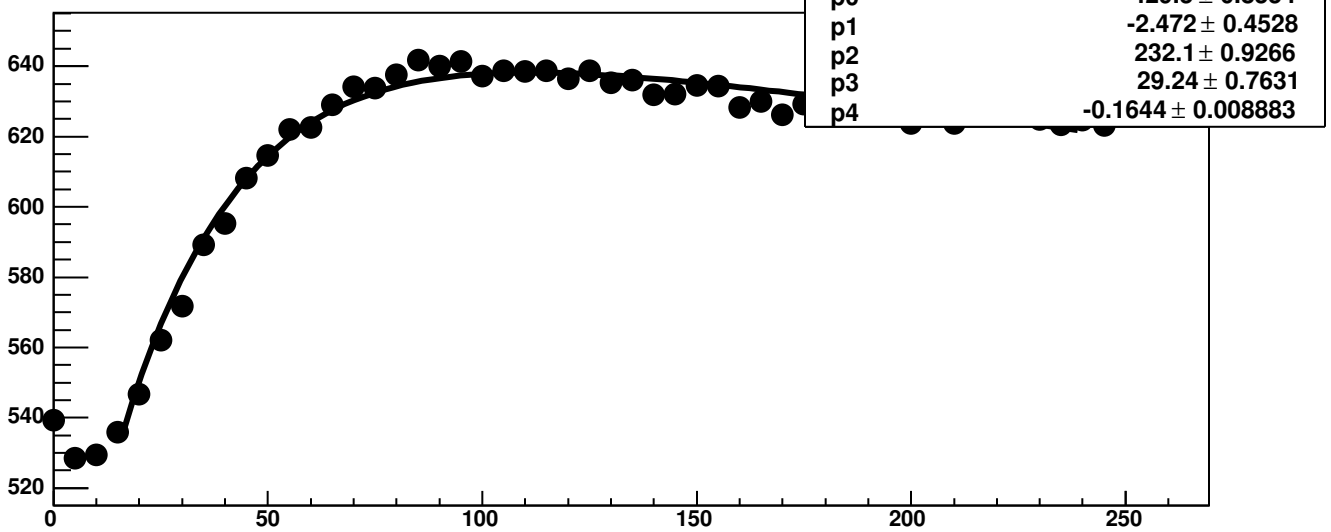
Chip 0, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold



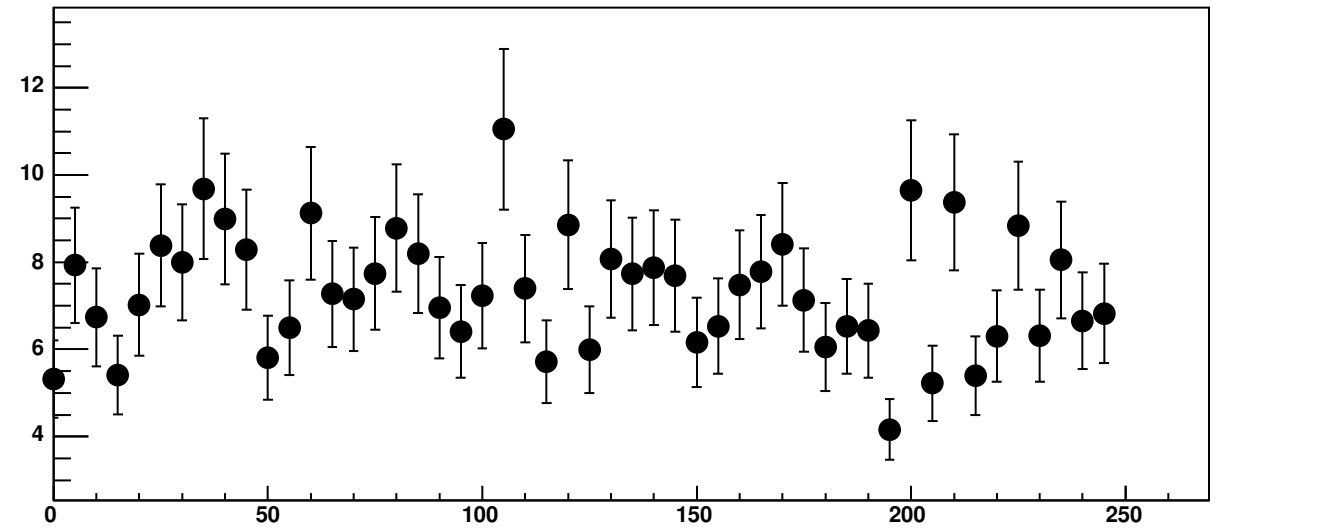
Chip 0, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold



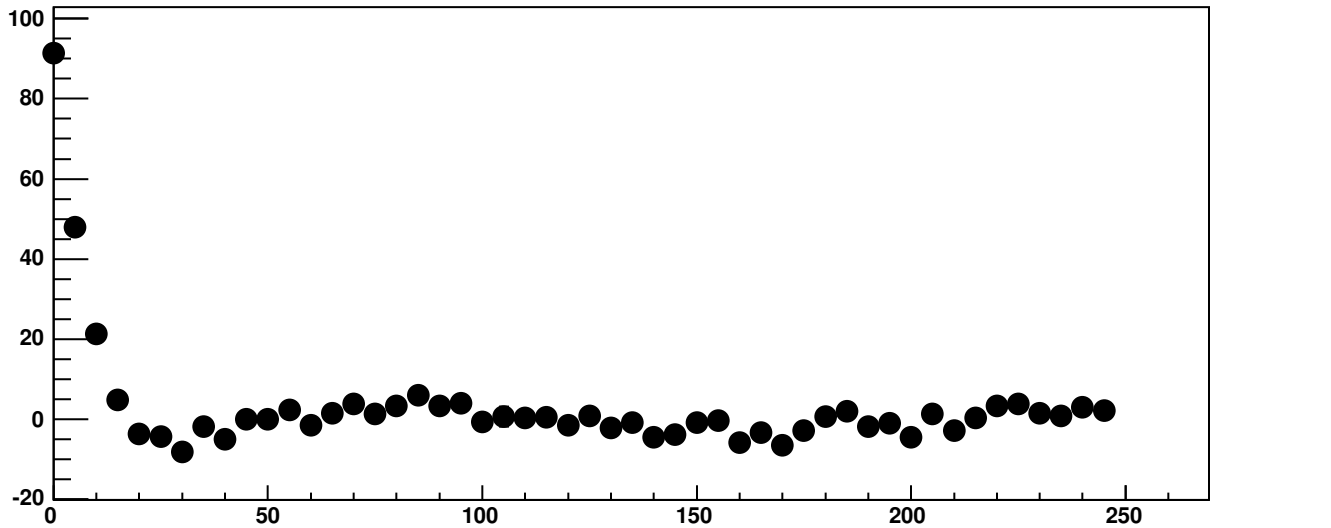
Chip 0, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold



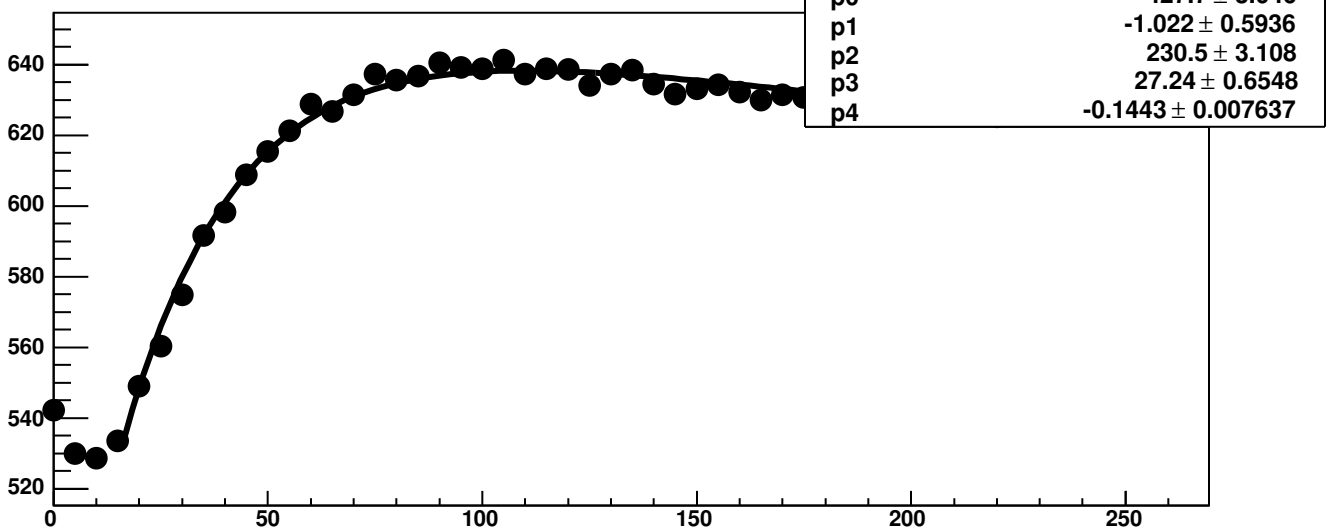
Chip 0, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



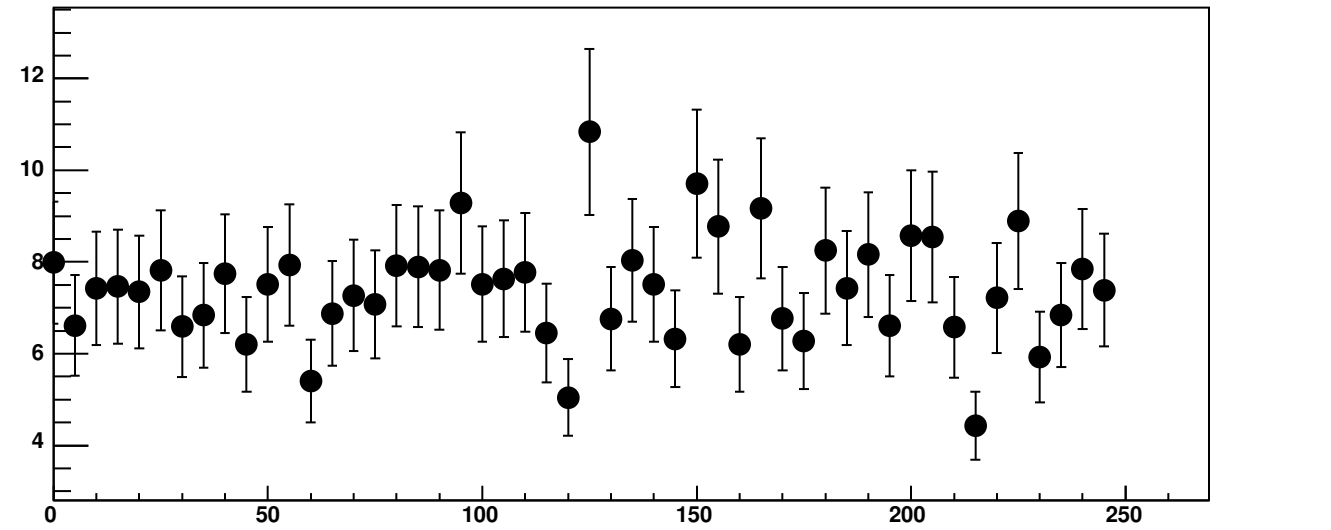
Chip 0, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold



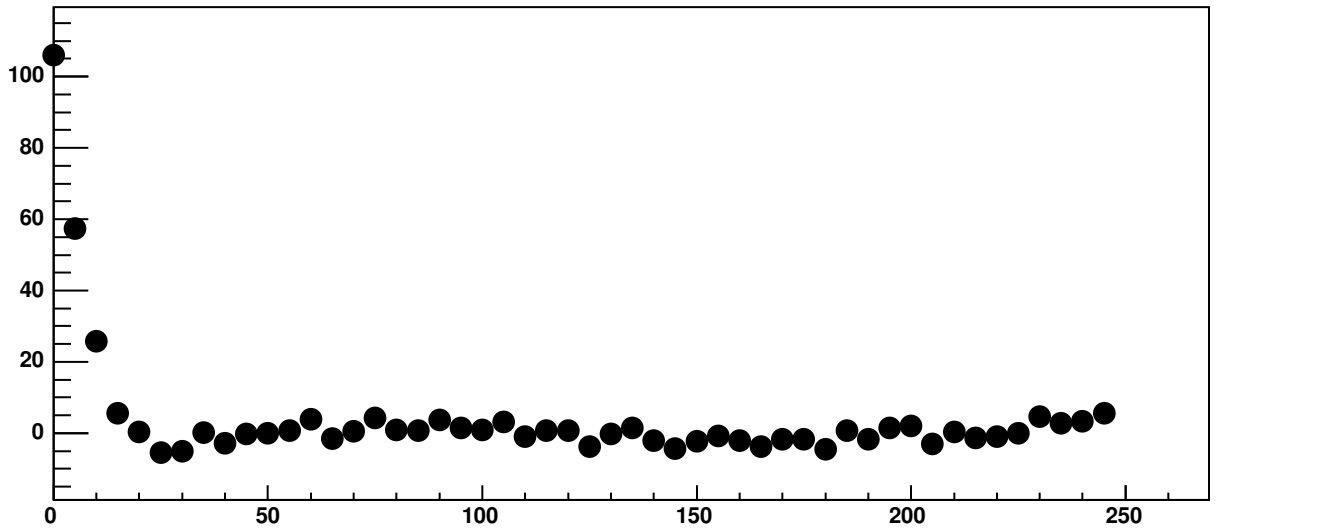
Chip 0, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

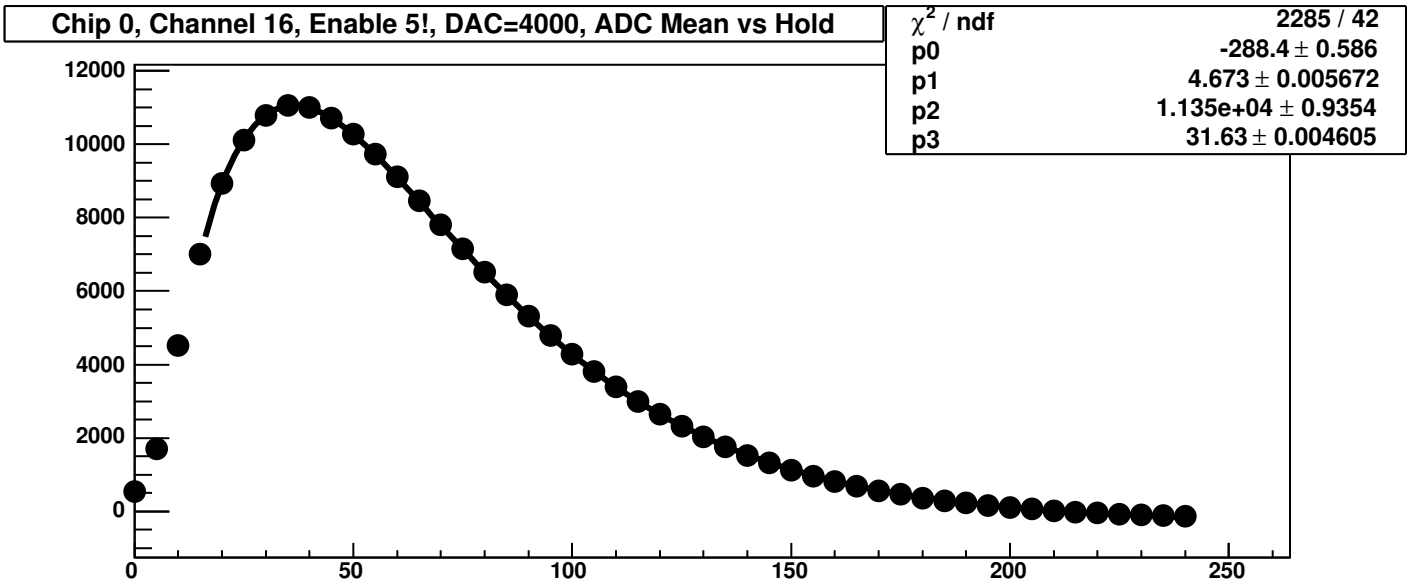


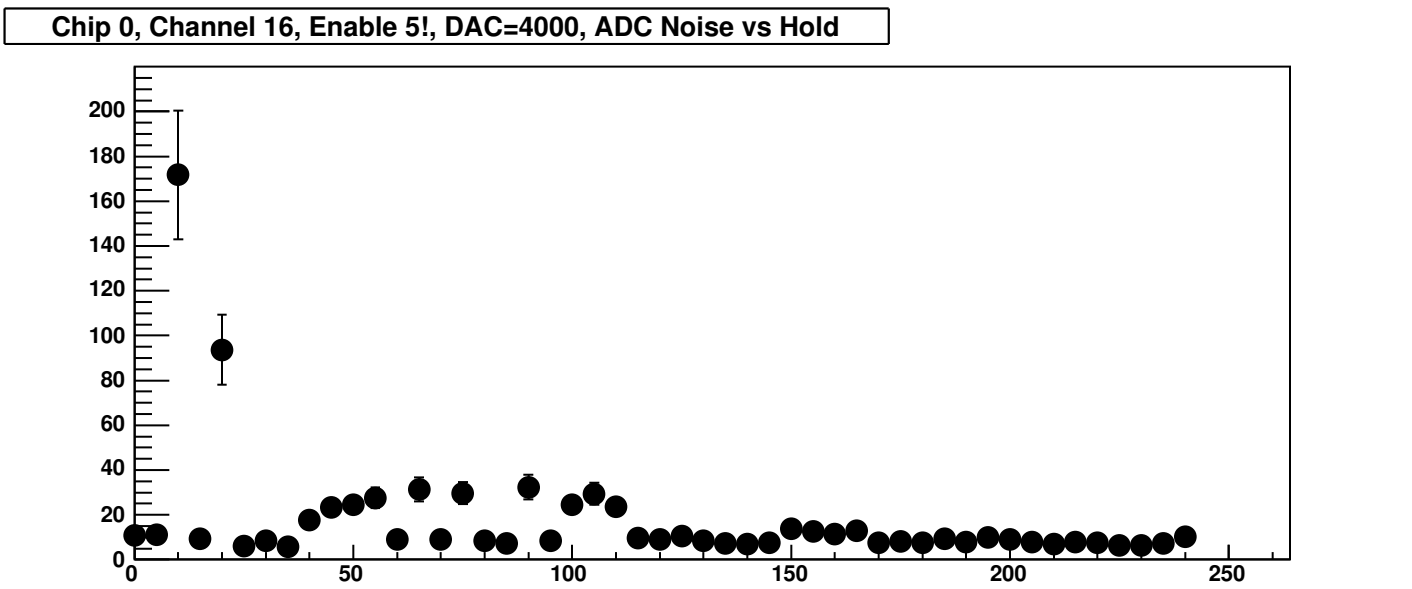
Chip 0, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold

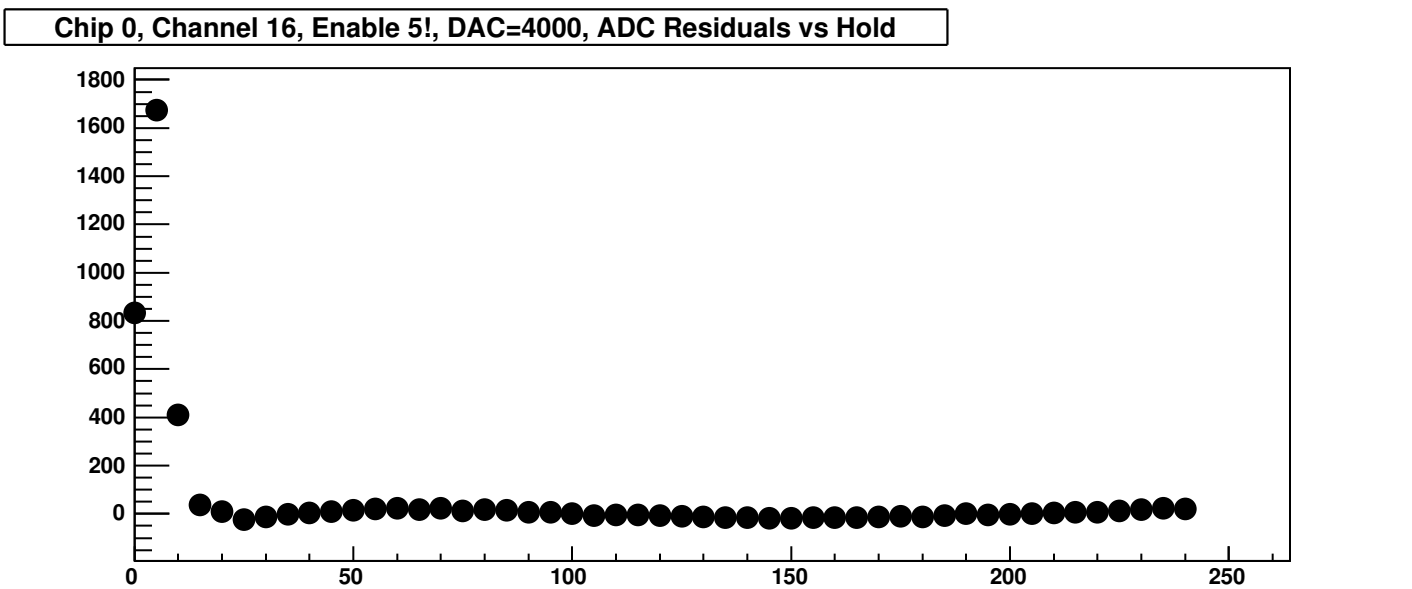


Chip 0, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

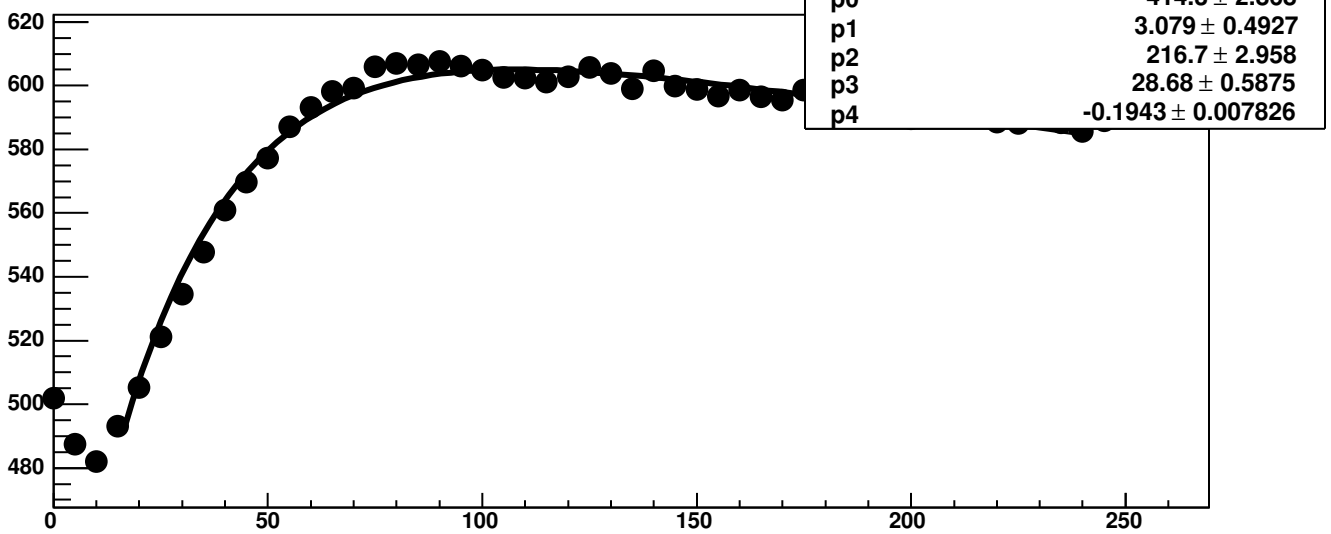



Chip 0, Channel 16, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 2285 / 42
p0 -288.4 ± 0.586
p1 4.673 ± 0.005672
p2 1.135e+04 ± 0.9354
p3 31.63 ± 0.004605
12000
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

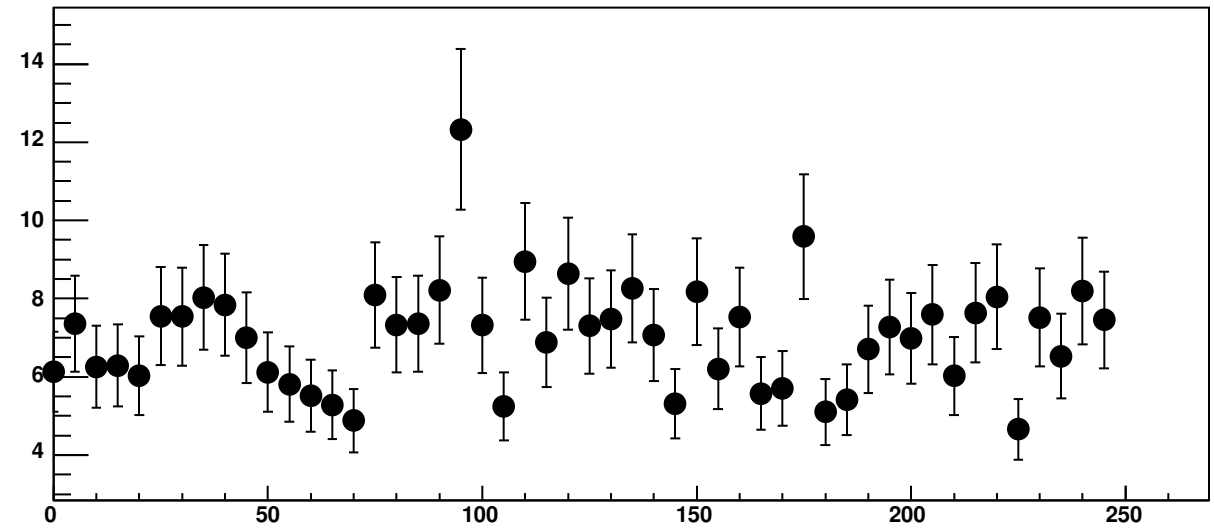

Chip 0, Channel 16, Enable 5!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 0, Channel 16, Enable 5!, DAC=4000, ADC Residuals vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

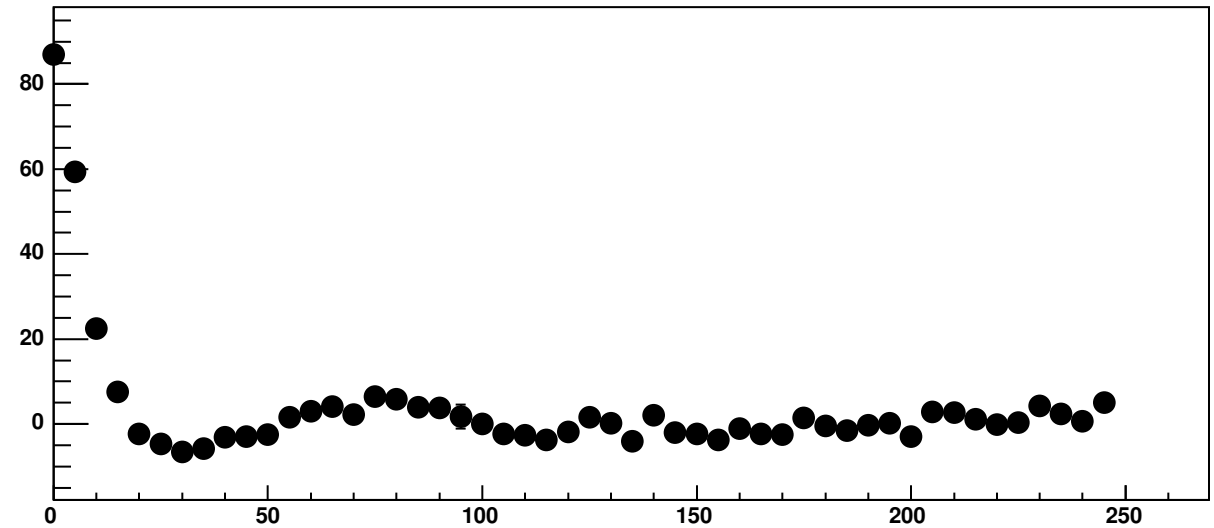
Chip 0, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold



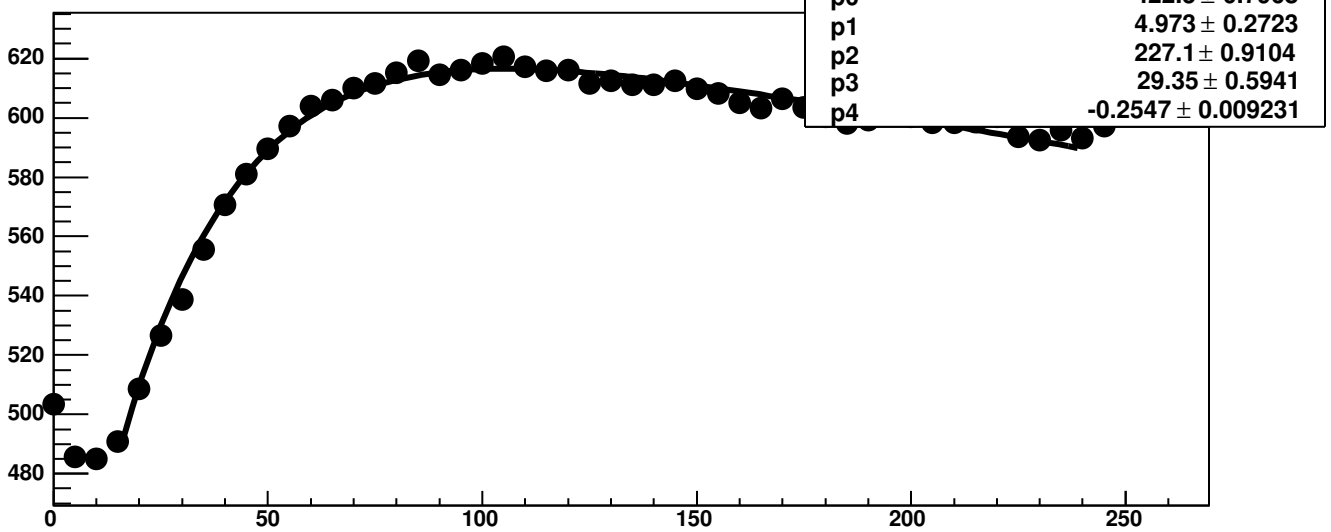
Chip 0, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



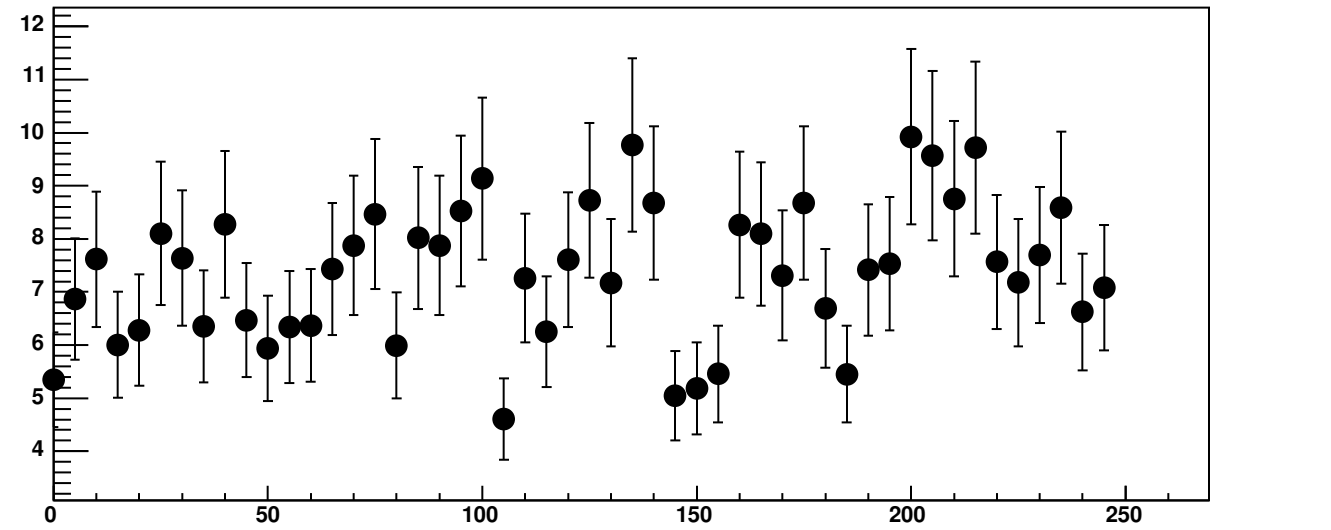
Chip 0, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold



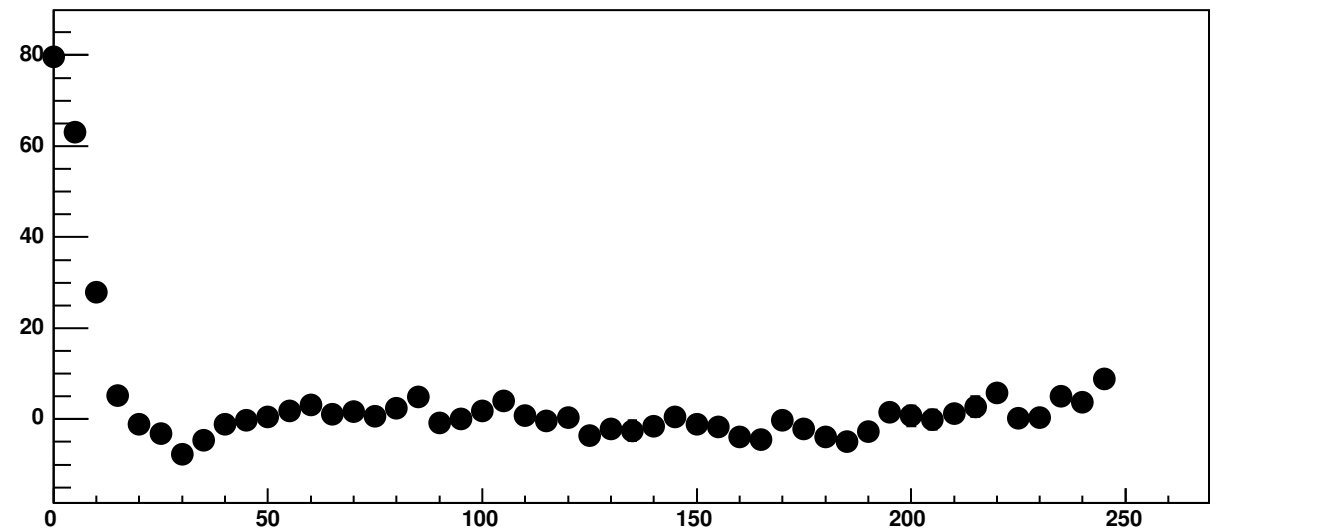
Chip 0, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 0, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold

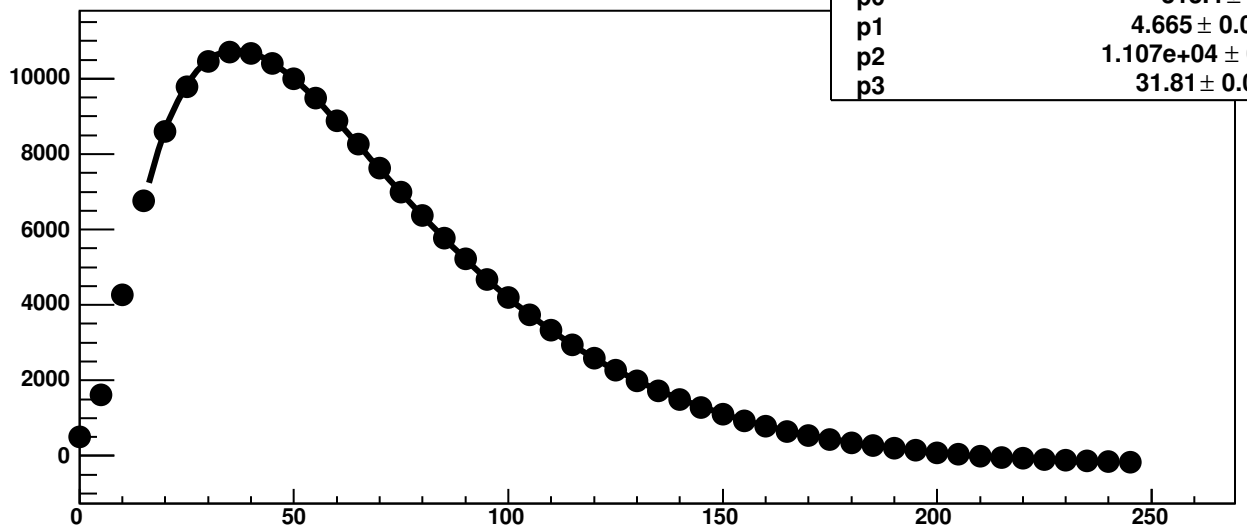


Chip 0, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

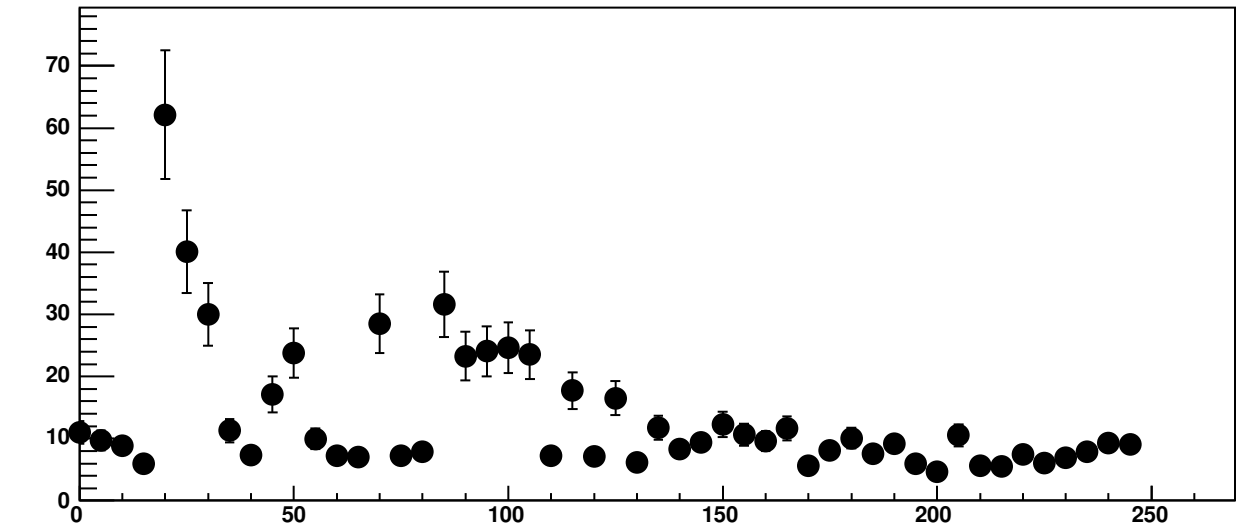


Chip 0, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold

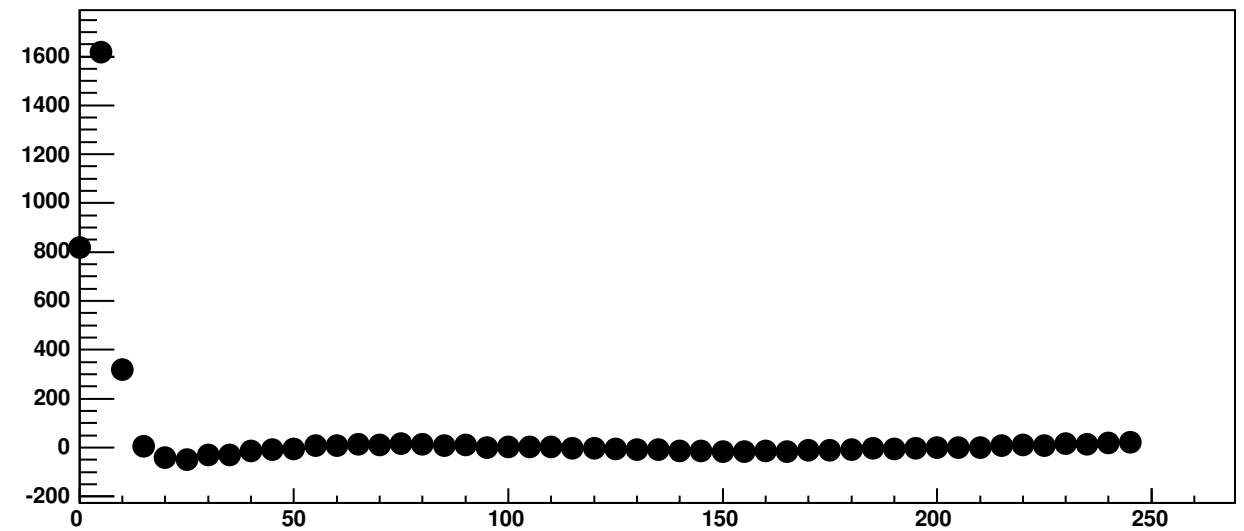
χ^2 / ndf	1309 / 42
p0	-313.4 ± 0.5529
p1	4.665 ± 0.004423
p2	1.107e+04 ± 0.9965
p3	31.81 ± 0.004942



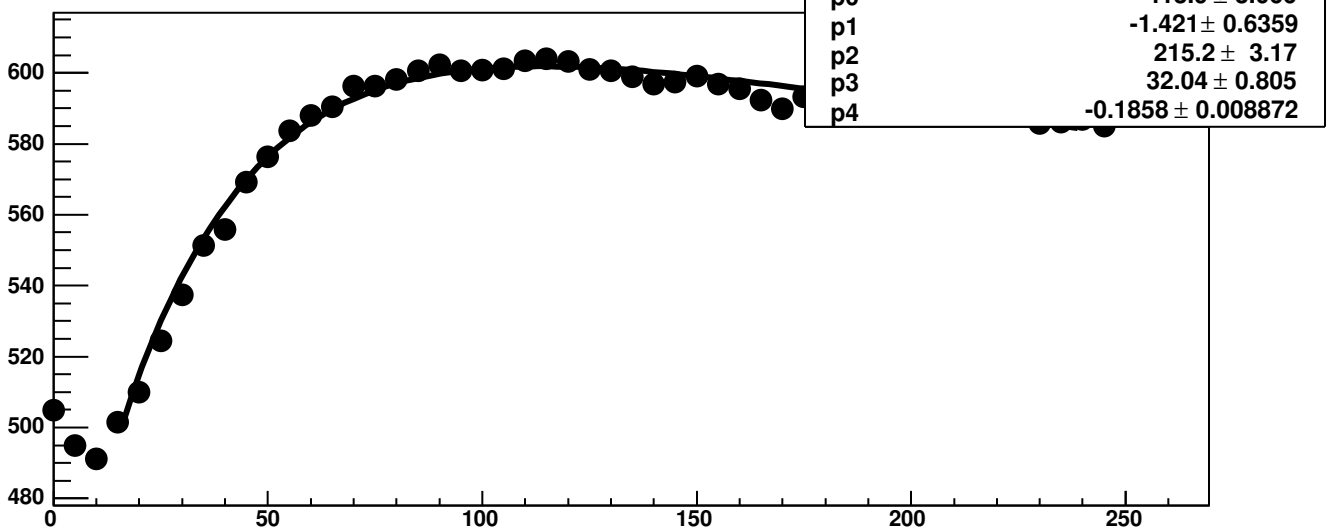
Chip 0, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold



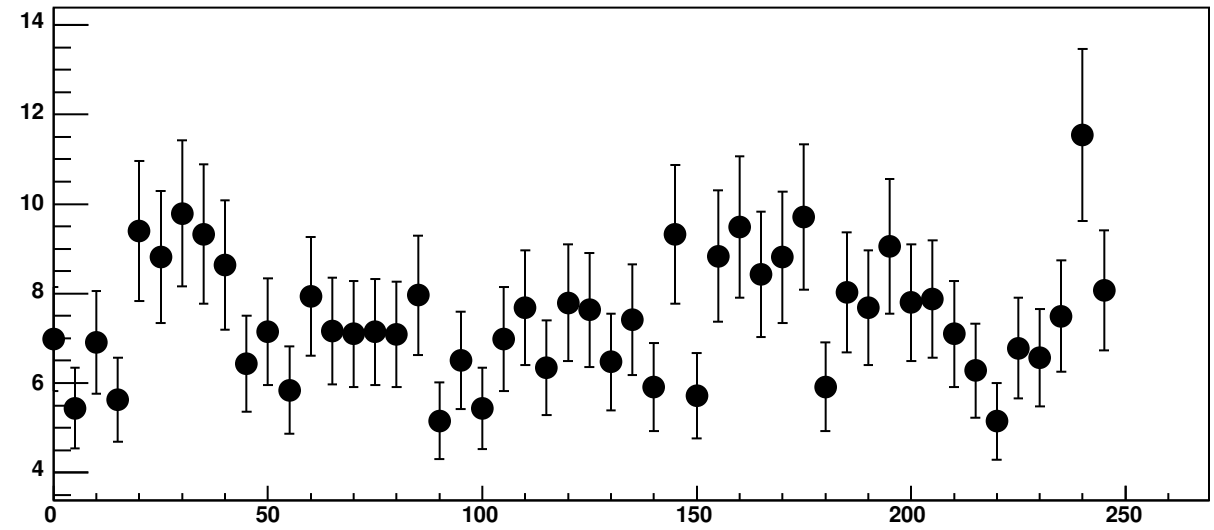
Chip 0, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold



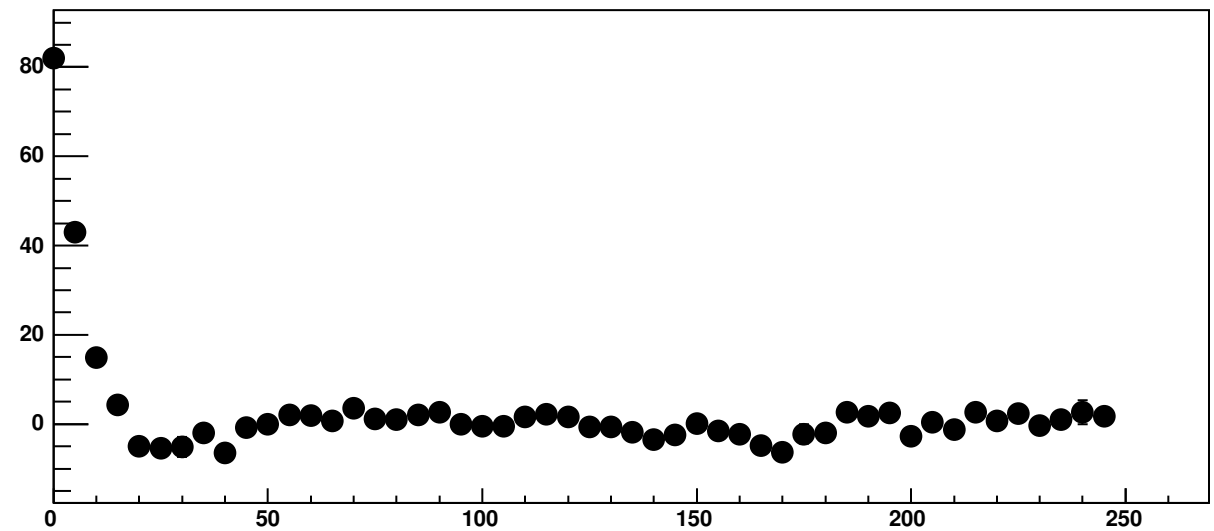
Chip 0, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



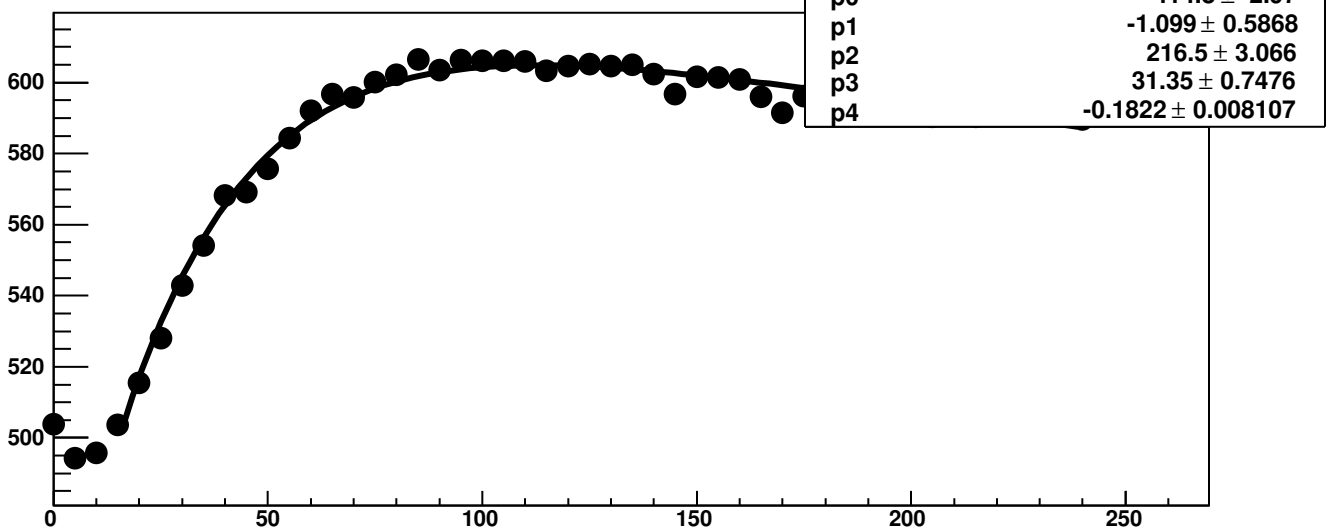
Chip 0, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



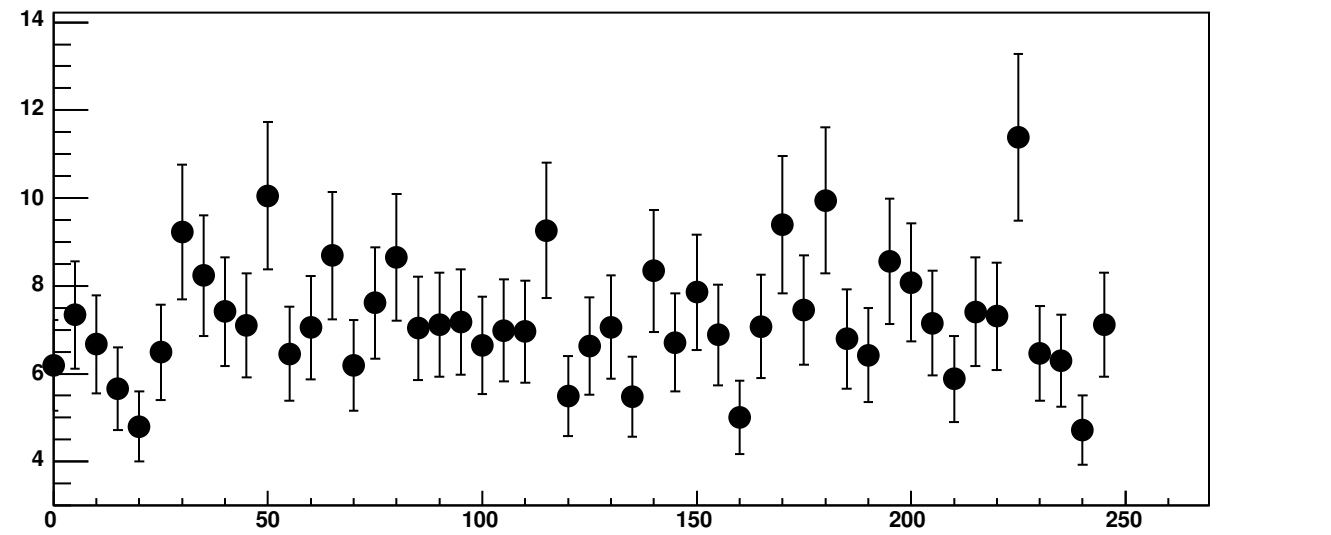
Chip 0, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold



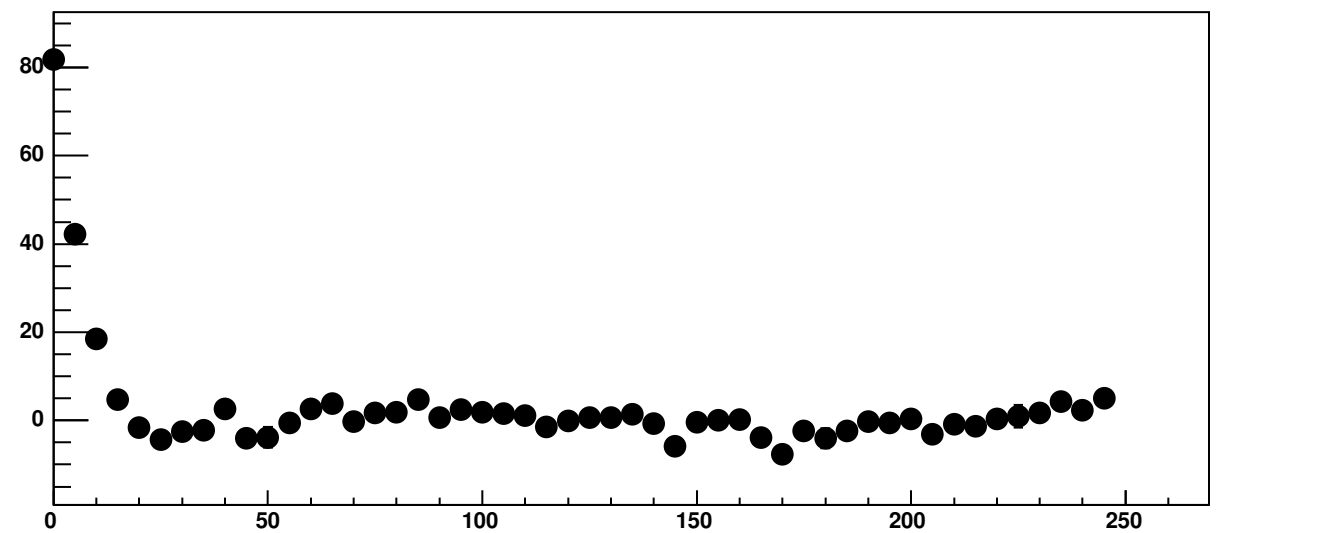
Chip 0, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



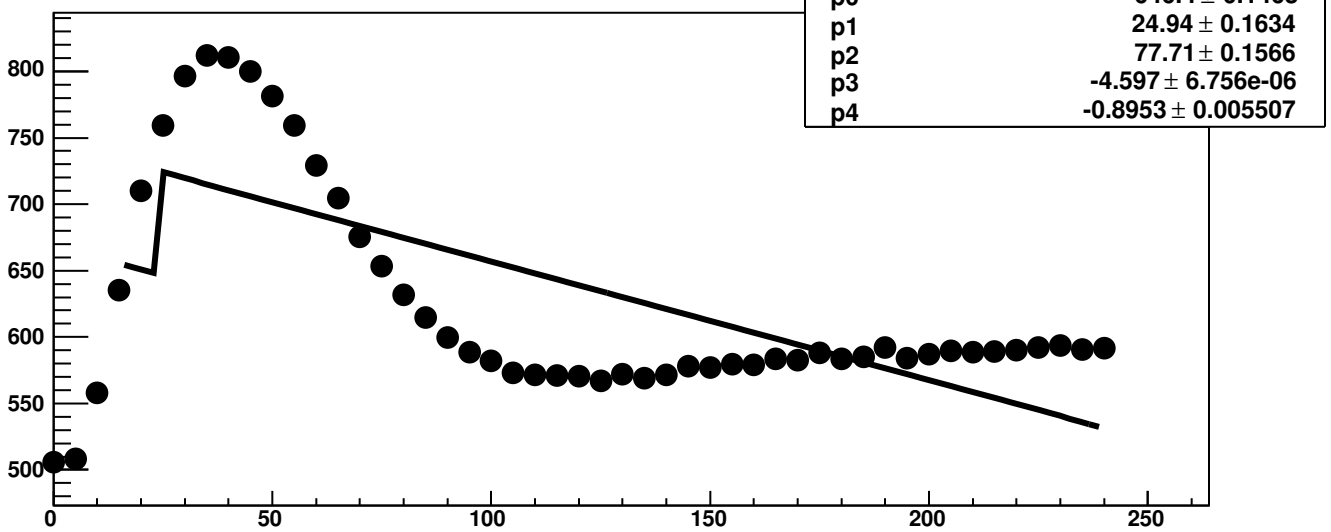
Chip 0, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



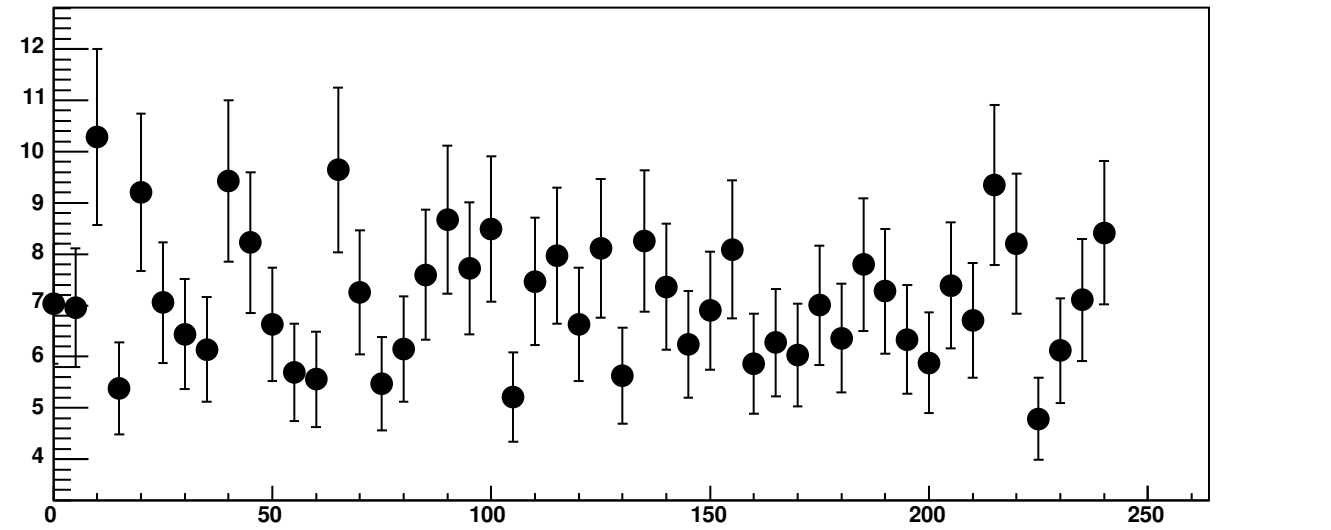
Chip 0, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold



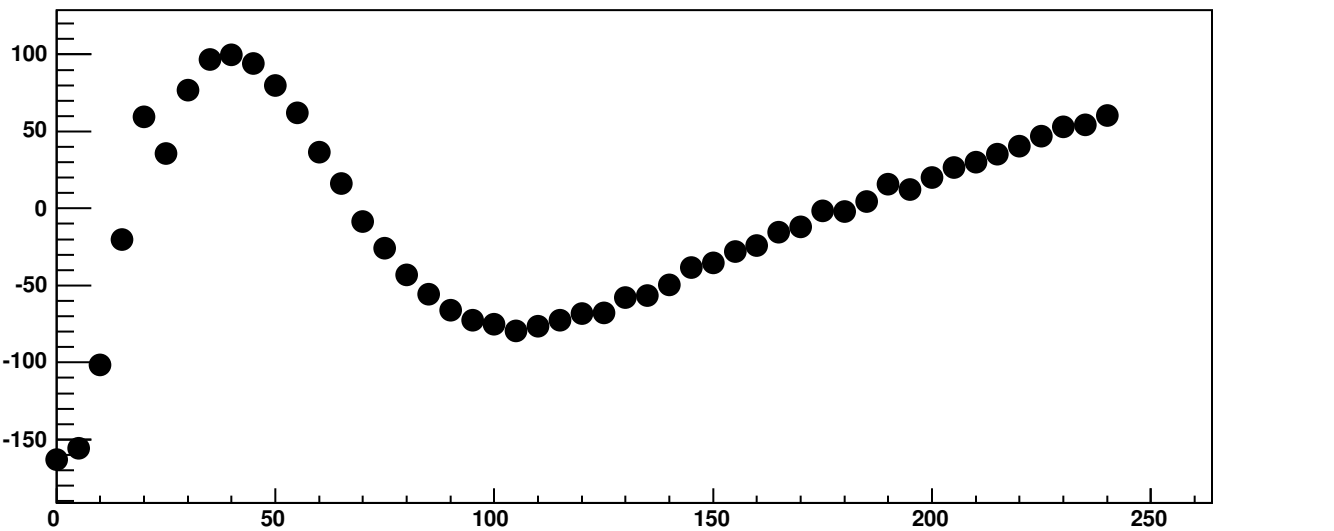
Chip 0, Channel 17, Enable 5, DAC=4000, ADC Mean vs Hold



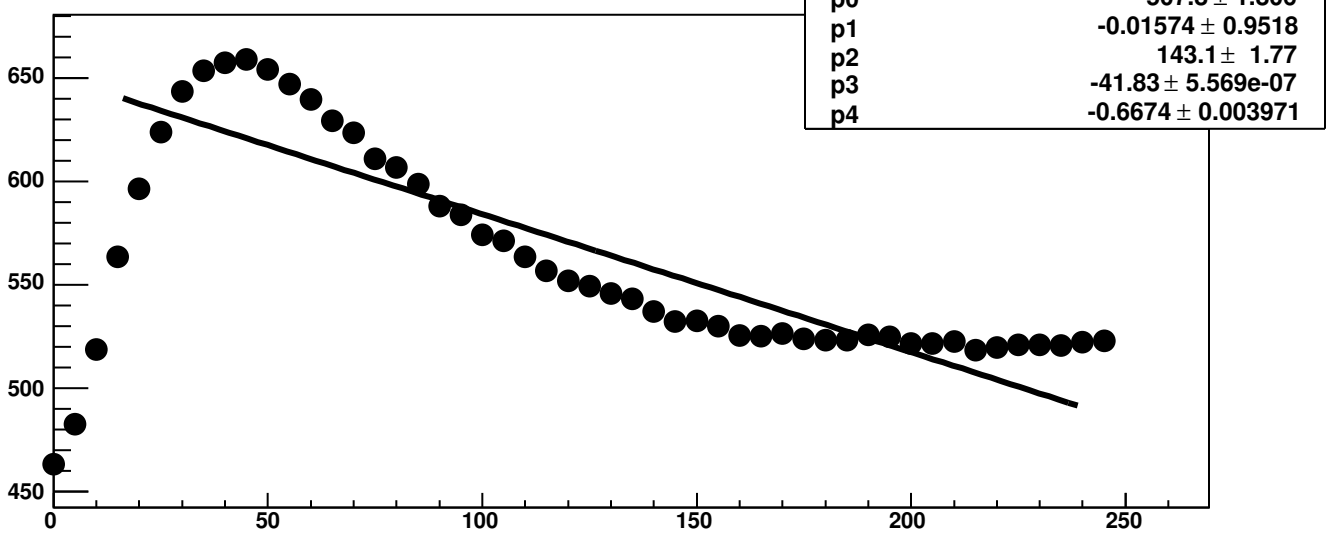
Chip 0, Channel 17, Enable 5, DAC=4000, ADC Noise vs Hold



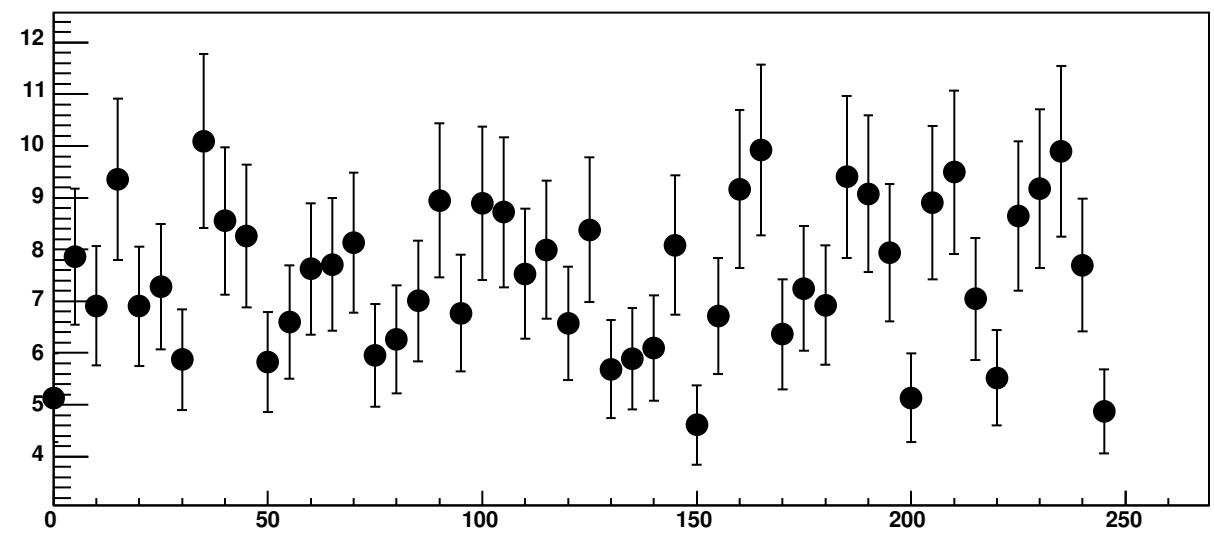
Chip 0, Channel 17, Enable 5, DAC=4000, ADC Residuals vs Hold



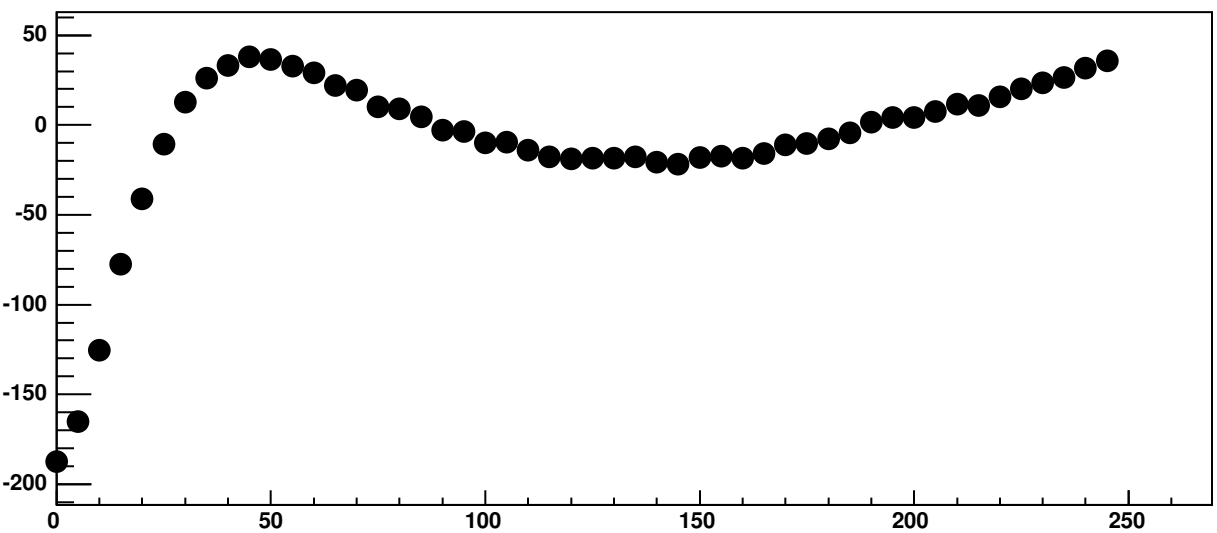
Chip 1, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold

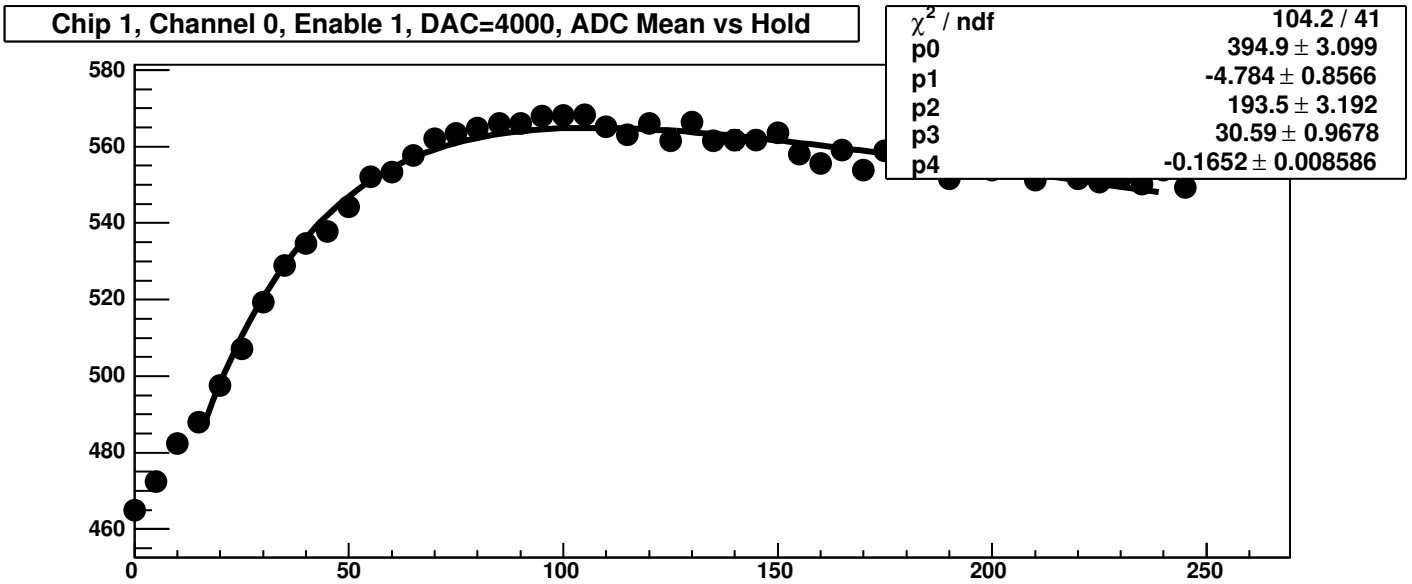


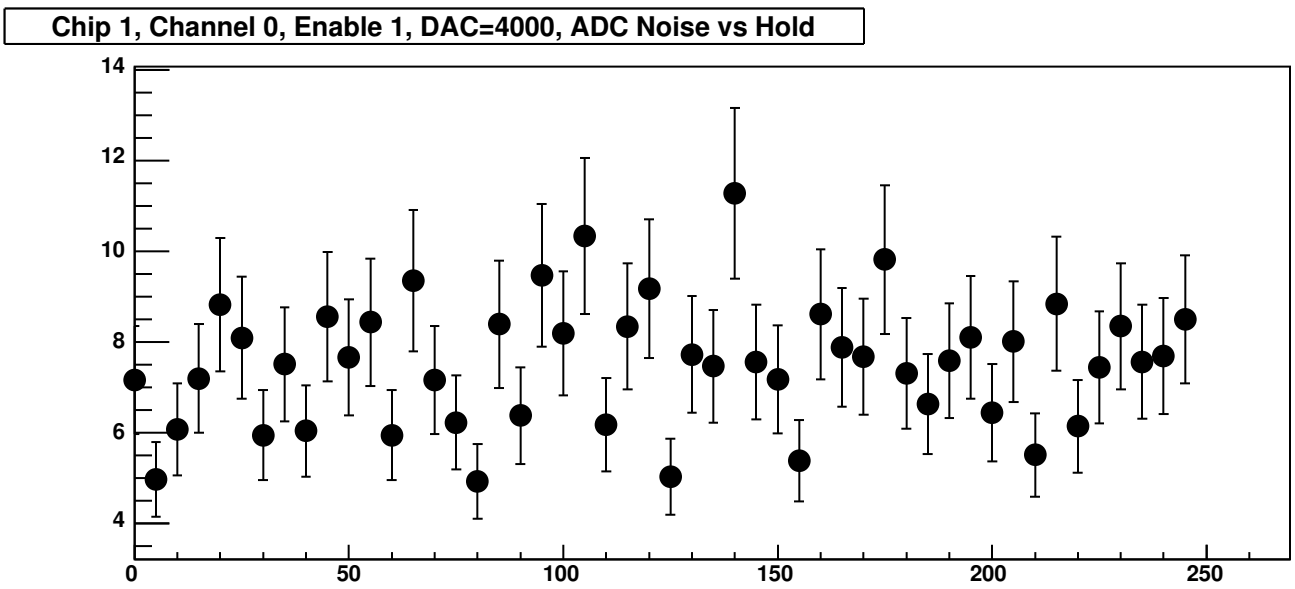
Chip 1, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold

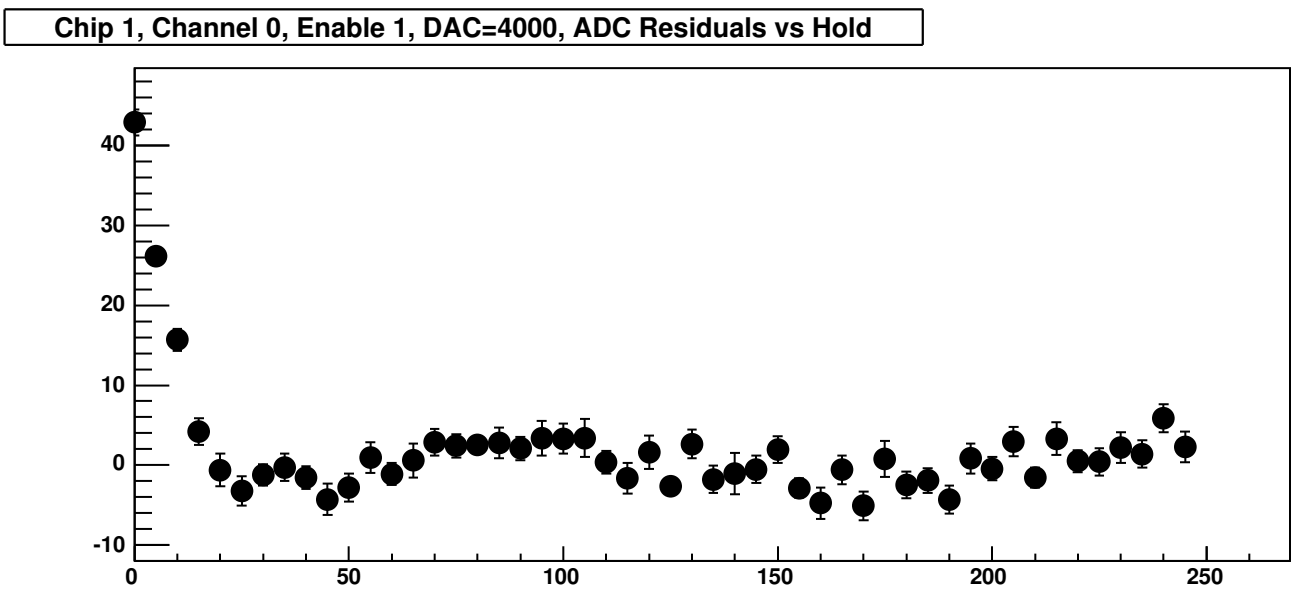


Chip 1, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold

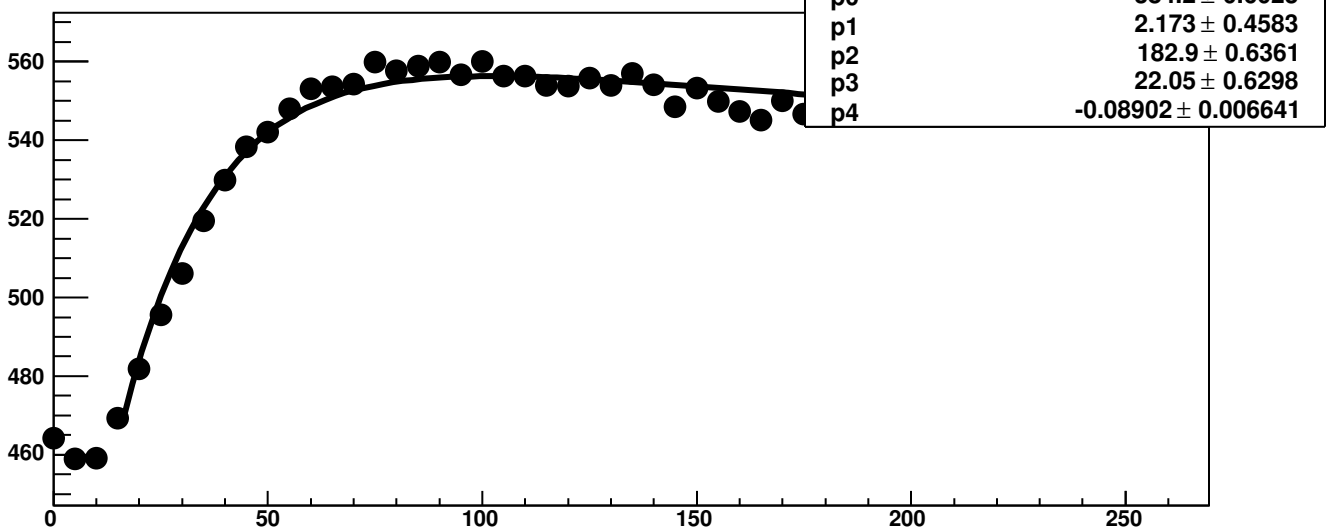



Chip 1, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 104.2 / 41
p0 394.9 ± 3.099
p1 -4.784 ± 0.8566
p2 193.5 ± 3.192
p3 30.59 ± 0.9678
p4 -0.1652 ± 0.008586
580
560
540
520
500
480
460
0
50
100
150
200
250

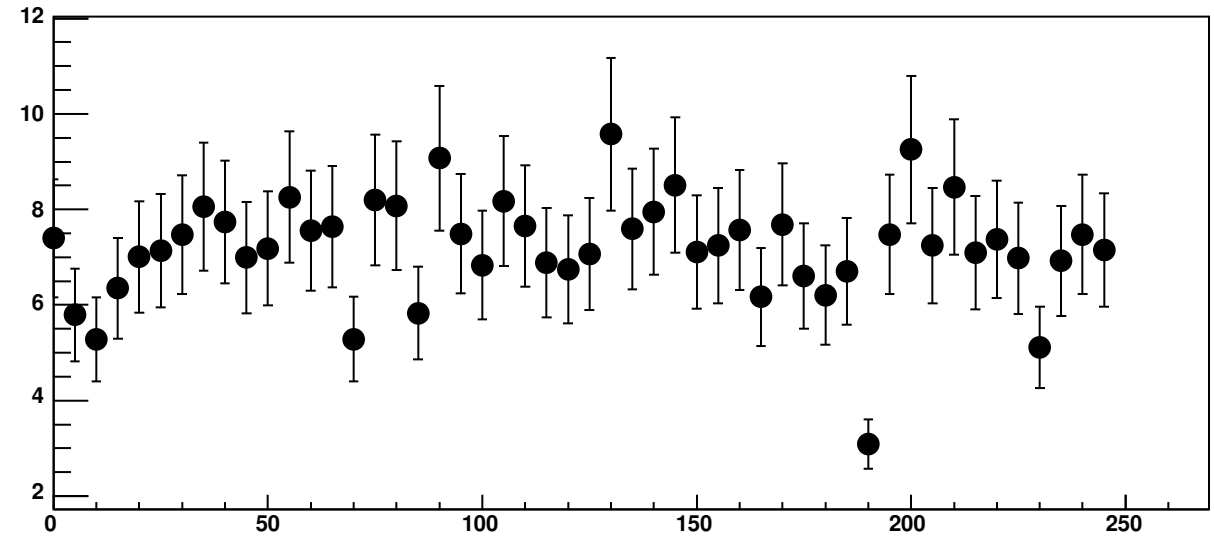

Chip 1, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 1, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

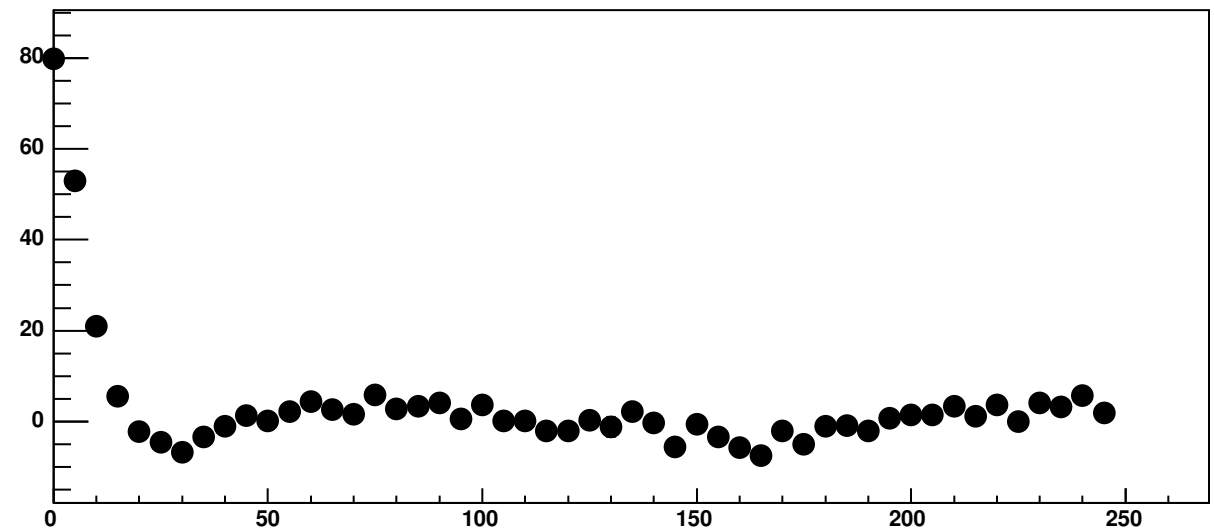
Chip 1, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold



Chip 1, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold

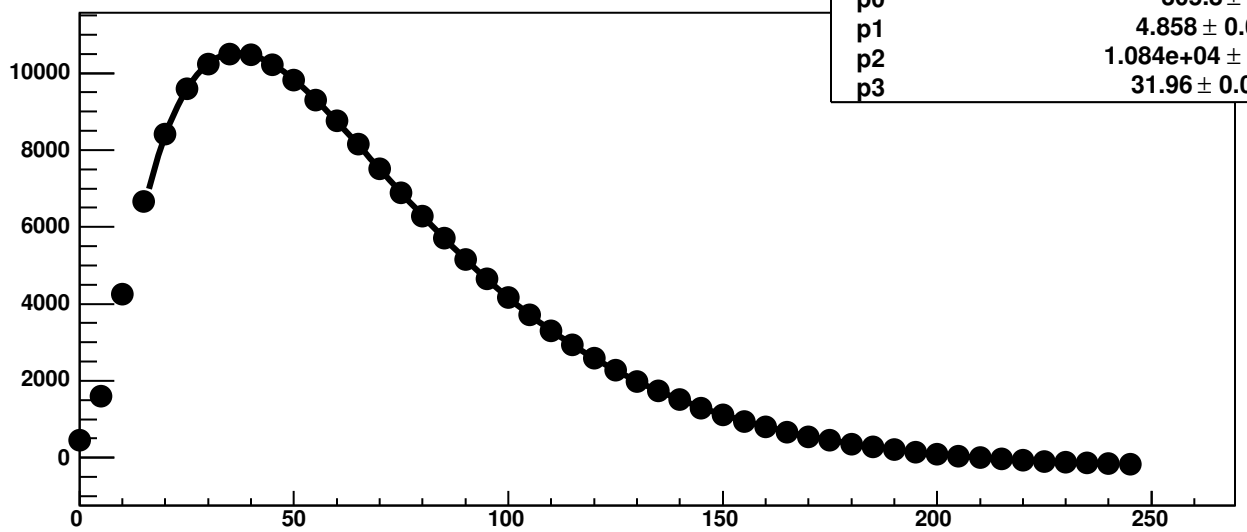


Chip 1, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold

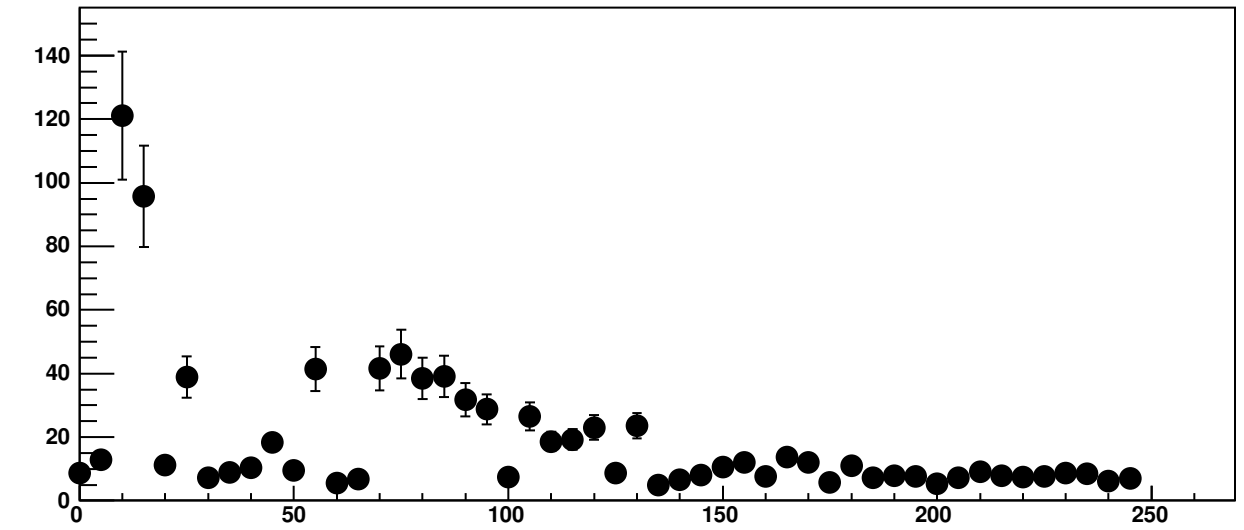


Chip 1, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold

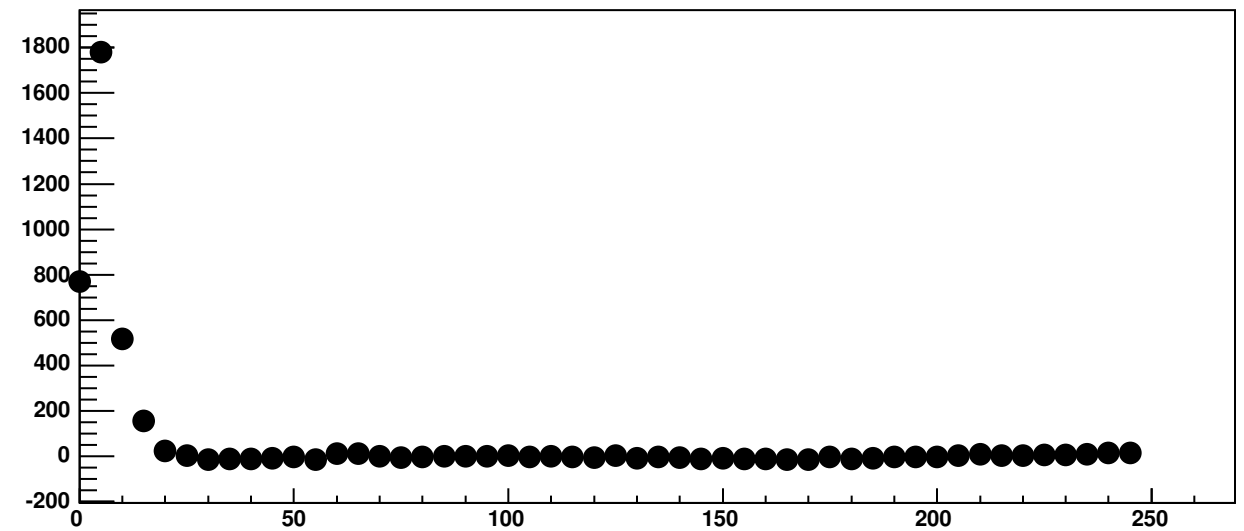
χ^2 / ndf	813.9 / 42
p0	-305.8 ± 0.6347
p1	4.858 ± 0.009461
p2	1.084e+04 ± 0.9168
p3	31.96 ± 0.006353



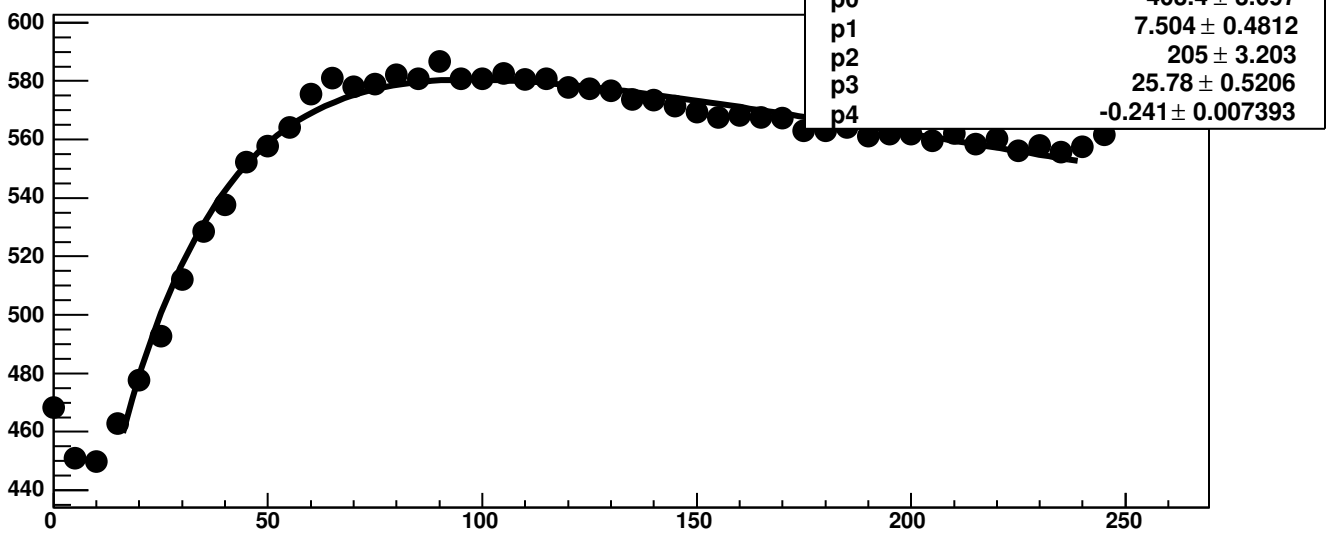
Chip 1, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold



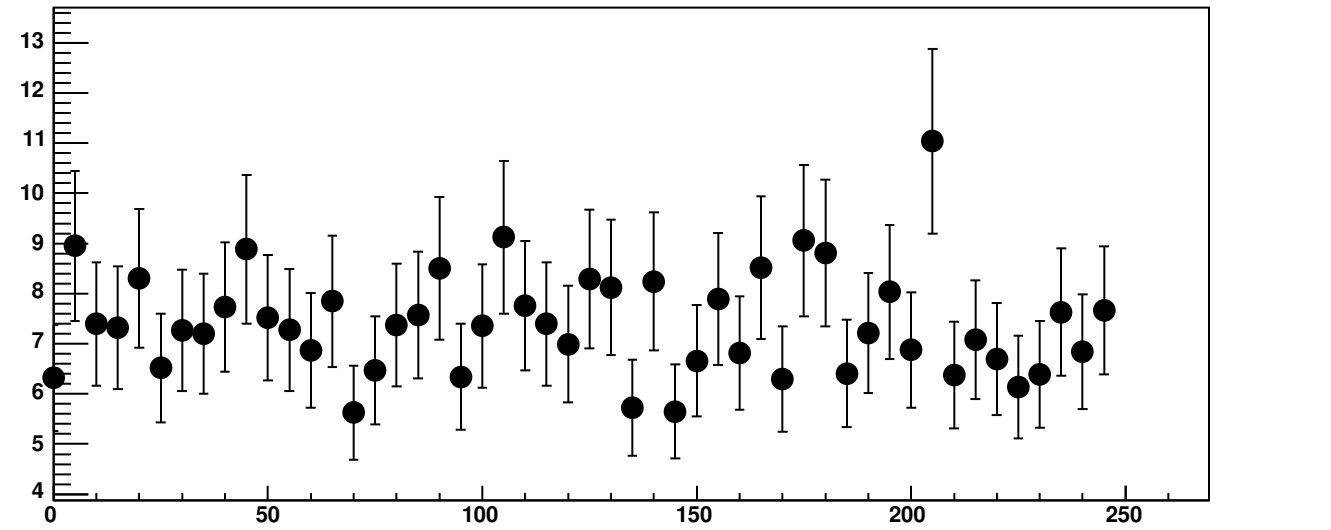
Chip 1, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold



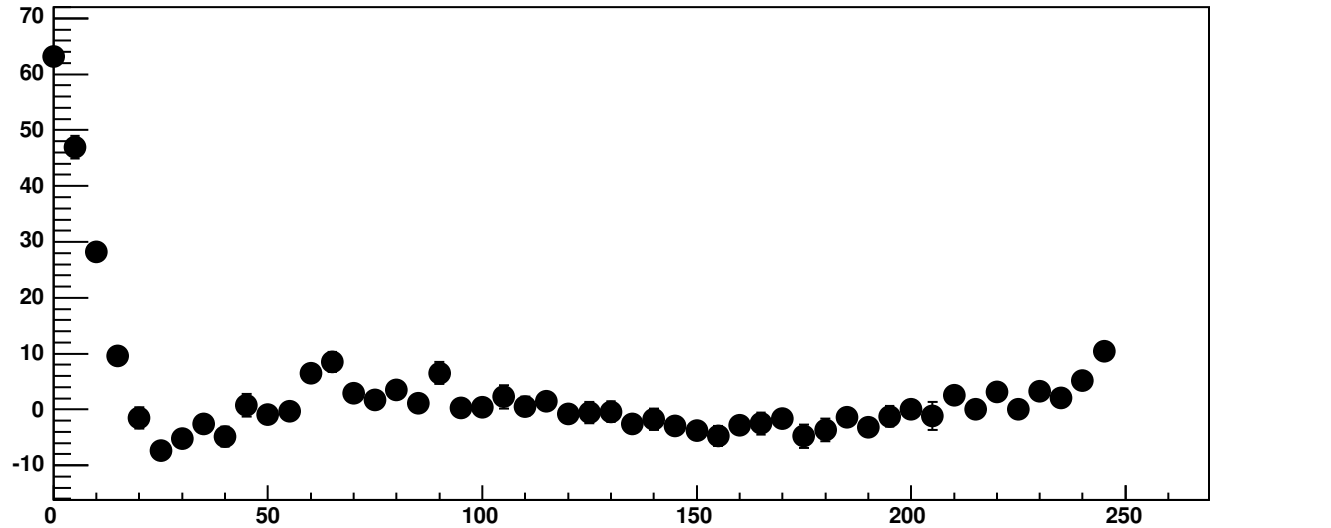
Chip 1, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold

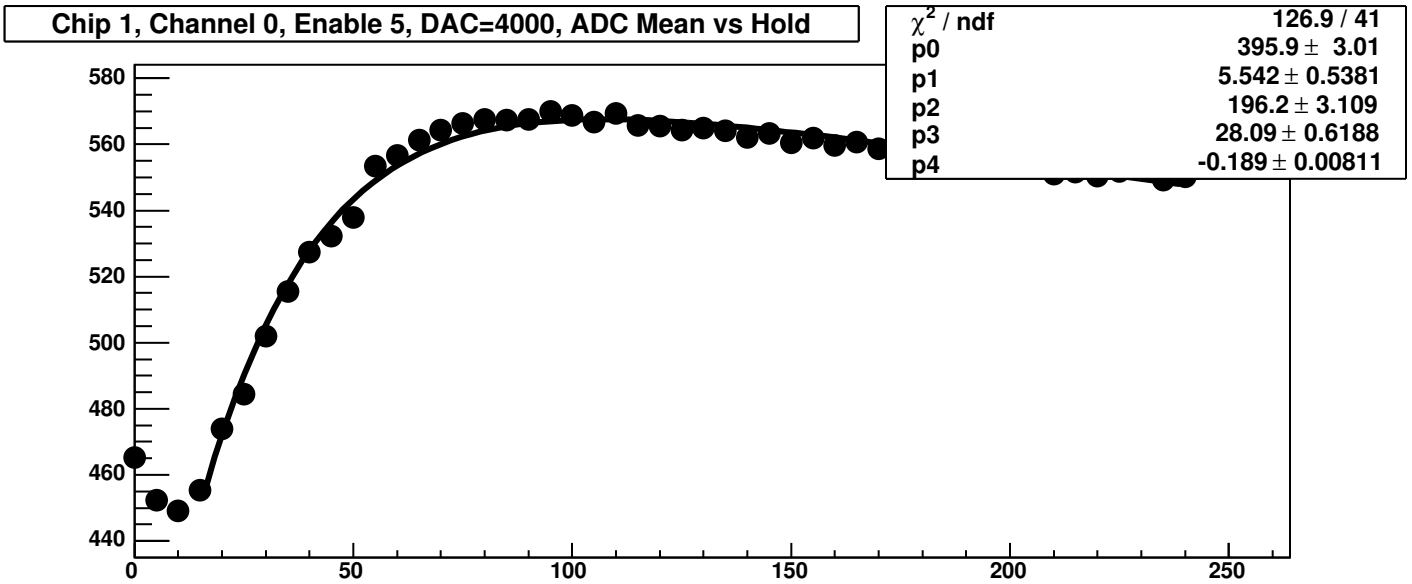


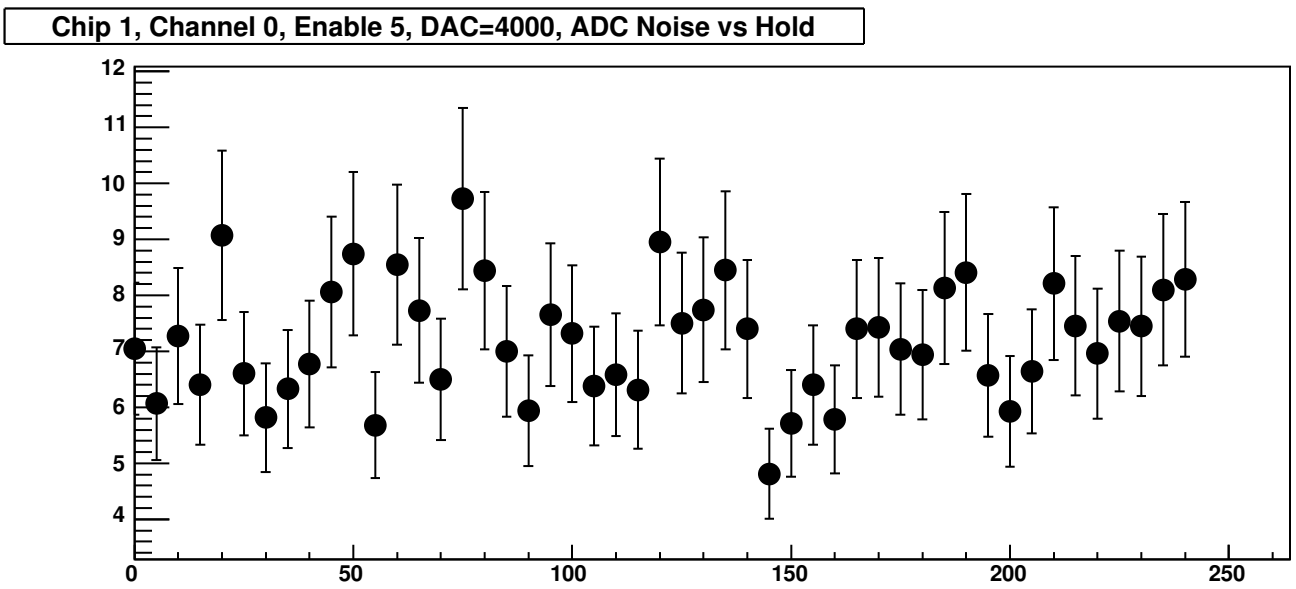
Chip 1, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold

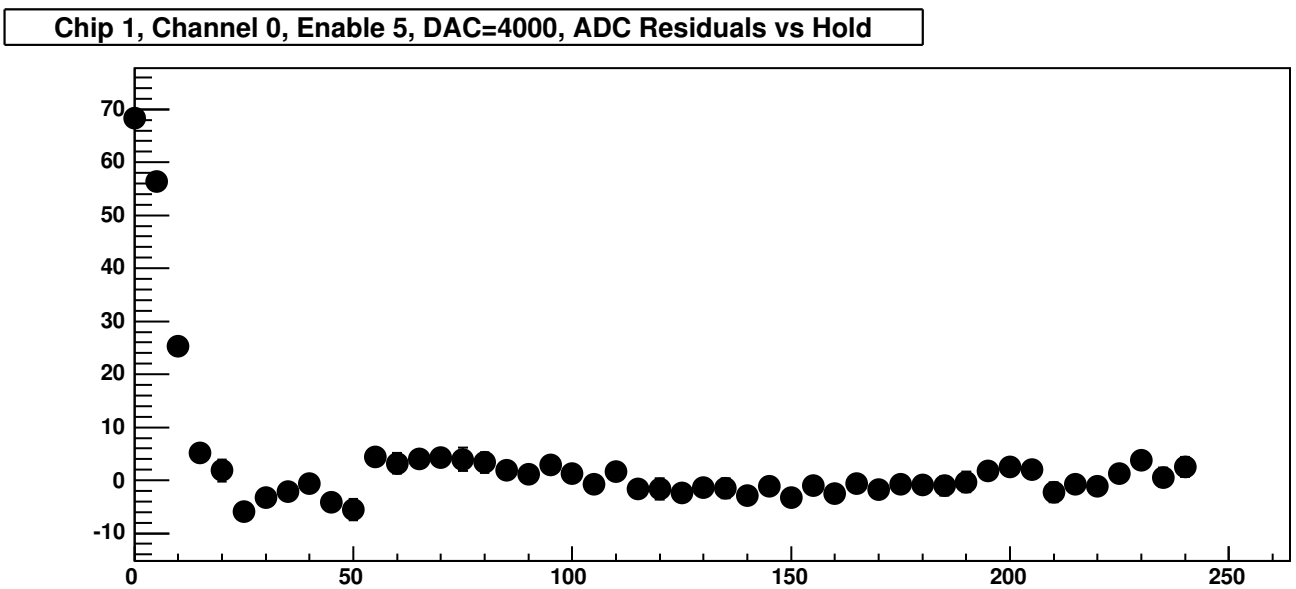


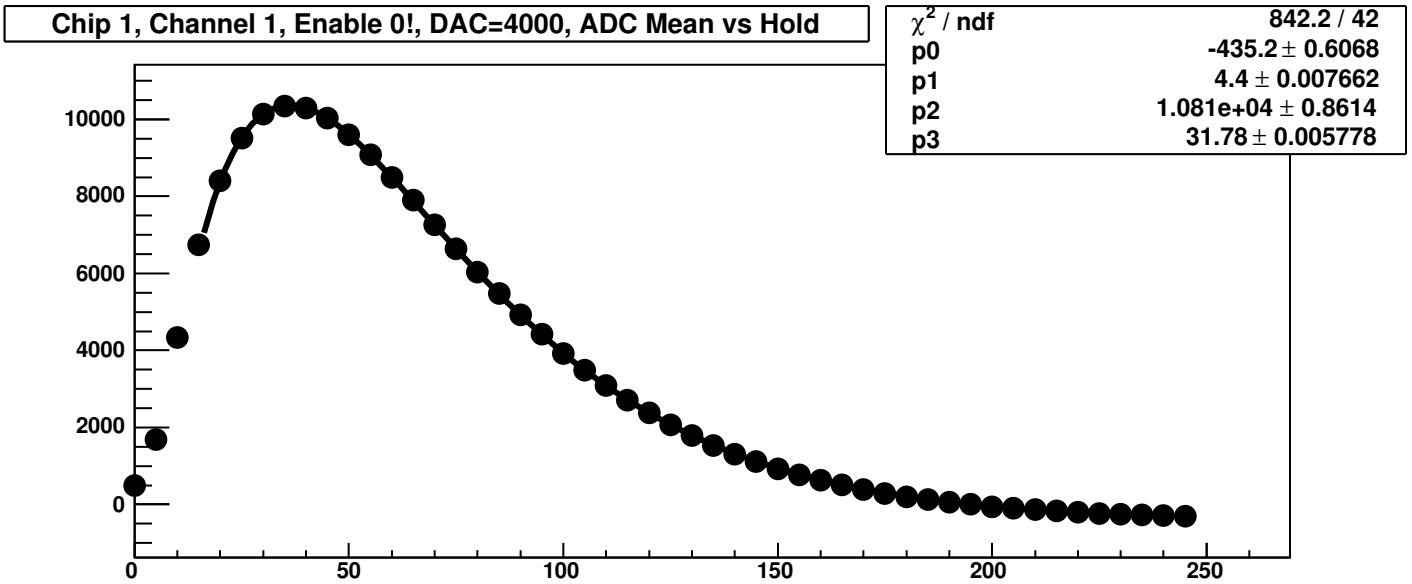
Chip 1, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold

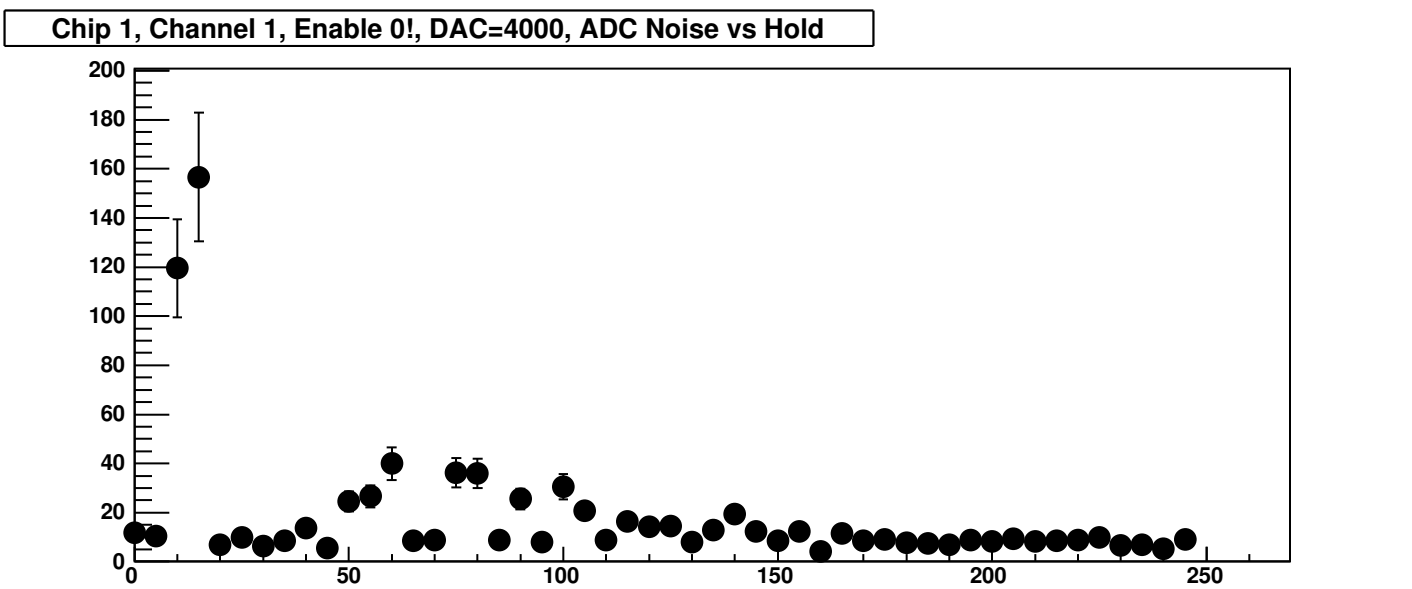


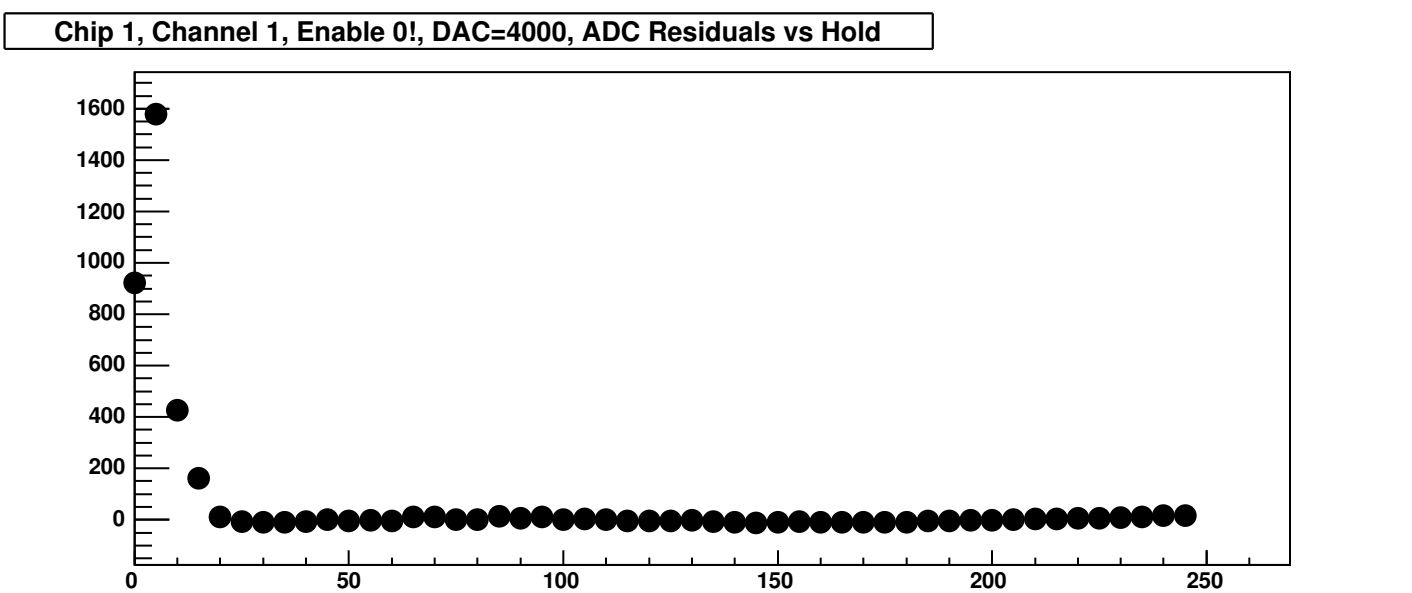

Chip 1, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 126.9 / 41
p0 395.9 ± 3.01
p1 5.542 ± 0.5381
p2 196.2 ± 3.109
p3 28.09 ± 0.6188
p4 -0.189 ± 0.00811
580
560
540
520
500
480
460
440
0
50
100
150
200
250

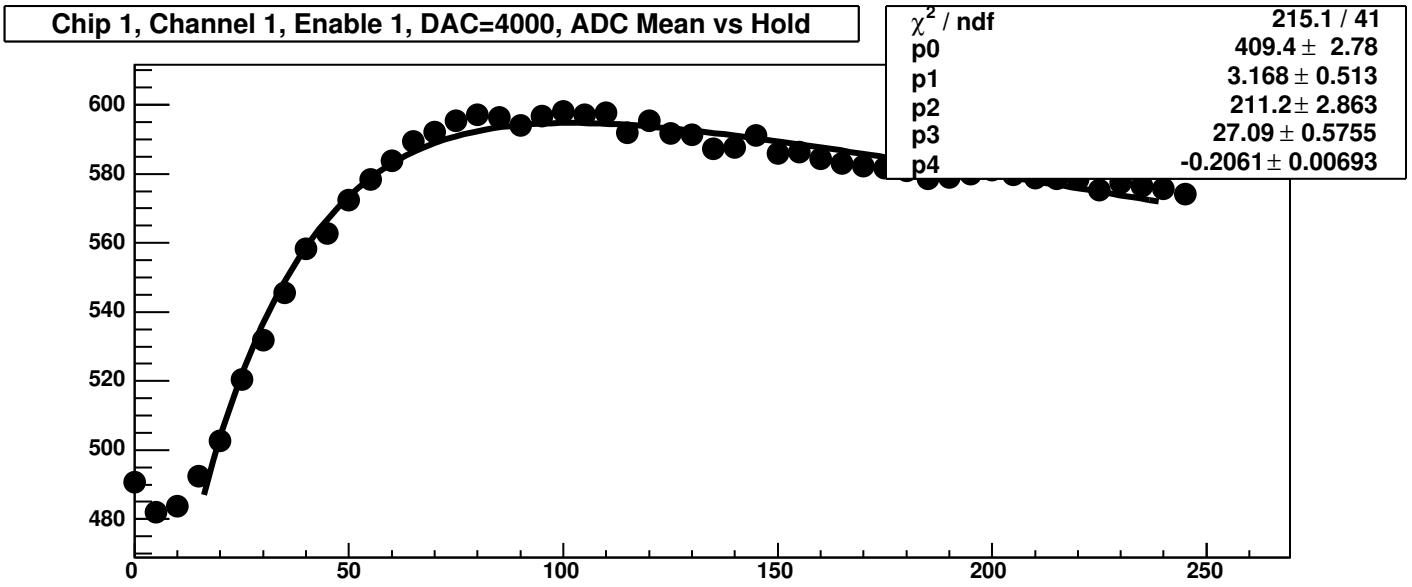

Chip 1, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

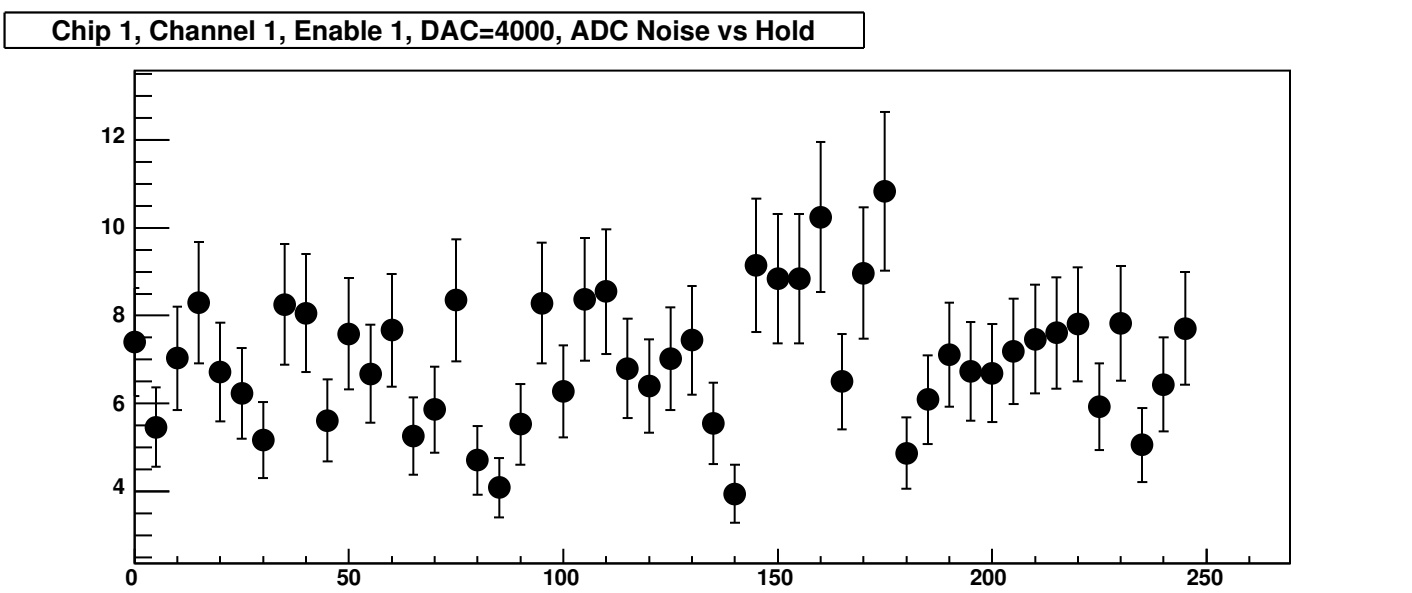

Chip 1, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

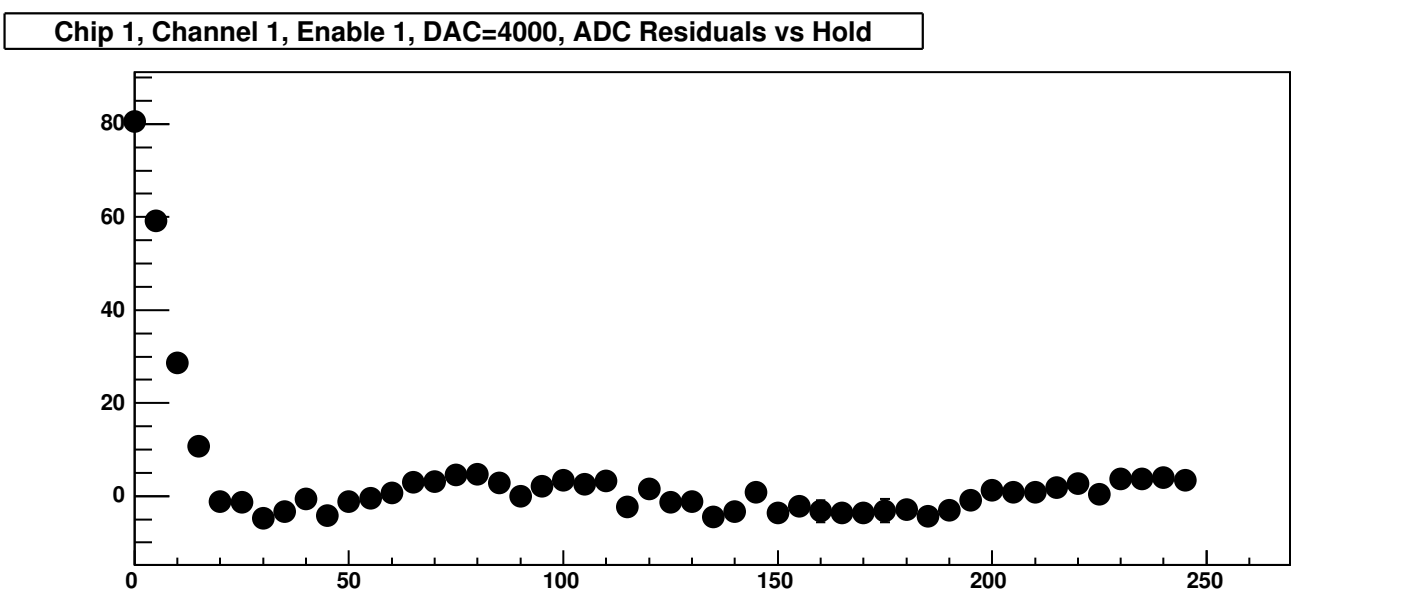

Chip 1, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 842.2 / 42
p0 -435.2 ± 0.6068
p1 4.4 ± 0.007662
p2 1.081e+04 ± 0.8614
p3 31.78 ± 0.005778
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 1, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

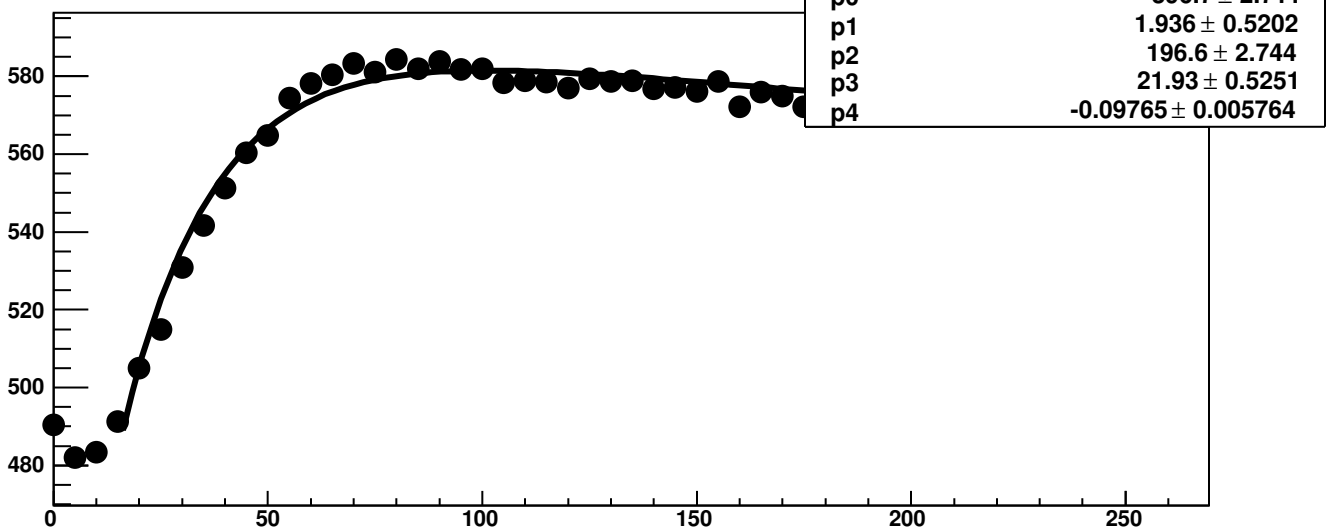

Chip 1, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 1, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 215.1 / 41
p0 409.4 ± 2.78
p1 3.168 ± 0.513
p2 211.2 ± 2.863
p3 27.09 ± 0.5755
p4 -0.2061 ± 0.00693
600
580
560
540
520
500
480
0
50
100
150
200
250

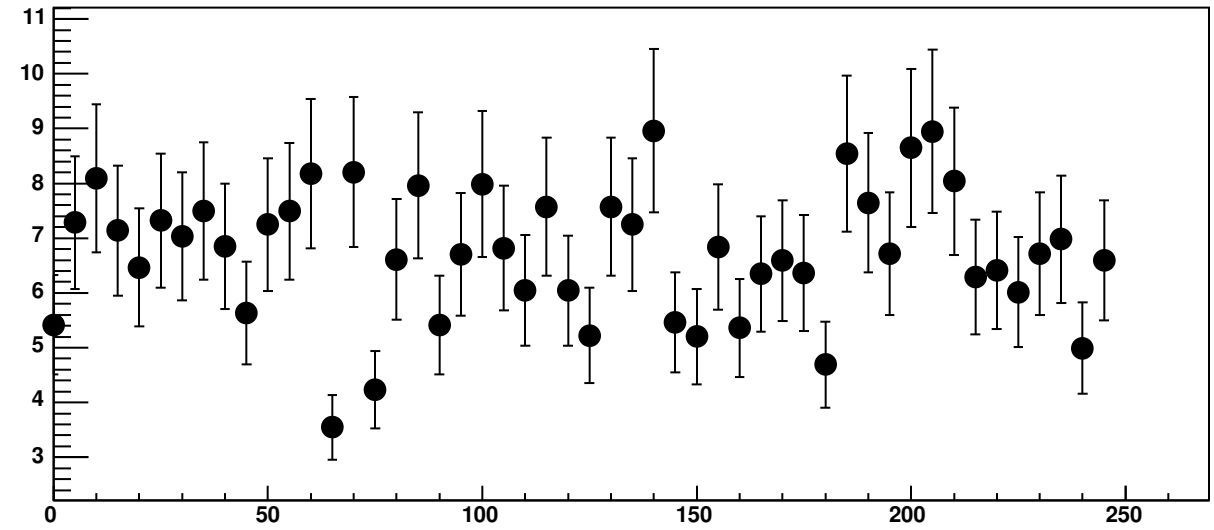

Chip 1, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 1, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold
80
60
40
20
0
0
50
100
150
200
250

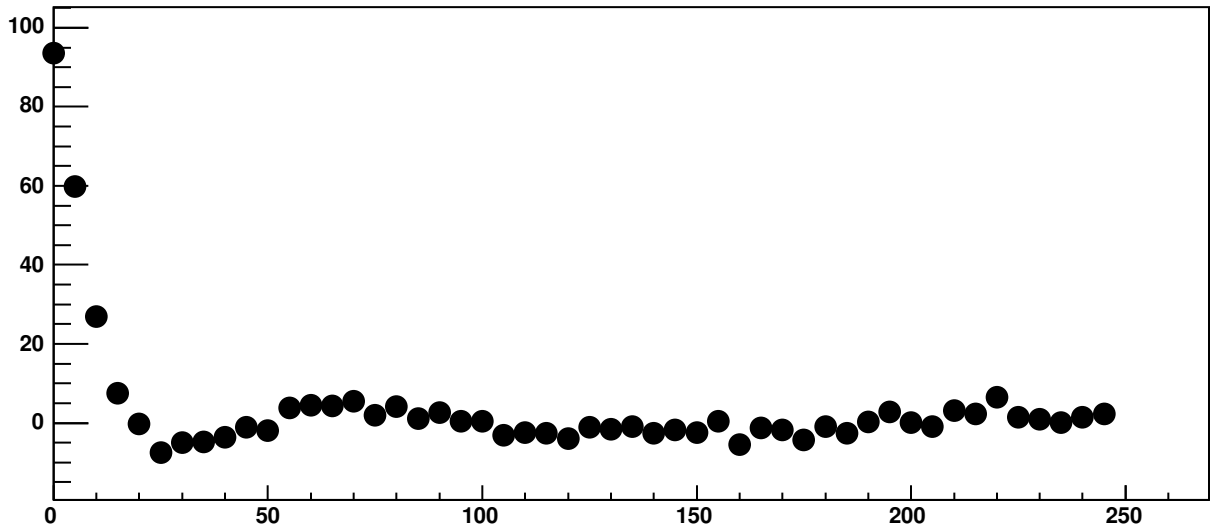
Chip 1, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

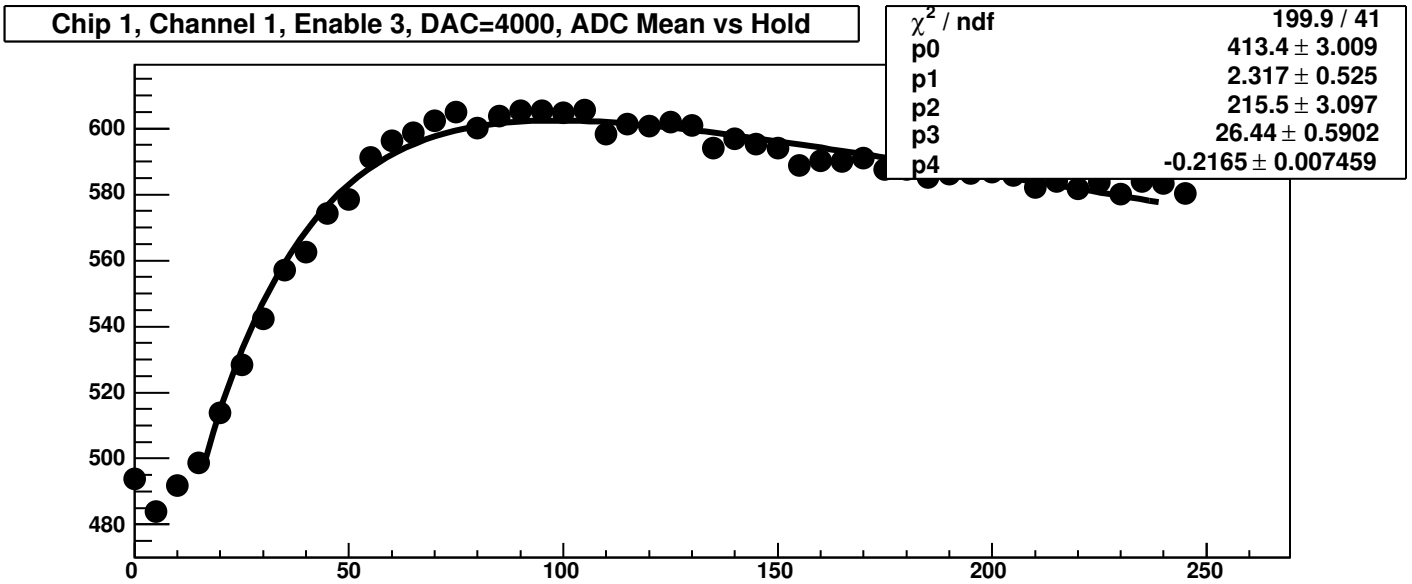


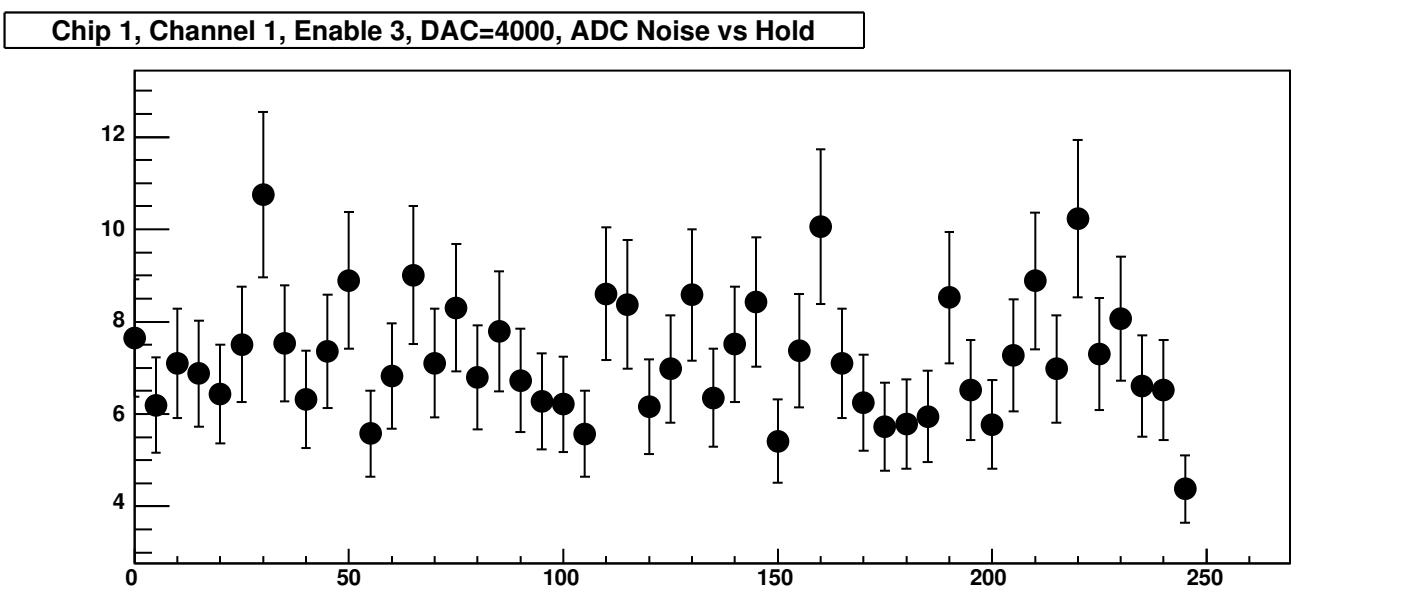
Chip 1, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold

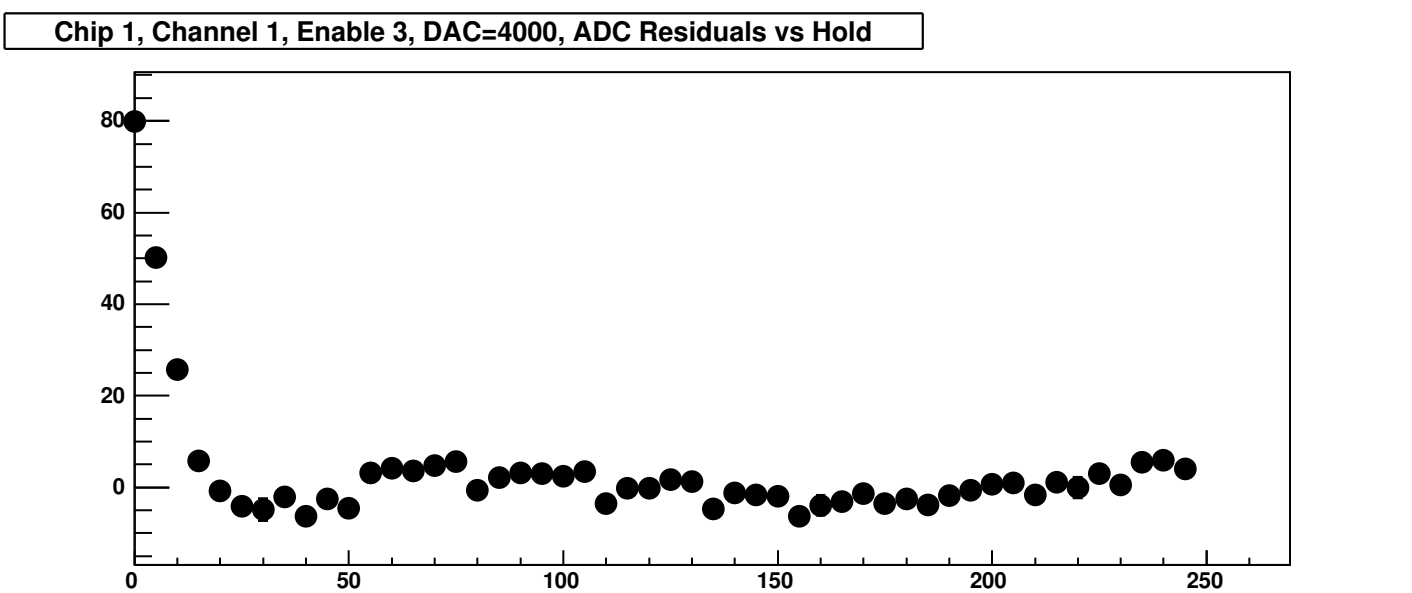


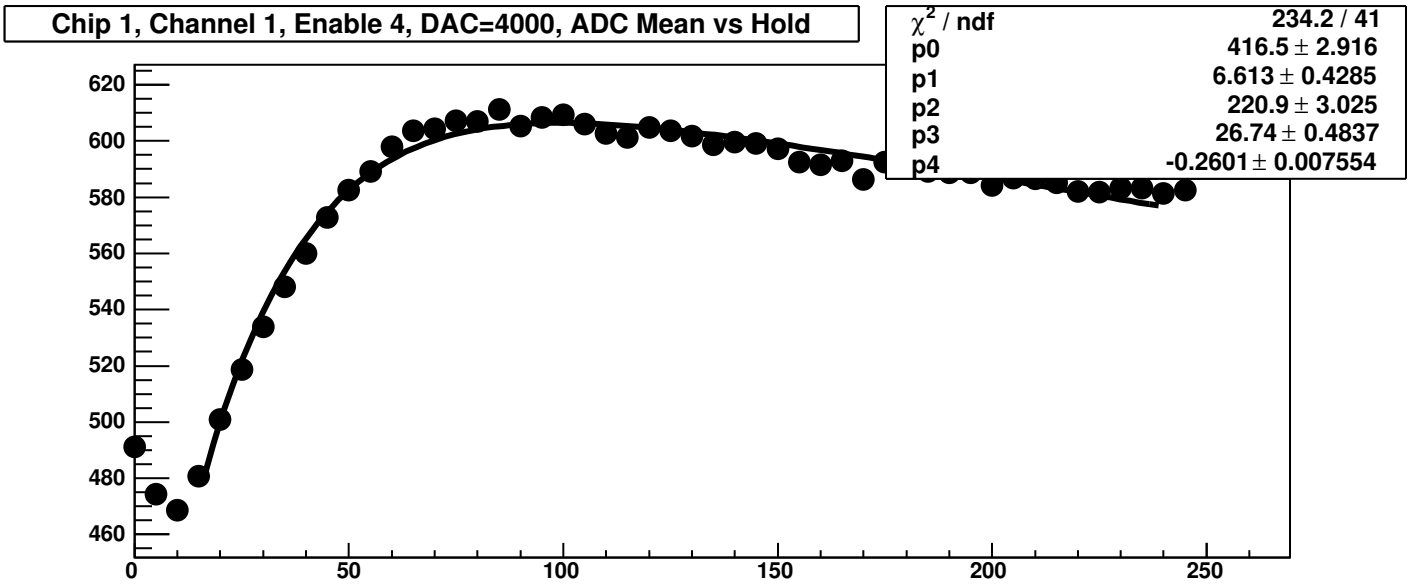
Chip 1, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

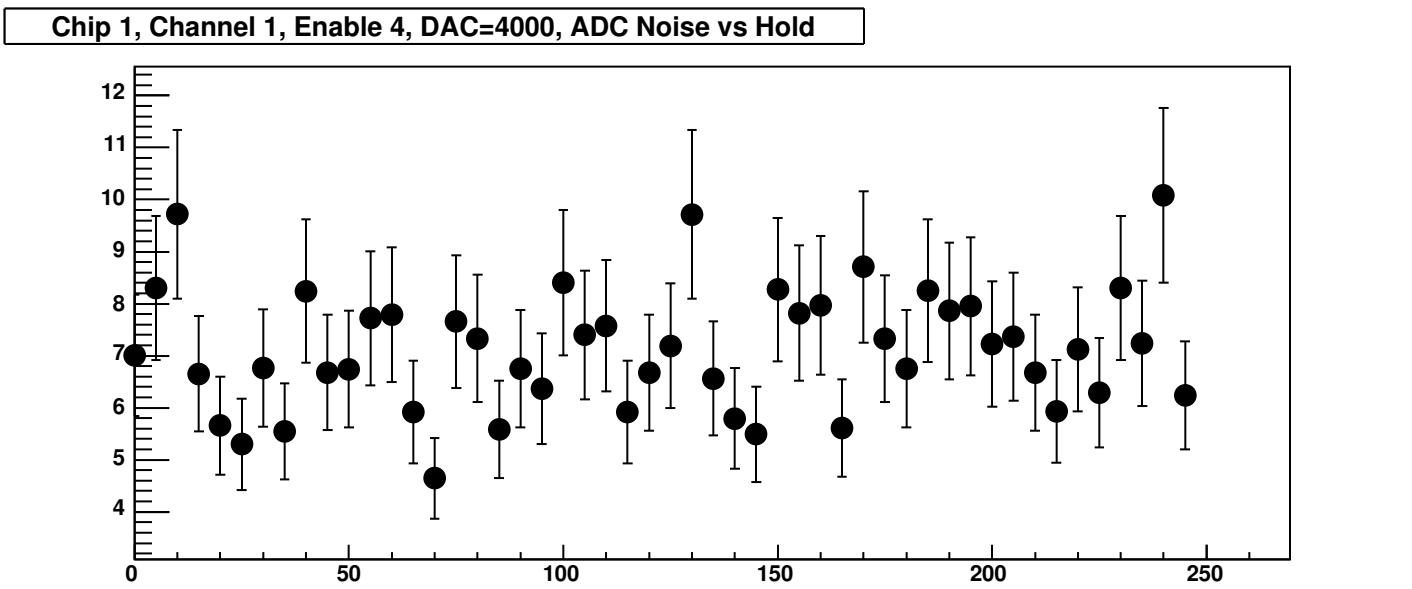


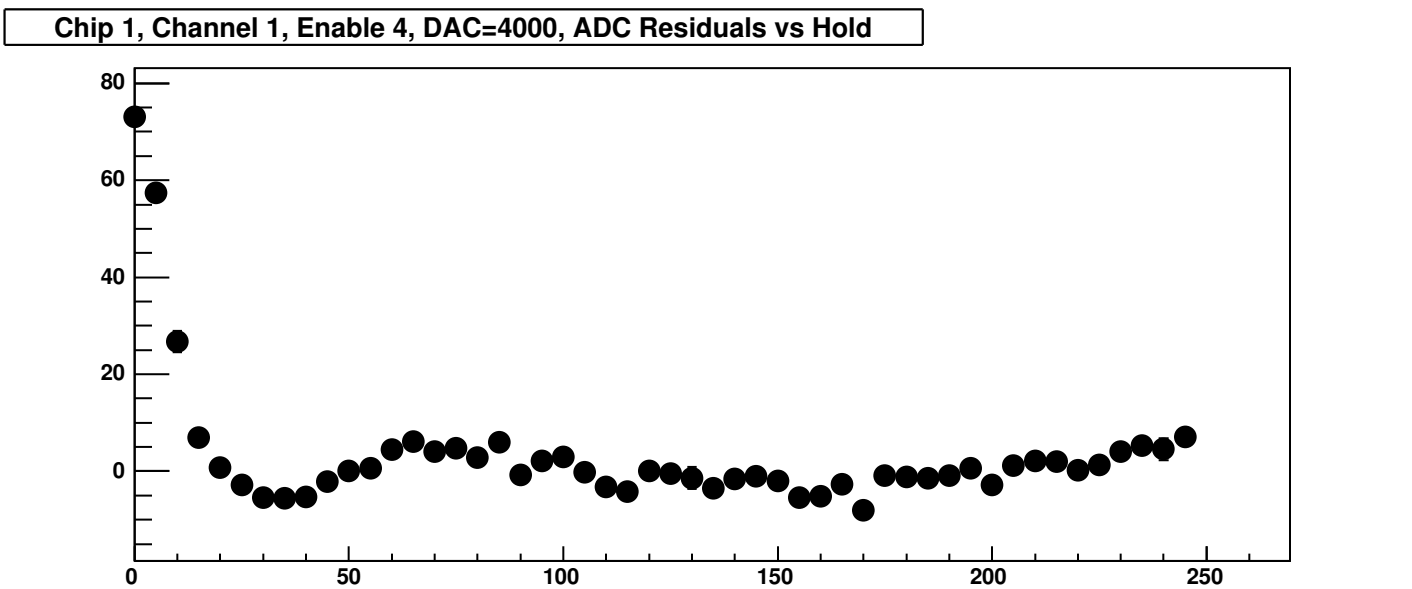

Chip 1, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 199.9 / 41
p0 413.4 ± 3.009
p1 2.317 ± 0.525
p2 215.5 ± 3.097
p3 26.44 ± 0.5902
p4 -0.2165 ± 0.007459
600
580
560
540
520
500
480
0
50
100
150
200
250

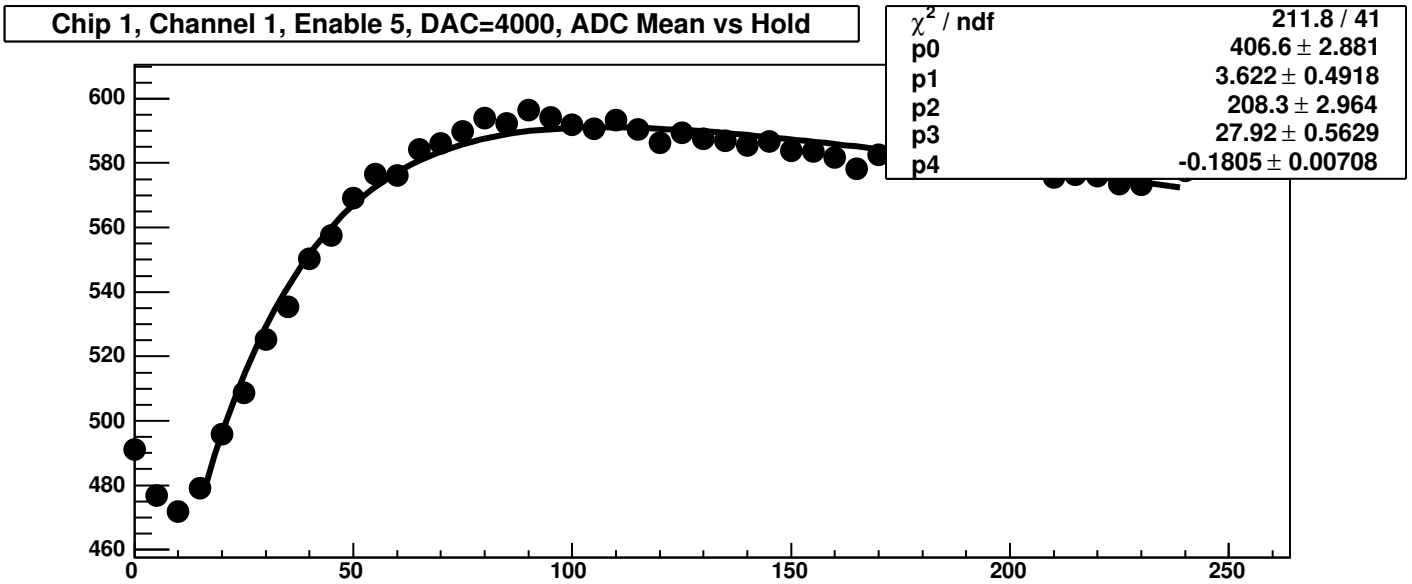

Chip 1, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

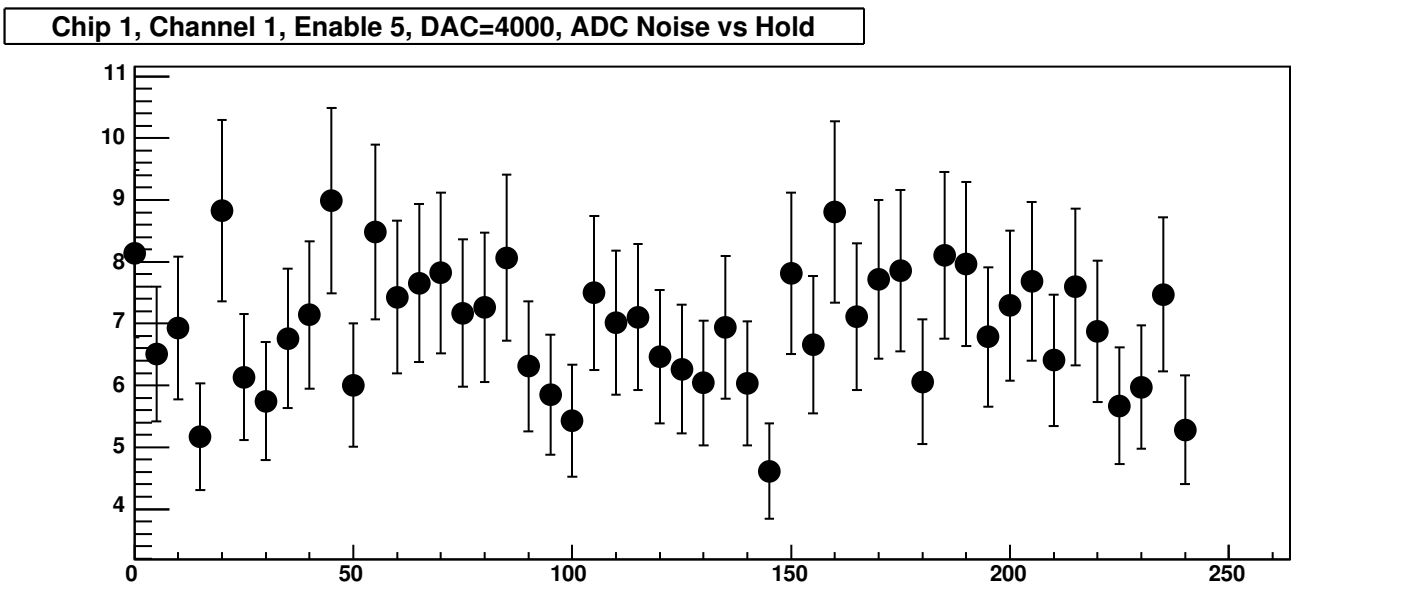

Chip 1, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold
80
60
40
20
0
0
50
100
150
200
250

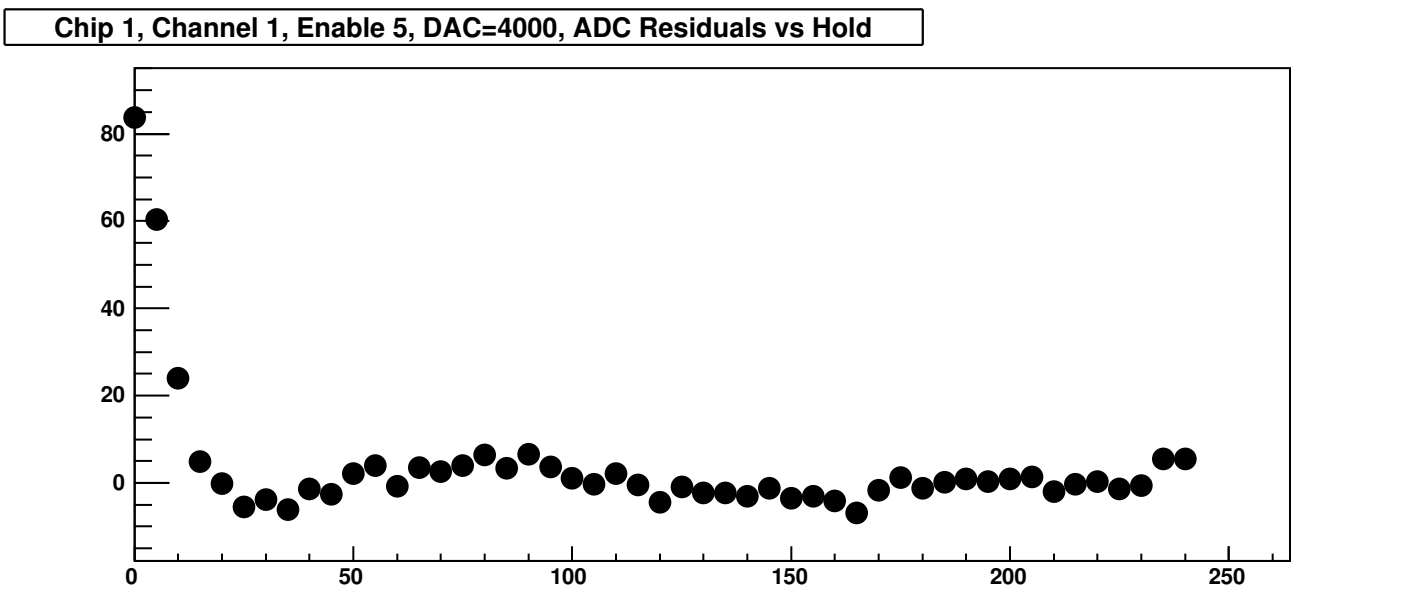

Chip 1, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 234.2 / 41
p0 416.5 ± 2.916
p1 6.613 ± 0.4285
p2 220.9 ± 3.025
p3 26.74 ± 0.4837
p4 -0.2601 ± 0.007554
620
600
580
560
540
520
500
480
460
0
50
100
150
200
250

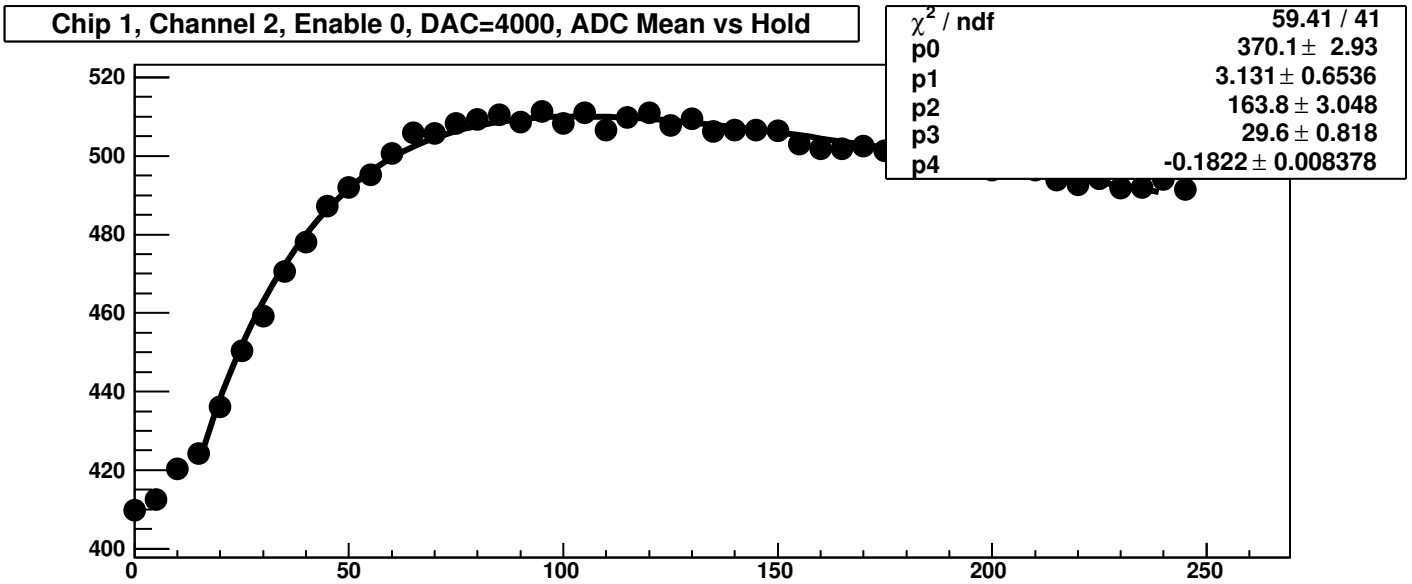

Chip 1, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

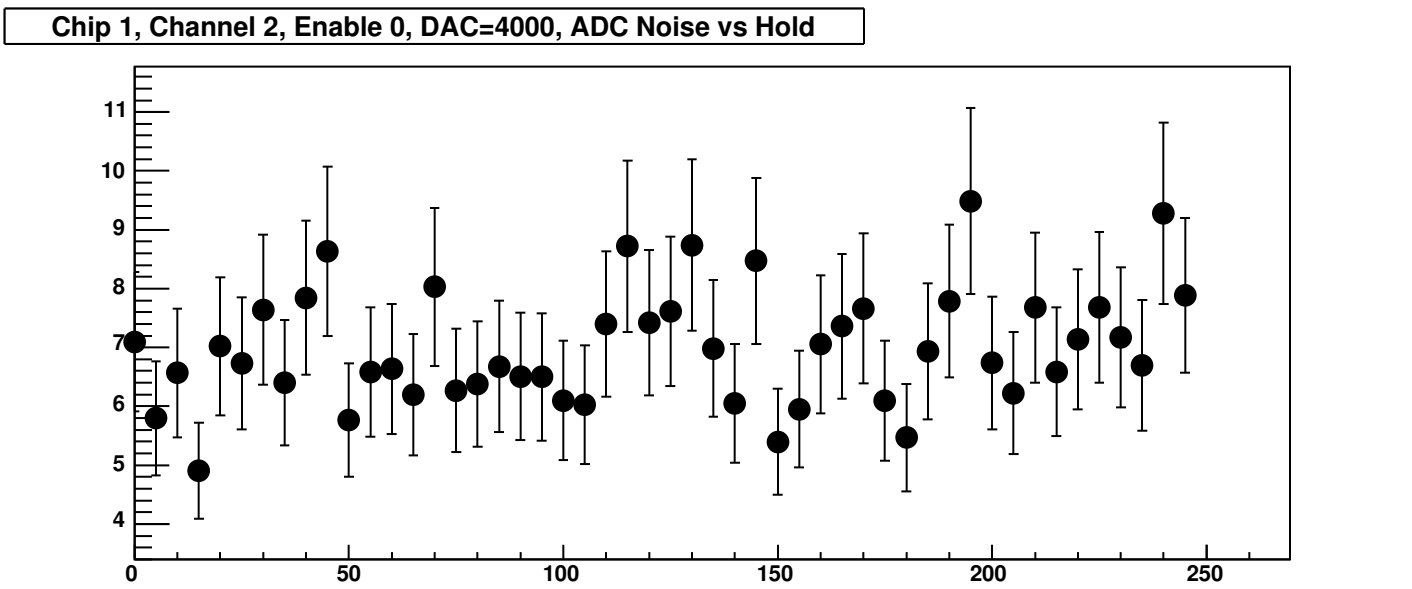

Chip 1, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold
80
60
40
20
0
0
50
100
150
200
250

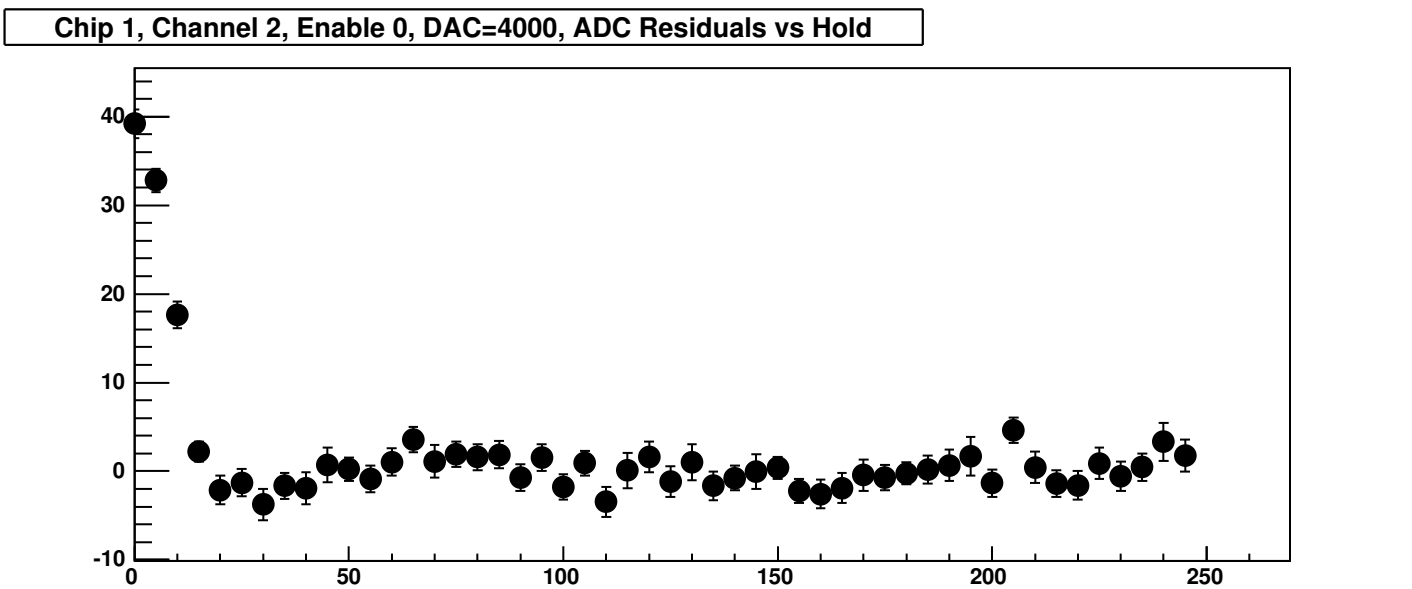

Chip 1, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 211.8 / 41
p0 406.6 ± 2.881
p1 3.622 ± 0.4918
p2 208.3 ± 2.964
p3 27.92 ± 0.5629
p4 -0.1805 ± 0.00708
600
580
560
540
520
500
480
460
0
50
100
150
200
250


Chip 1, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250

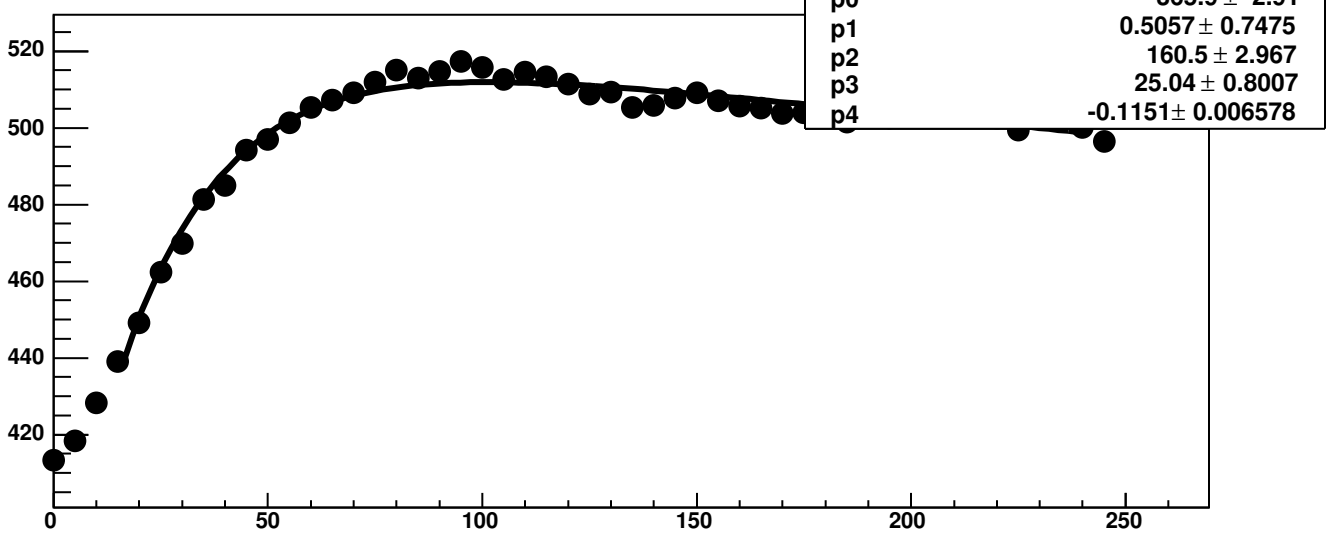

Chip 1, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold
80
60
40
20
0
0
50
100
150
200
250


Chip 1, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 59.41 / 41
p0 370.1 ± 2.93
p1 3.131 ± 0.6536
p2 163.8 ± 3.048
p3 29.6 ± 0.818
p4 -0.1822 ± 0.008378
520
500
480
460
440
420
400
0
50
100
150
200
250

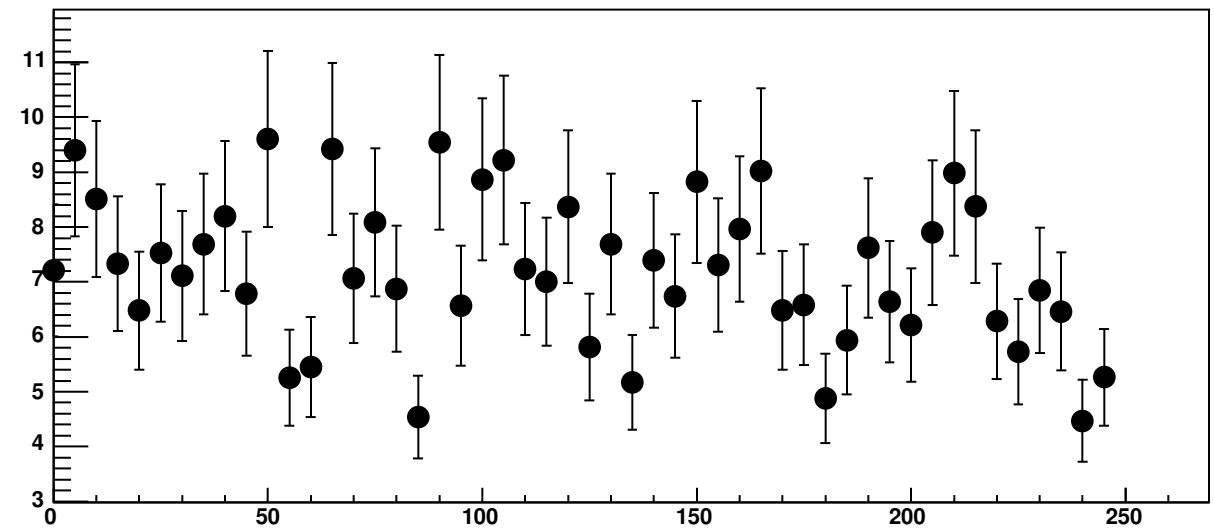

Chip 1, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 1, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

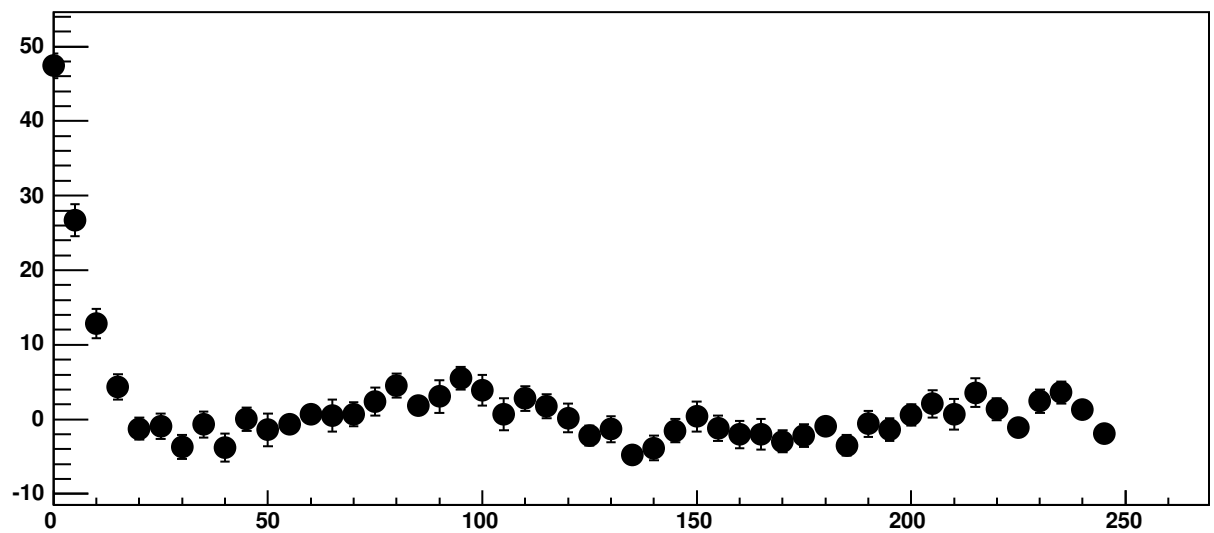
Chip 1, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

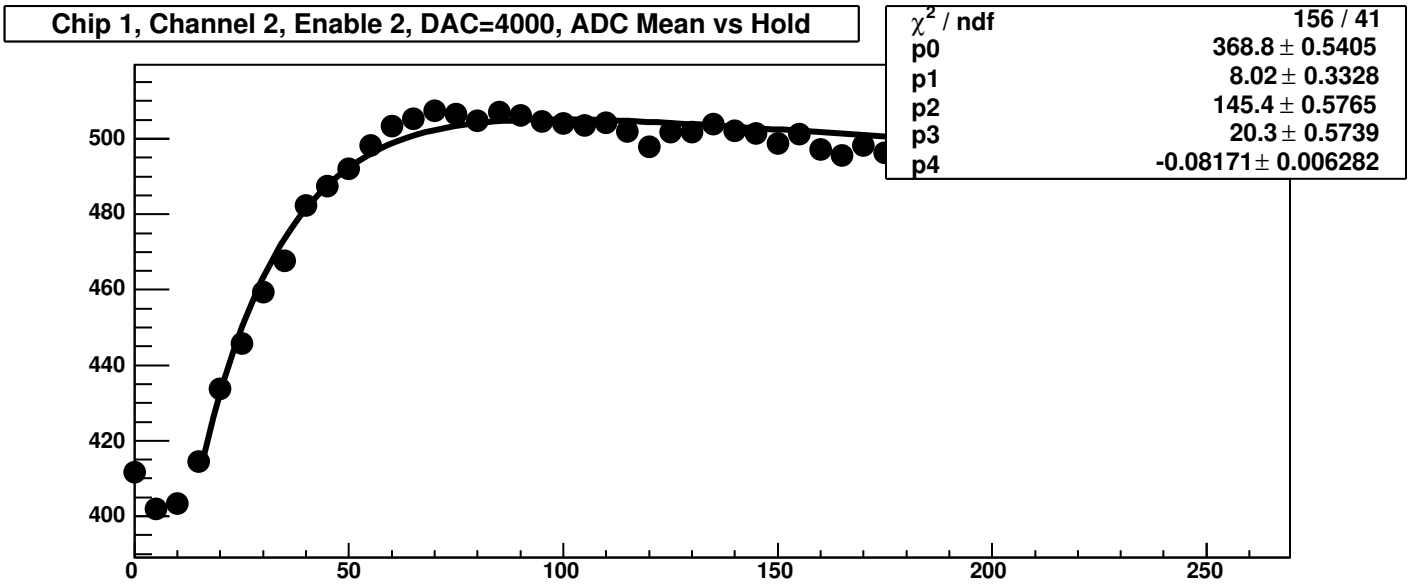


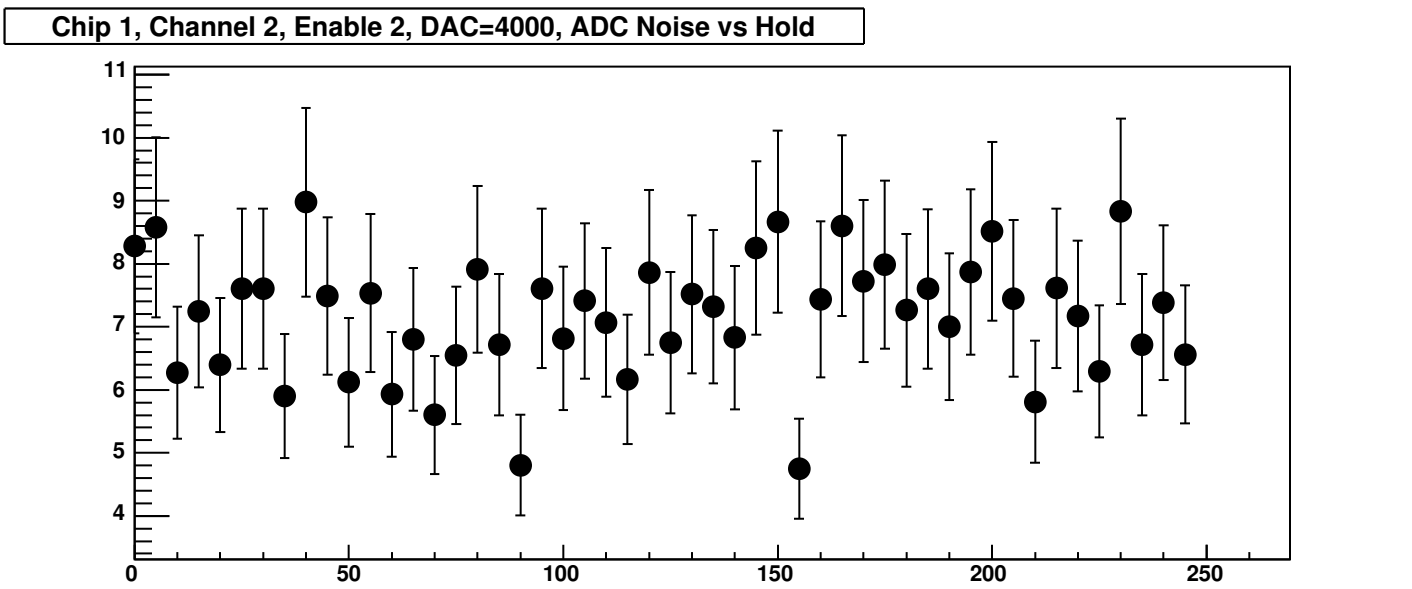
Chip 1, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold

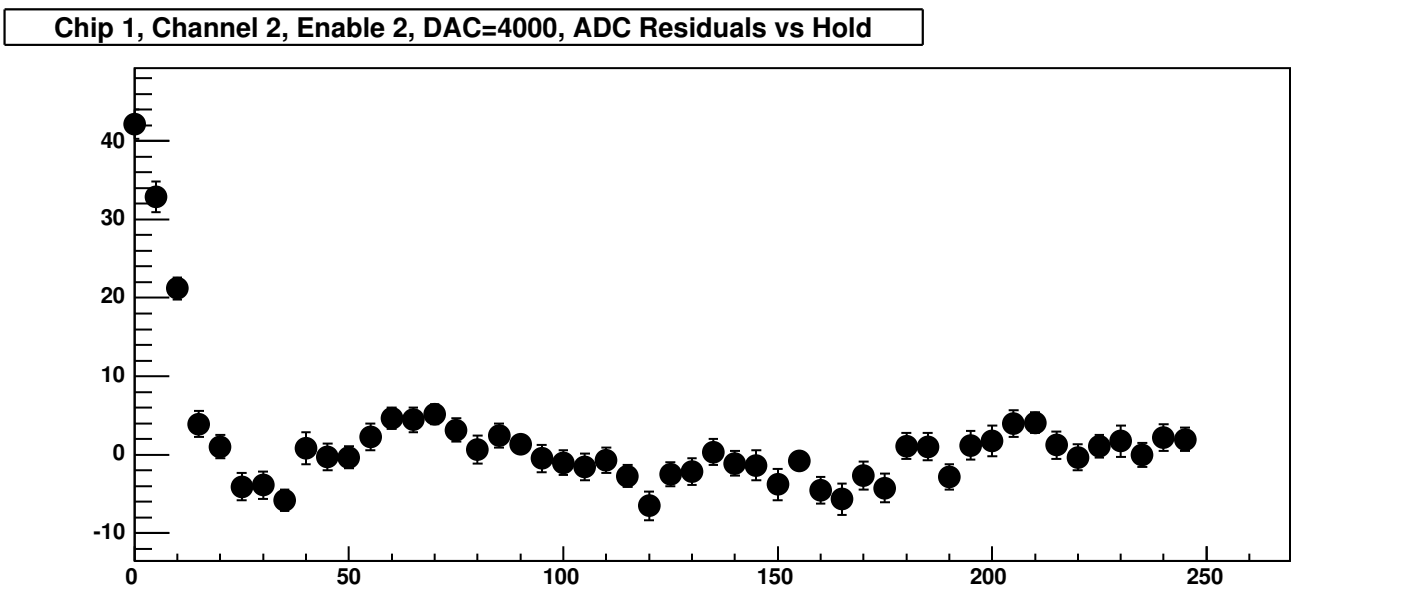


Chip 1, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold



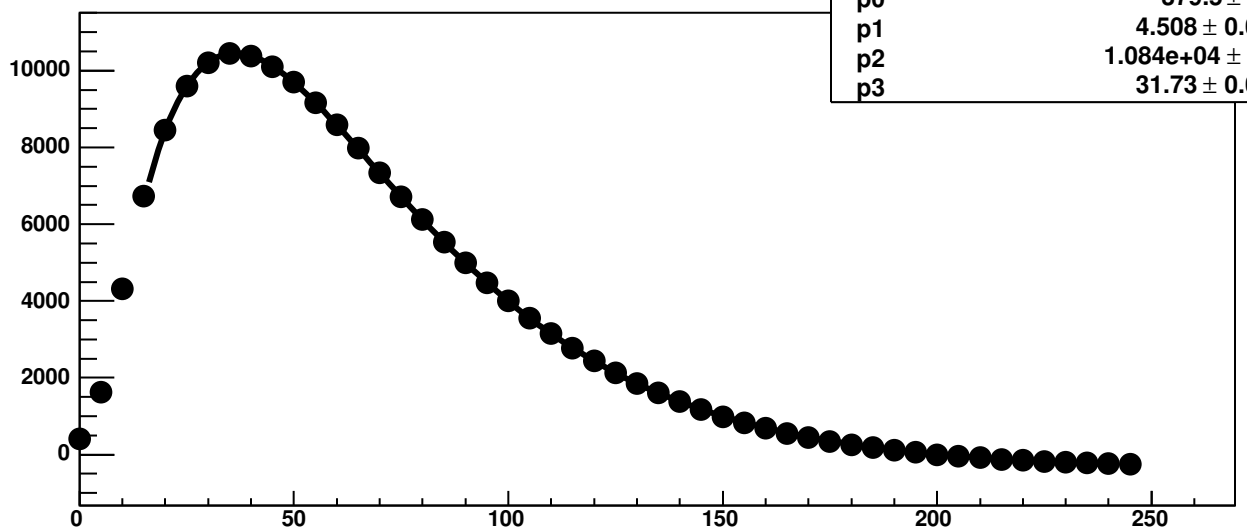

Chip 1, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 156 / 41
p0 368.8 ± 0.5405
p1 8.02 ± 0.3328
p2 145.4 ± 0.5765
p3 20.3 ± 0.5739
p4 -0.08171 ± 0.006282
500
480
460
440
420
400
0
50
100
150
200
250


Chip 1, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250

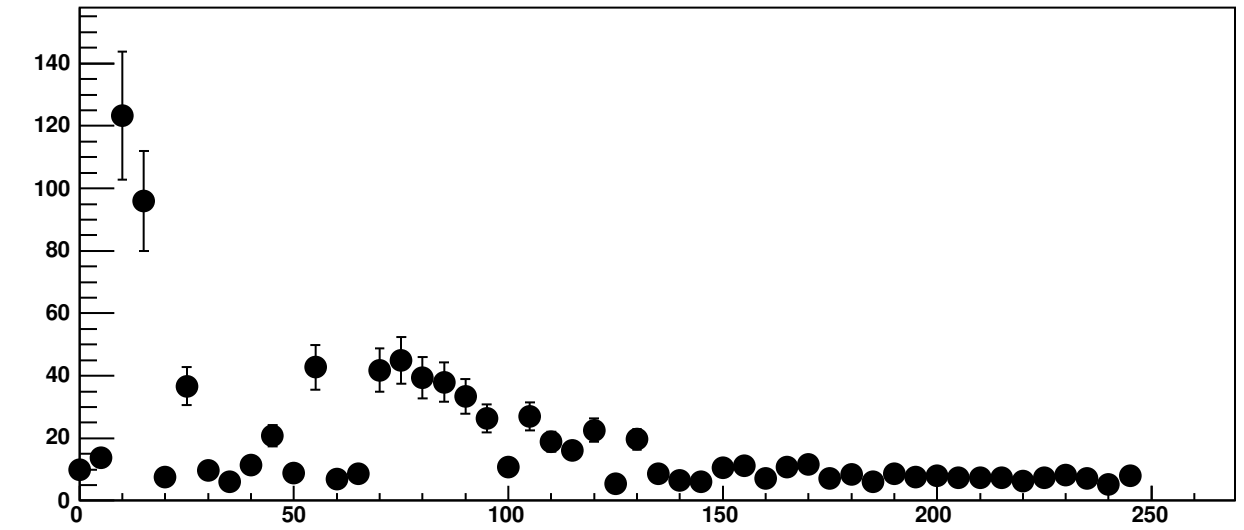

Chip 1, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

Chip 1, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold

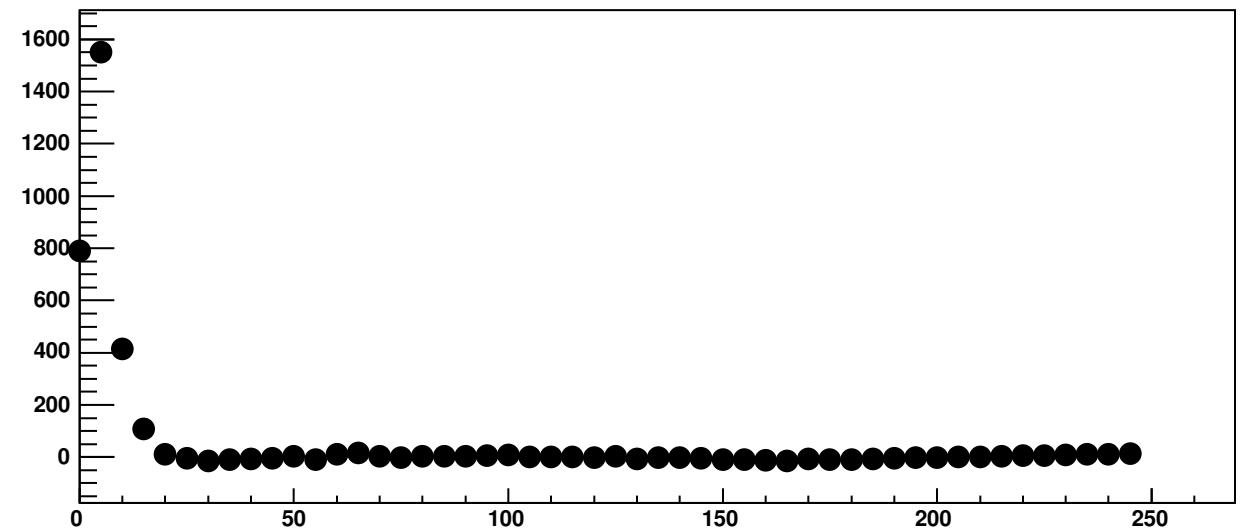
χ^2 / ndf	690 / 42
p0	-379.5 ± 0.5952
p1	4.508 ± 0.007971
p2	1.084e+04 ± 0.9023
p3	31.73 ± 0.005901

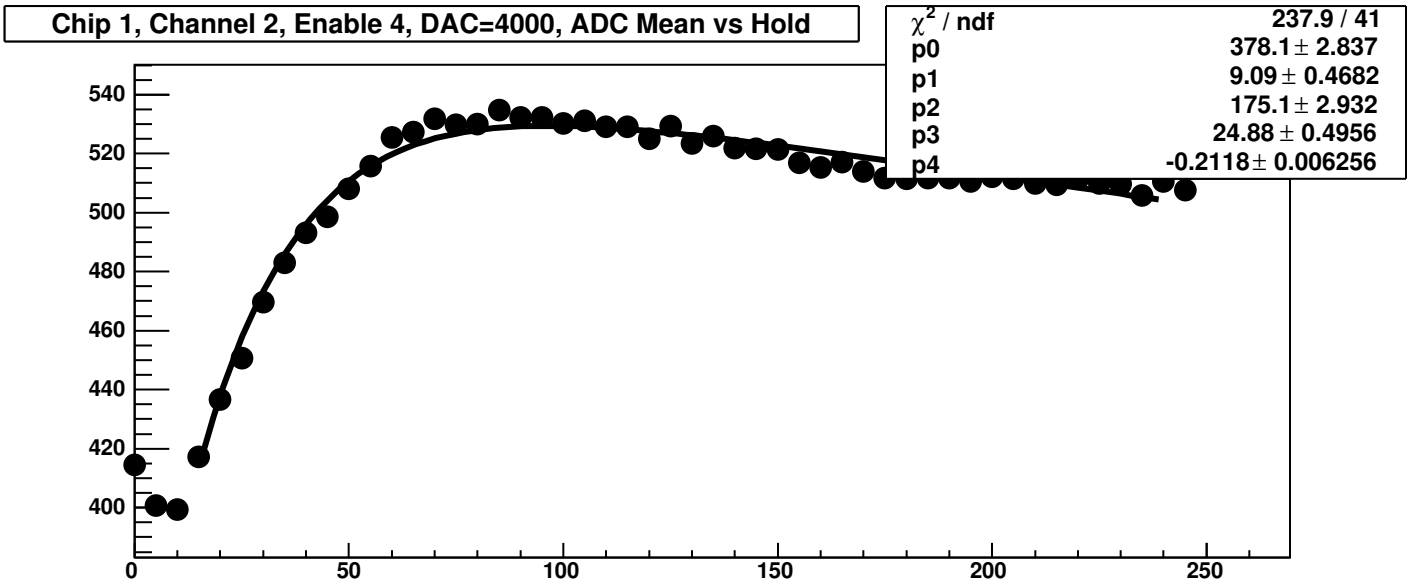


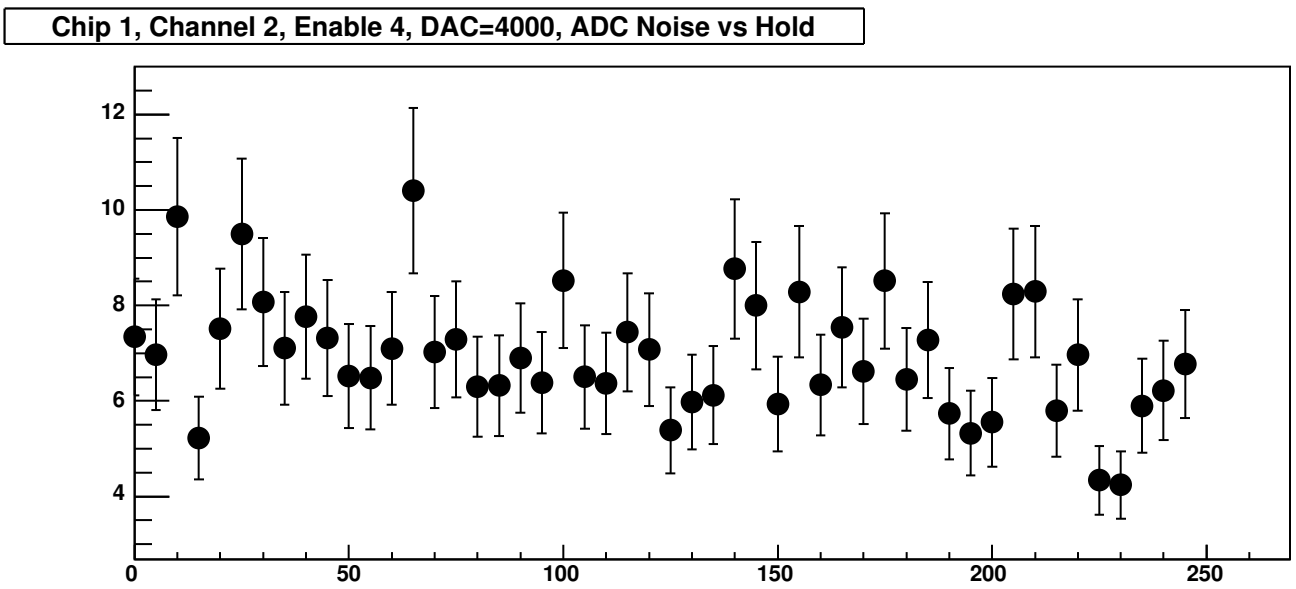
Chip 1, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold

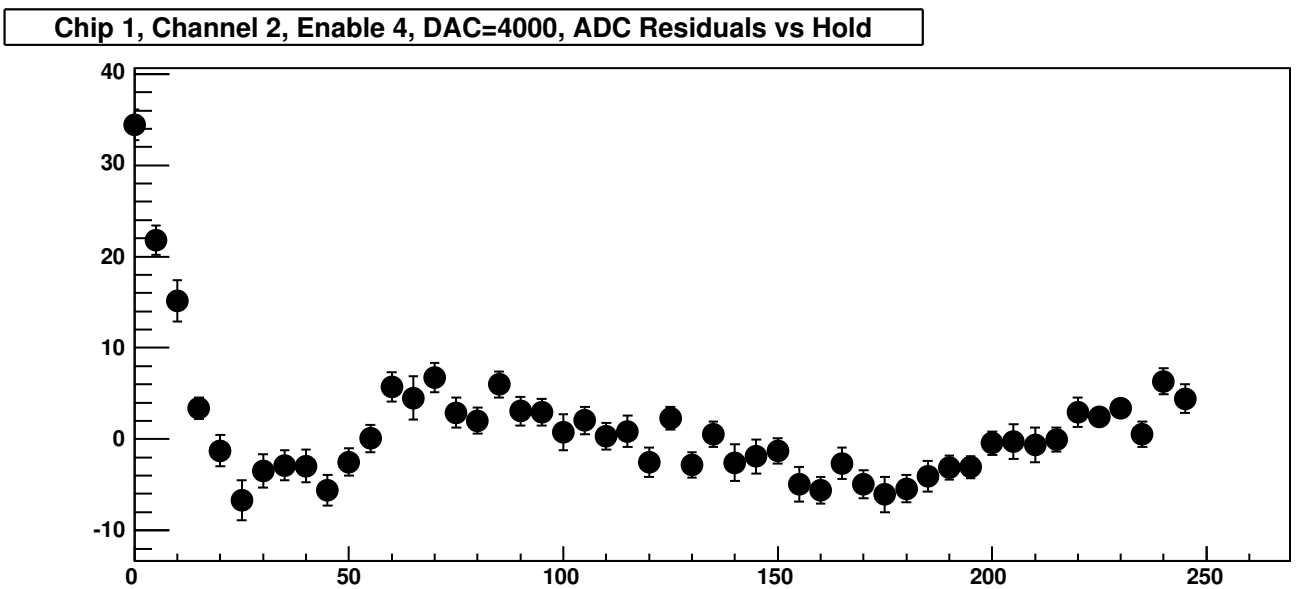


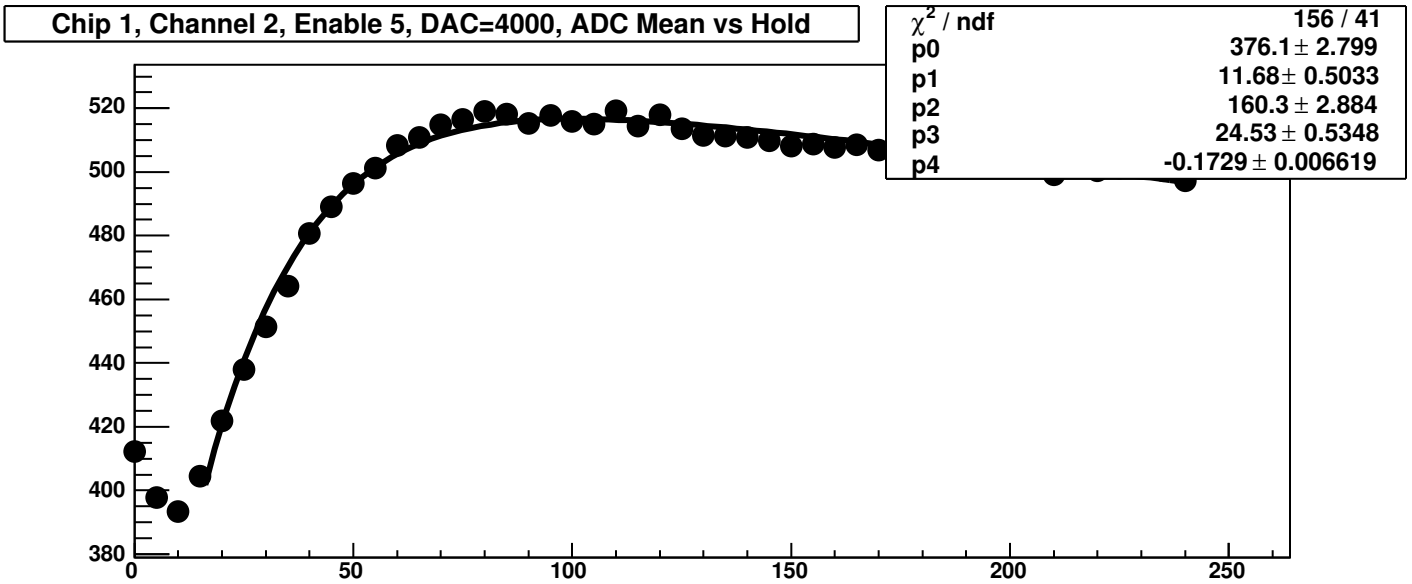
Chip 1, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold

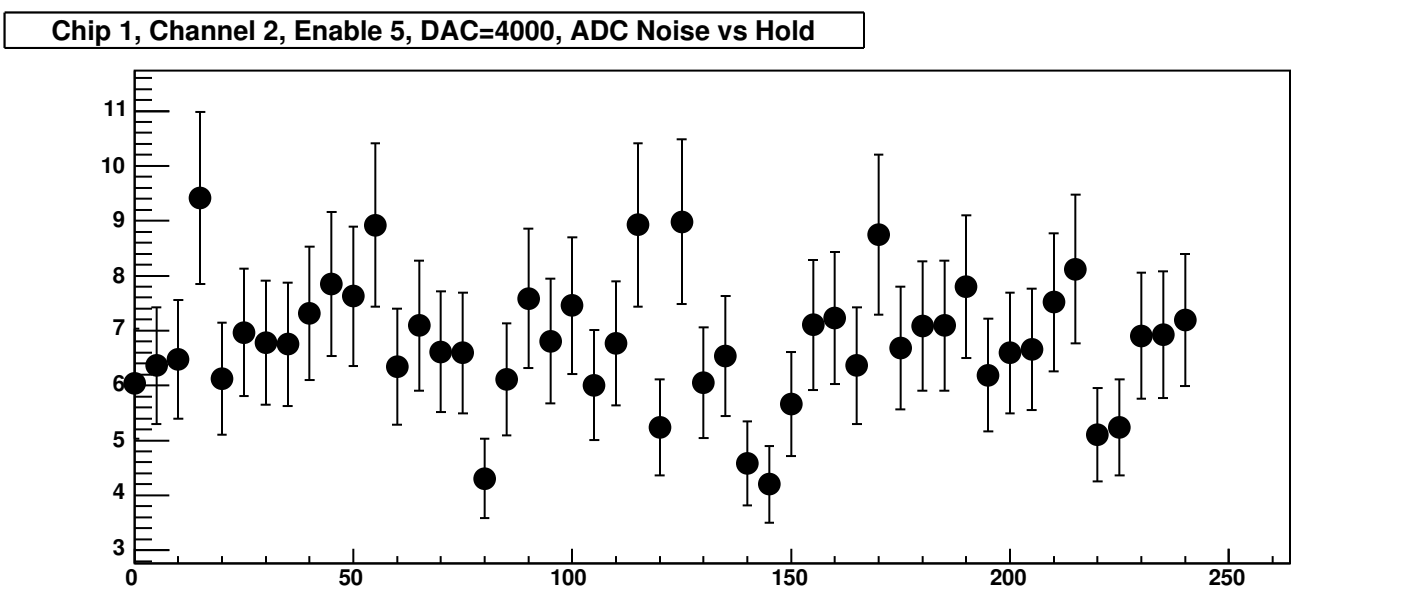


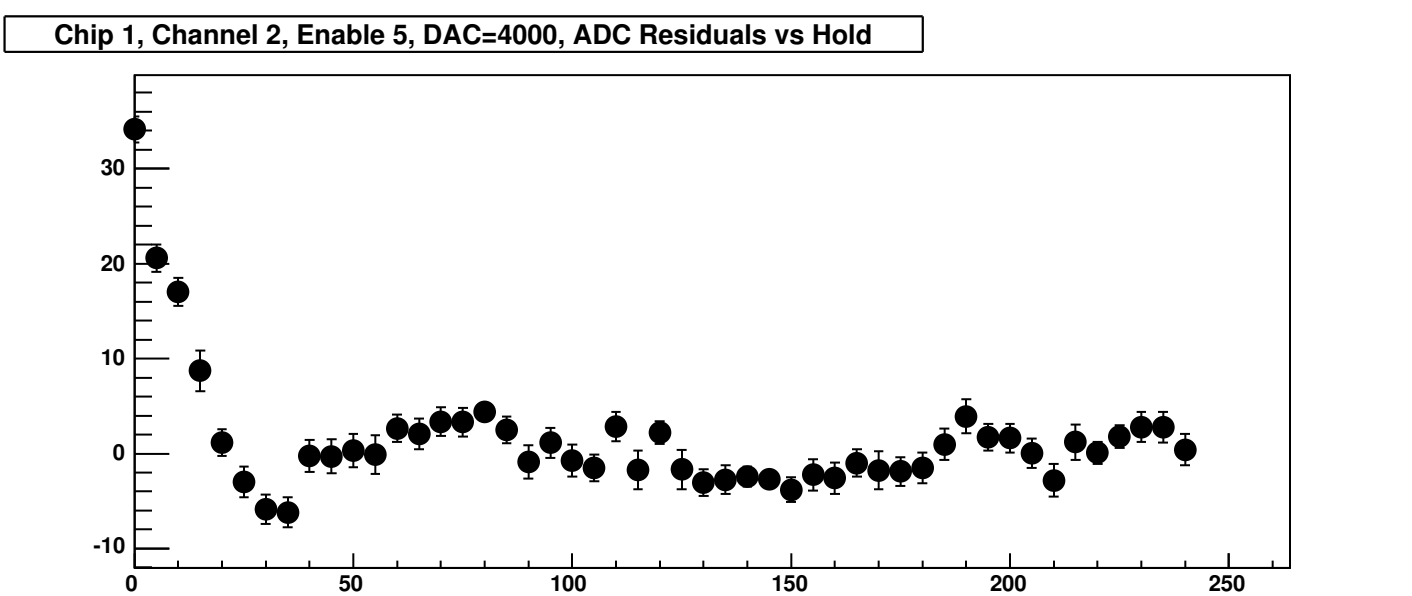

Chip 1, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 237.9 / 41
p0 378.1 ± 2.837
p1 9.09 ± 0.4682
p2 175.1 ± 2.932
p3 24.88 ± 0.4956
p4 -0.2118 ± 0.006256
540
520
500
480
460
440
420
400
0
50
100
150
200
250

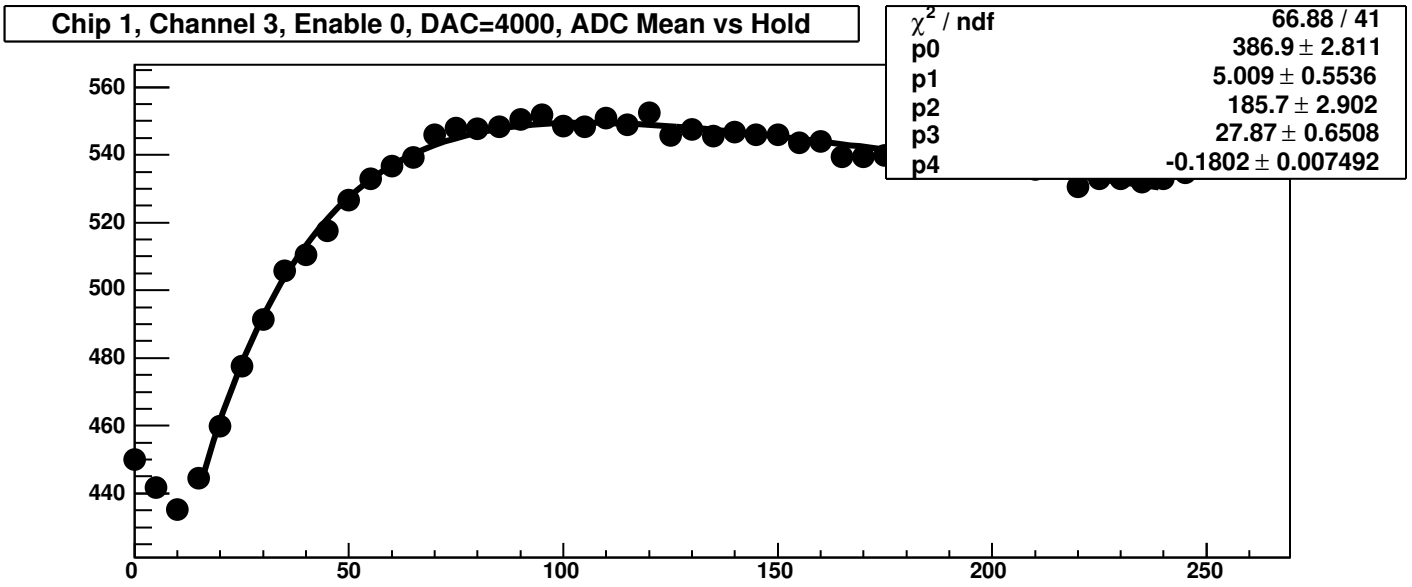

Chip 1, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

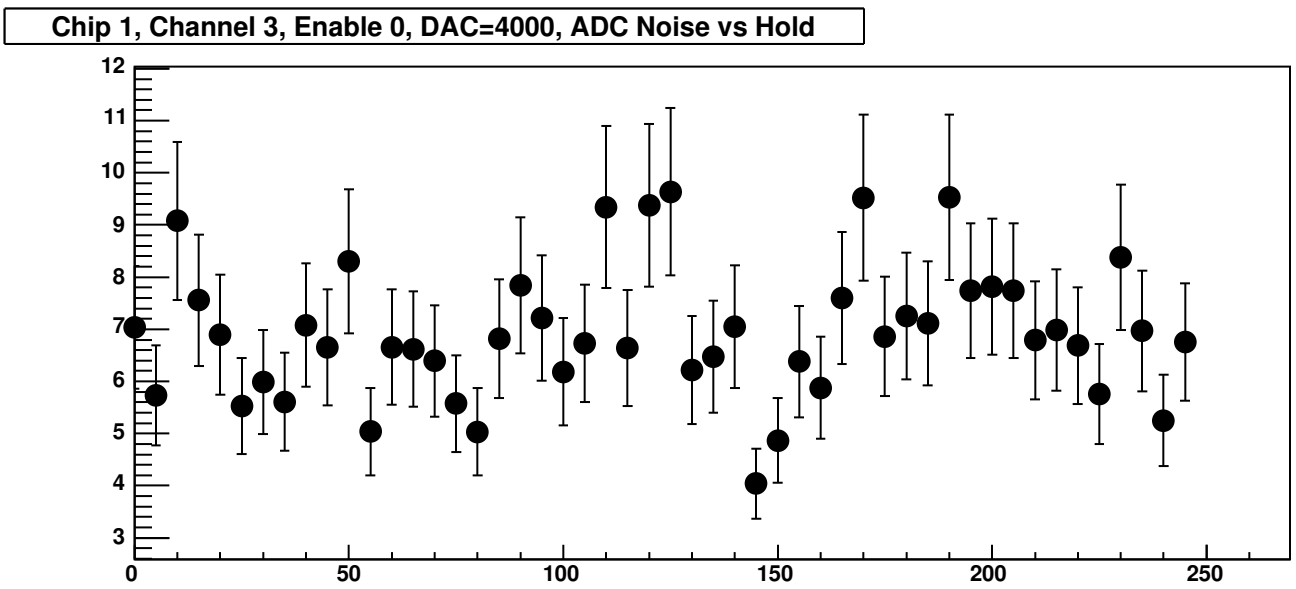

Chip 1, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

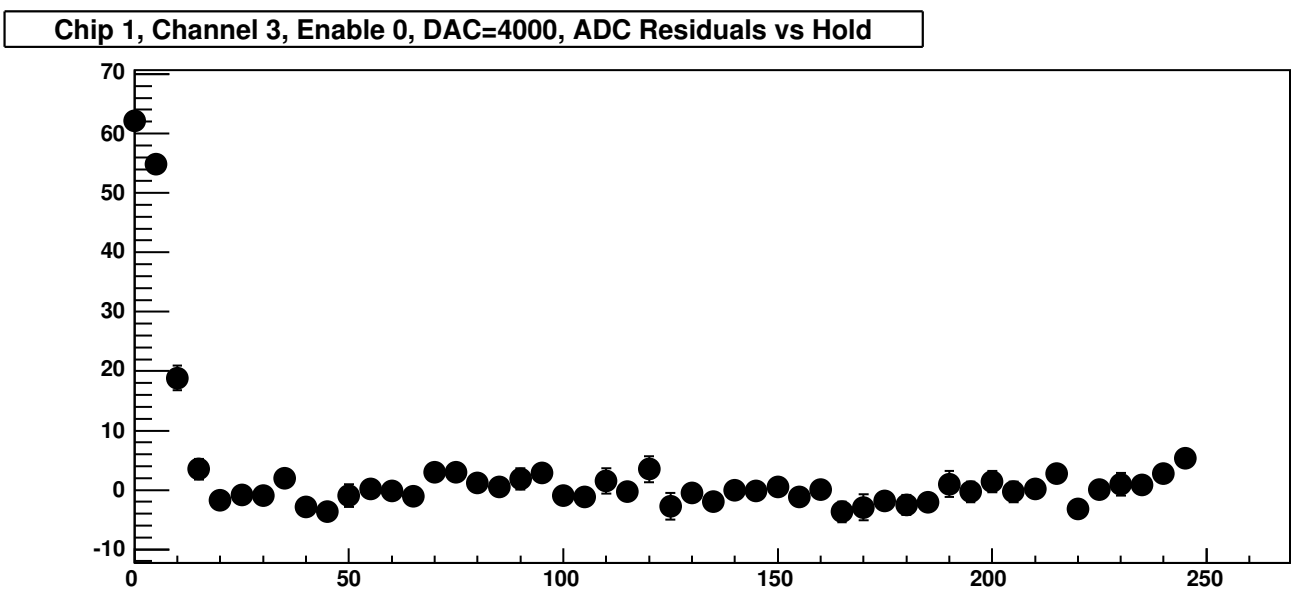

Chip 1, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 156 / 41
p0 376.1 ± 2.799
p1 11.68 ± 0.5033
p2 160.3 ± 2.884
p3 24.53 ± 0.5348
p4 -0.1729 ± 0.006619
520
500
480
460
440
420
400
380
0
50
100
150
200
250


Chip 1, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

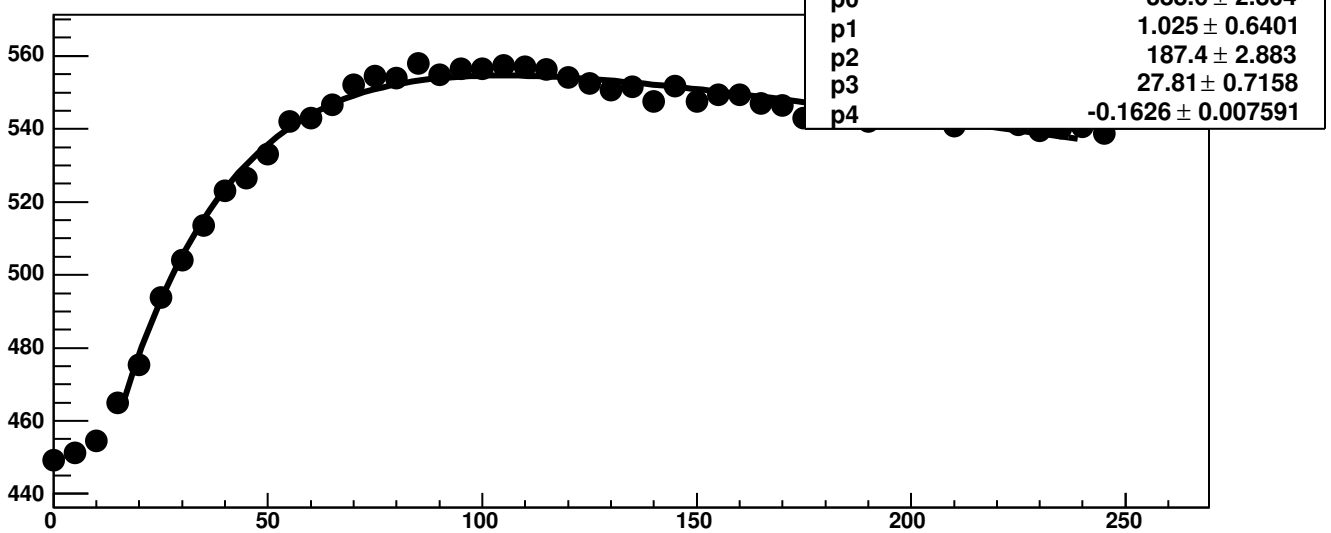

Chip 1, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
0
50
100
150
200
250


Chip 1, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 66.88 / 41
p0 386.9 ± 2.811
p1 5.009 ± 0.5536
p2 185.7 ± 2.902
p3 27.87 ± 0.6508
p4 -0.1802 ± 0.007492
560
540
520
500
480
460
440
0
50
100
150
200
250

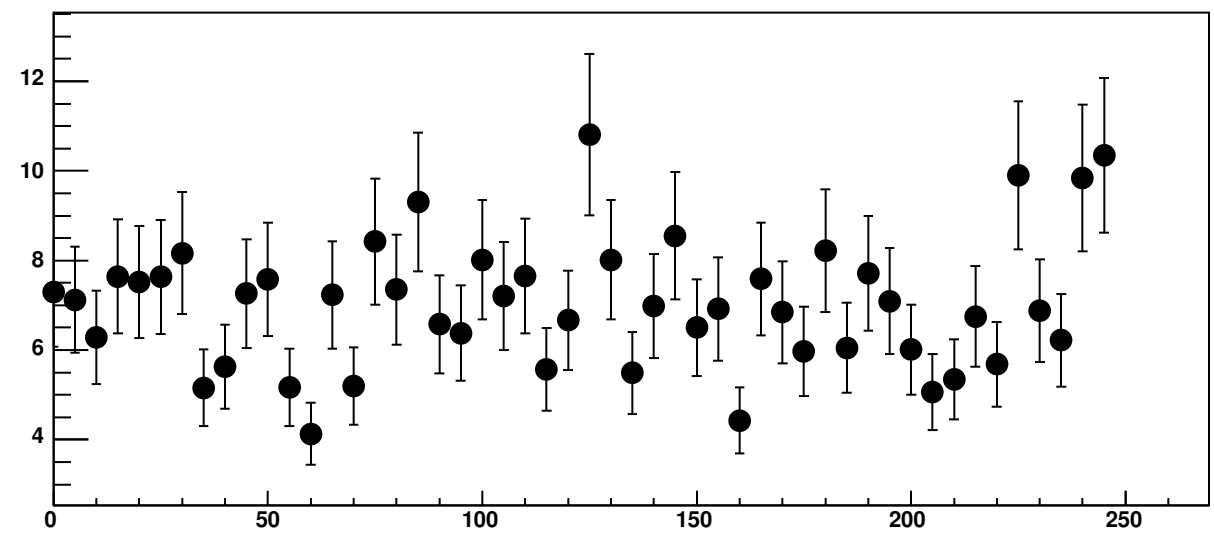

Chip 1, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 1, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

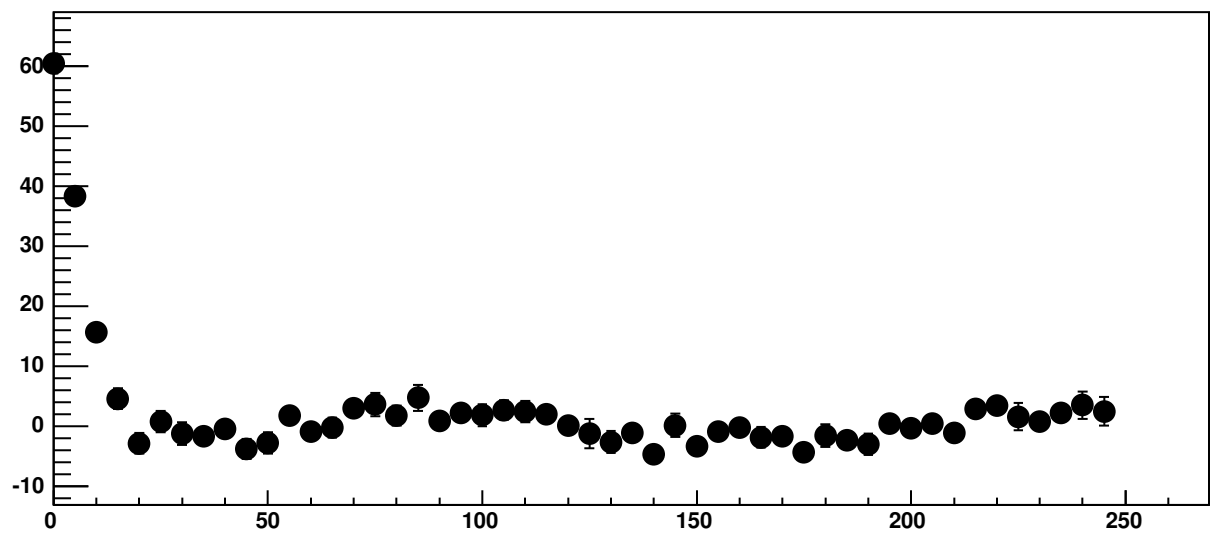
Chip 1, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold

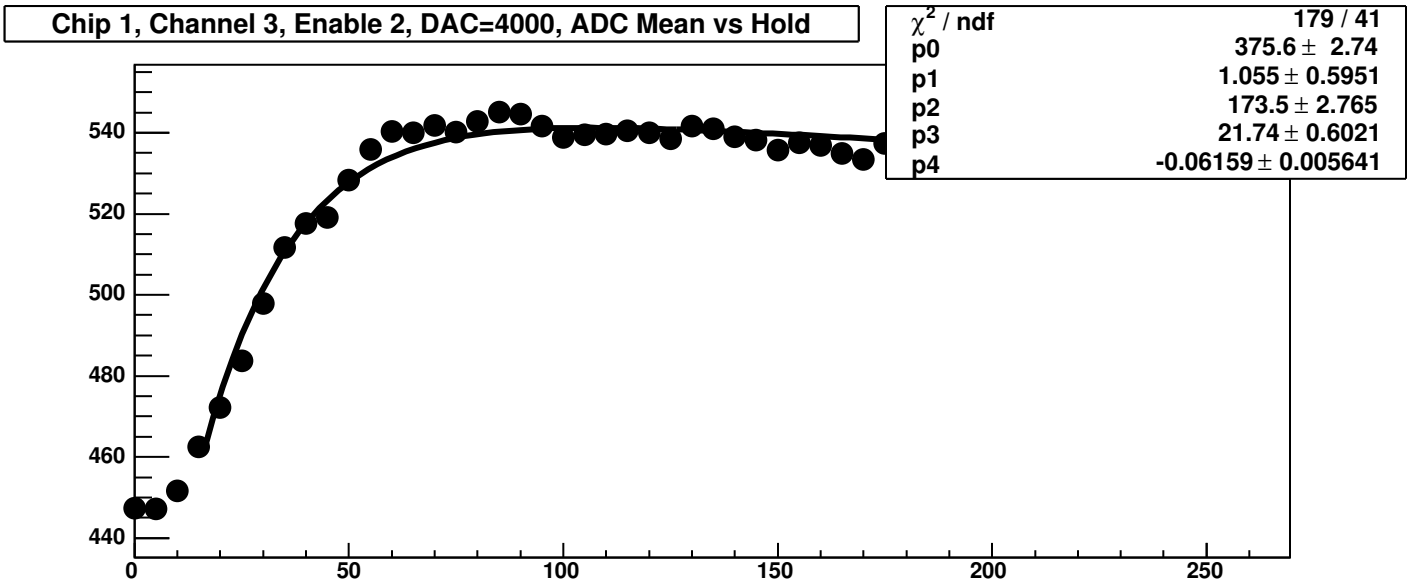


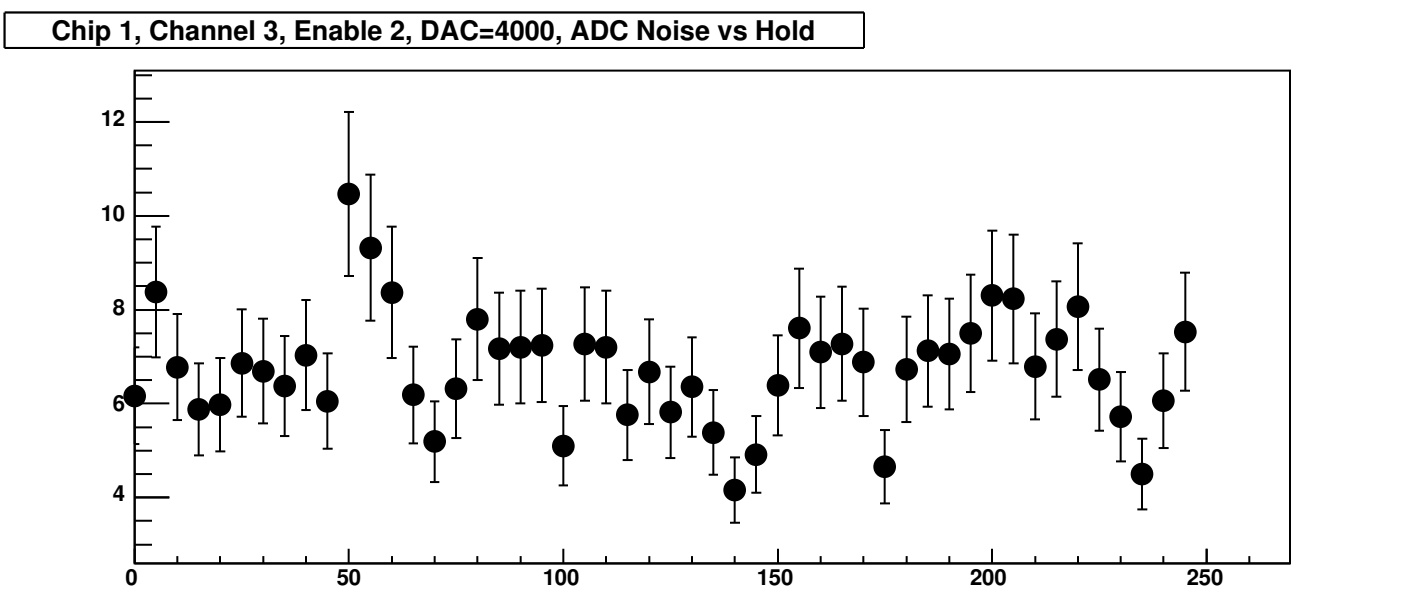
Chip 1, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold

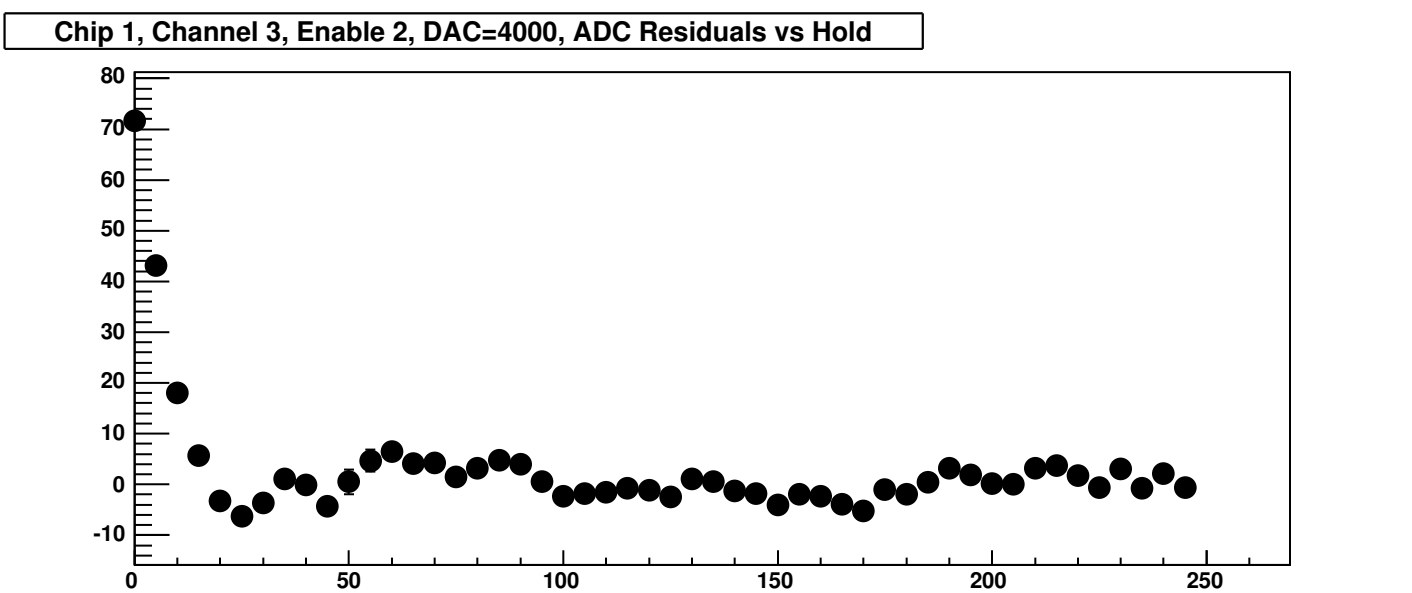


Chip 1, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold

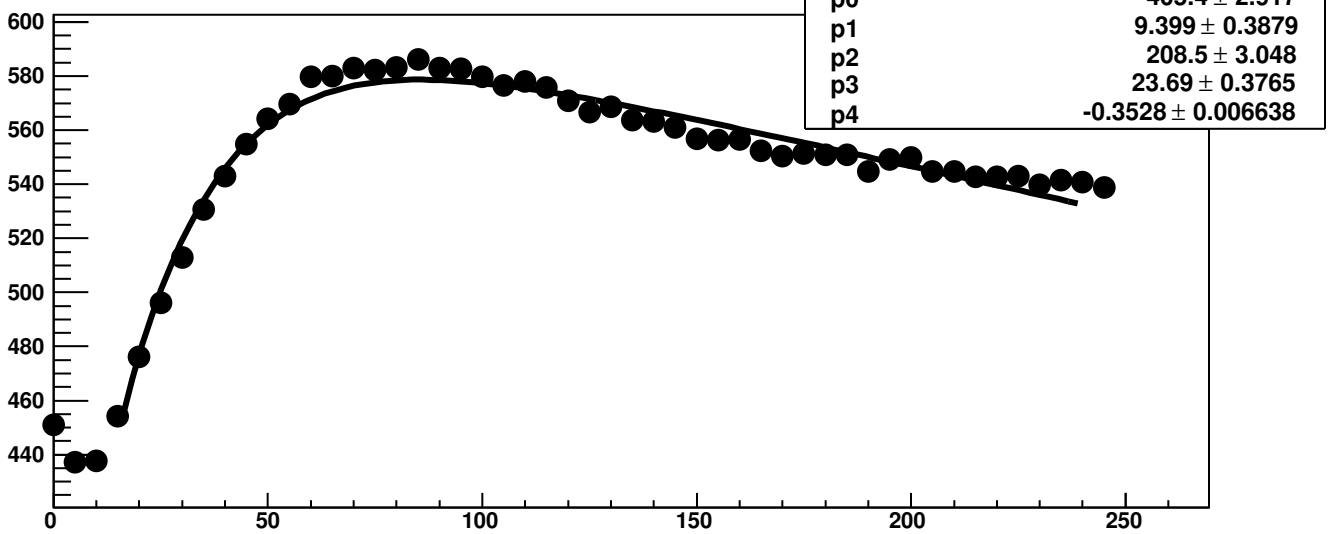



Chip 1, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 179 / 41
p0 375.6 ± 2.74
p1 1.055 ± 0.5951
p2 173.5 ± 2.765
p3 21.74 ± 0.6021
p4 -0.06159 ± 0.005641
540
520
500
480
460
440
0
50
100
150
200
250

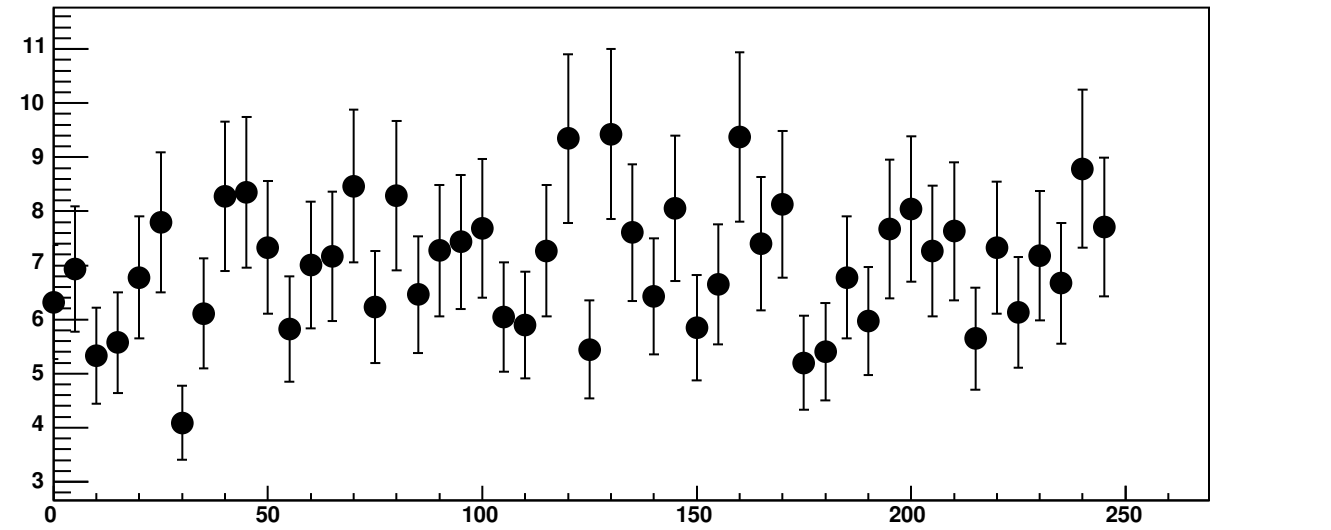

Chip 1, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 1, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold
80
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

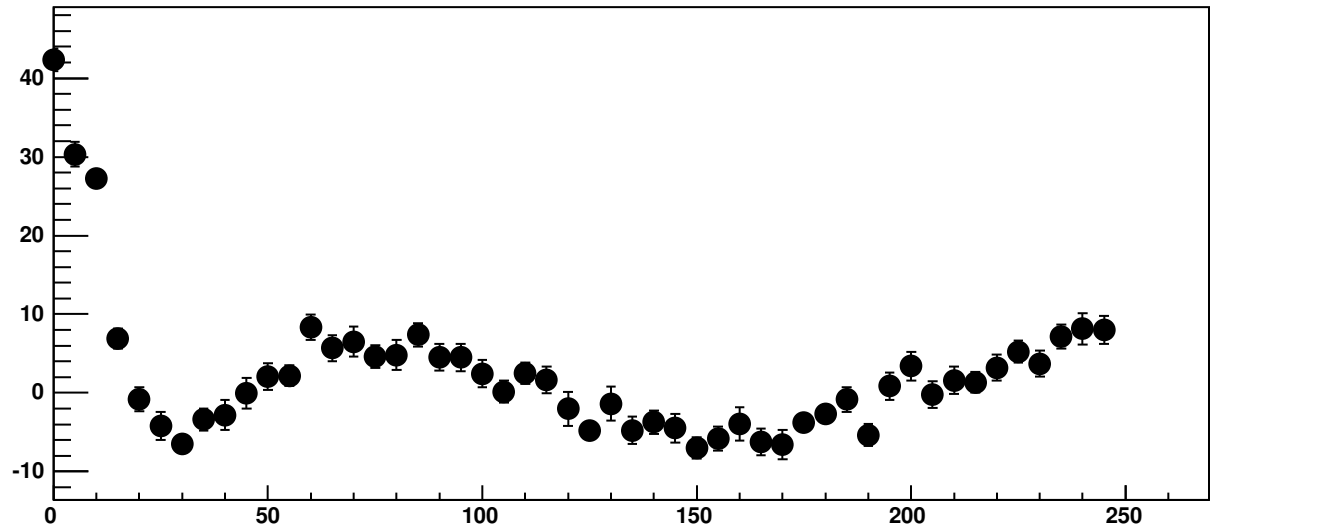
Chip 1, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold



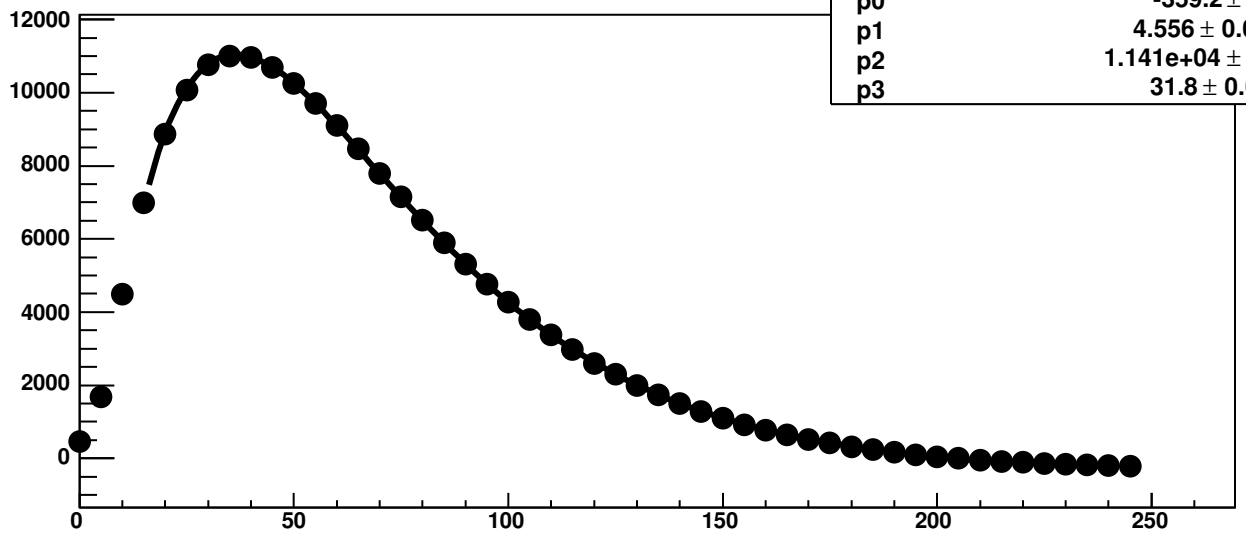
Chip 1, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold



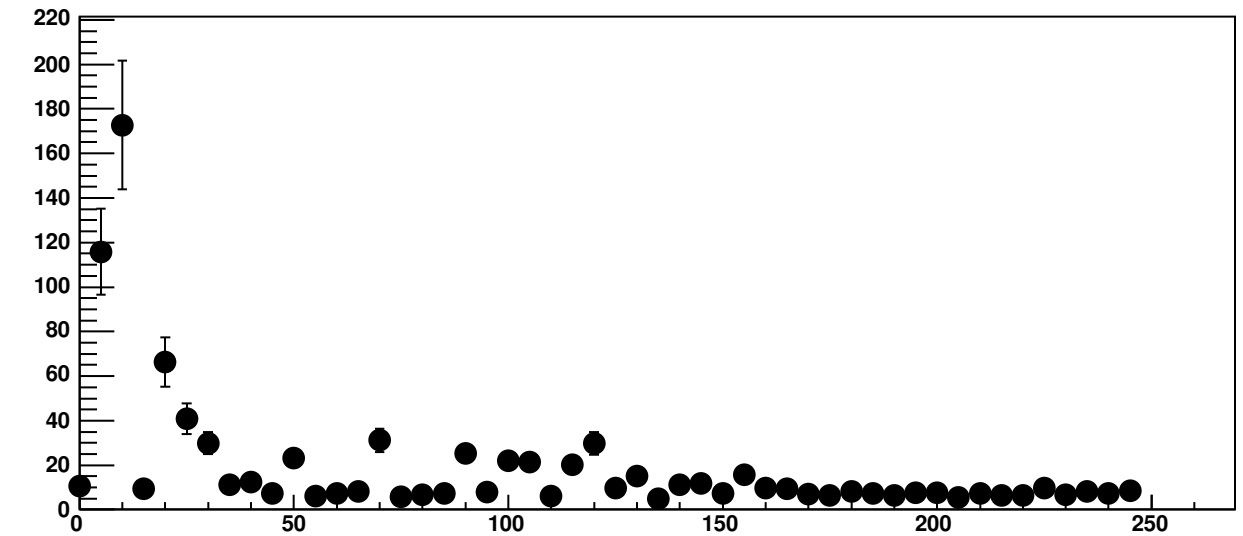
Chip 1, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold



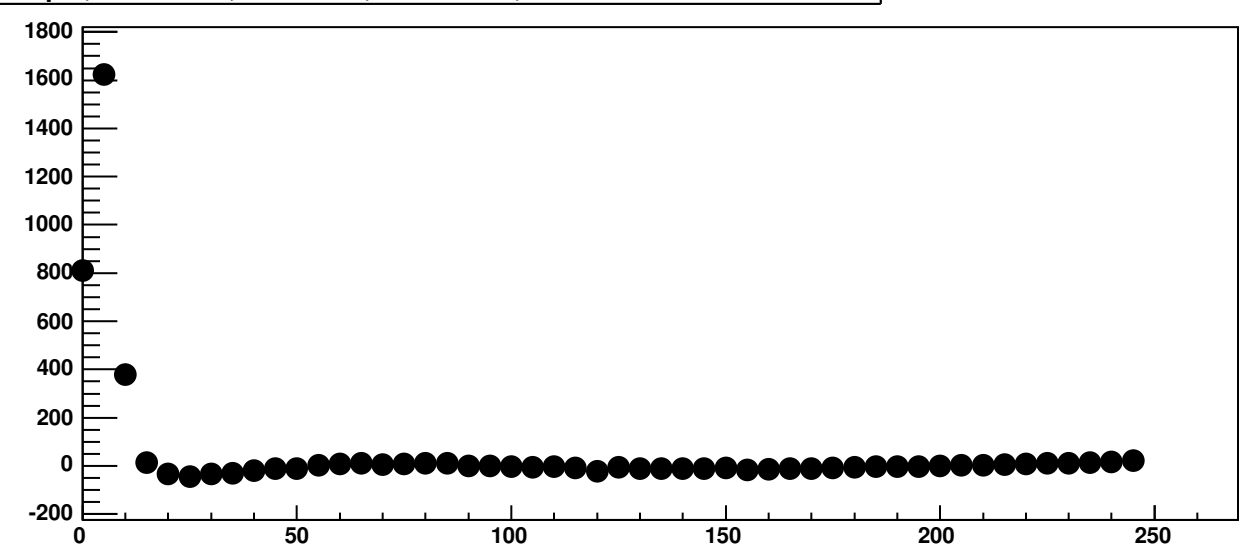
Chip 1, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold

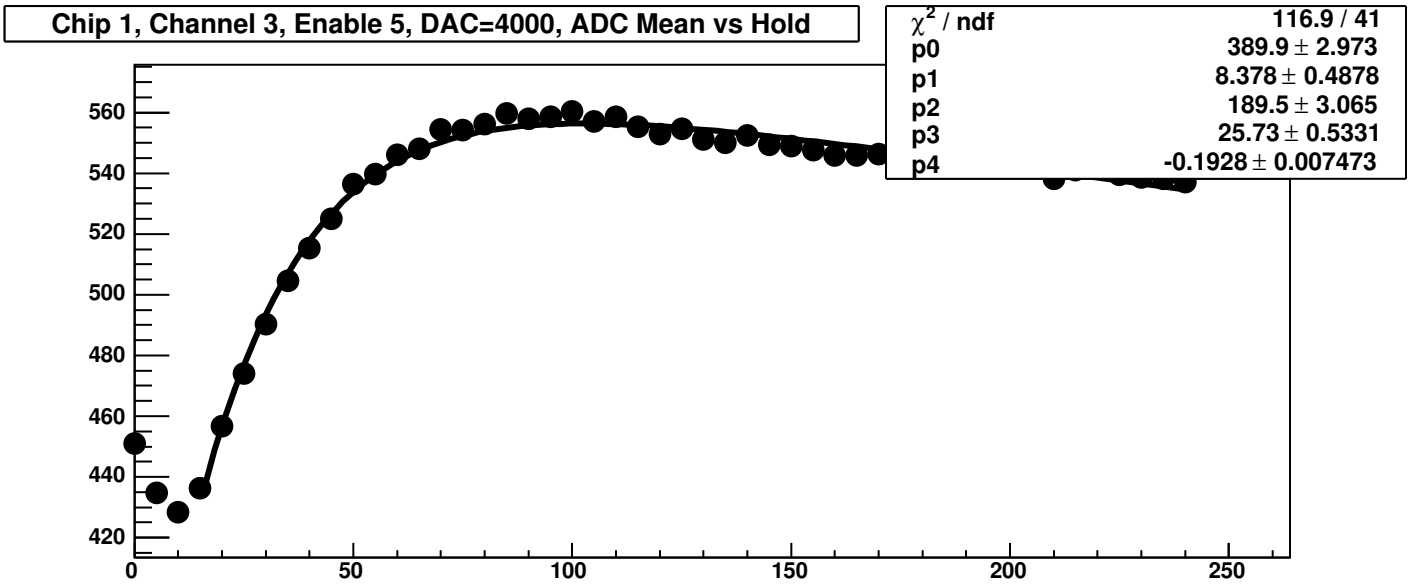


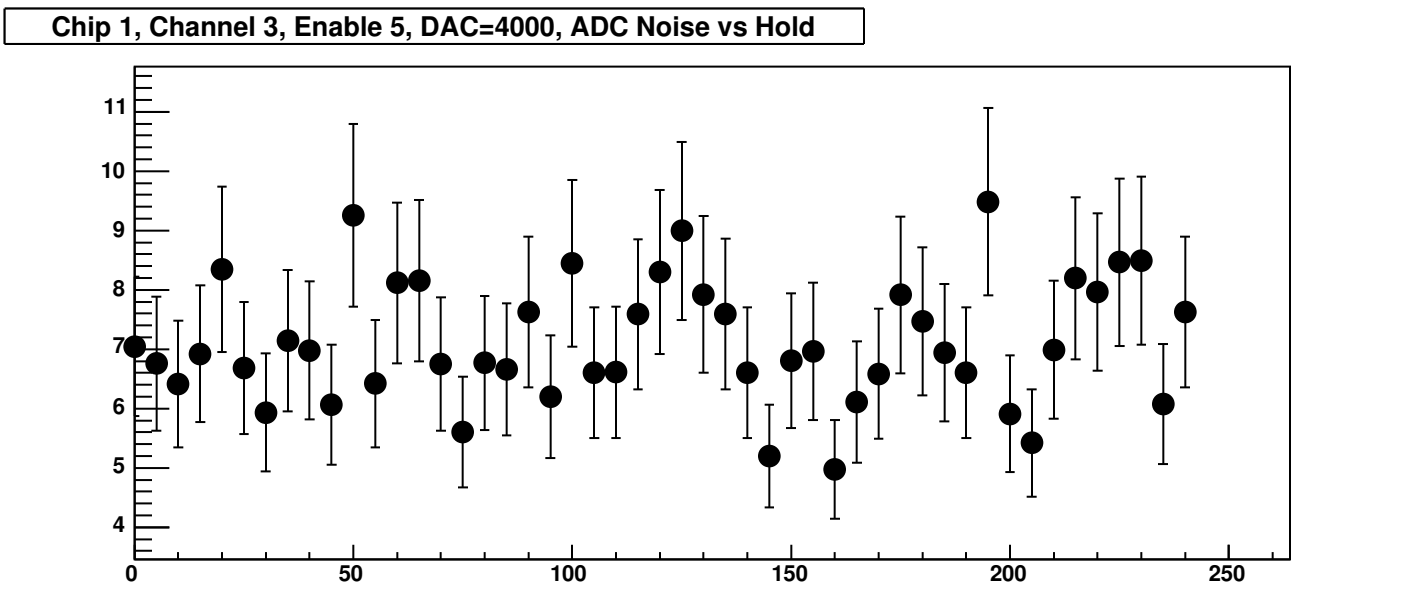
Chip 1, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold

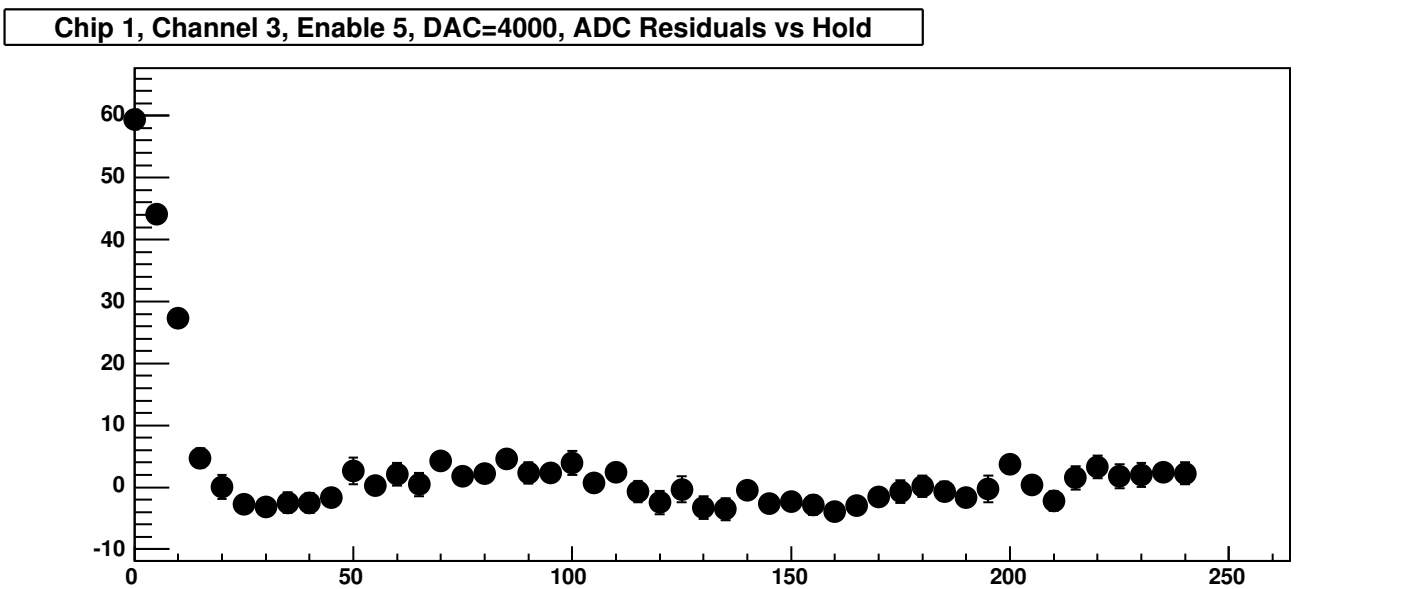


Chip 1, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold

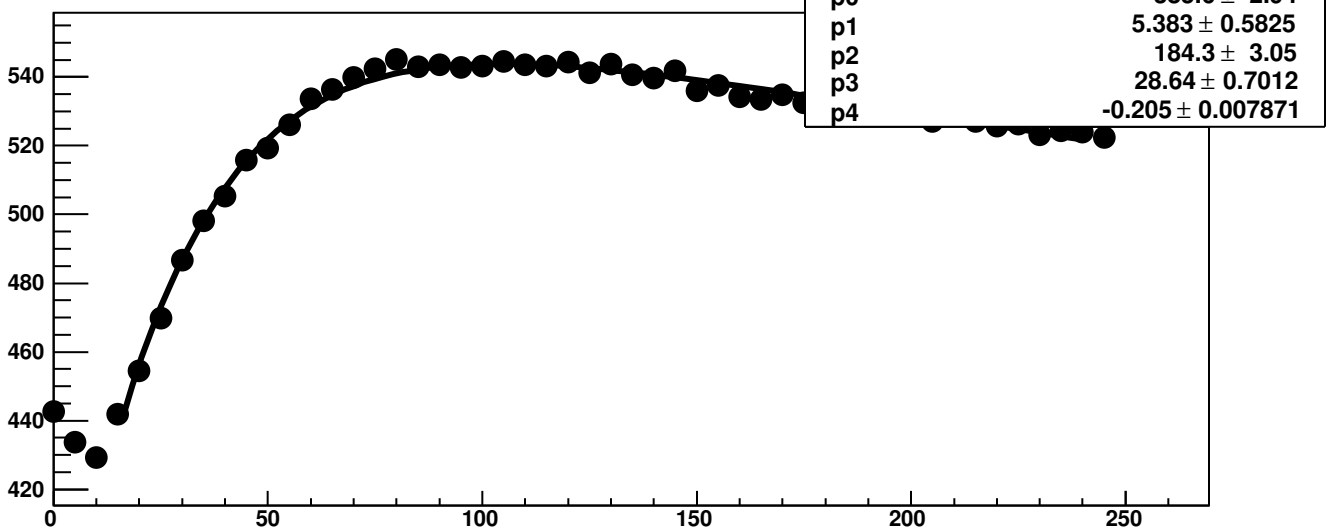



Chip 1, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 116.9 / 41
p0 389.9 ± 2.973
p1 8.378 ± 0.4878
p2 189.5 ± 3.065
p3 25.73 ± 0.5331
p4 -0.1928 ± 0.007473
560
540
520
500
480
460
440
420
0
50
100
150
200
250

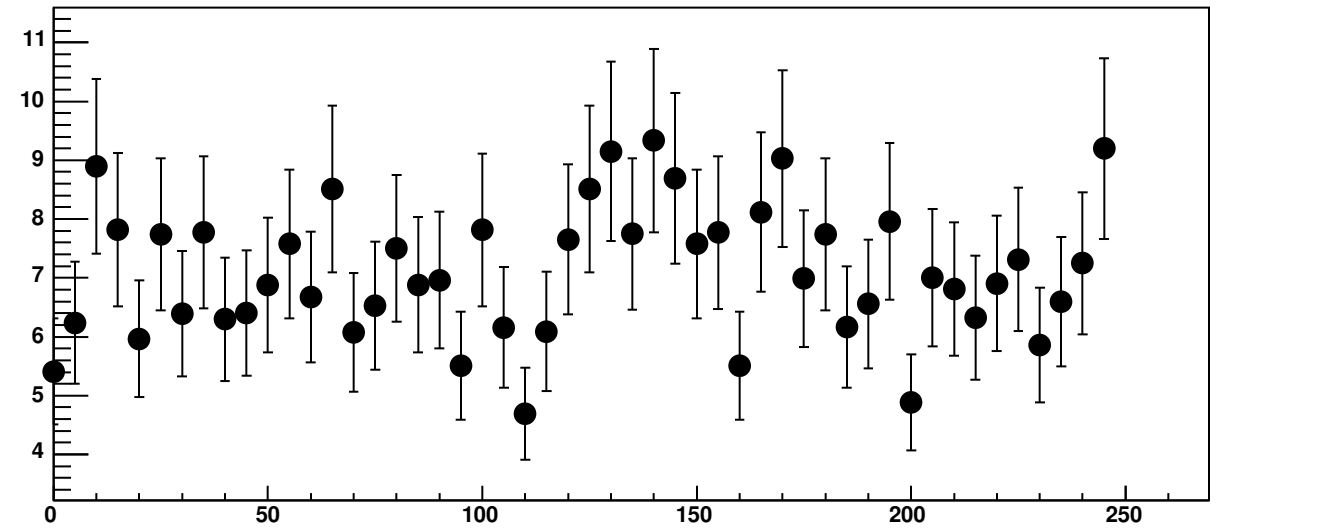

Chip 1, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 1, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

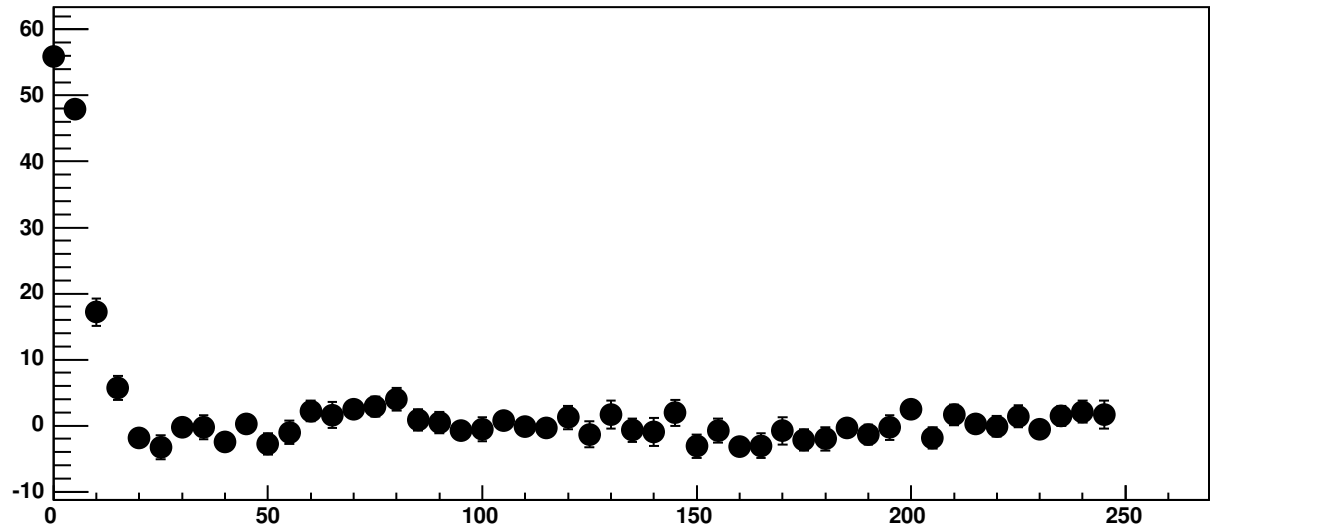
Chip 1, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

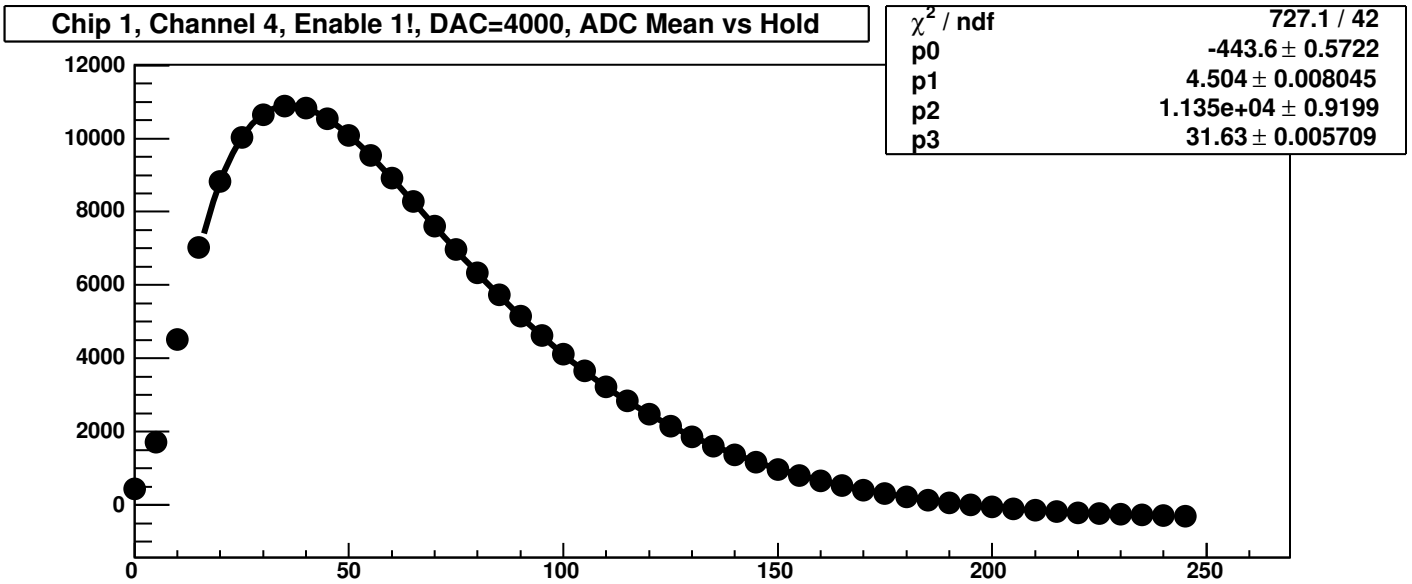


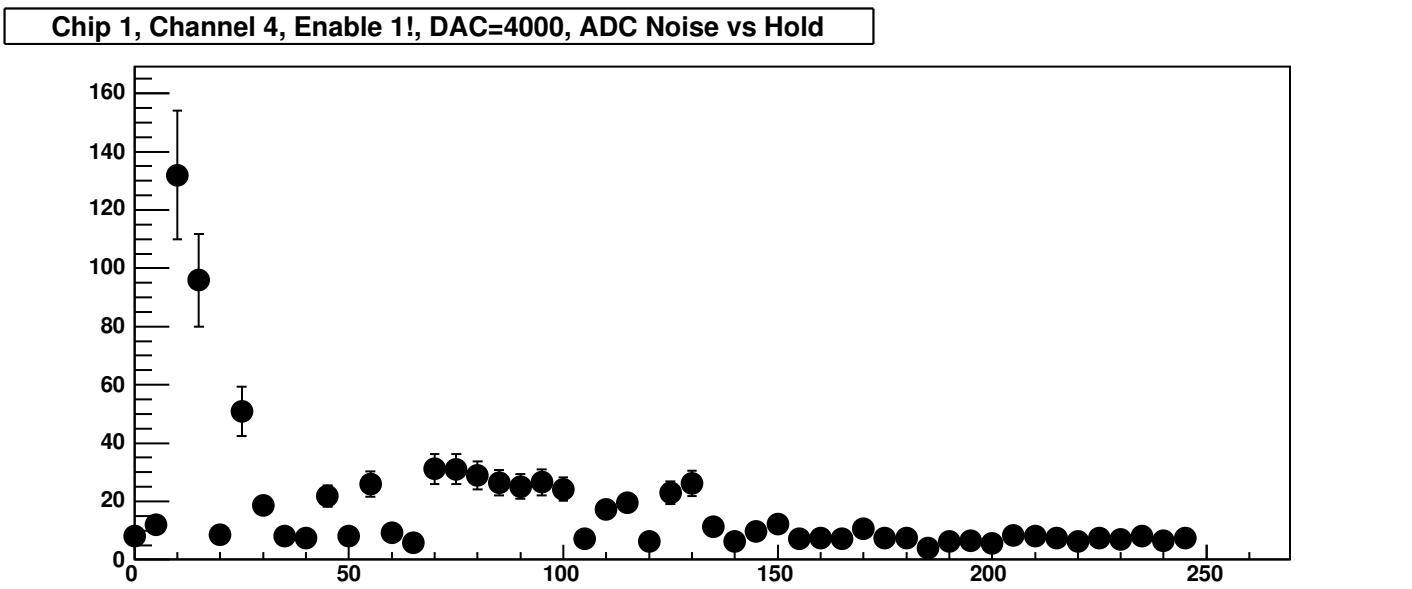
Chip 1, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold

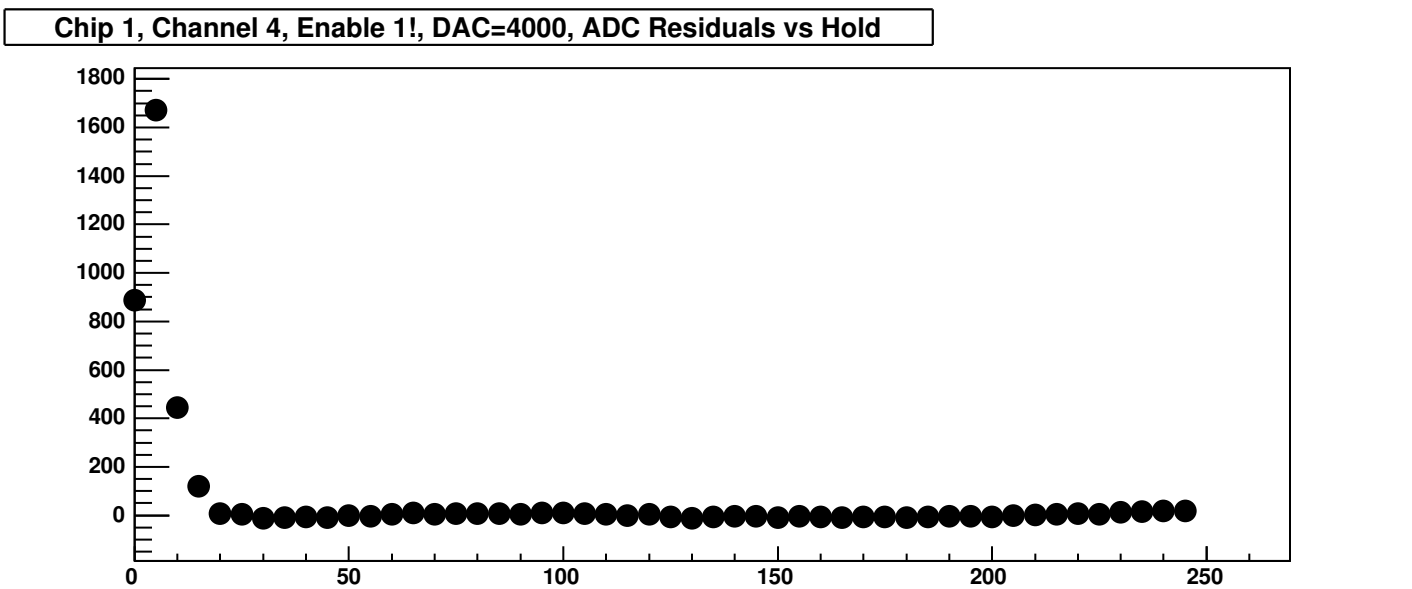


Chip 1, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

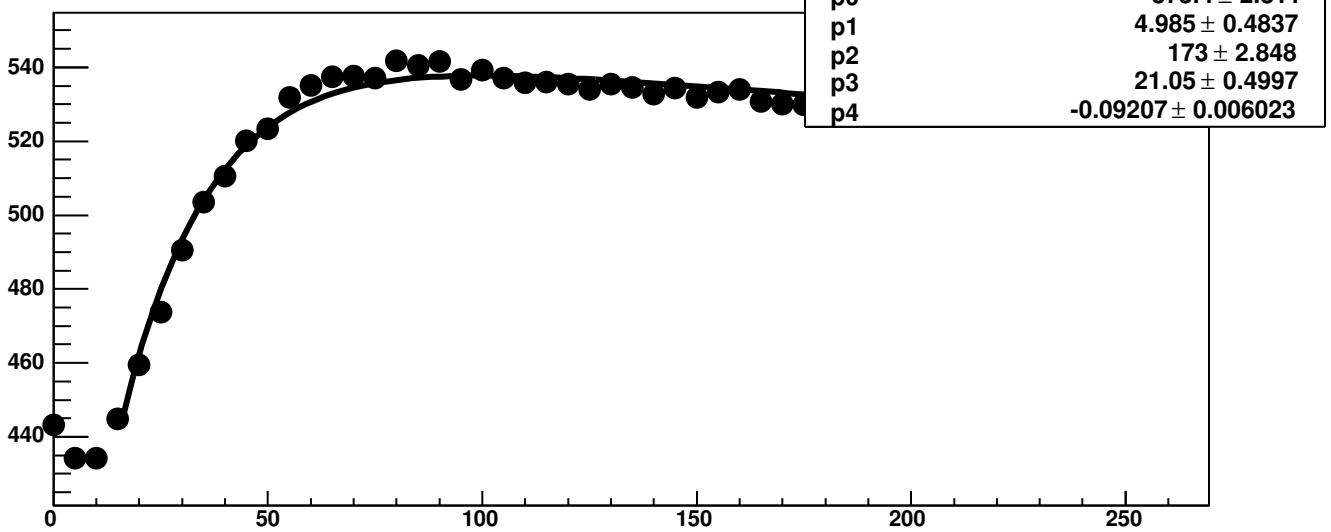



Chip 1, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 727.1 / 42
p0 -443.6 ± 0.5722
p1 4.504 ± 0.008045
p2 1.135e+04 ± 0.9199
p3 31.63 ± 0.005709
12000
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

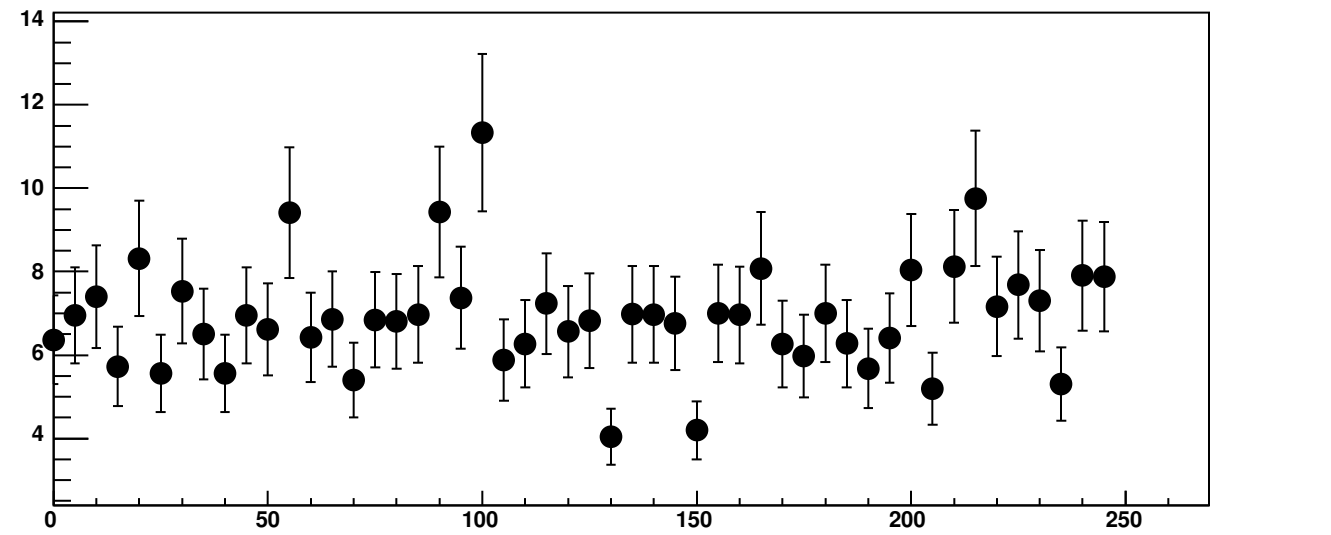

Chip 1, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 1, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

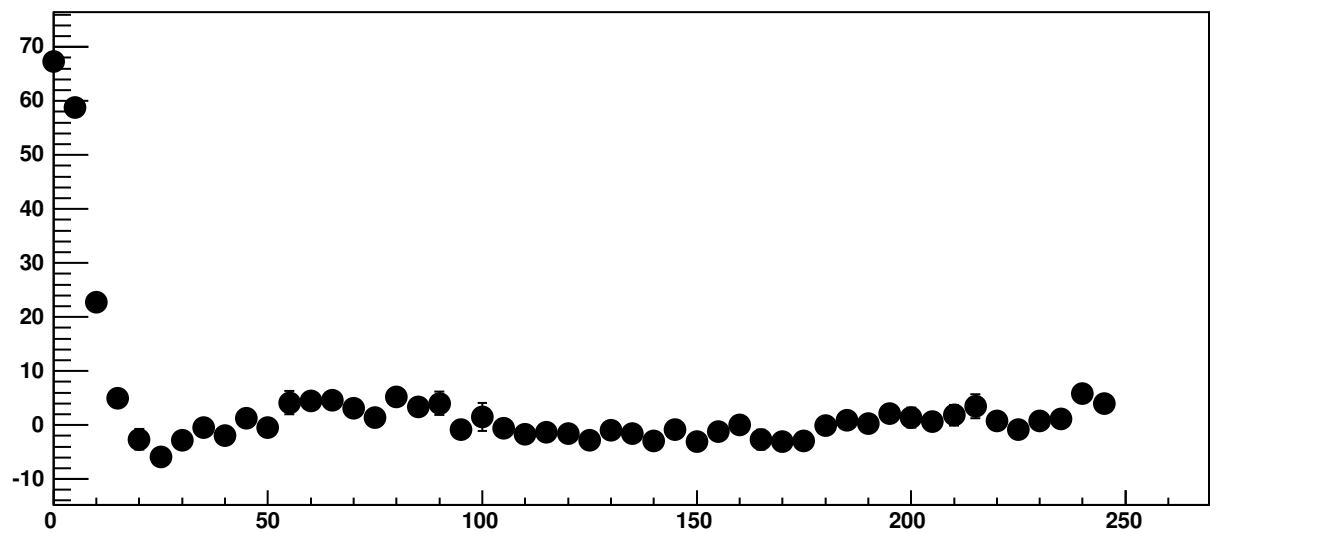
Chip 1, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold



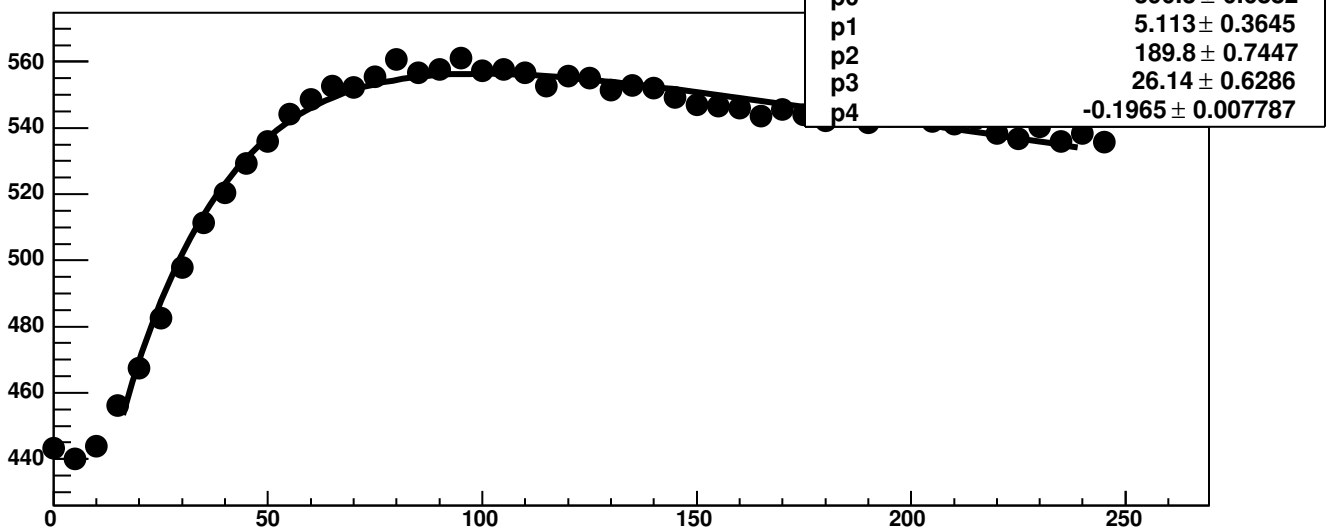
Chip 1, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



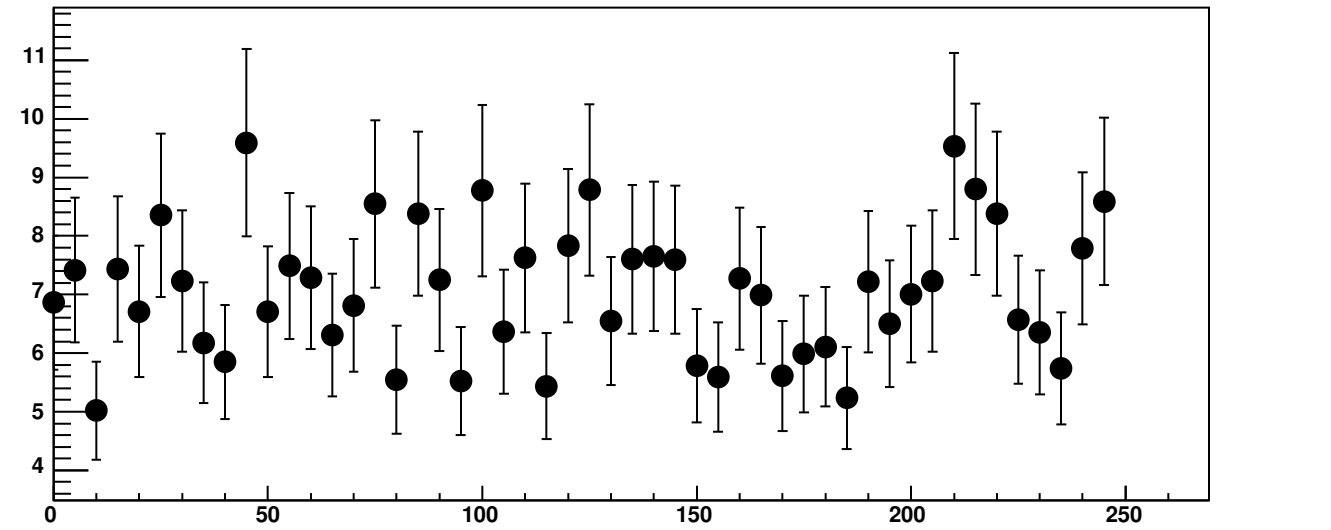
Chip 1, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold



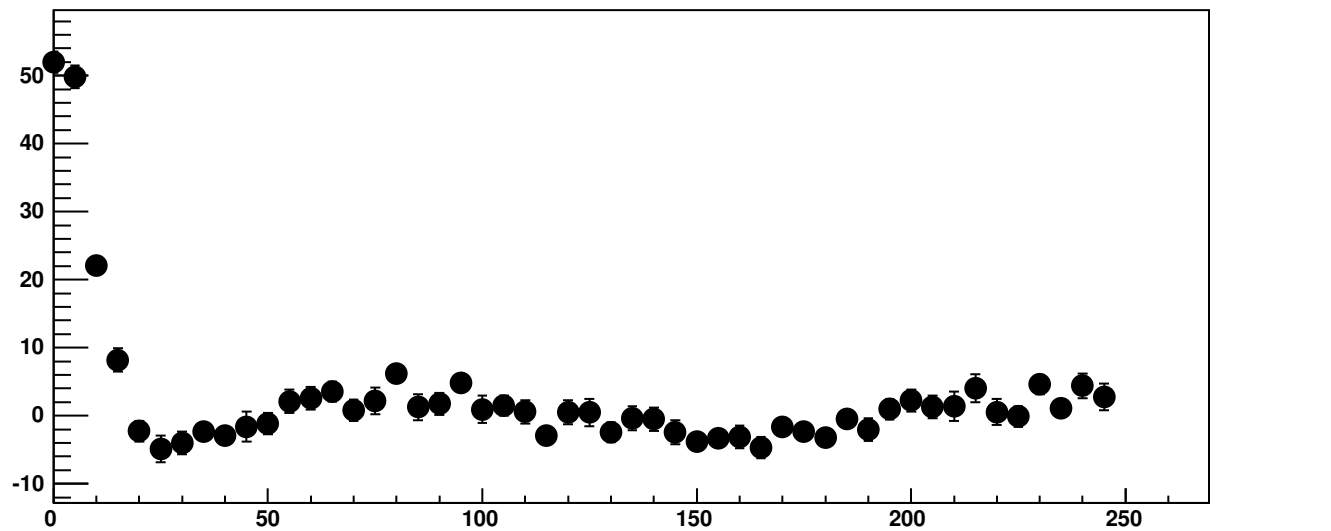
Chip 1, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold

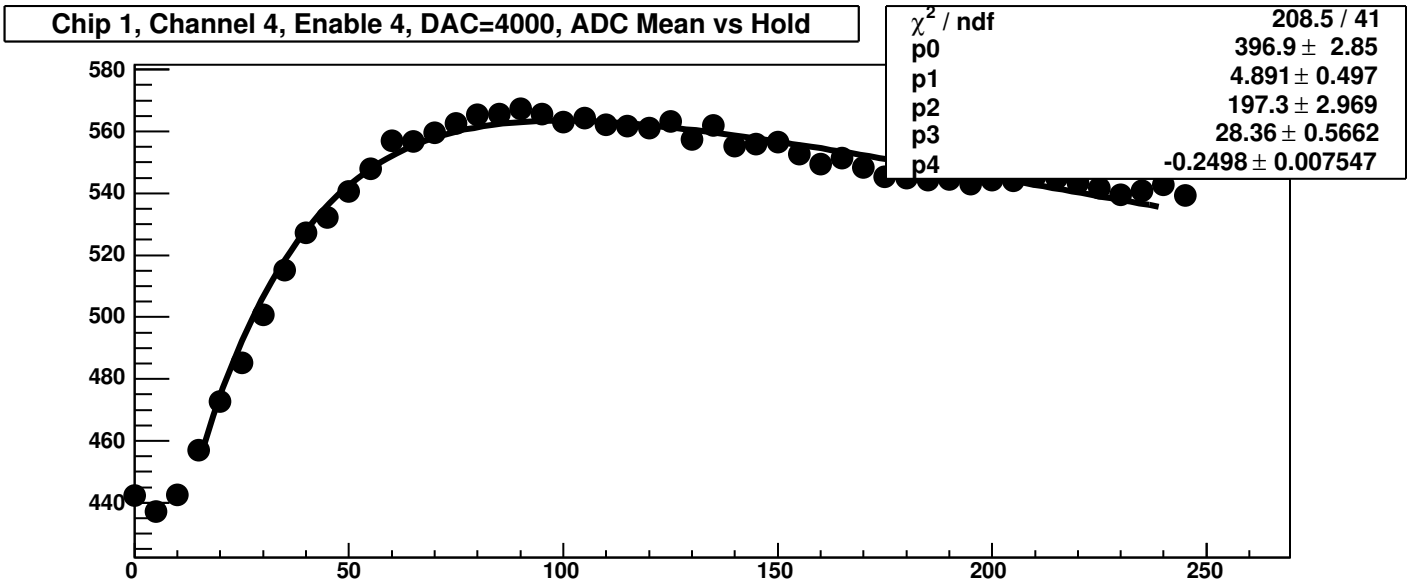


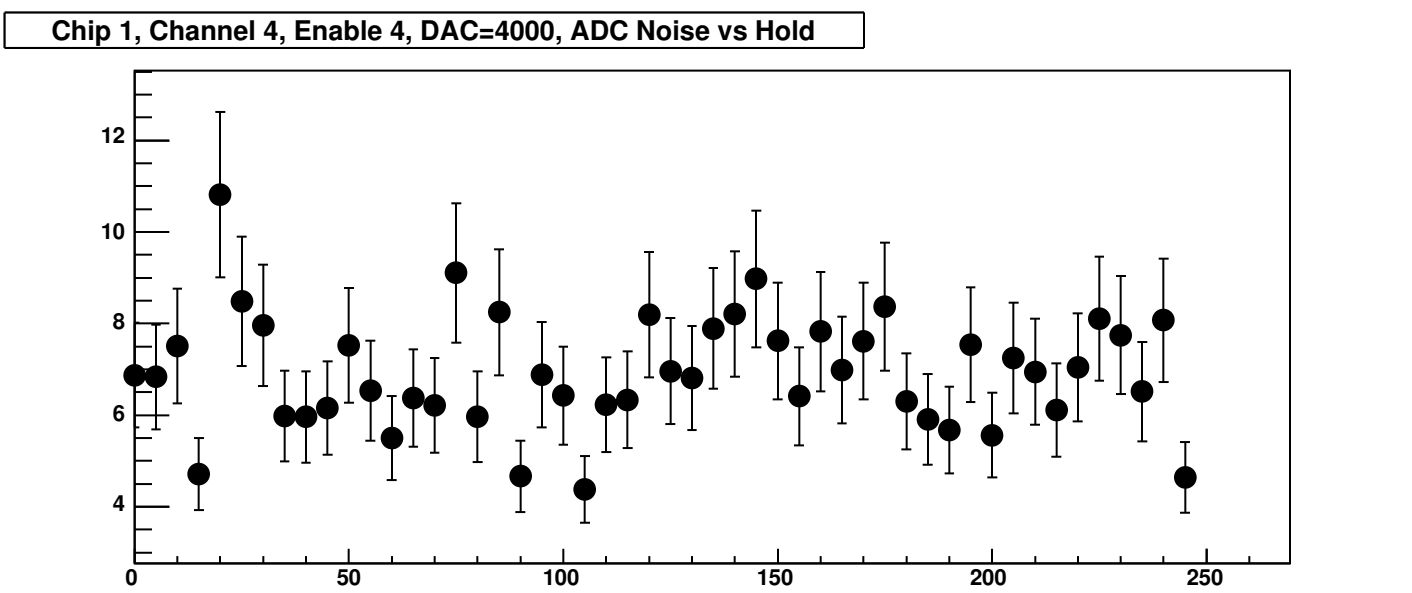
Chip 1, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold

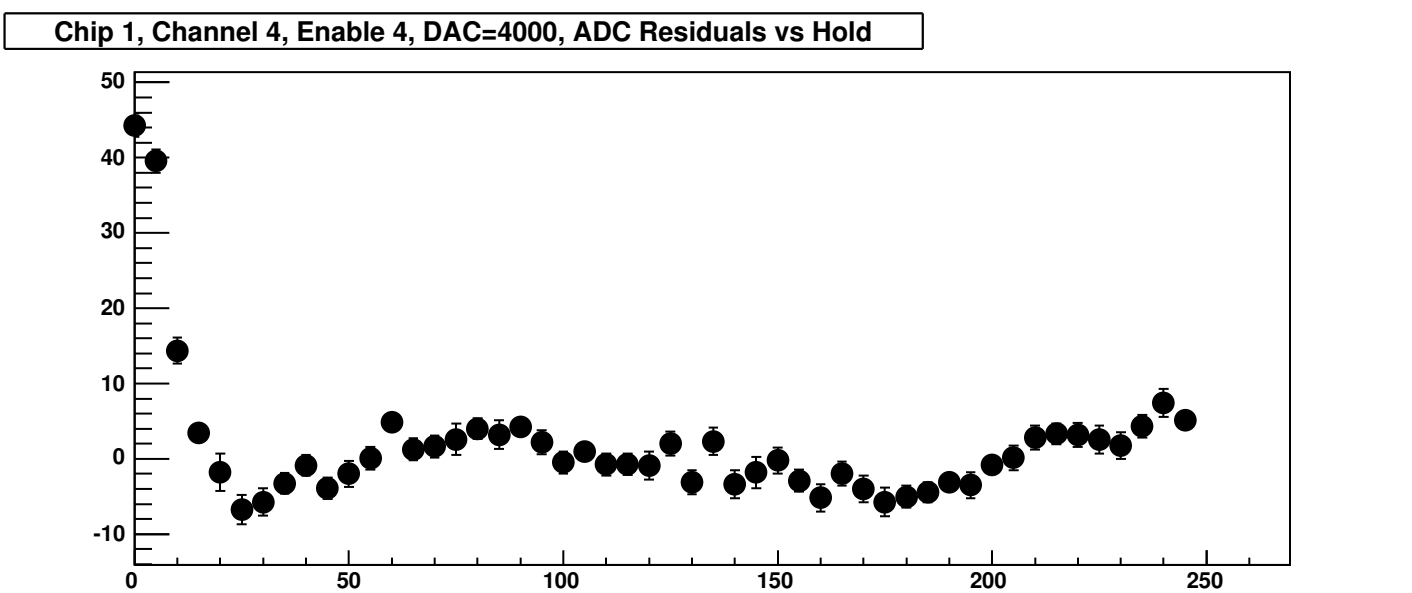


Chip 1, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold

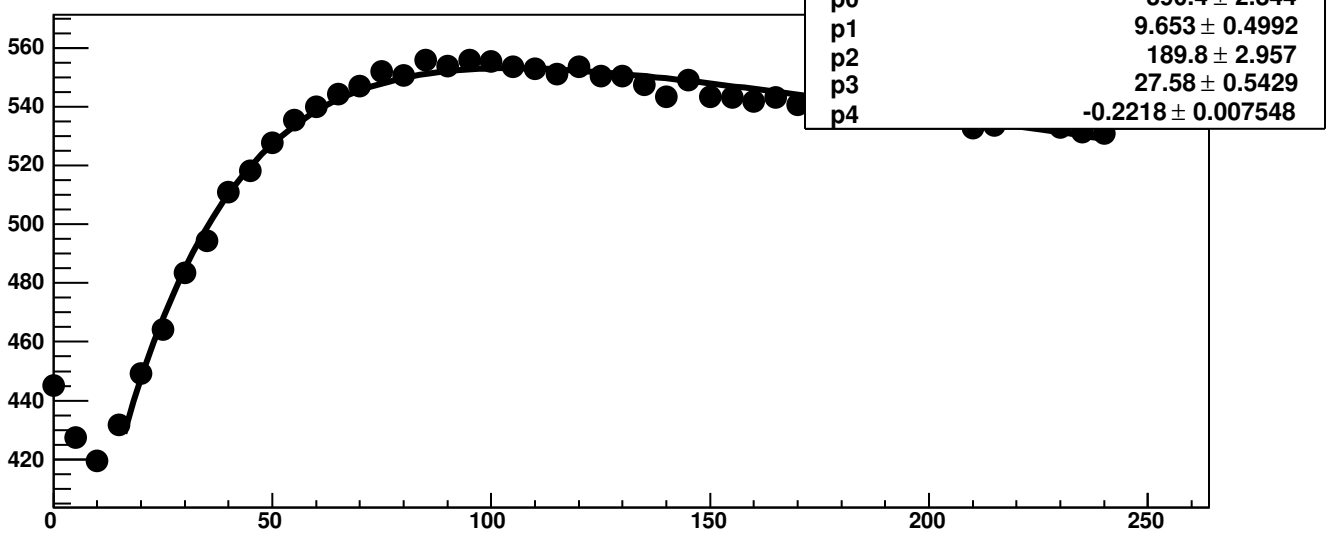



Chip 1, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 208.5 / 41
p0 396.9 ± 2.85
p1 4.891 ± 0.497
p2 197.3 ± 2.969
p3 28.36 ± 0.5662
p4 -0.2498 ± 0.007547
580
560
540
520
500
480
460
440
0
50
100
150
200
250

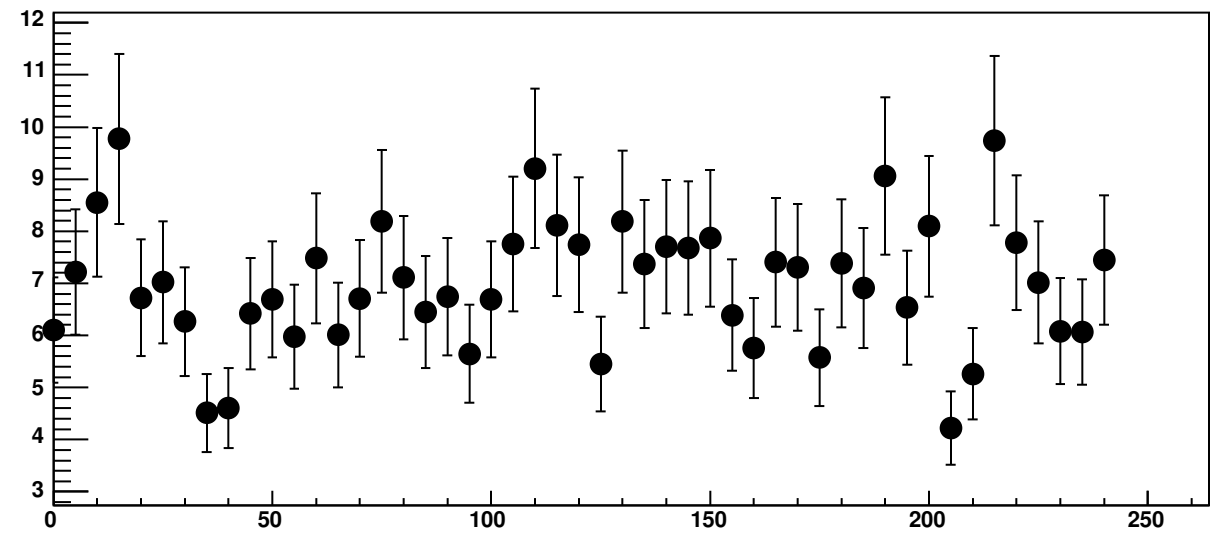

Chip 1, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 1, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250

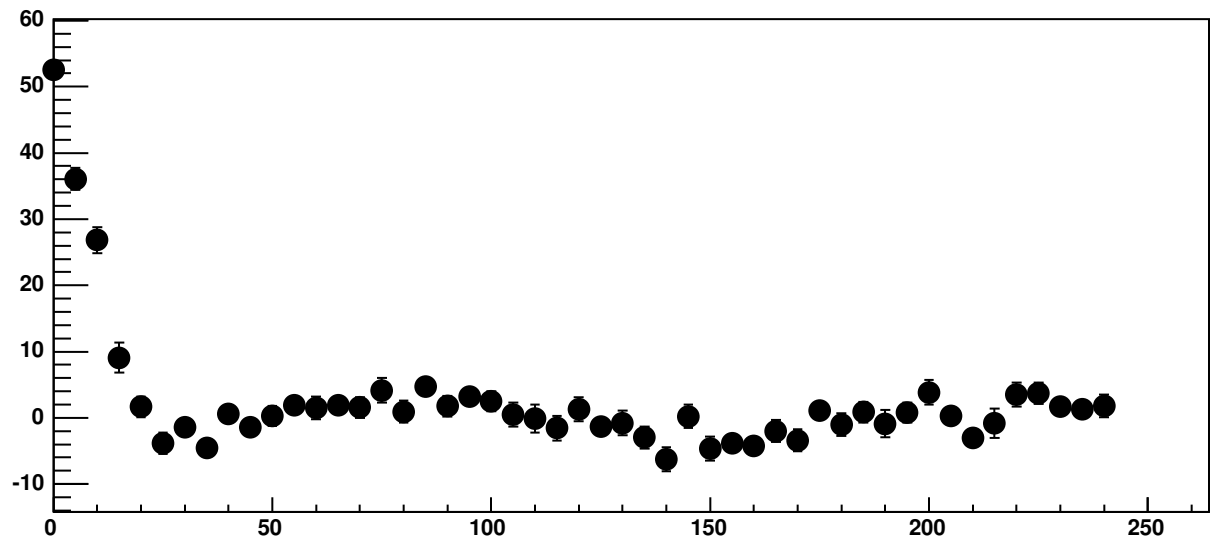
Chip 1, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold



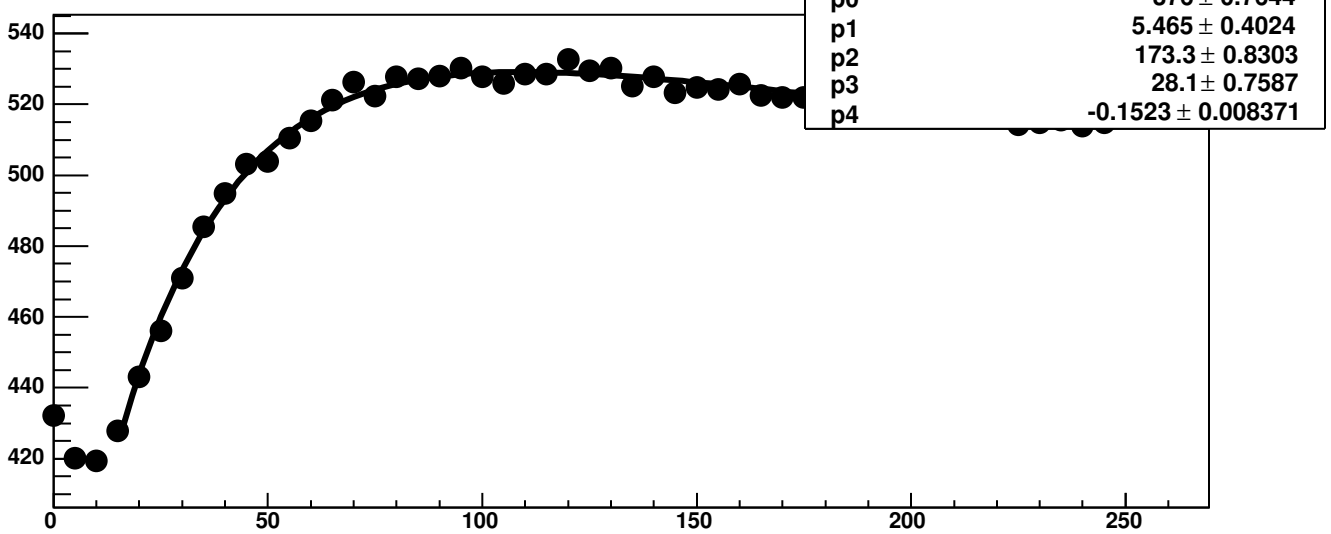
Chip 1, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold



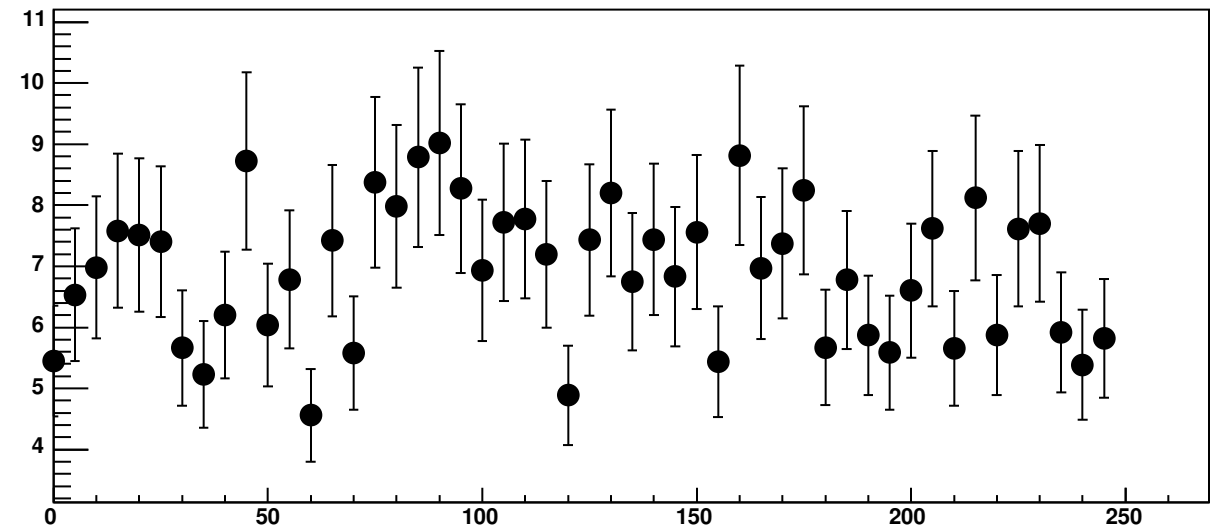
Chip 1, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold



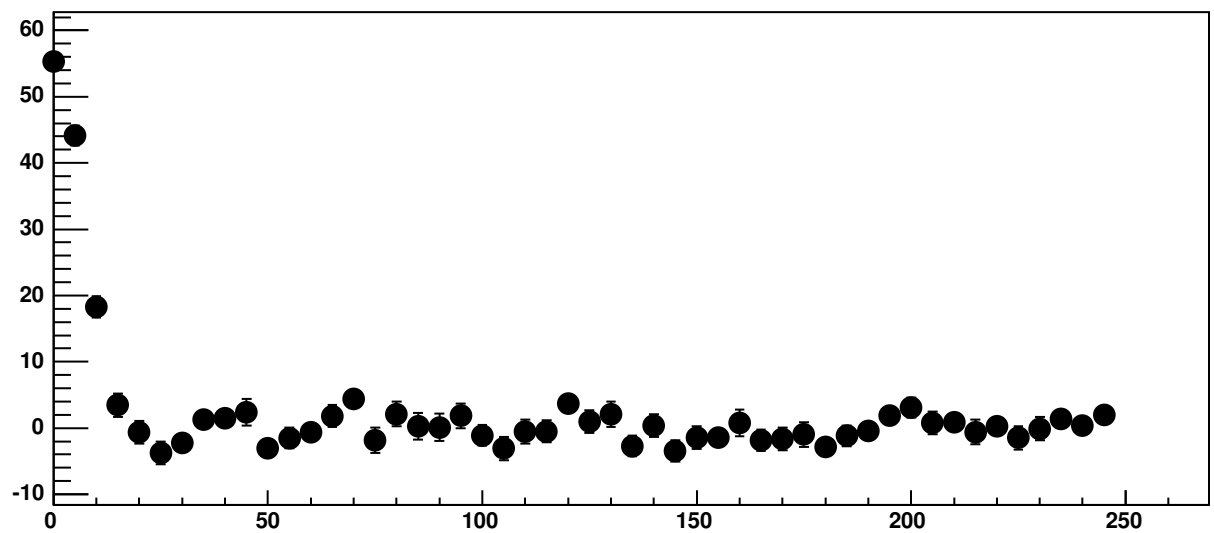
Chip 1, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold



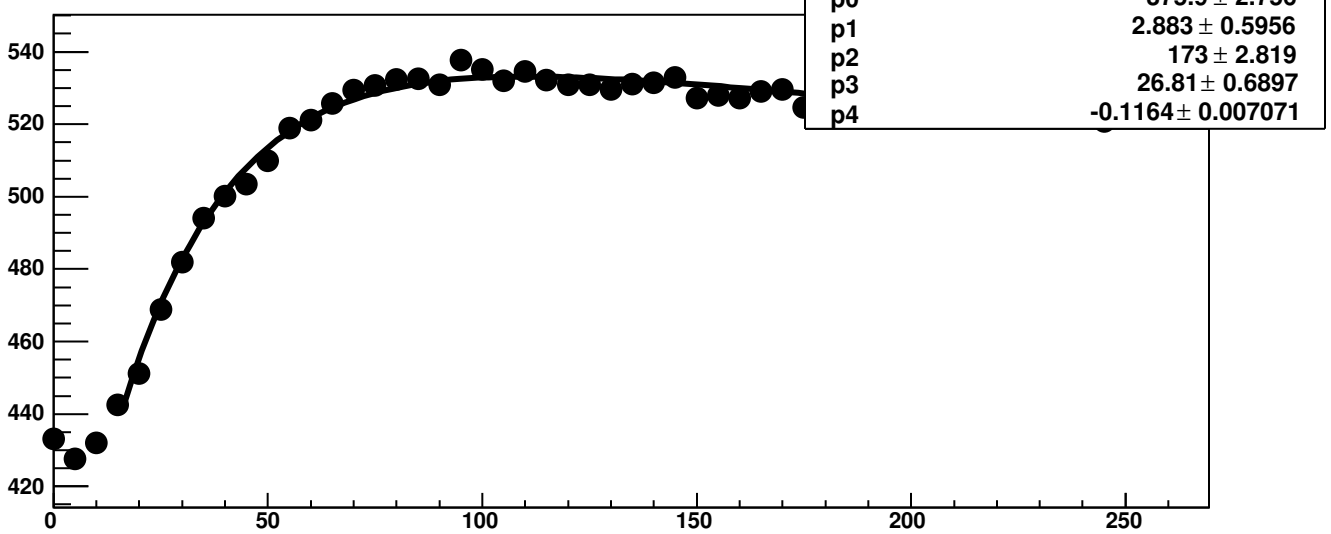
Chip 1, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



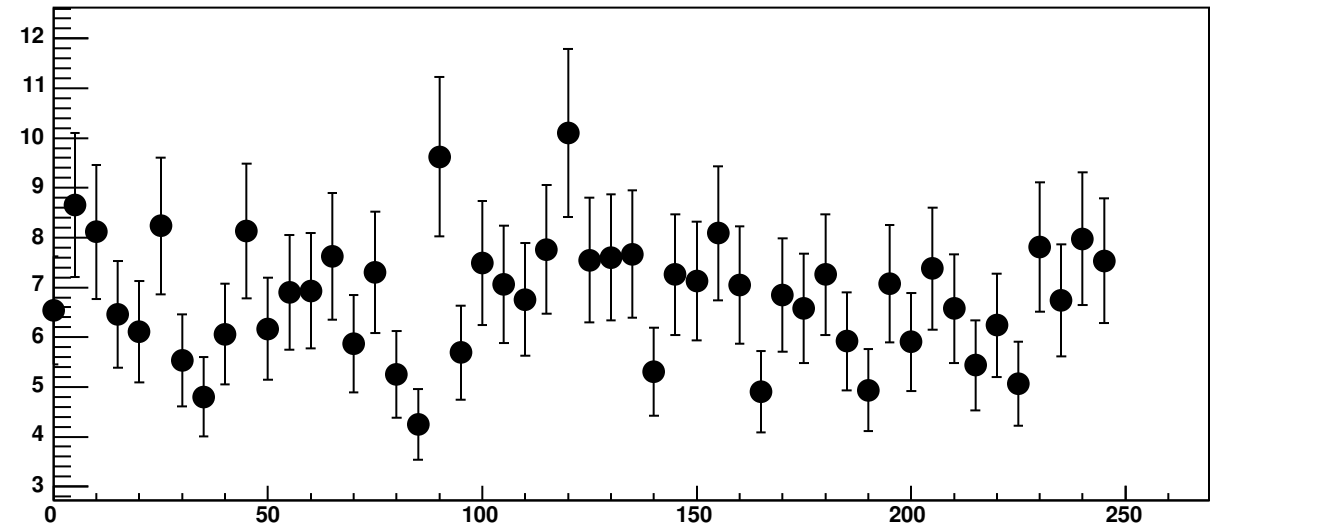
Chip 1, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold



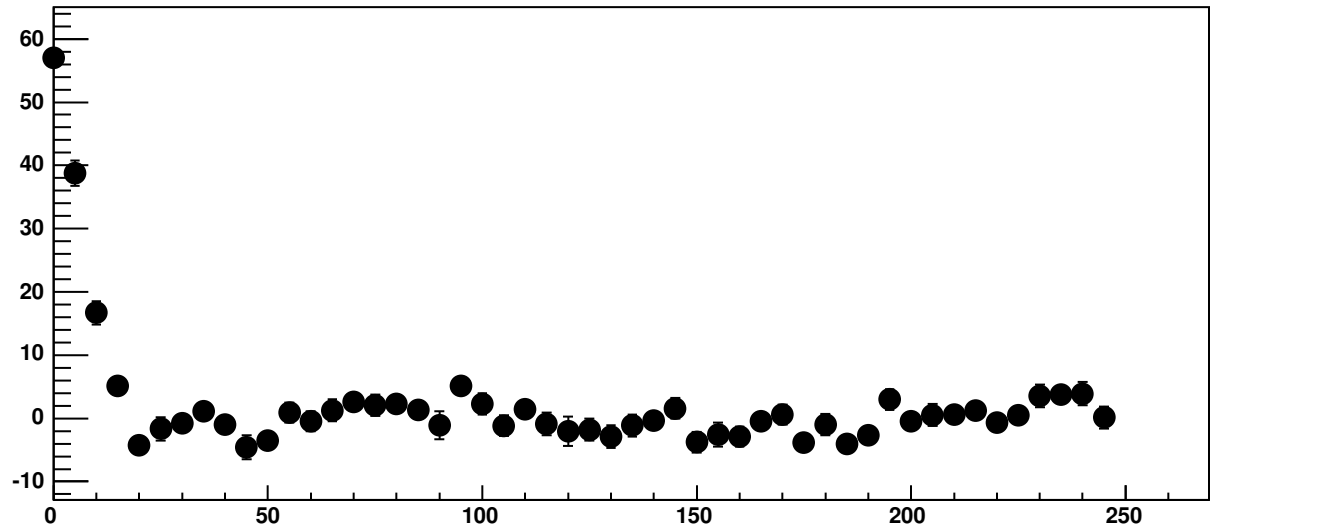
Chip 1, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold



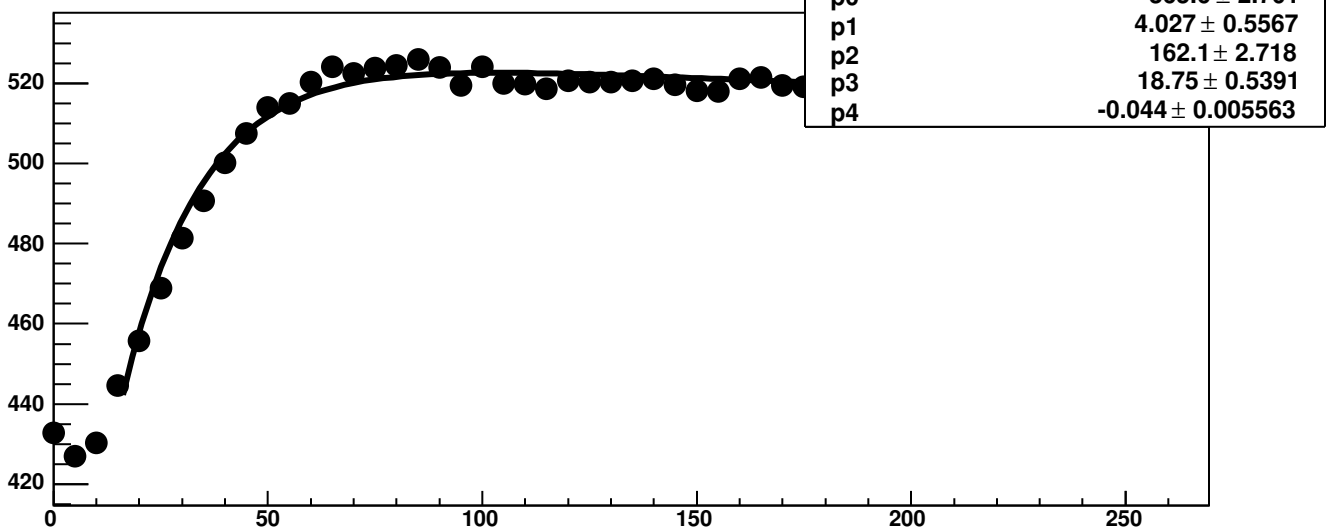
Chip 1, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold



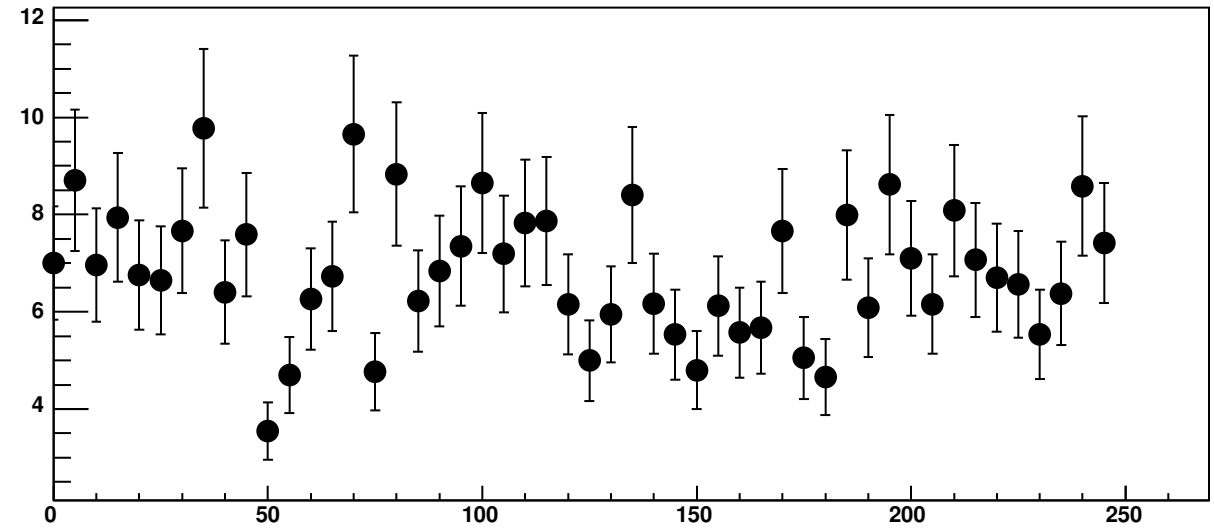
Chip 1, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold



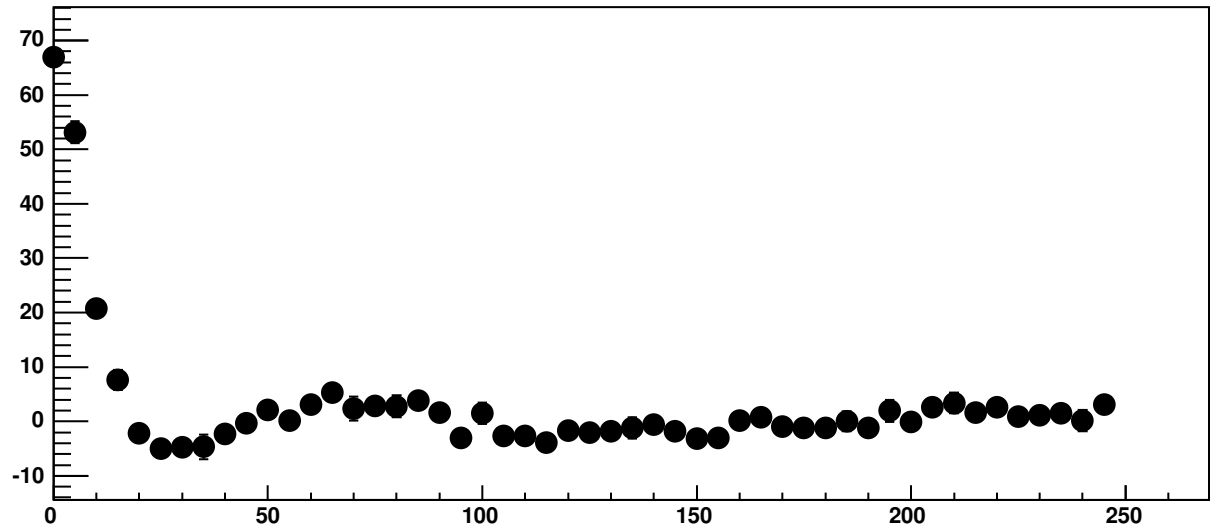
Chip 1, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold

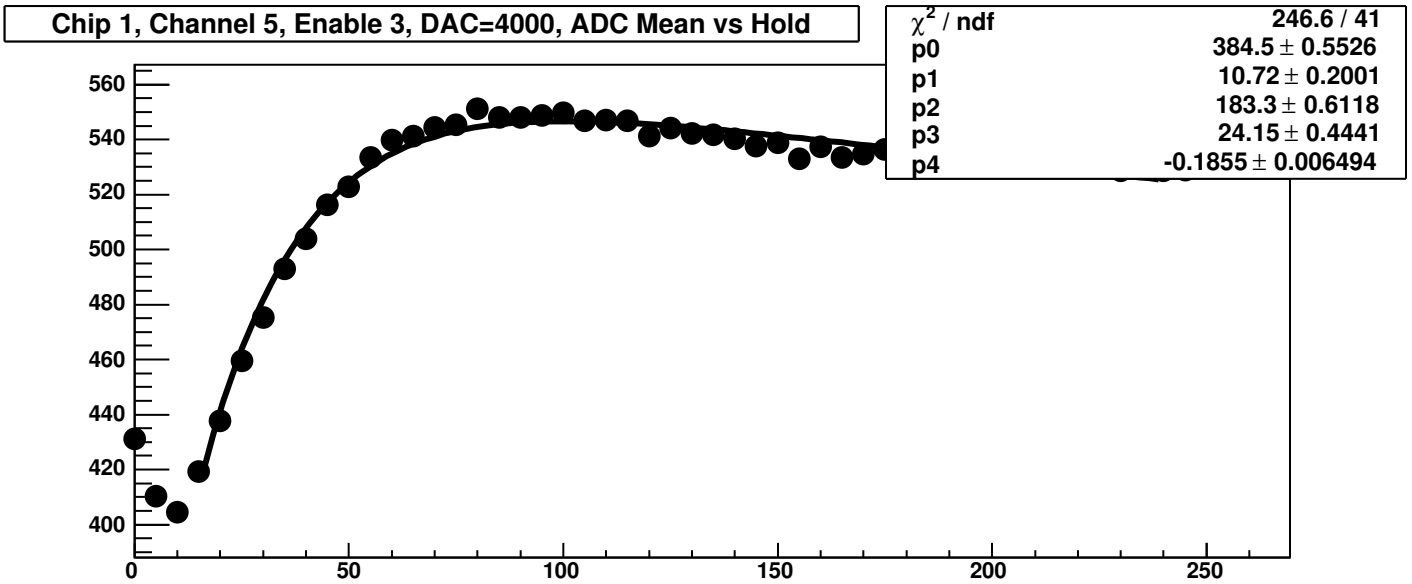


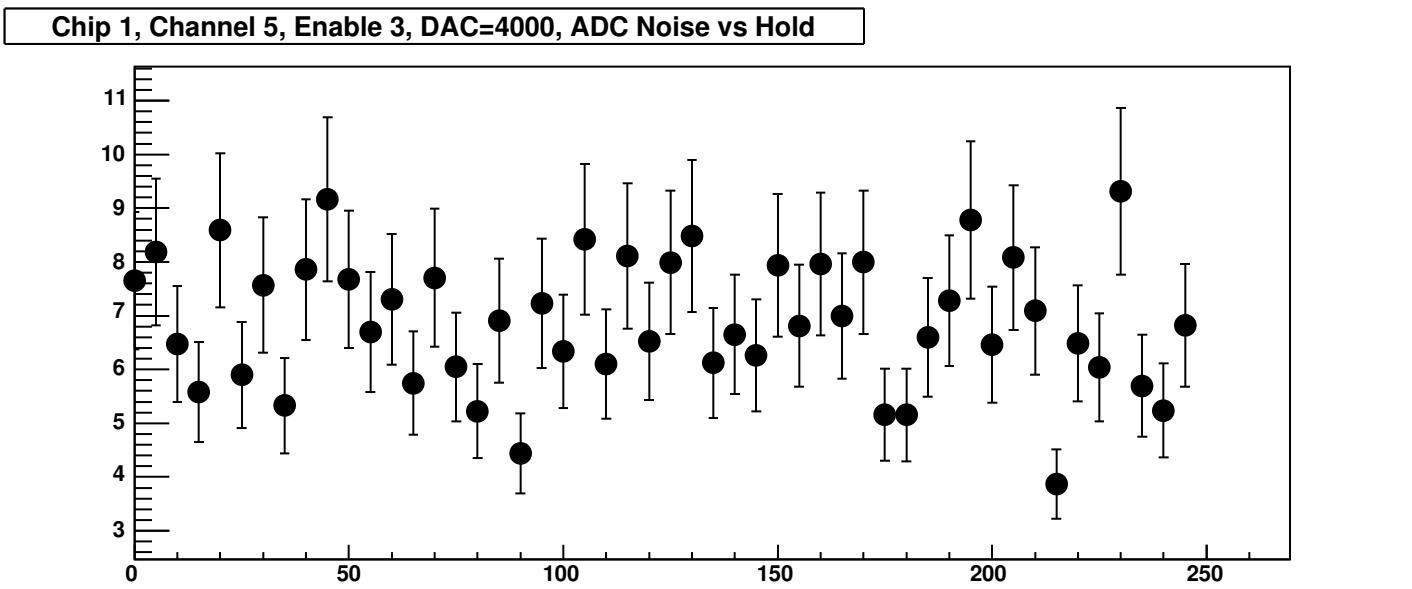
Chip 1, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold

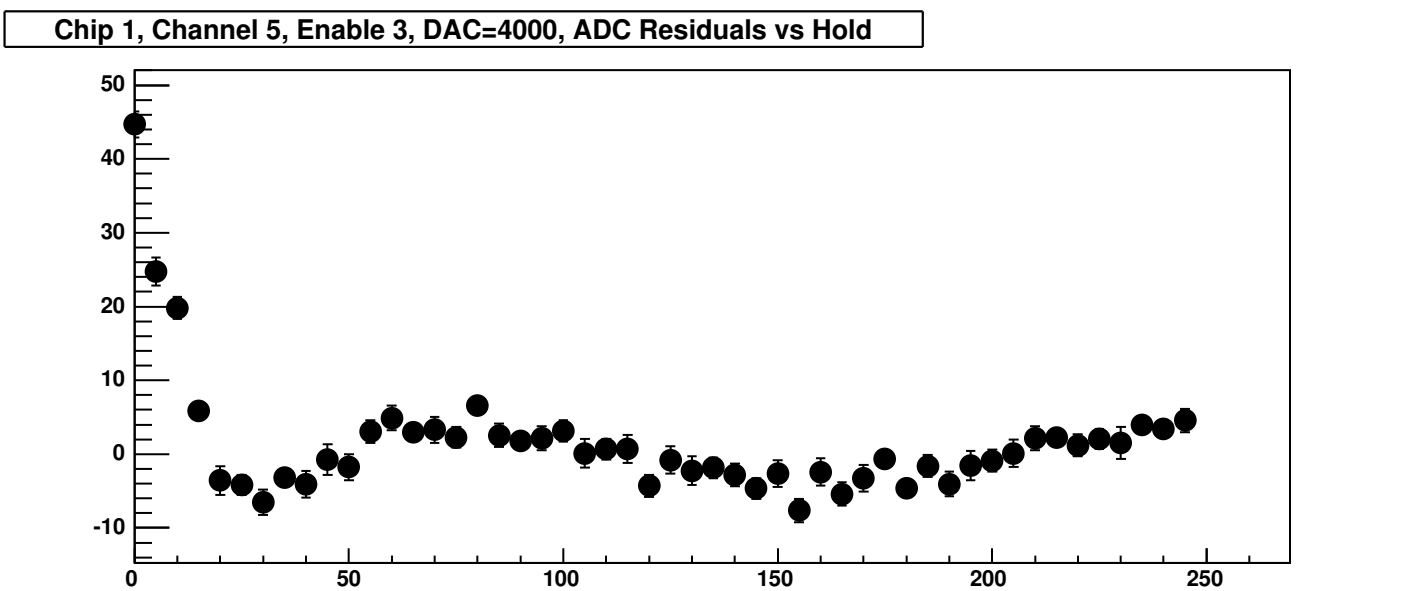


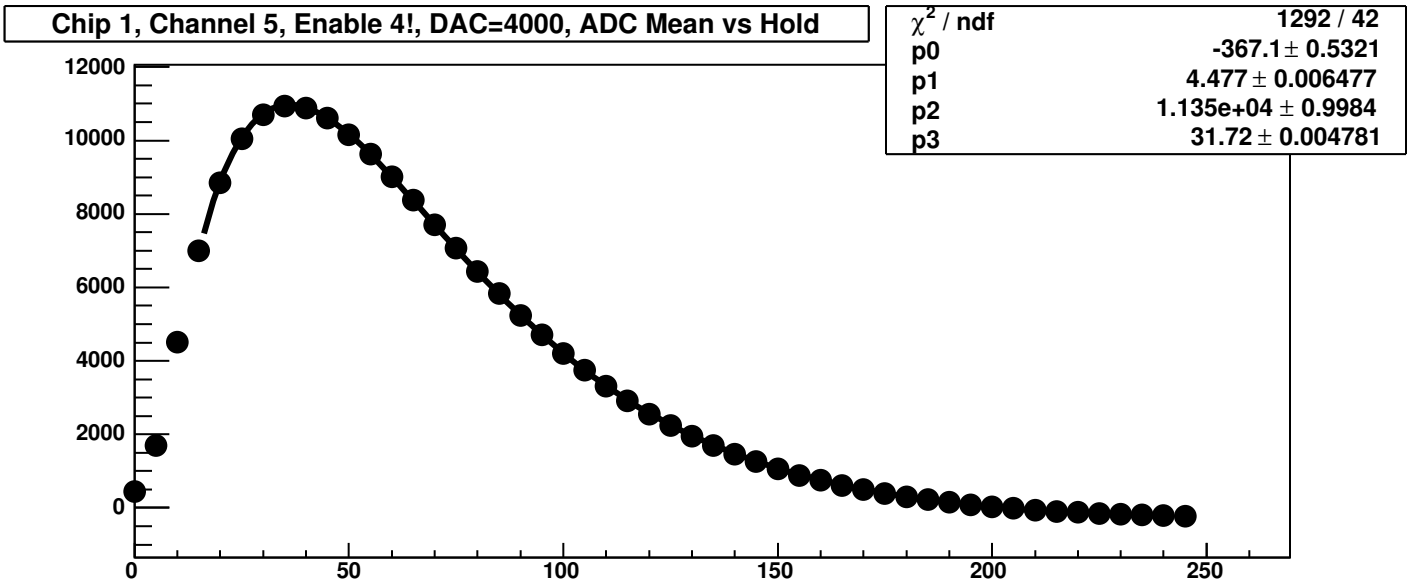
Chip 1, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold

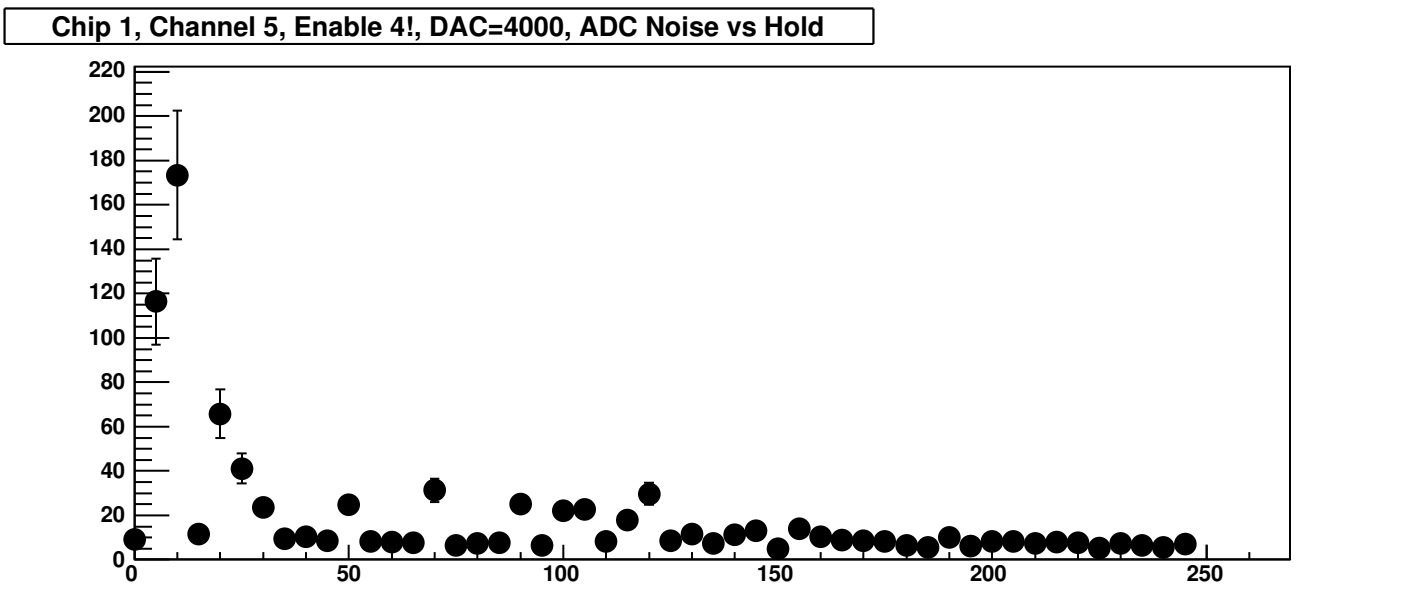


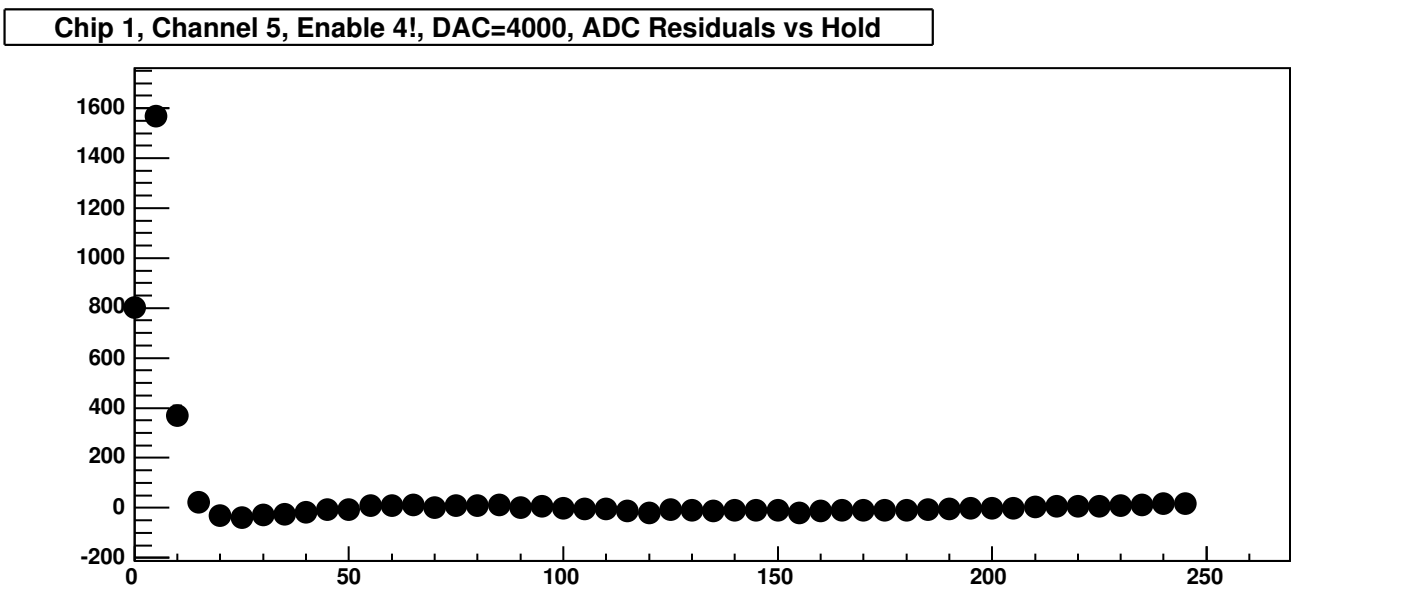

Chip 1, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 246.6 / 41
p0 384.5 ± 0.5526
p1 10.72 ± 0.2001
p2 183.3 ± 0.6118
p3 24.15 ± 0.4441
p4 -0.1855 ± 0.006494
560
540
520
500
480
460
440
420
400
0
50
100
150
200
250


Chip 1, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

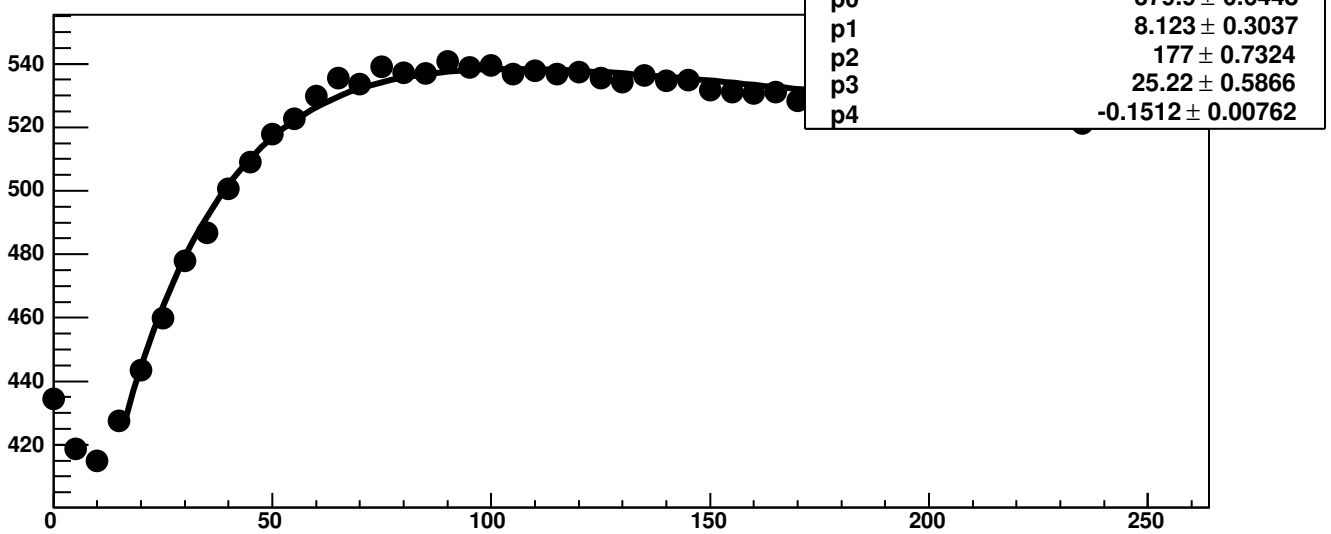

Chip 1, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250


Chip 1, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1292 / 42
p0 -367.1 ± 0.5321
p1 4.477 ± 0.006477
p2 1.135e+04 ± 0.9984
p3 31.72 ± 0.004781
12000
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

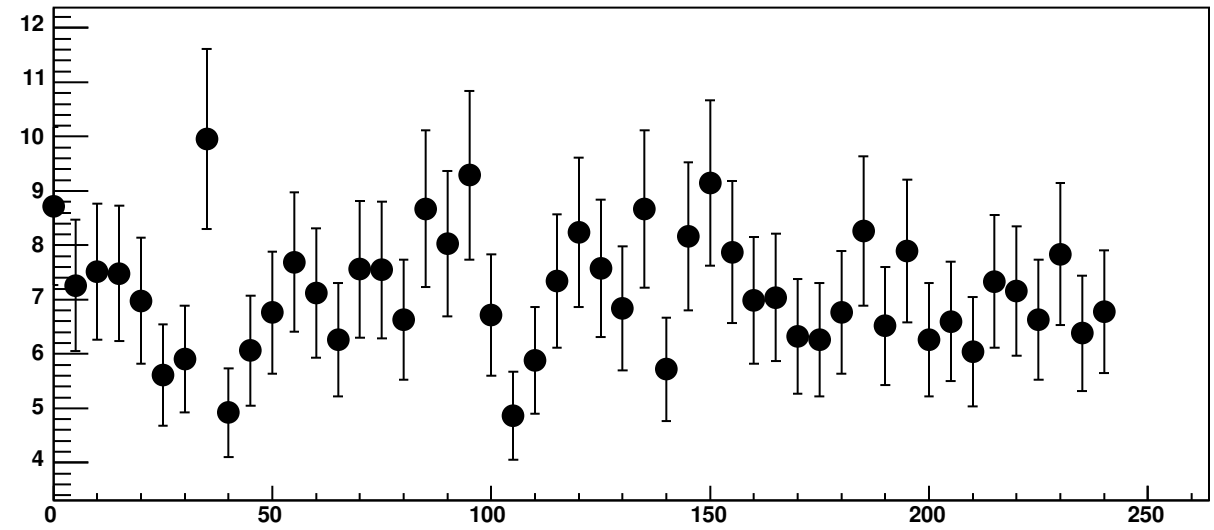

Chip 1, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 1, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

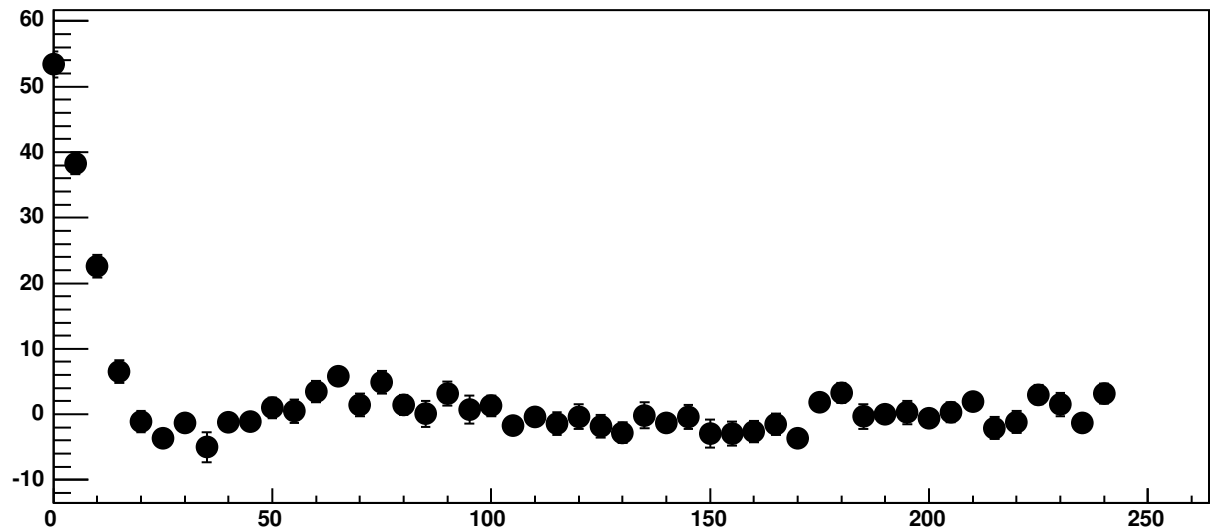
Chip 1, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold

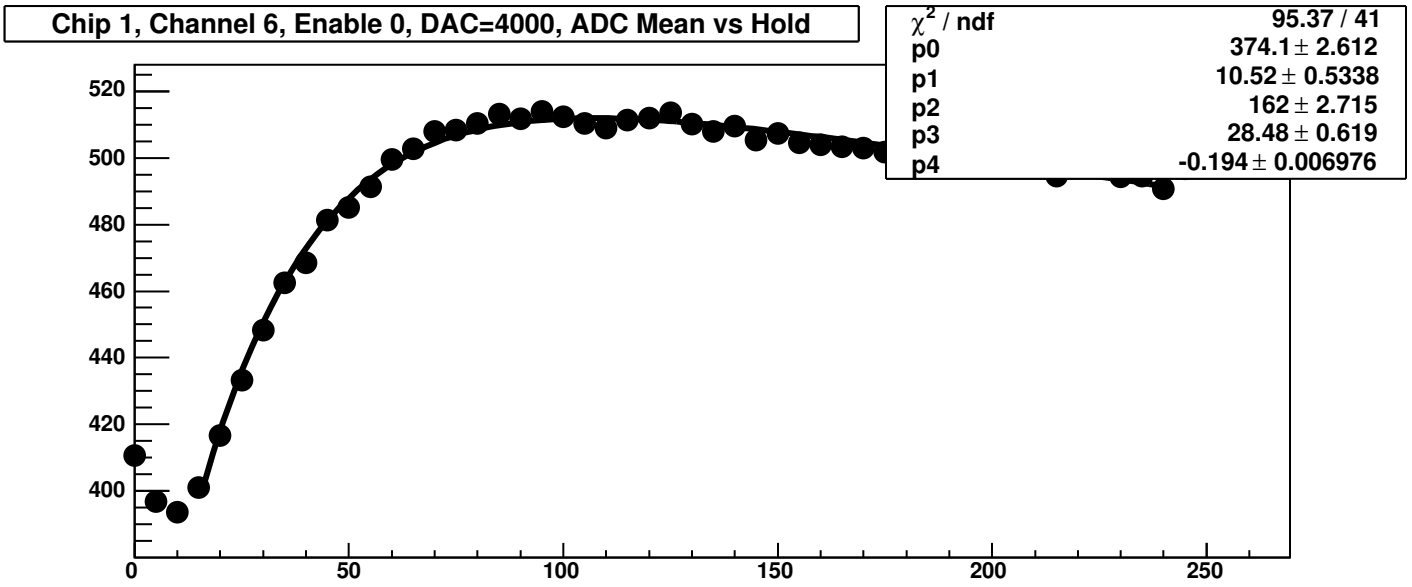


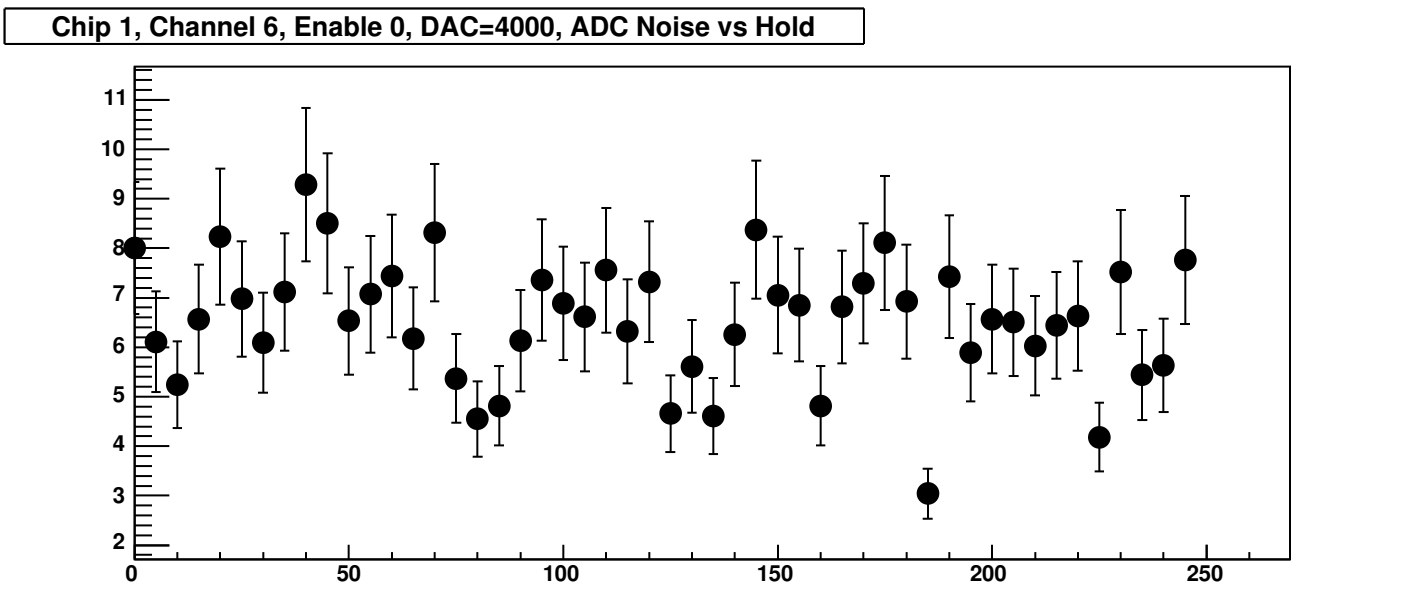
Chip 1, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold

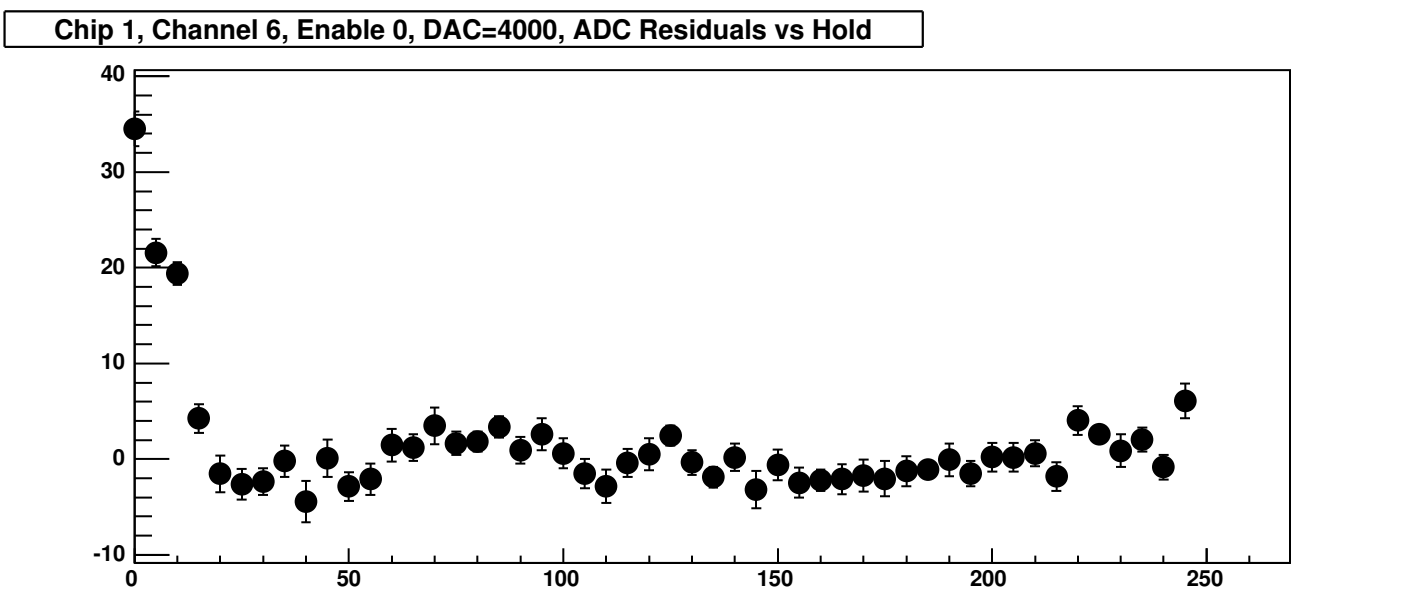


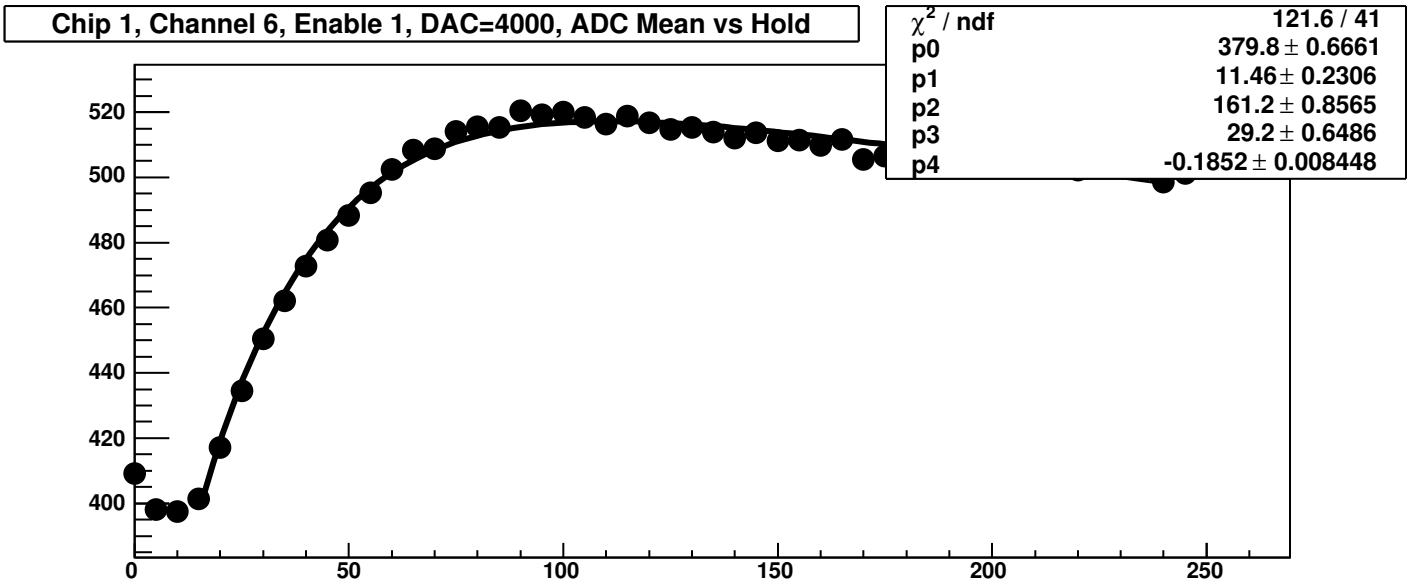
Chip 1, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold

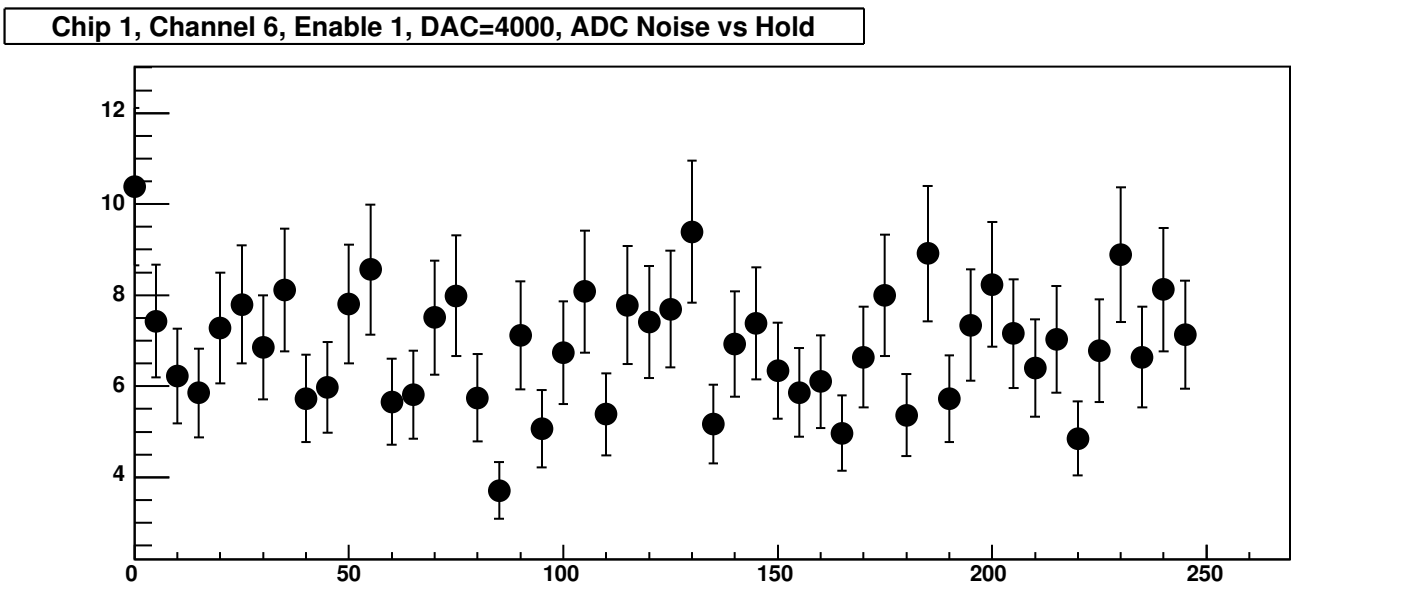


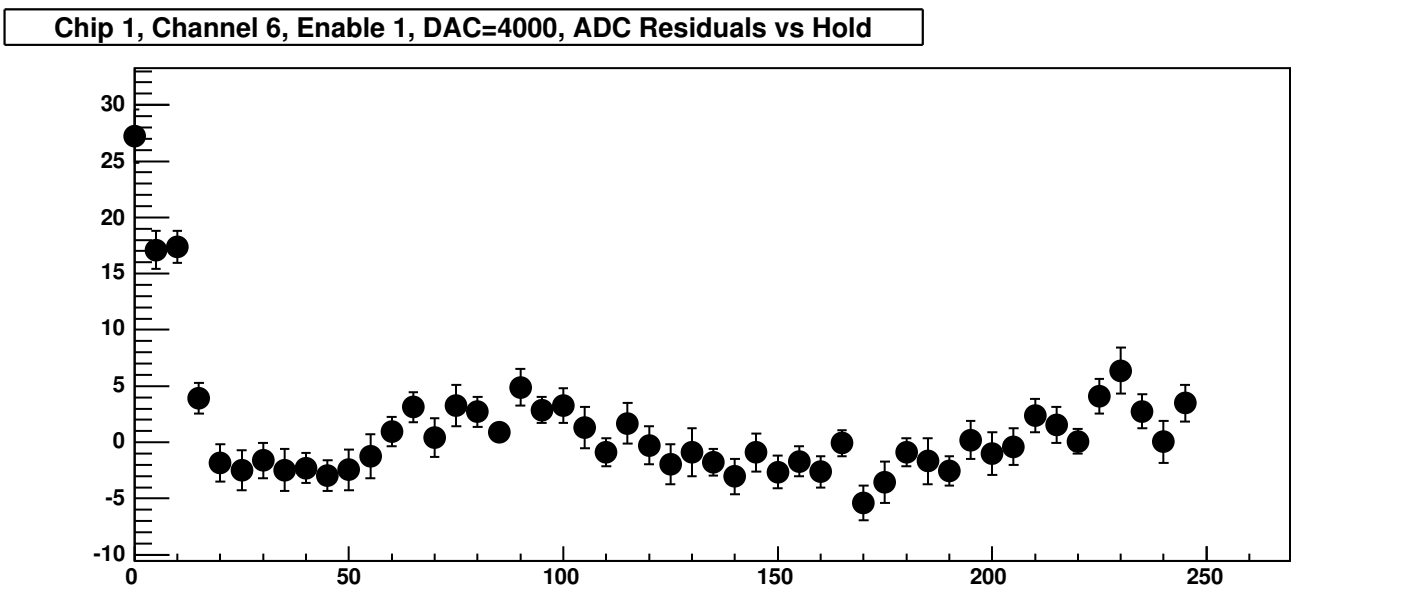

Chip 1, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 95.37 / 41
p0 374.1 ± 2.612
p1 10.52 ± 0.5338
p2 162 ± 2.715
p3 28.48 ± 0.619
p4 -0.194 ± 0.006976
520
500
480
460
440
420
400
0
50
100
150
200
250


Chip 1, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
2
0
50
100
150
200
250

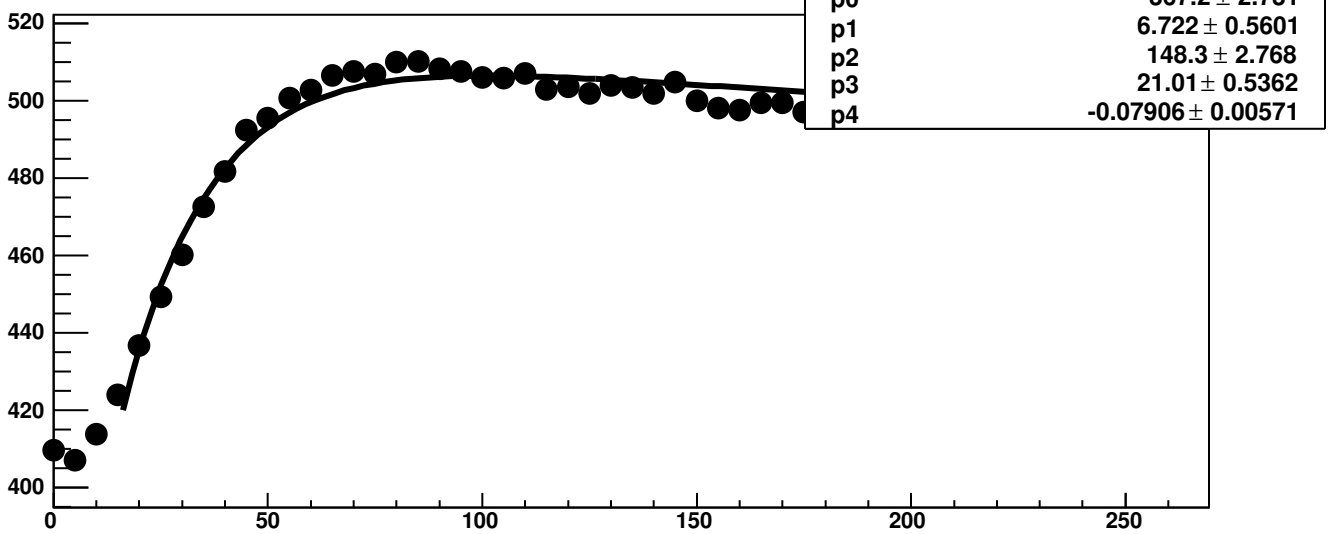

Chip 1, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250


Chip 1, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 121.6 / 41
p0 379.8 ± 0.6661
p1 11.46 ± 0.2306
p2 161.2 ± 0.8565
p3 29.2 ± 0.6486
p4 -0.1852 ± 0.008448
520
500
480
460
440
420
400
0
50
100
150
200
250

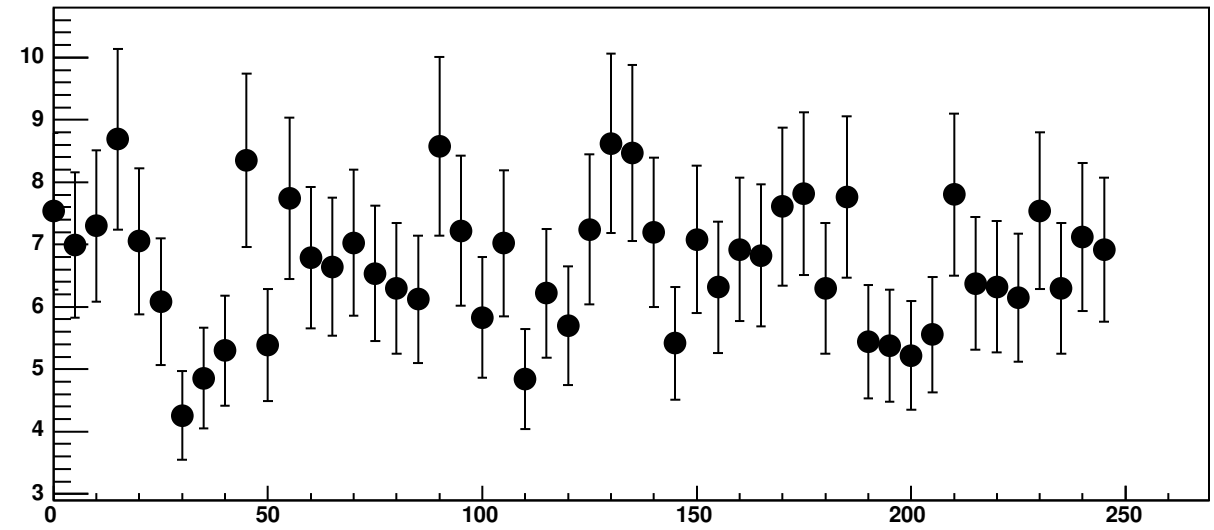

Chip 1, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 1, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
0
50
100
150
200
250

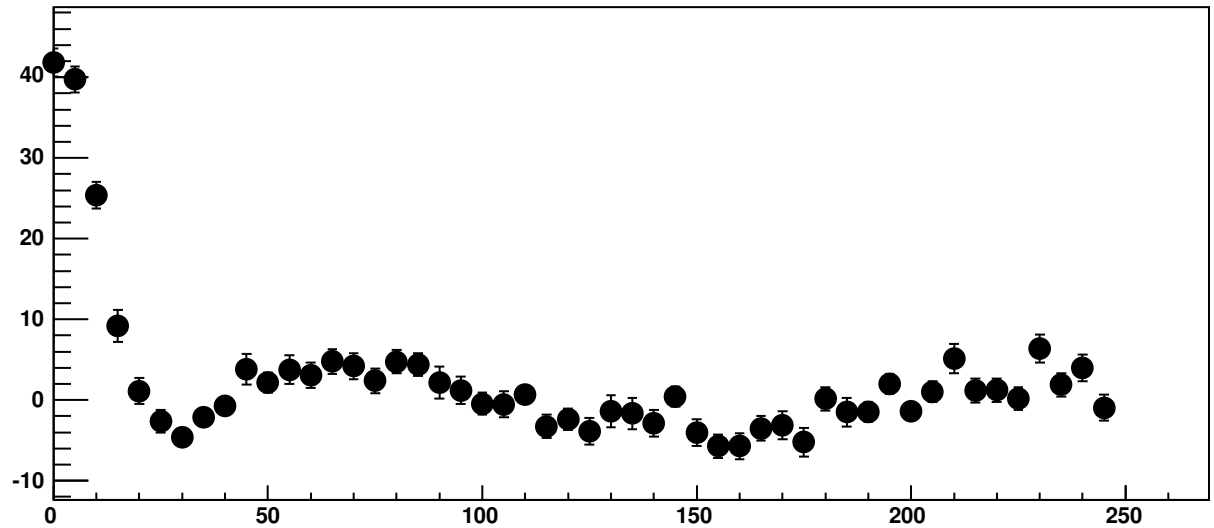
Chip 1, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold



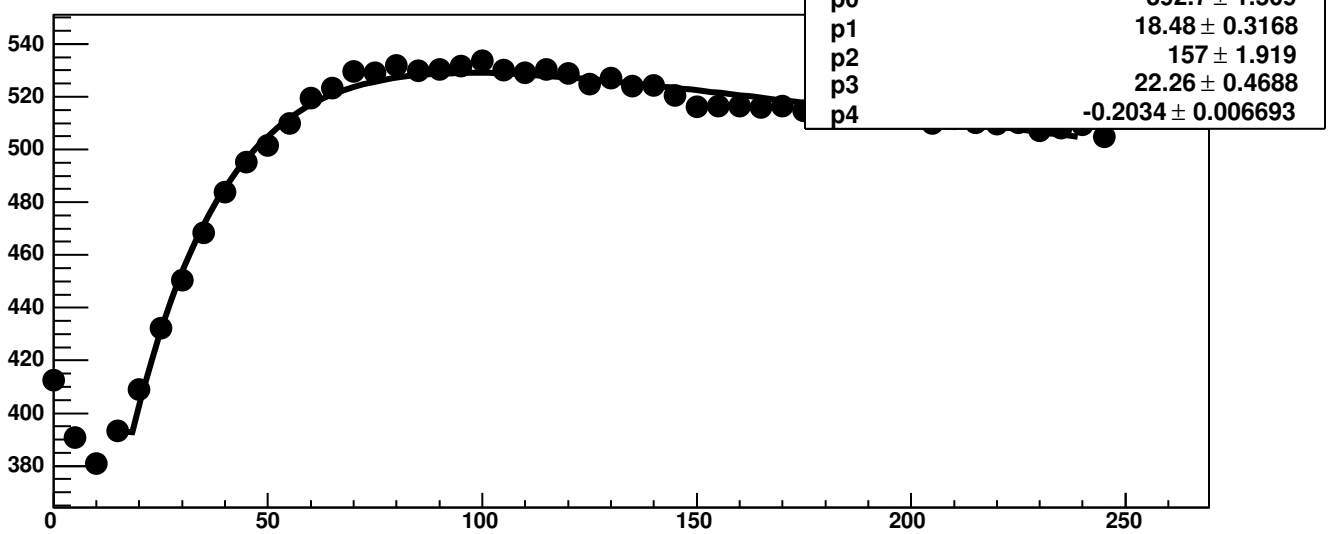
Chip 1, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold



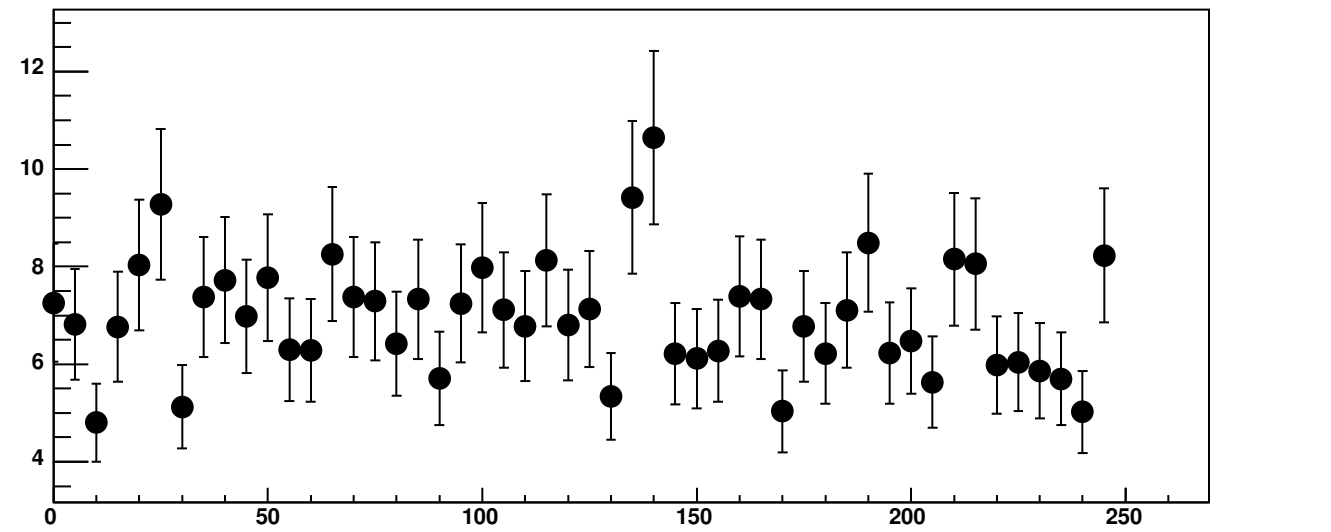
Chip 1, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold



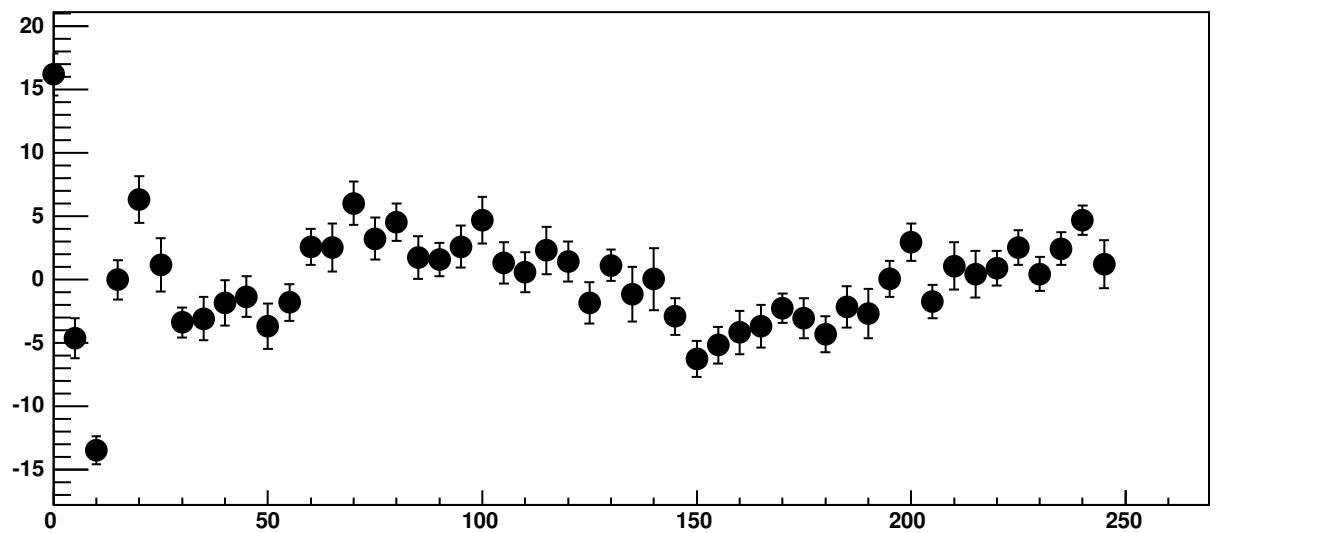
Chip 1, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold



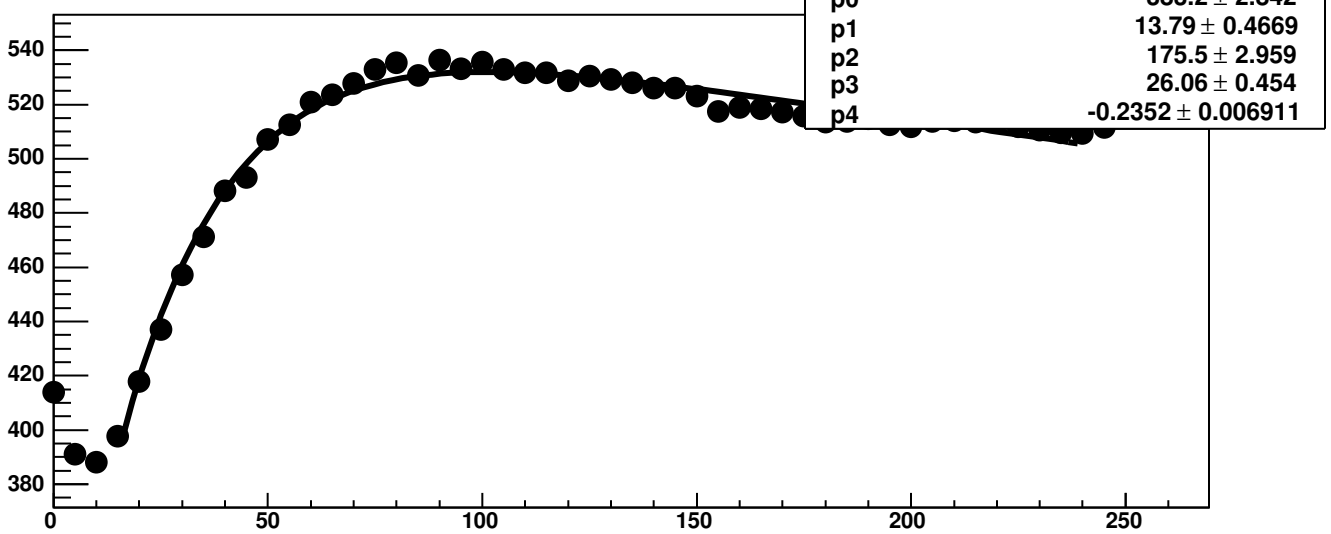
Chip 1, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



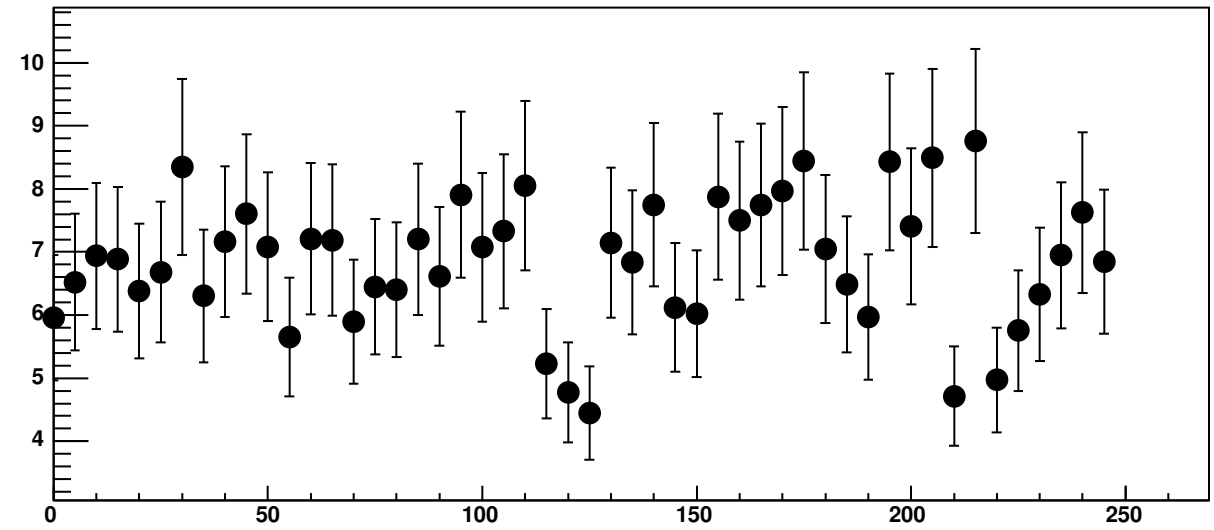
Chip 1, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold



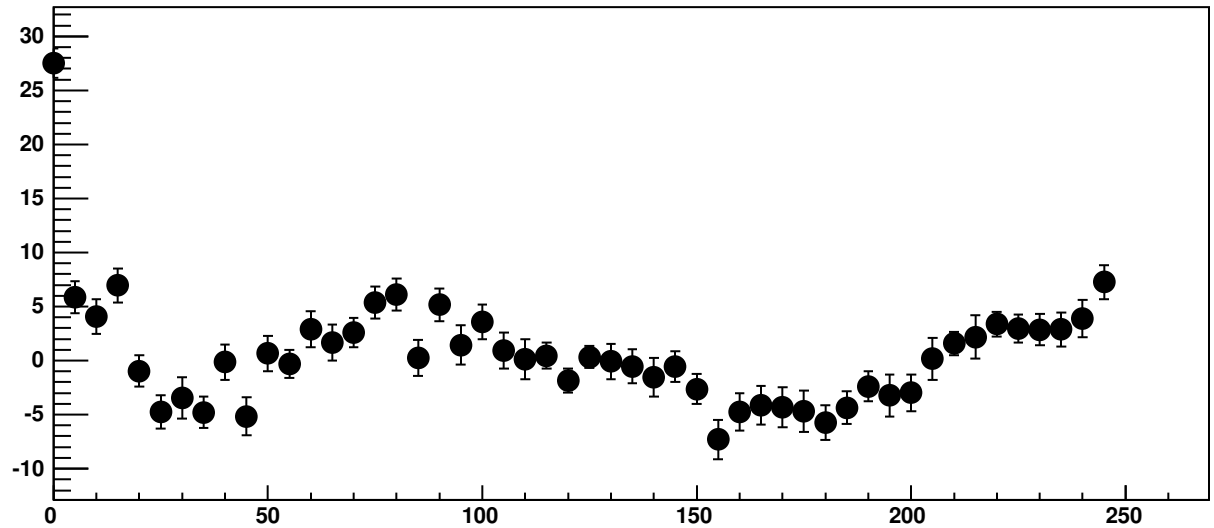
Chip 1, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold

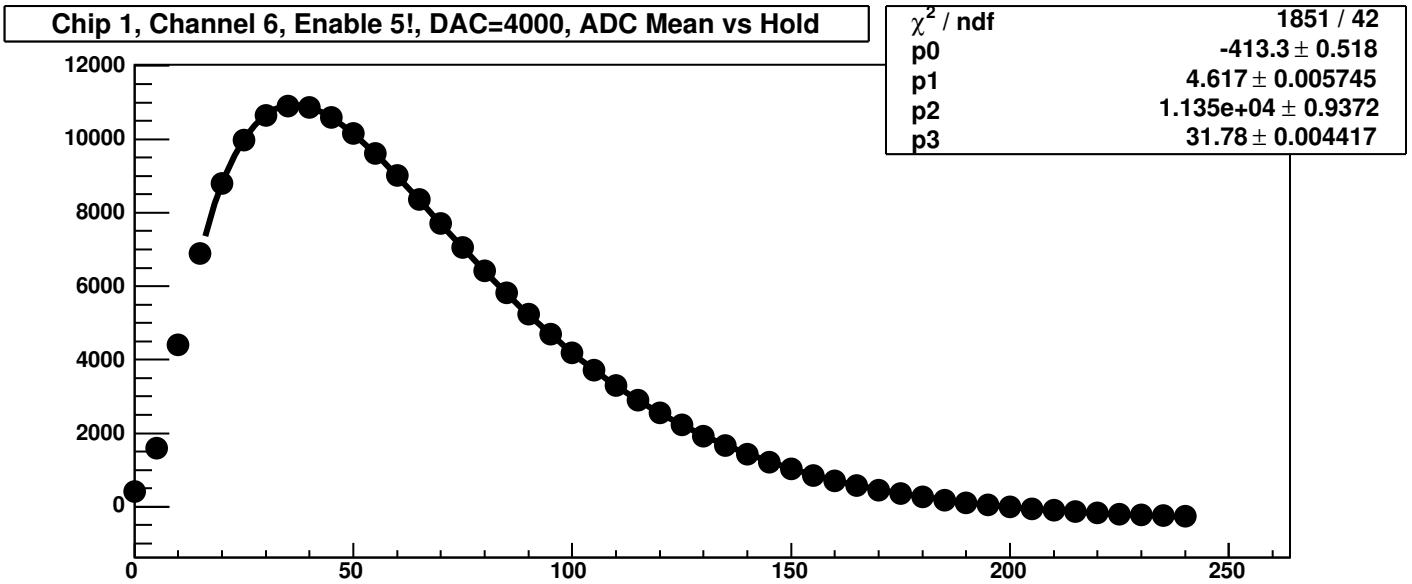


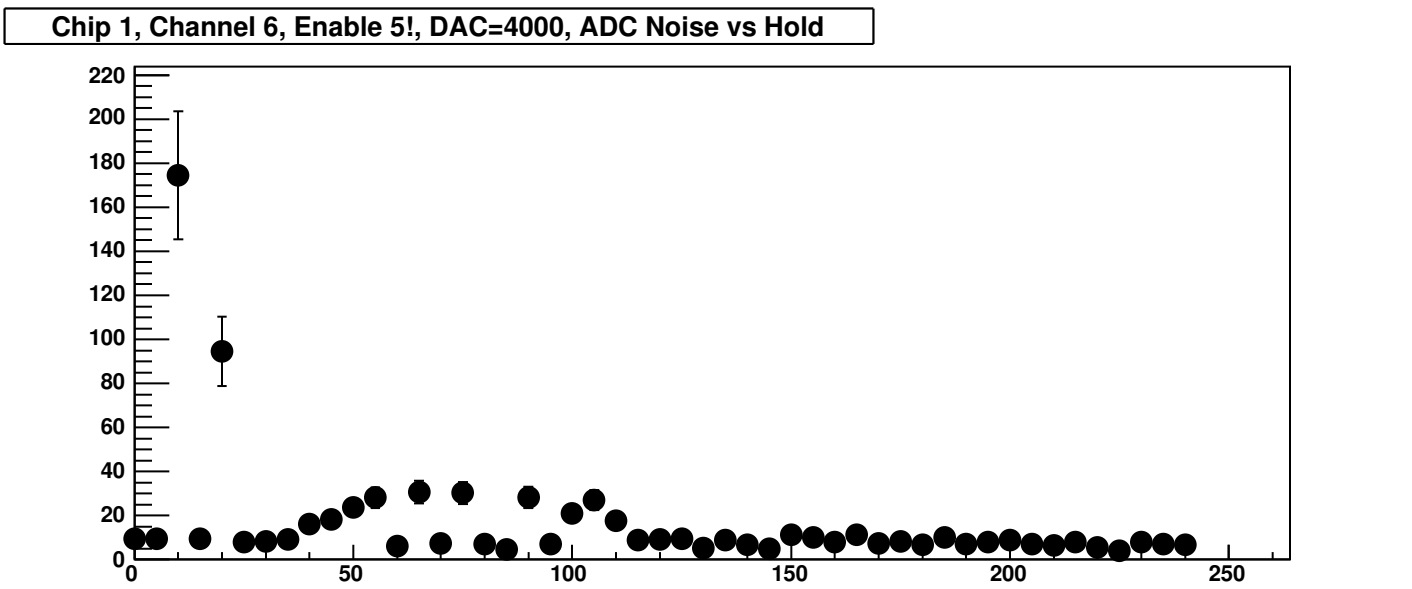
Chip 1, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold

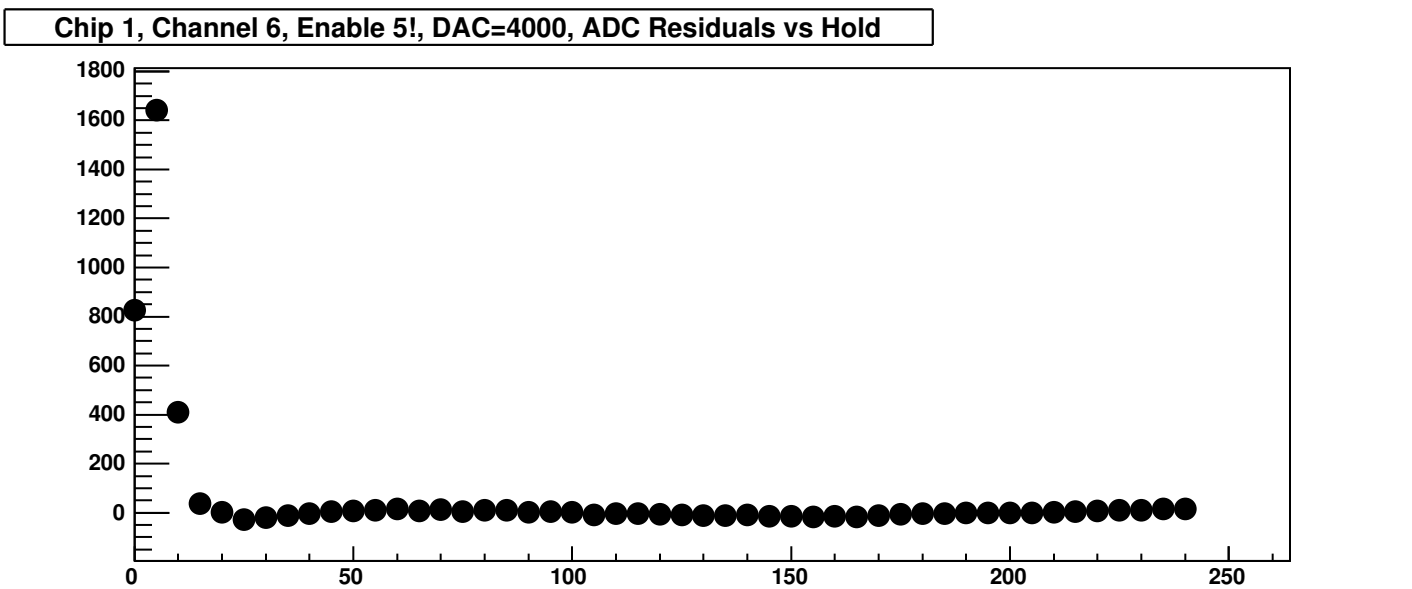


Chip 1, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold

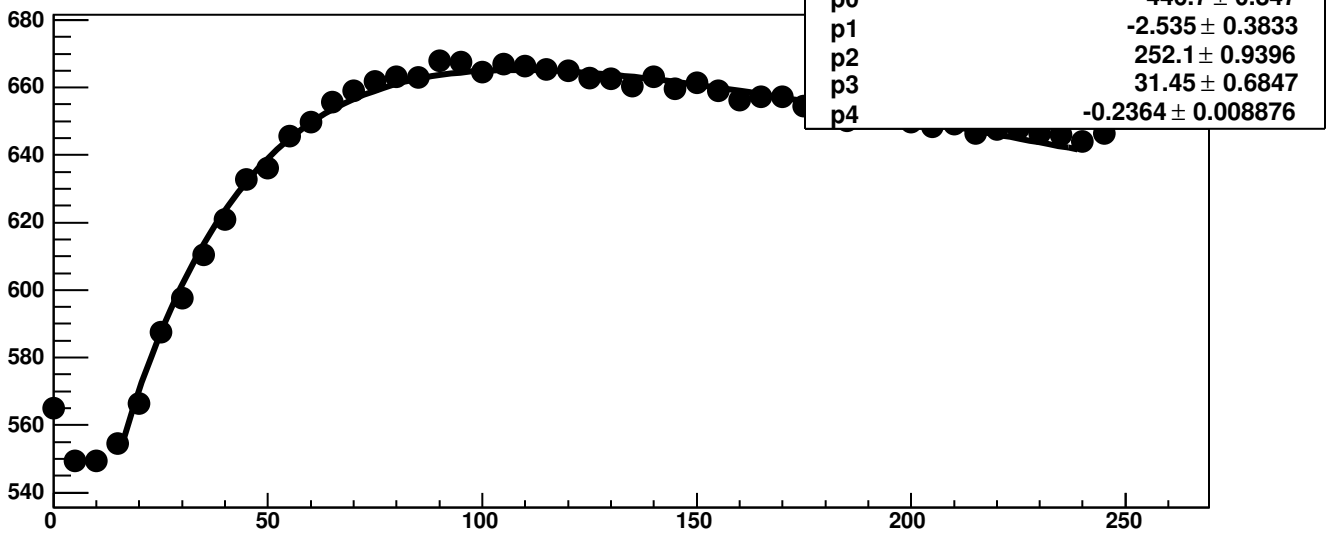



Chip 1, Channel 6, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1851 / 42
p0 -413.3 ± 0.518
p1 4.617 ± 0.005745
p2 1.135e+04 ± 0.9372
p3 31.78 ± 0.004417
12000
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

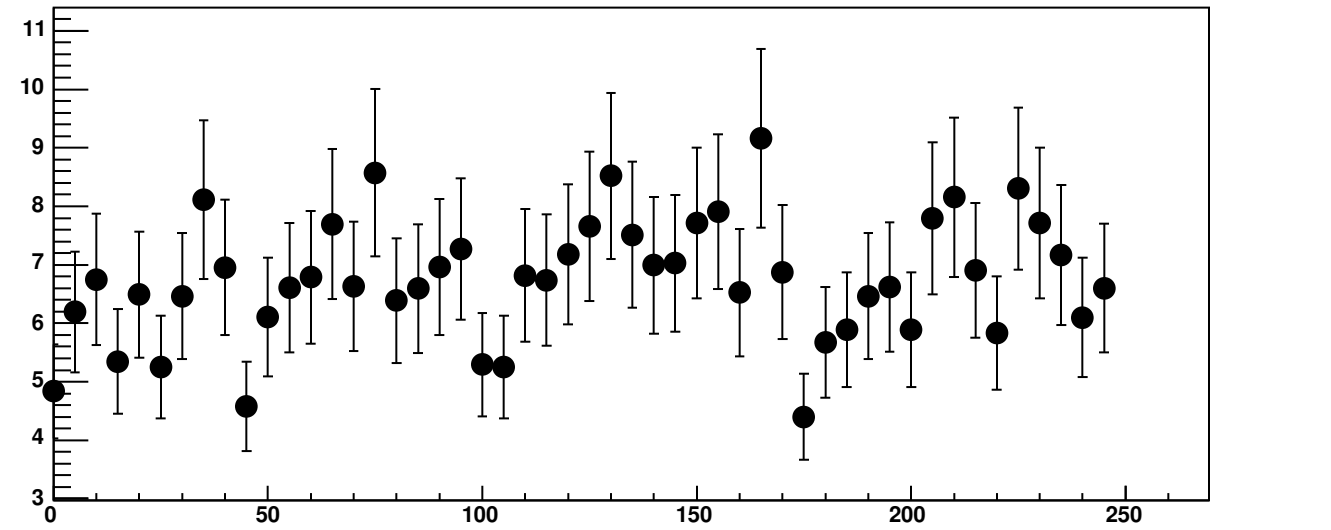

Chip 1, Channel 6, Enable 5!, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 1, Channel 6, Enable 5!, DAC=4000, ADC Residuals vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

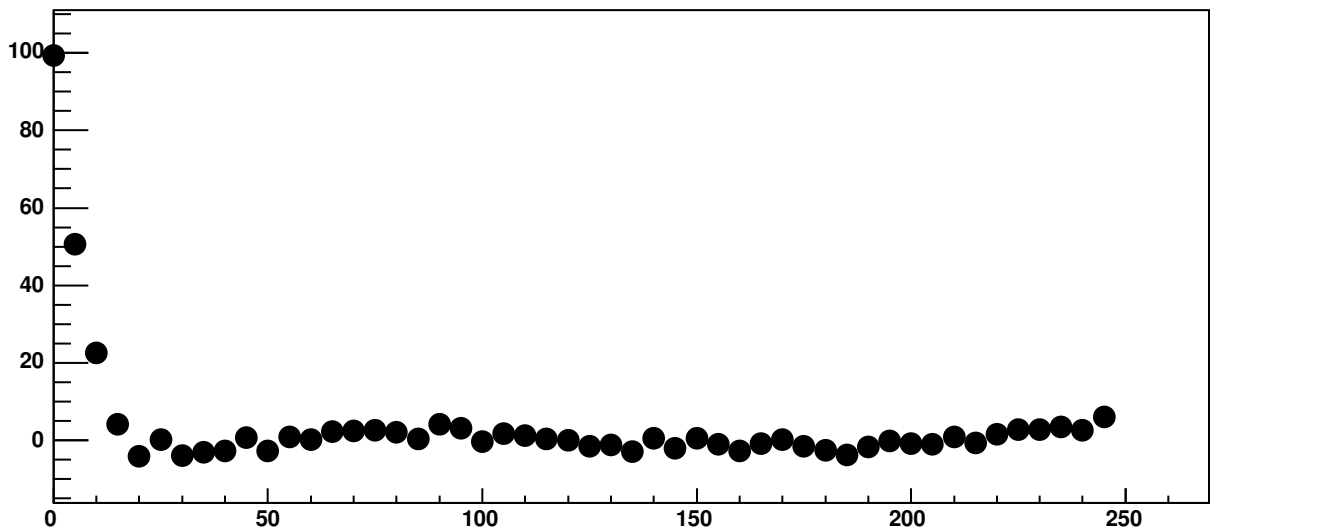
Chip 1, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold

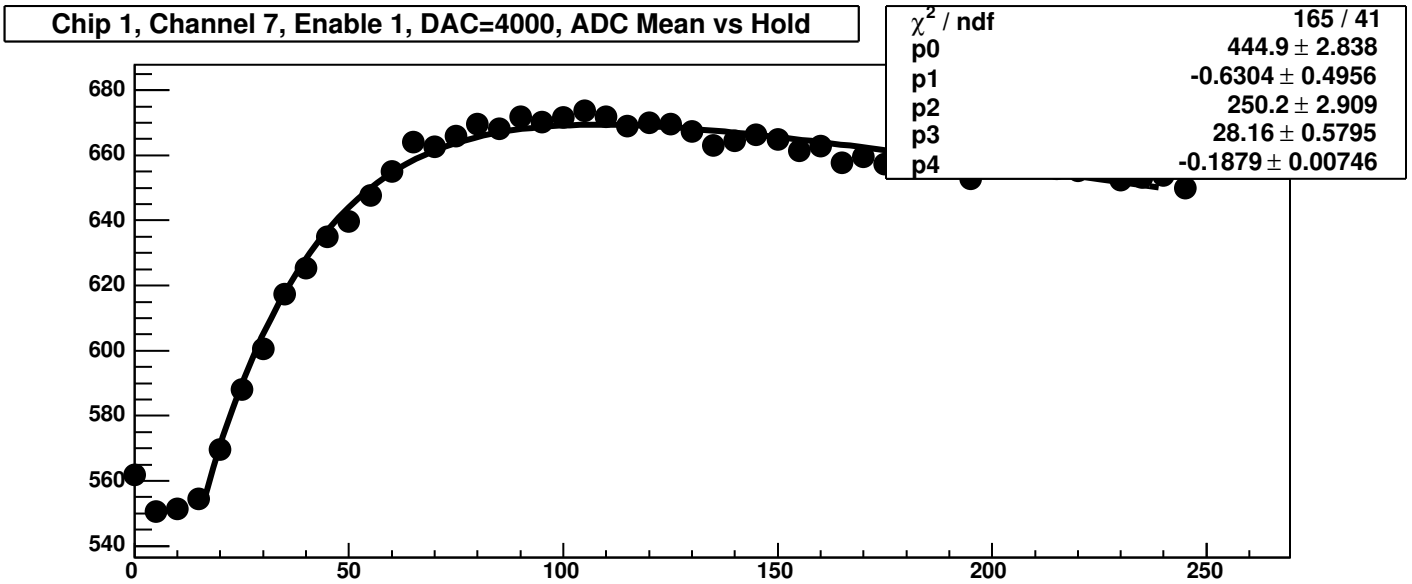


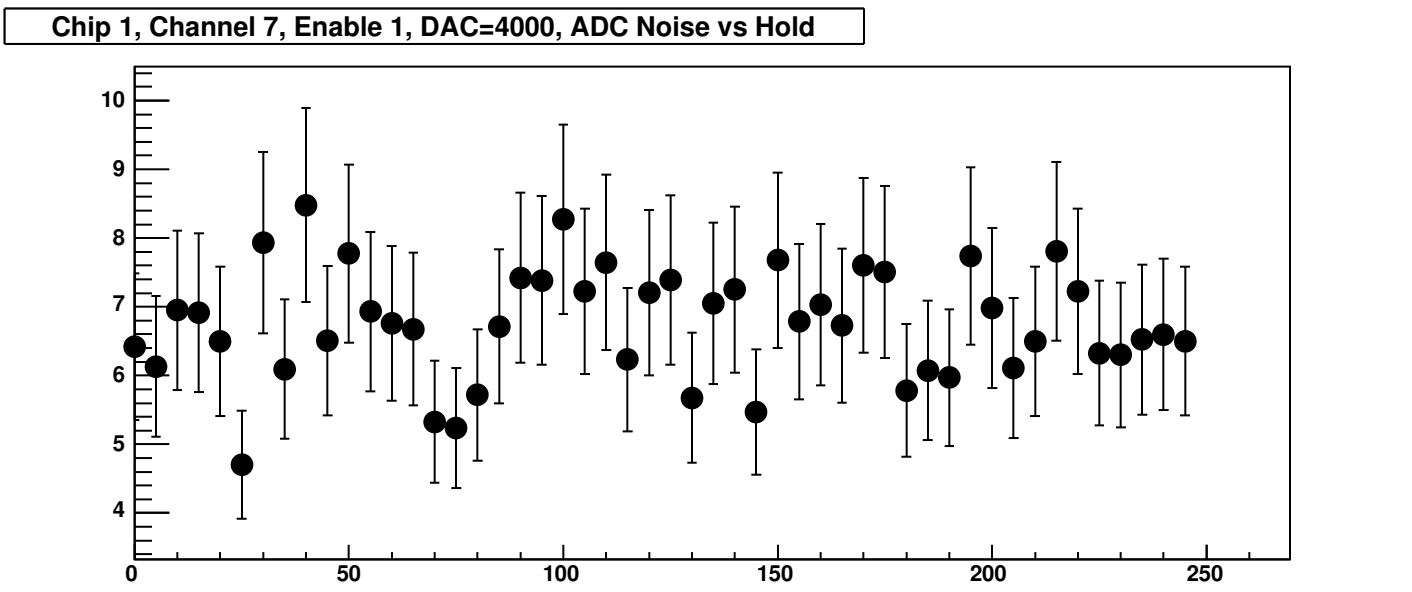
Chip 1, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold

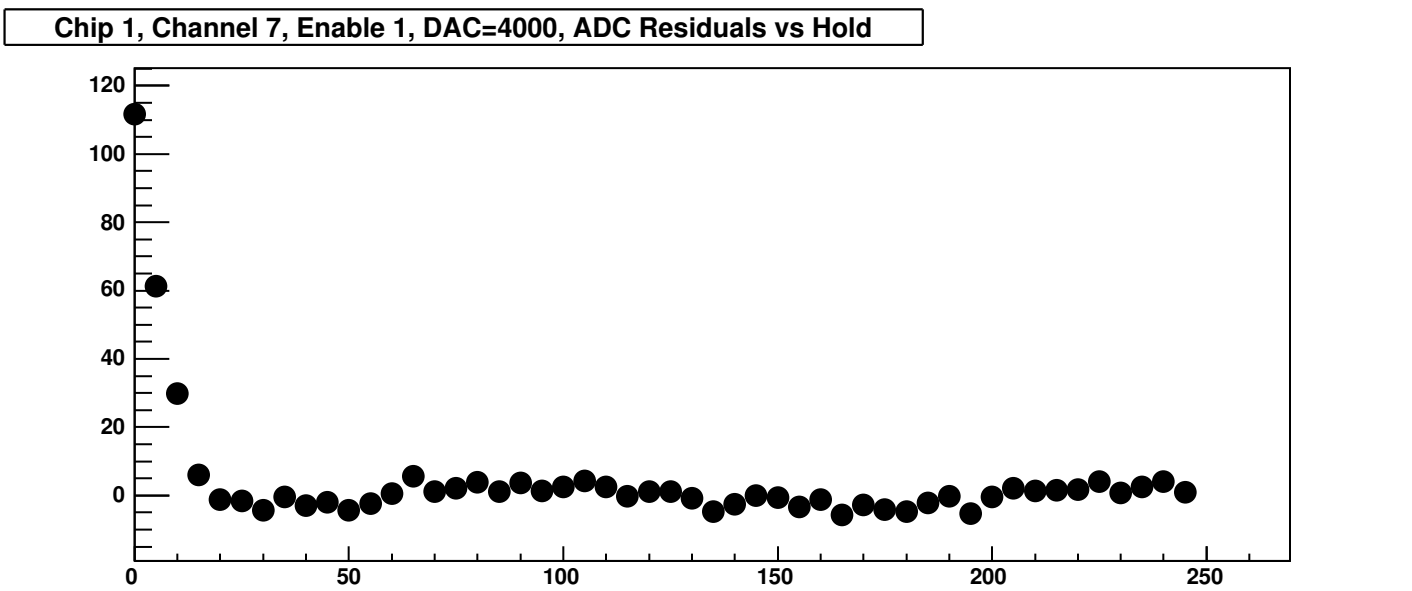


Chip 1, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold

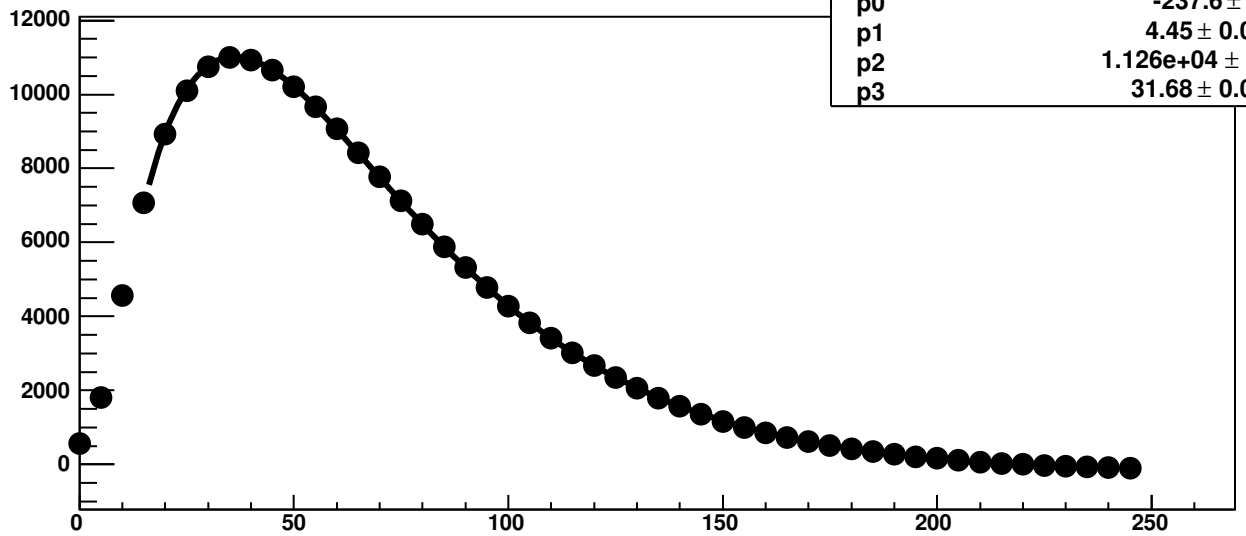



Chip 1, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 165 / 41
p0 444.9 ± 2.838
p1 -0.6304 ± 0.4956
p2 250.2 ± 2.909
p3 28.16 ± 0.5795
p4 -0.1879 ± 0.00746
680
660
640
620
600
580
560
540
0
50
100
150
200
250

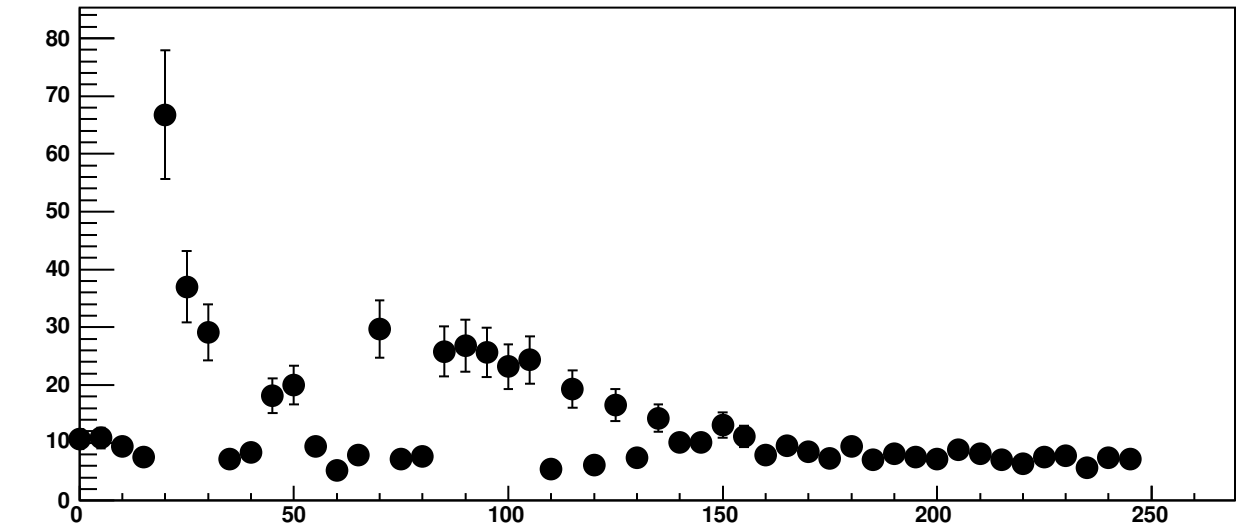

Chip 1, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 1, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250

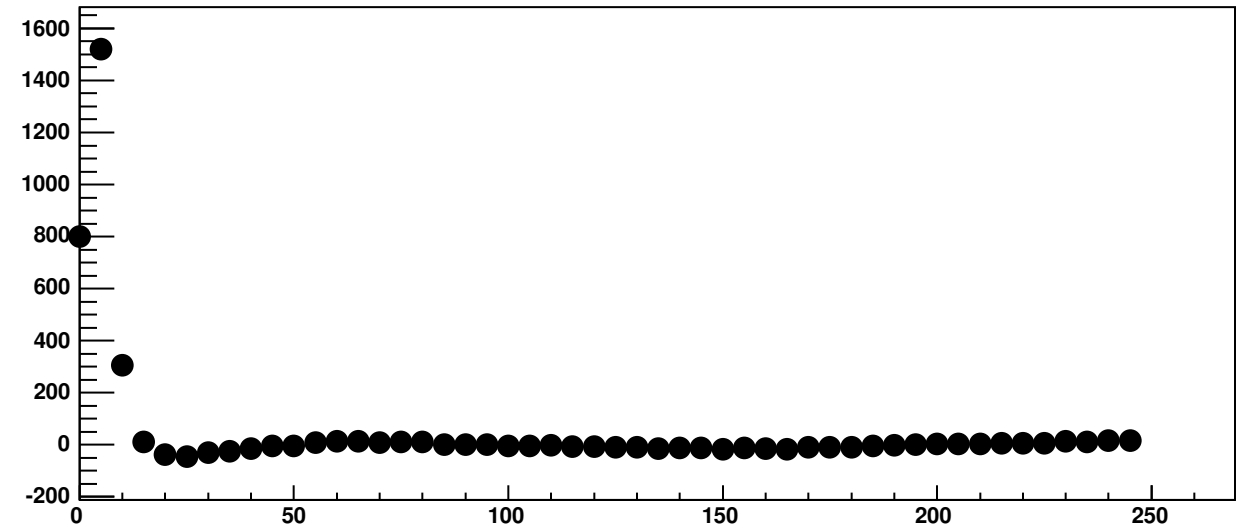
Chip 1, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold

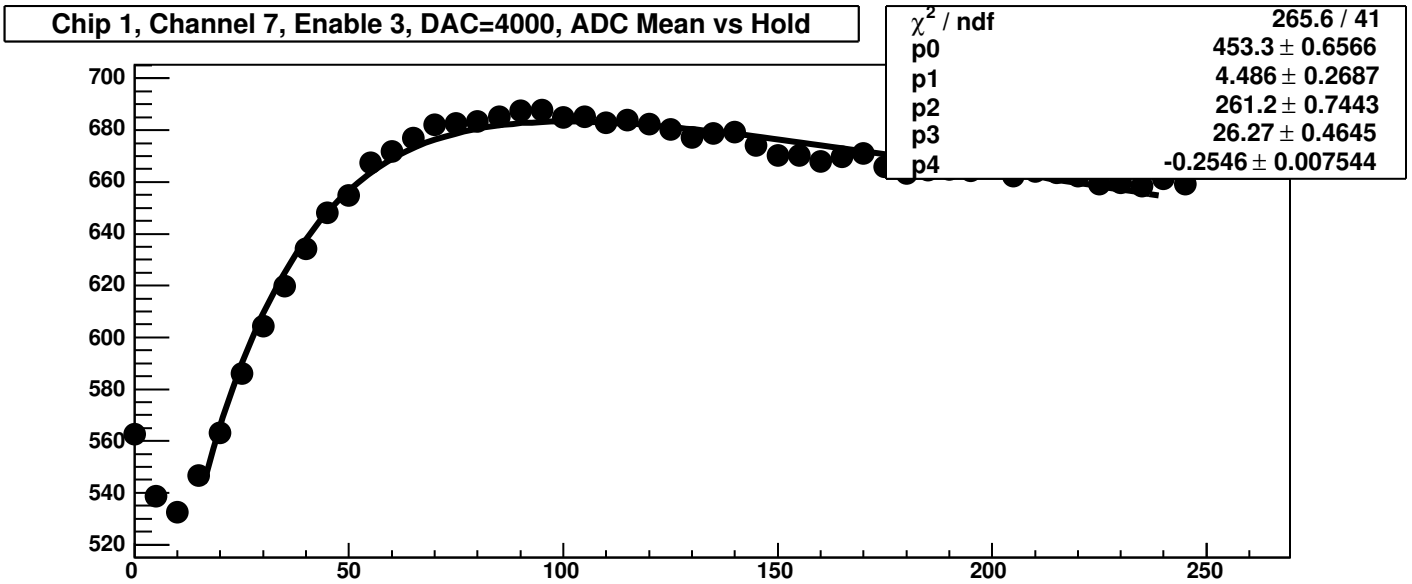


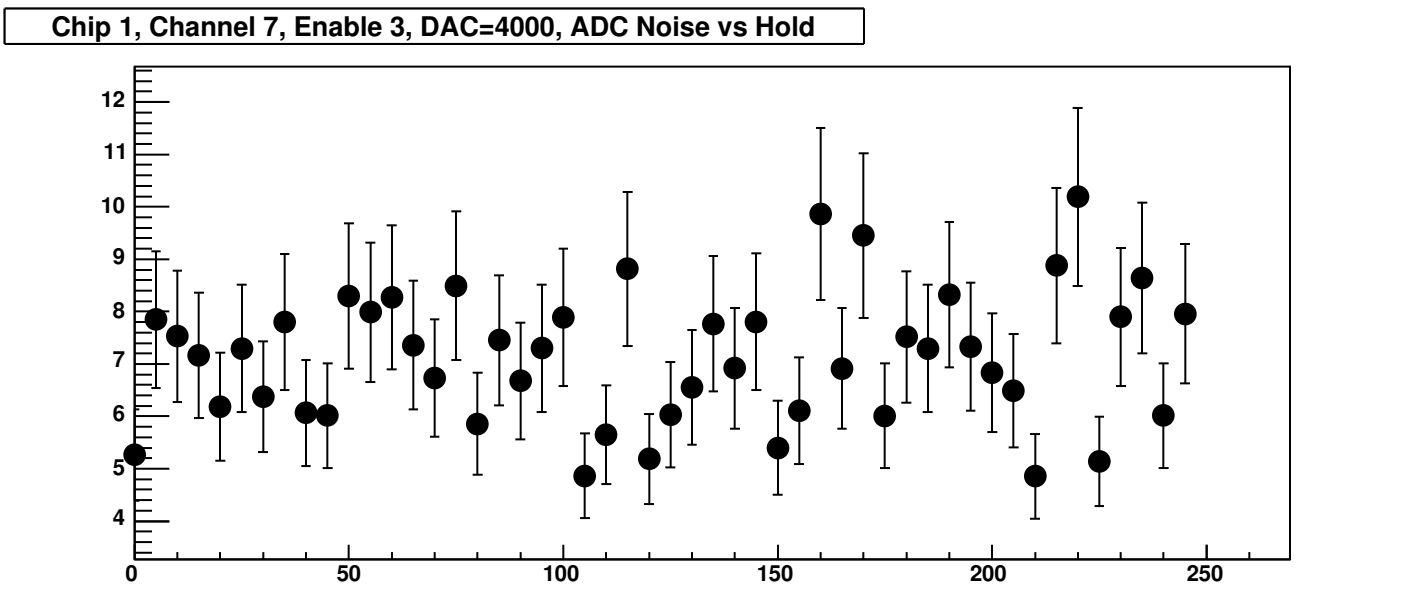
Chip 1, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold

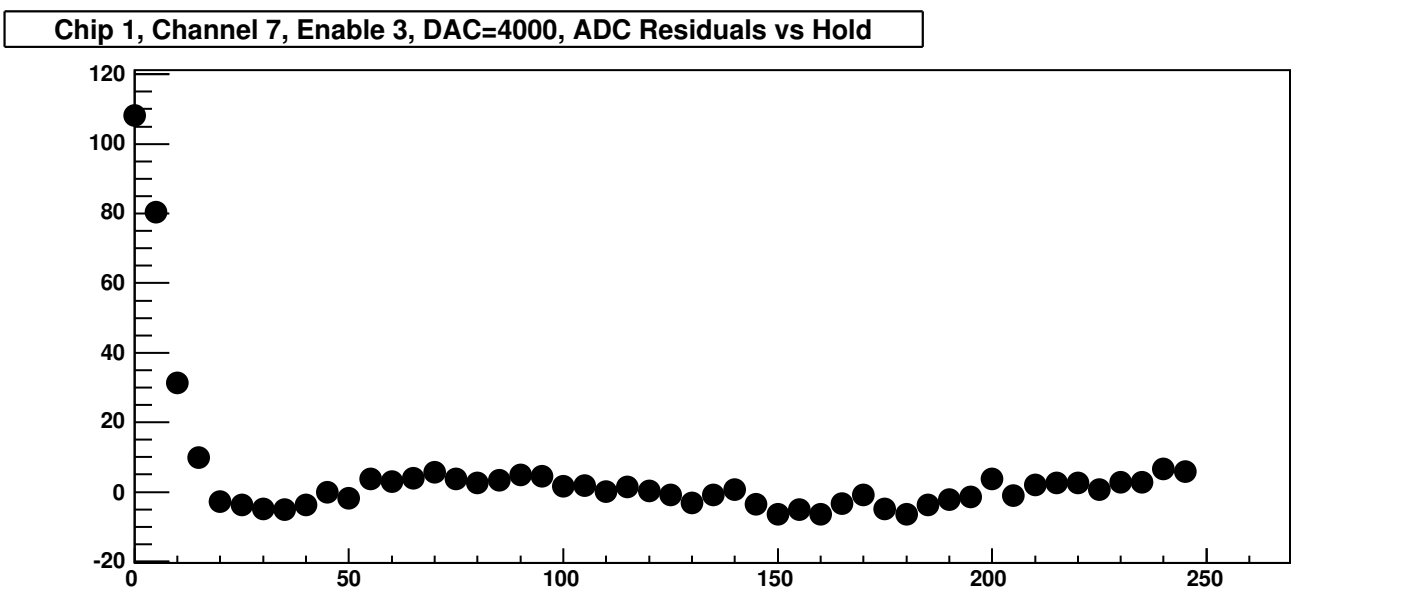


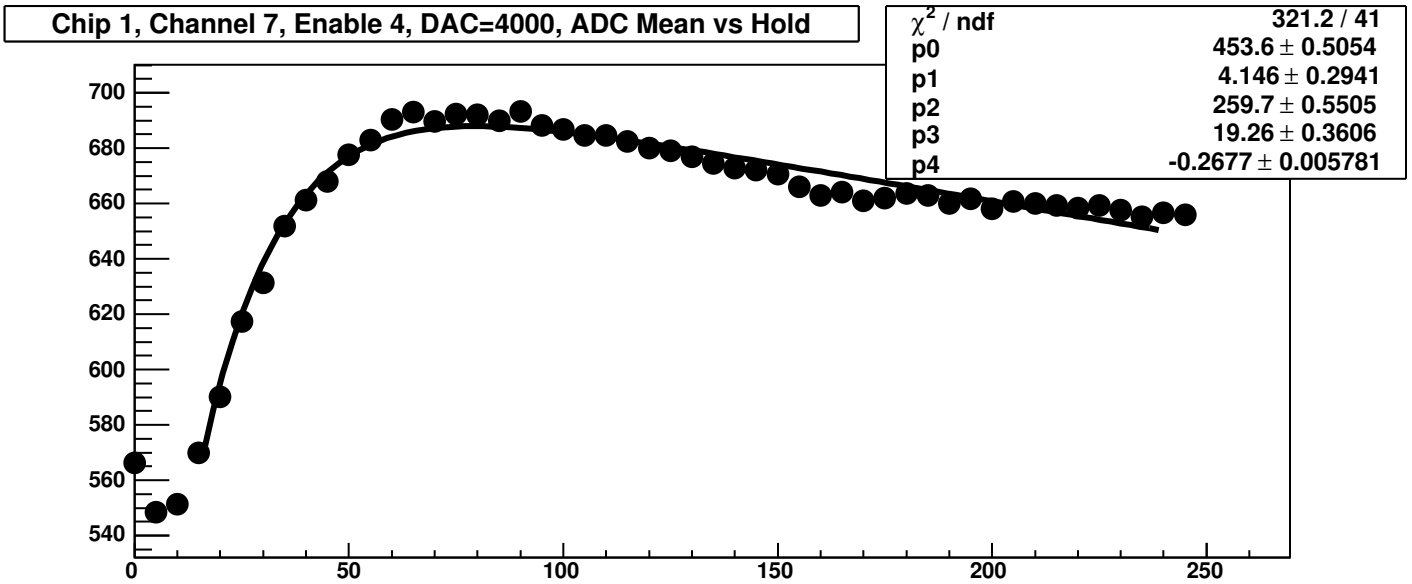
Chip 1, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold

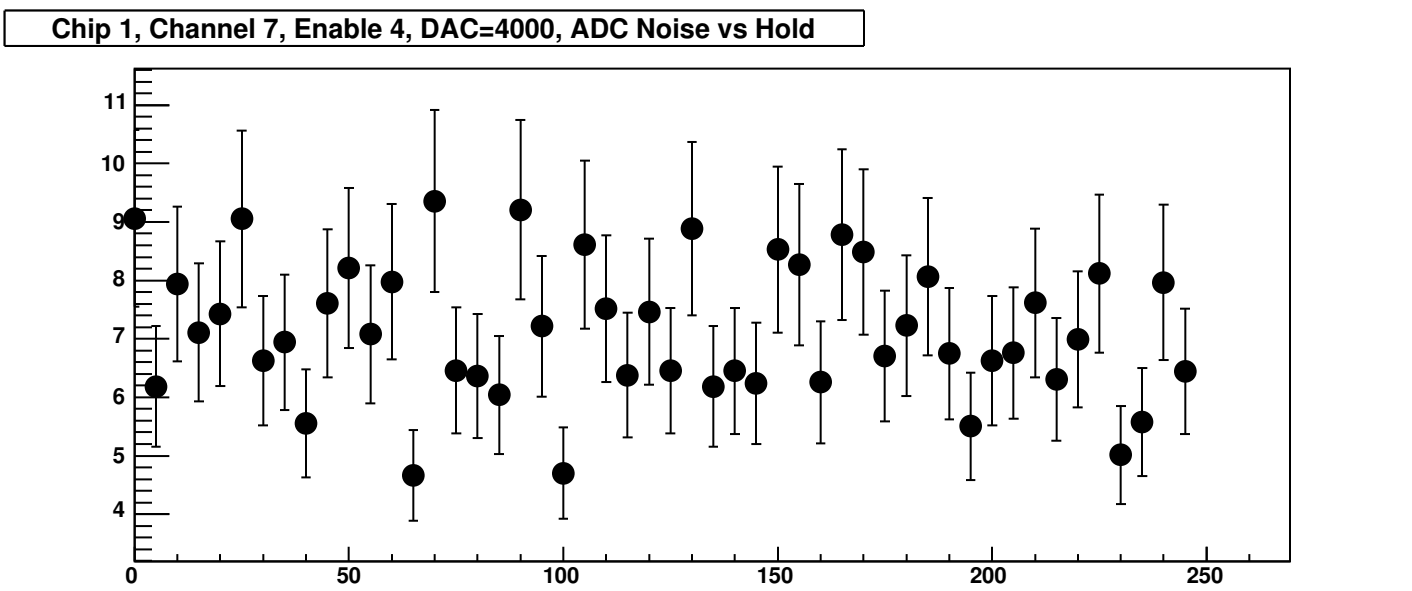


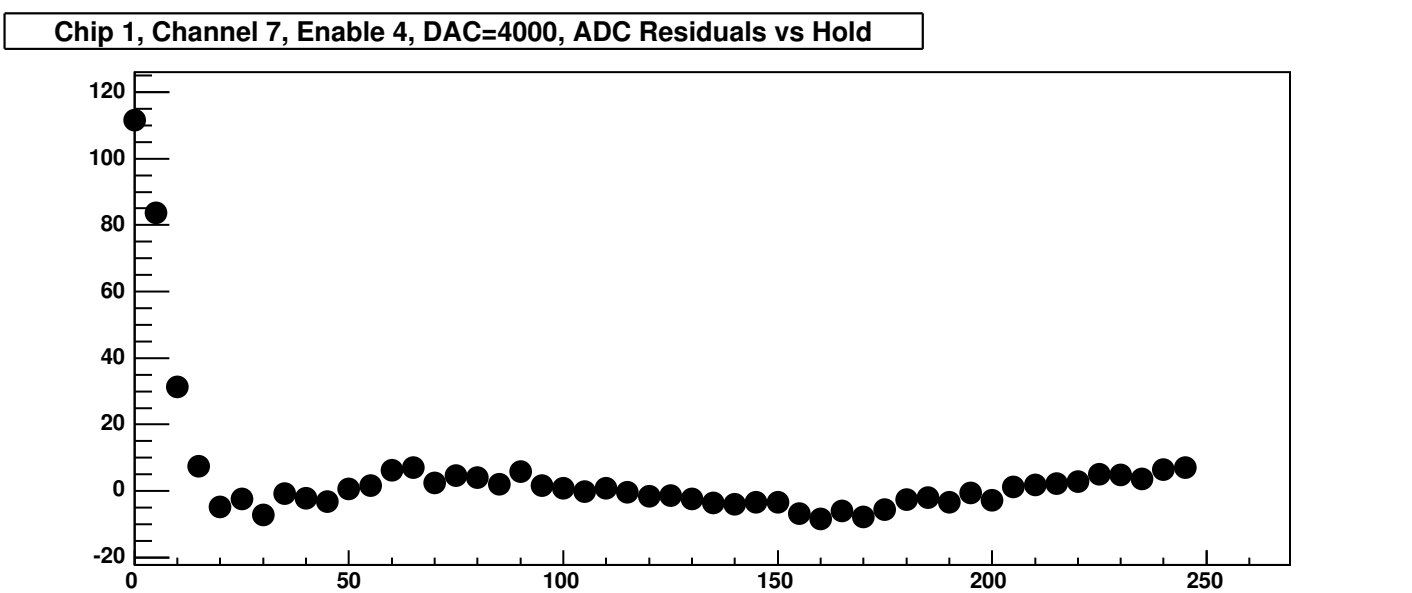

Chip 1, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 265.6 / 41
p0 453.3 ± 0.6566
p1 4.486 ± 0.2687
p2 261.2 ± 0.7443
p3 26.27 ± 0.4645
p4 -0.2546 ± 0.007544
700
680
660
640
620
600
580
560
540
520
0
50
100
150
200
250


Chip 1, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

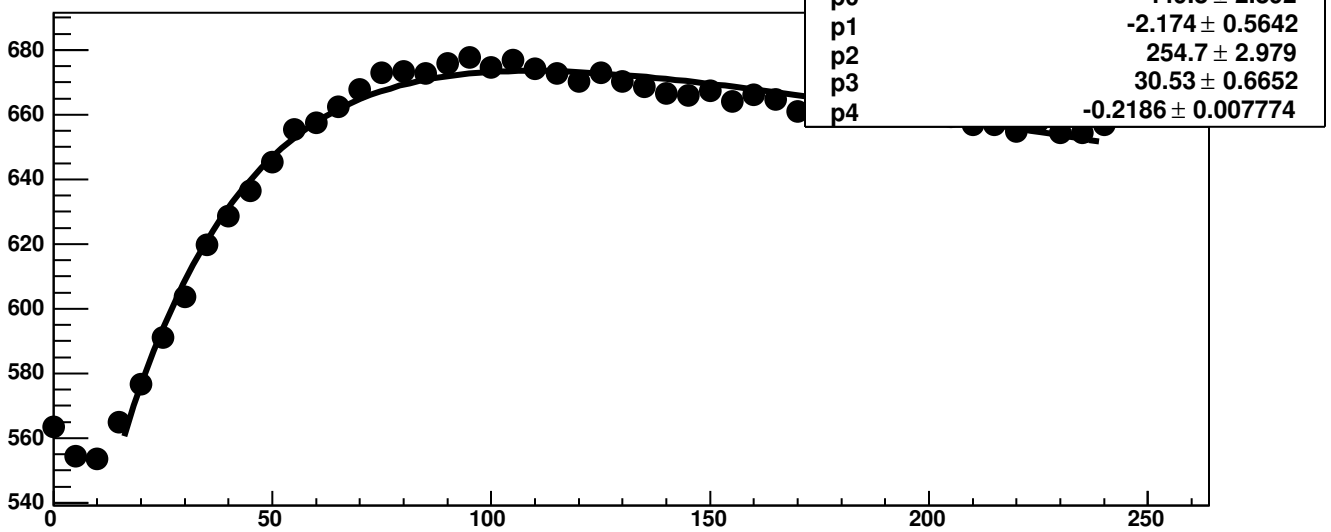

Chip 1, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold
120
100
80
60
40
20
0
-20
0
50
100
150
200
250


Chip 1, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 321.2 / 41
p0 453.6 ± 0.5054
p1 4.146 ± 0.2941
p2 259.7 ± 0.5505
p3 19.26 ± 0.3606
p4 -0.2677 ± 0.005781
700
680
660
640
620
600
580
560
540
0
50
100
150
200
250

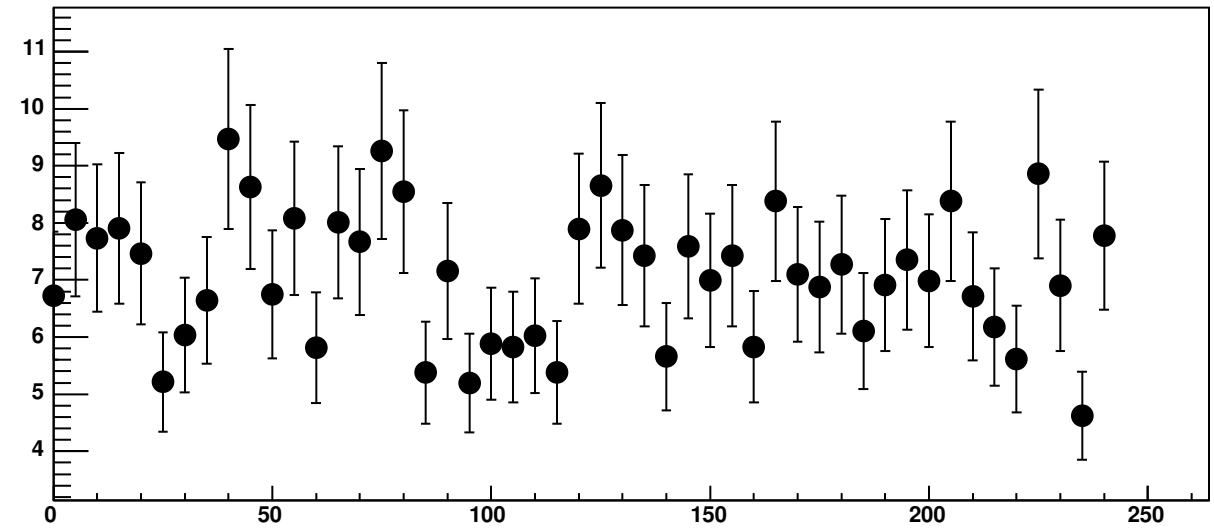

Chip 1, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 1, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold
120
100
80
60
40
20
0
-20
0
50
100
150
200
250

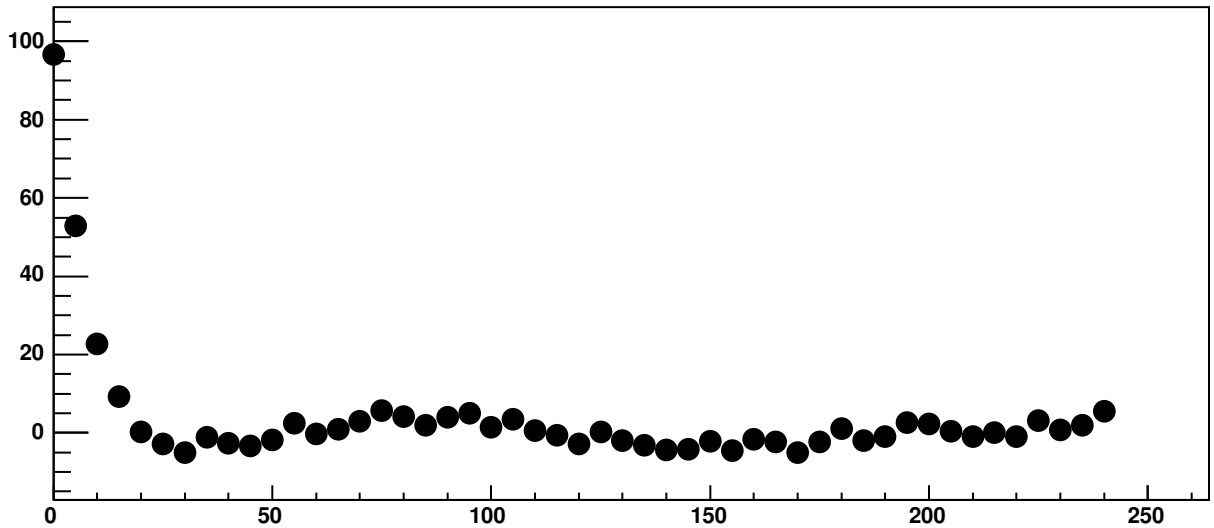
Chip 1, Channel 7, Enable 5, DAC=4000, ADC Mean vs Hold



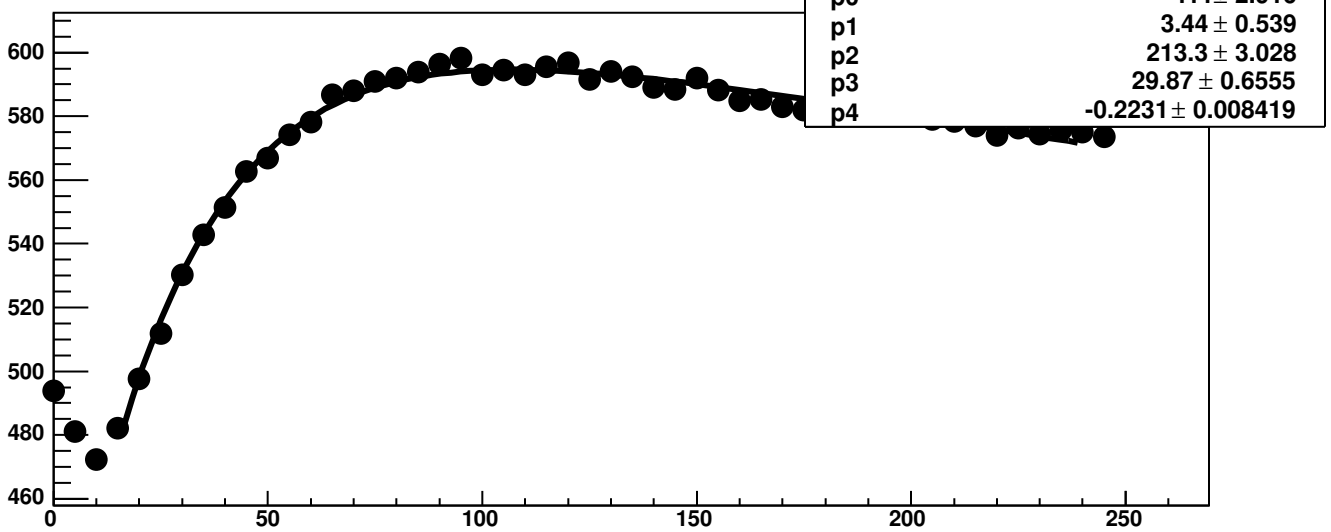
Chip 1, Channel 7, Enable 5, DAC=4000, ADC Noise vs Hold



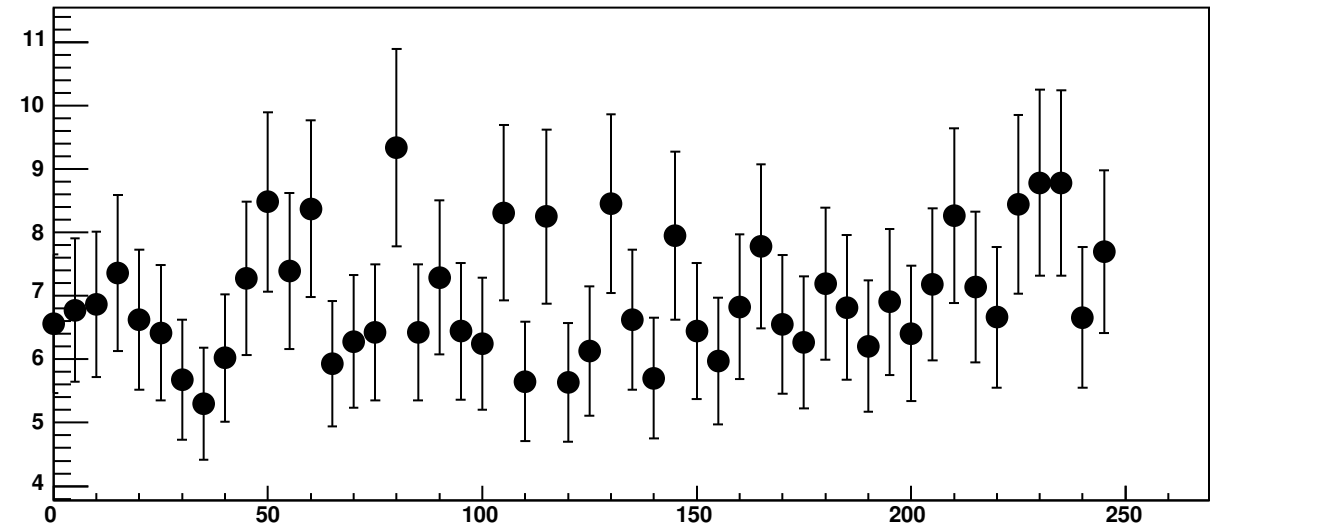
Chip 1, Channel 7, Enable 5, DAC=4000, ADC Residuals vs Hold



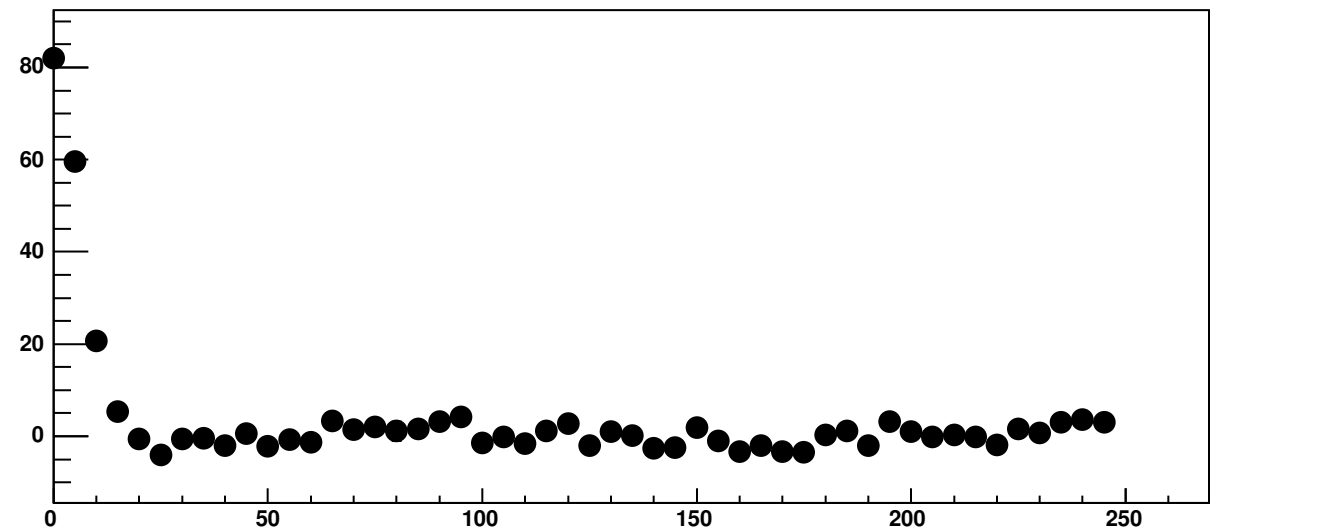
Chip 1, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold

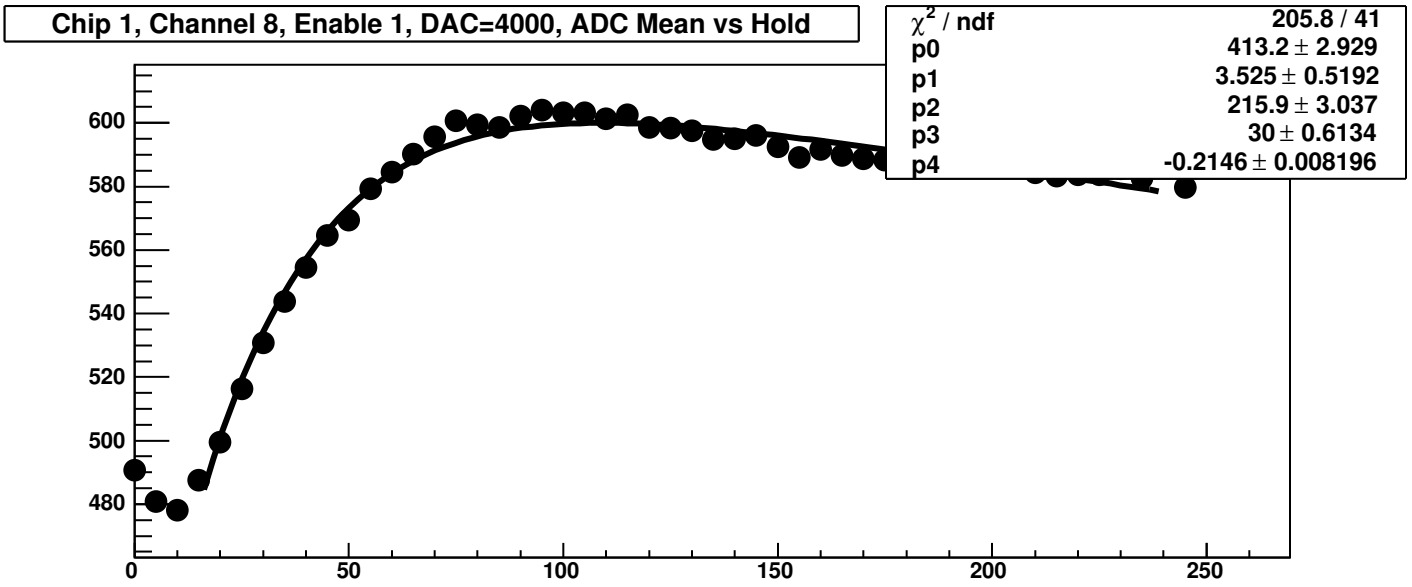


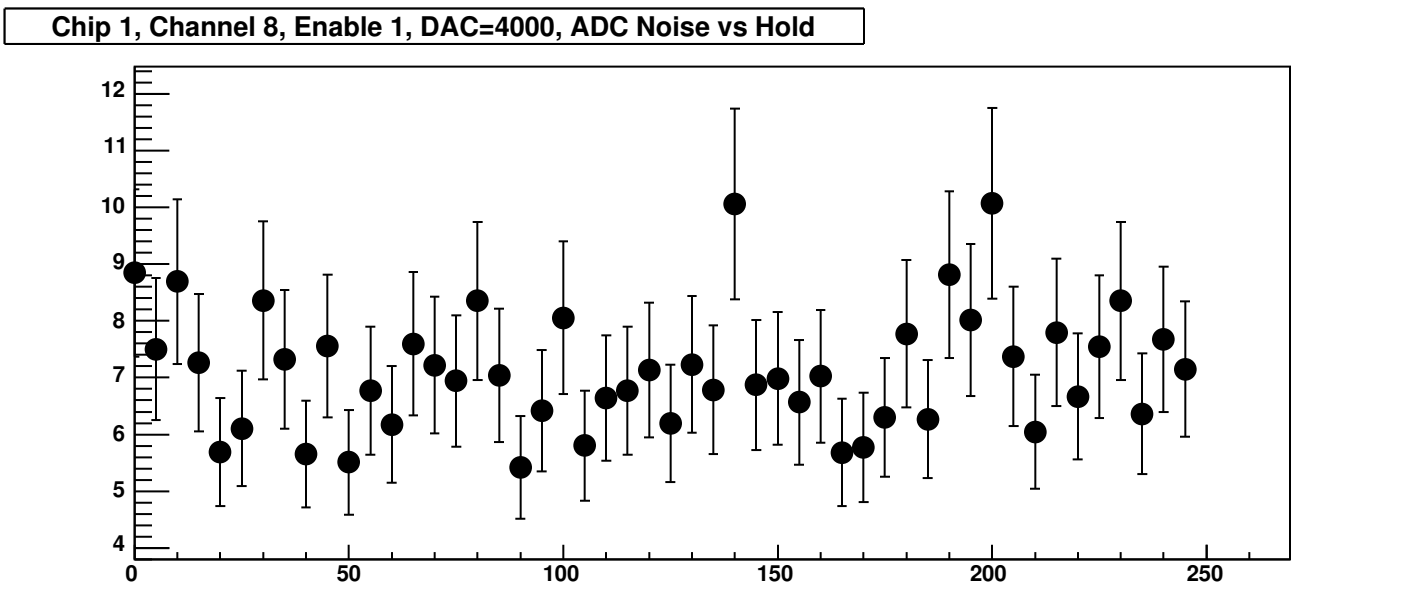
Chip 1, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold

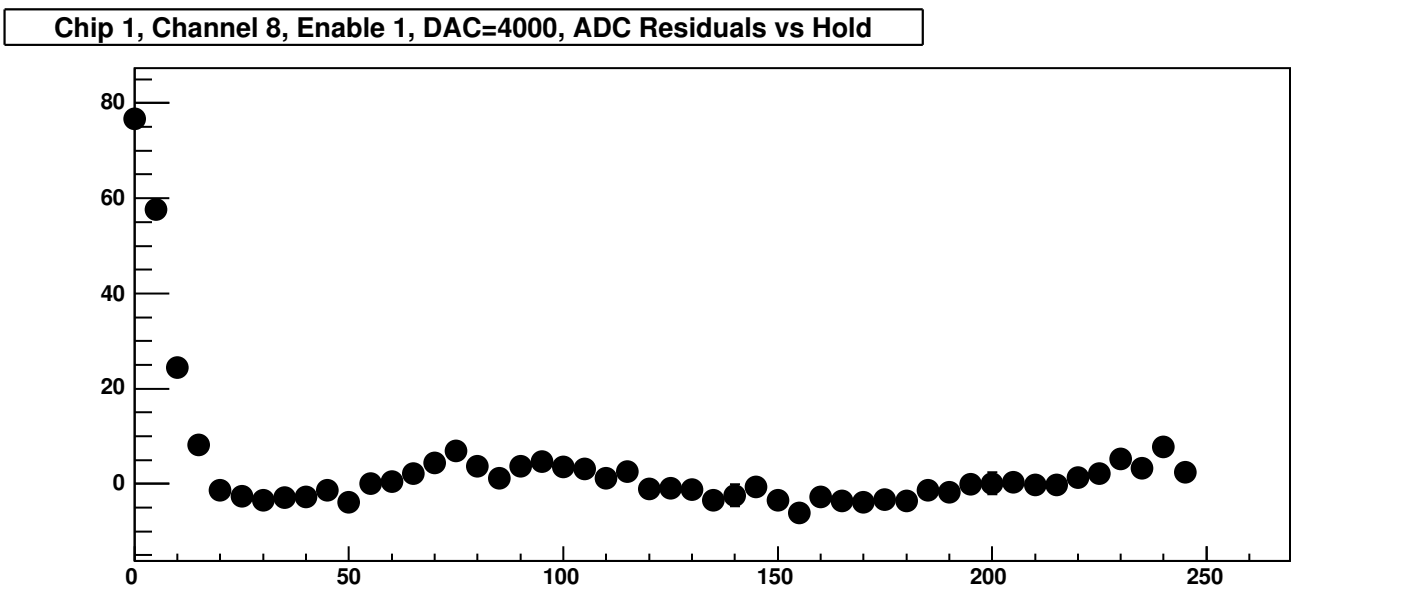


Chip 1, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold

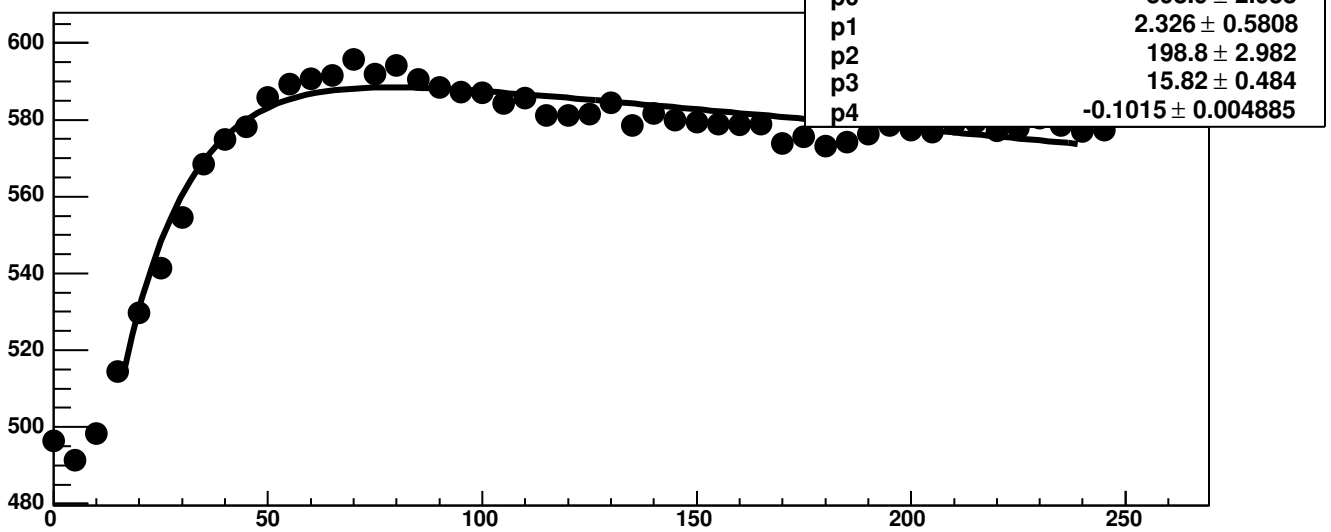



Chip 1, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 205.8 / 41
p0 413.2 ± 2.929
p1 3.525 ± 0.5192
p2 215.9 ± 3.037
p3 30 ± 0.6134
p4 -0.2146 ± 0.008196
600
580
560
540
520
500
480
0
50
100
150
200
250

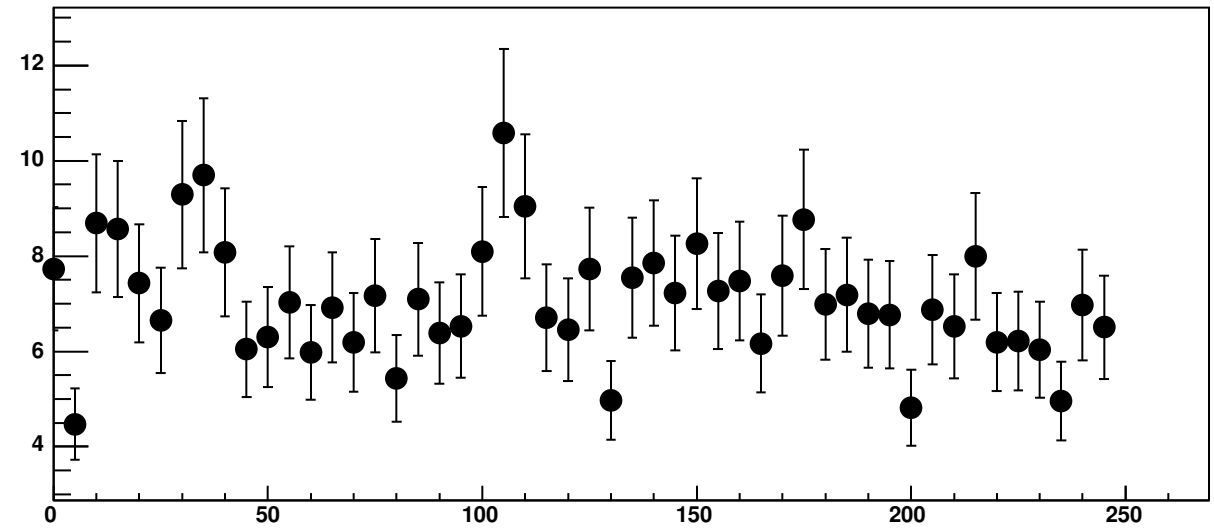

Chip 1, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 1, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold
80
60
40
20
0
0
50
100
150
200
250

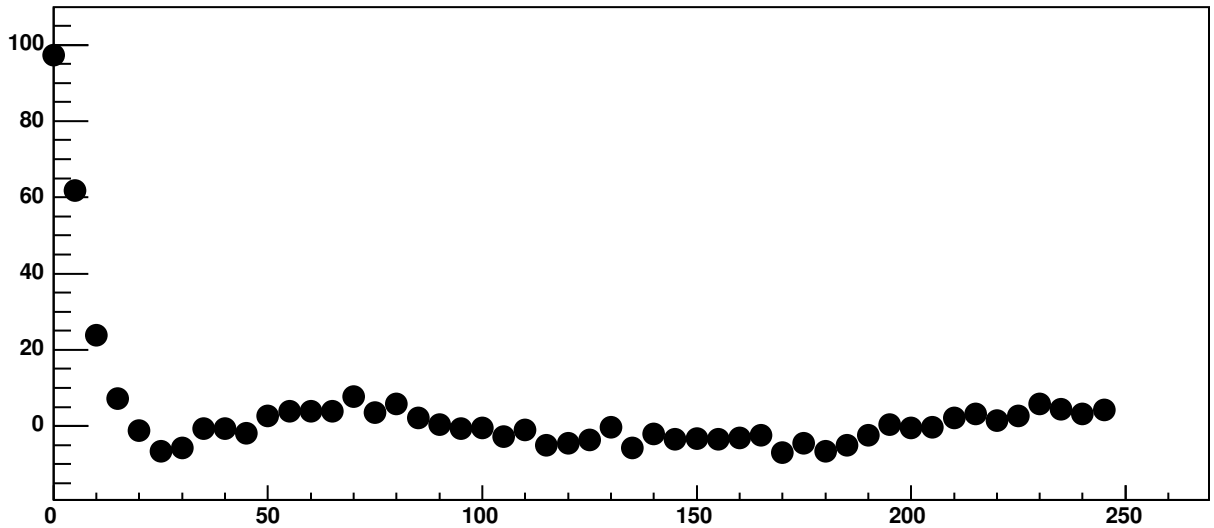
Chip 1, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold

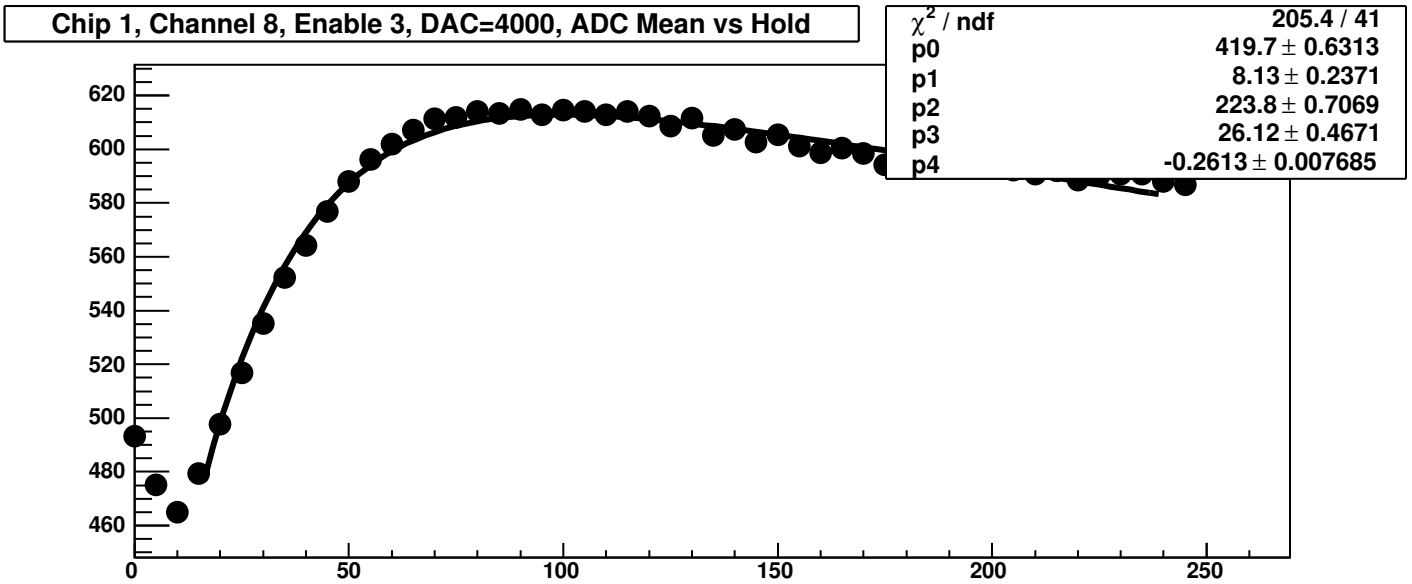


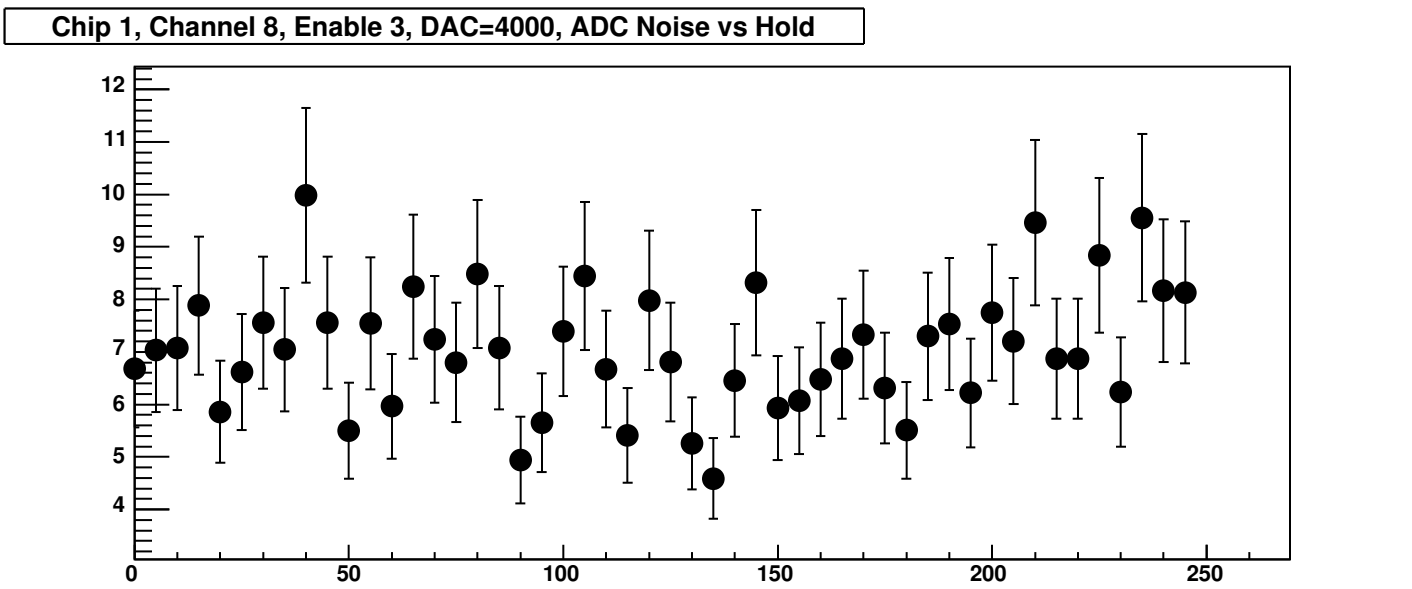
Chip 1, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold

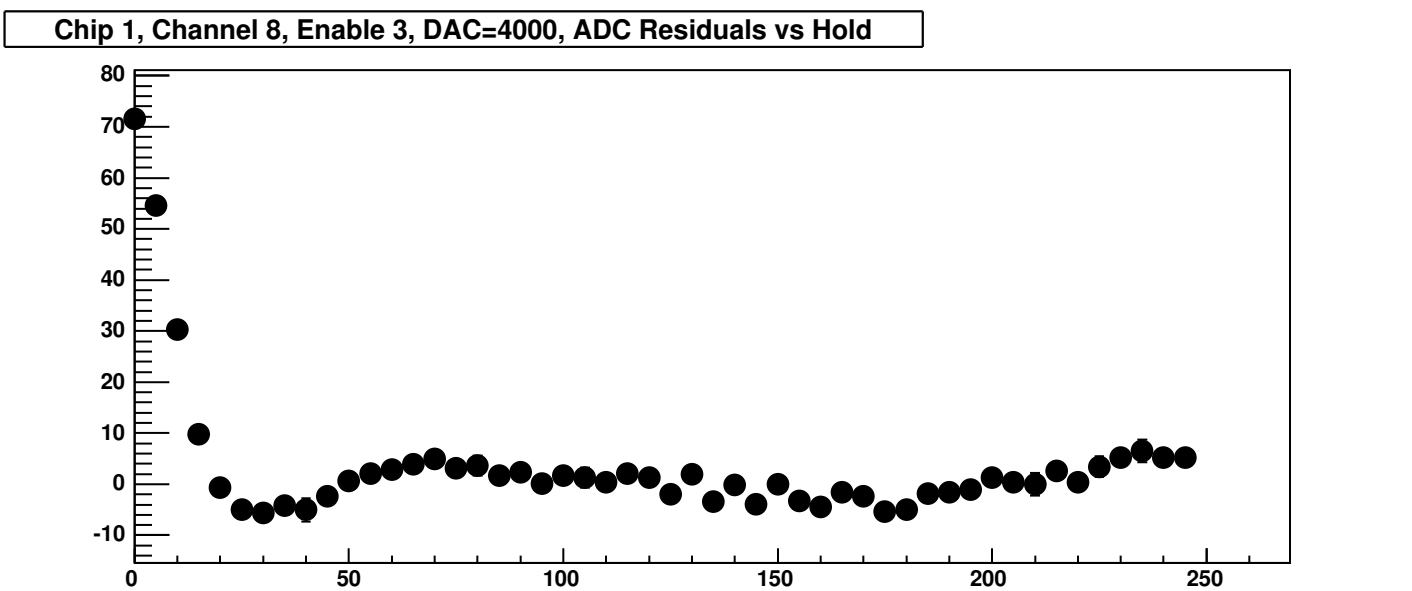


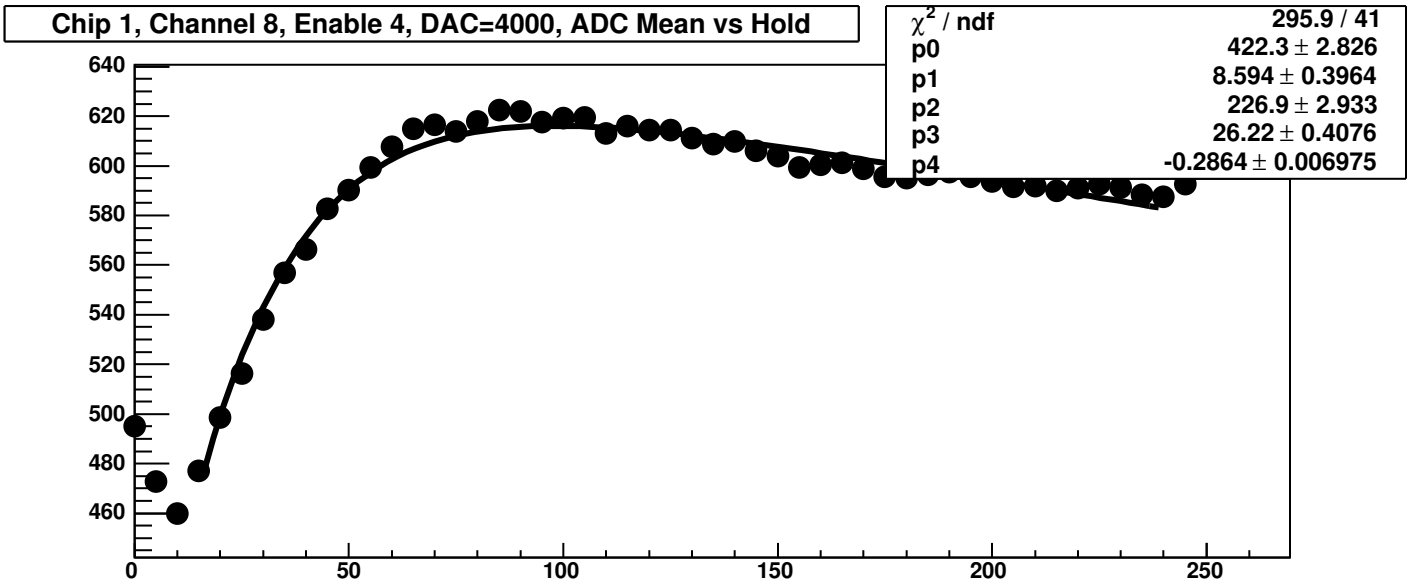
Chip 1, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold

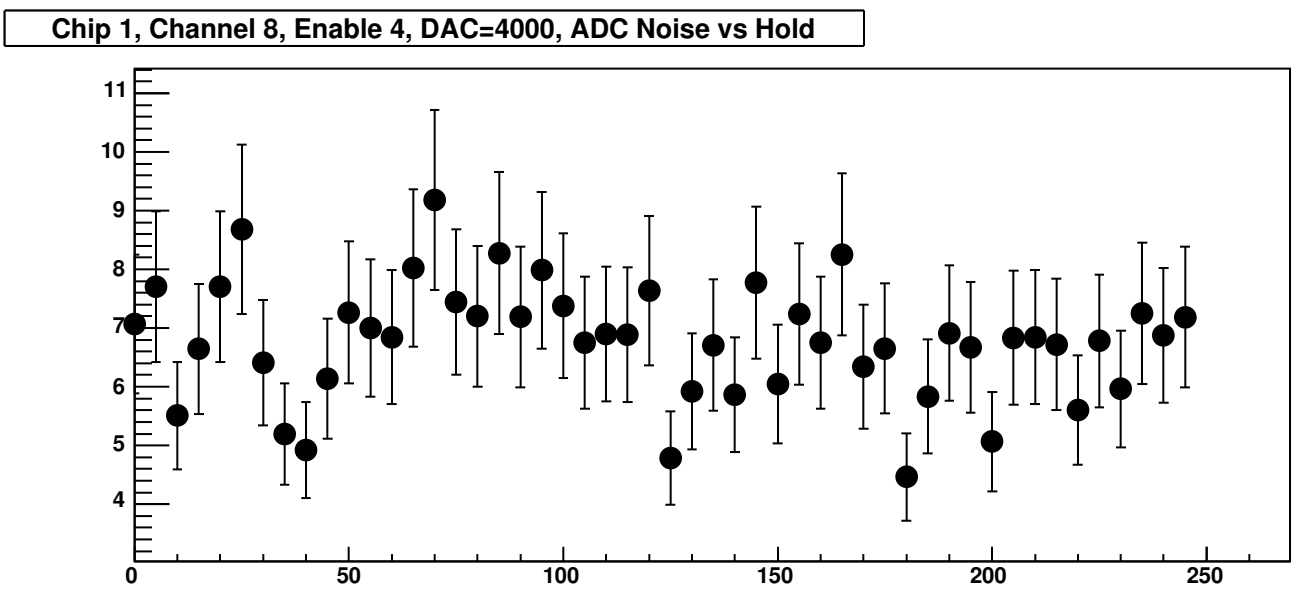


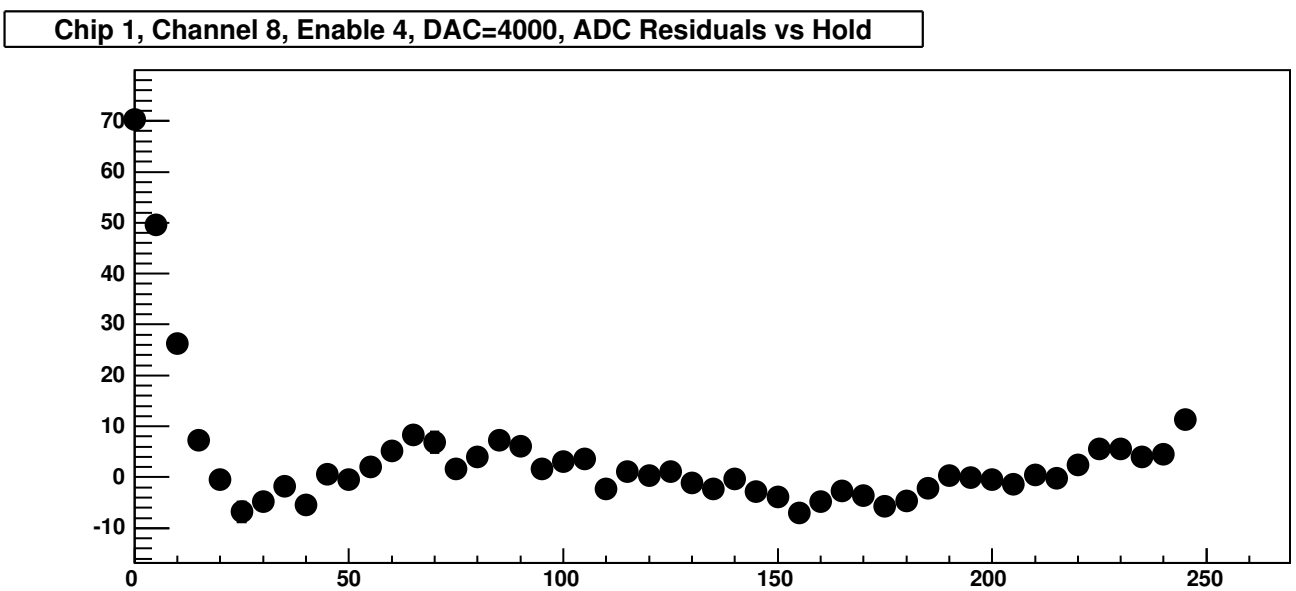

Chip 1, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 205.4 / 41
p0 419.7 ± 0.6313
p1 8.13 ± 0.2371
p2 223.8 ± 0.7069
p3 26.12 ± 0.4671
p4 -0.2613 ± 0.007685
620
600
580
560
540
520
500
480
460
0
50
100
150
200
250

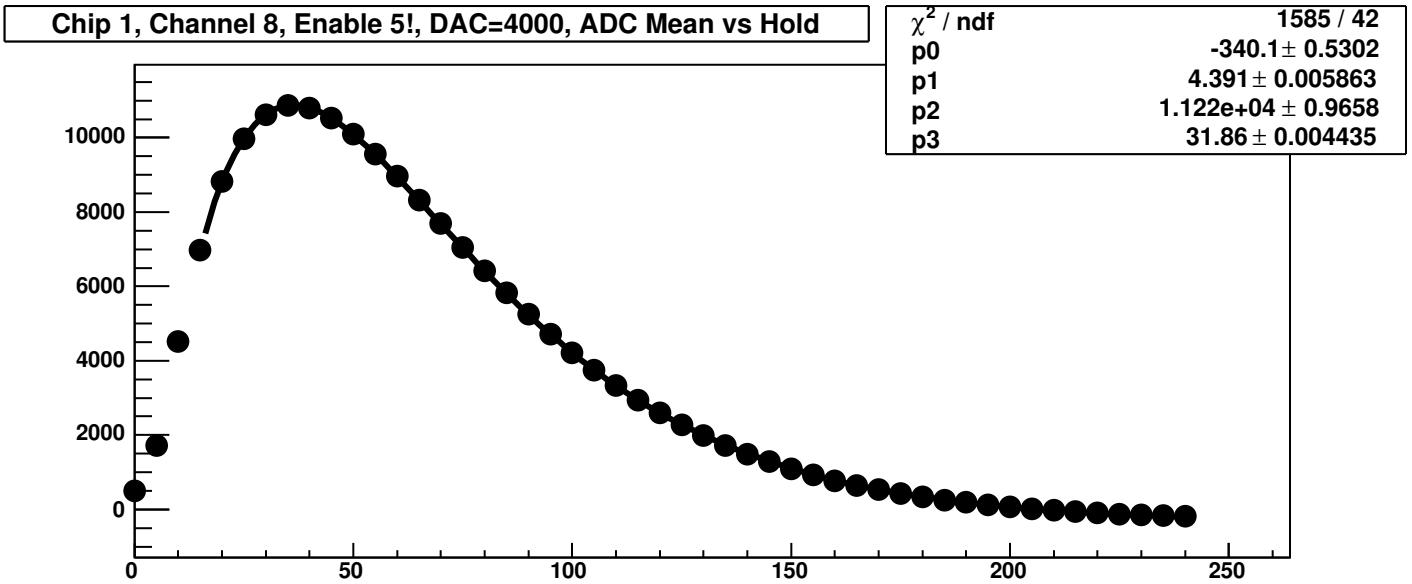

Chip 1, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

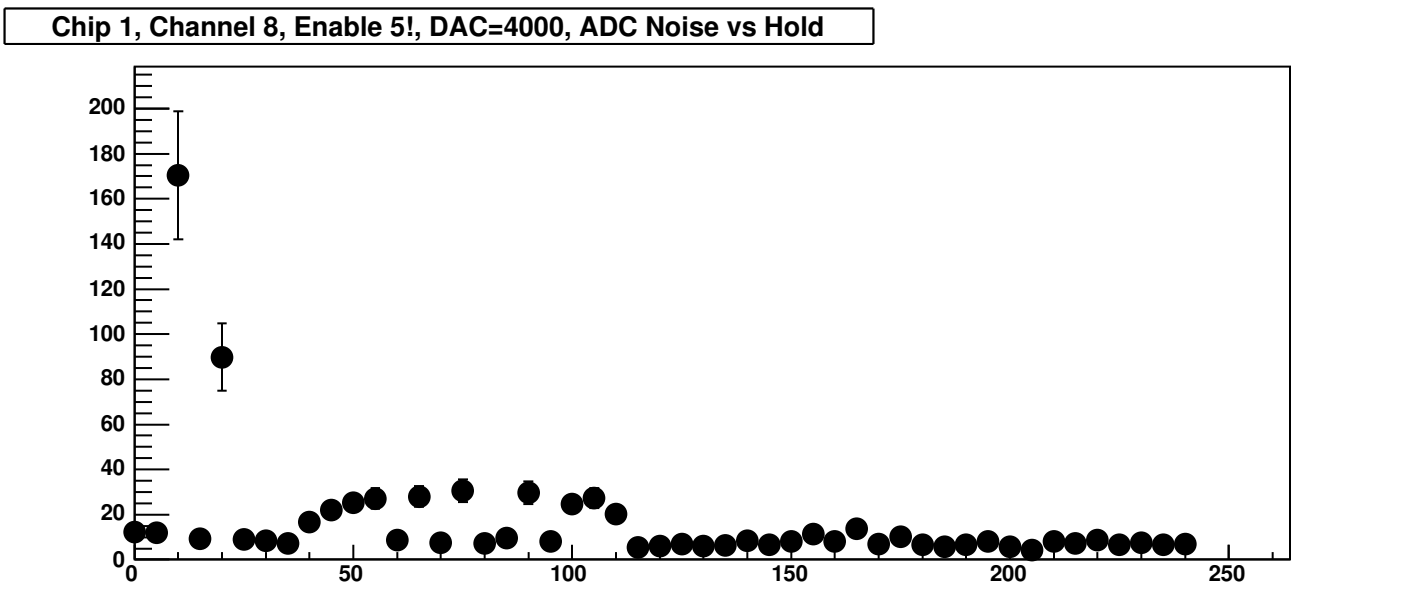

Chip 1, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold
80
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

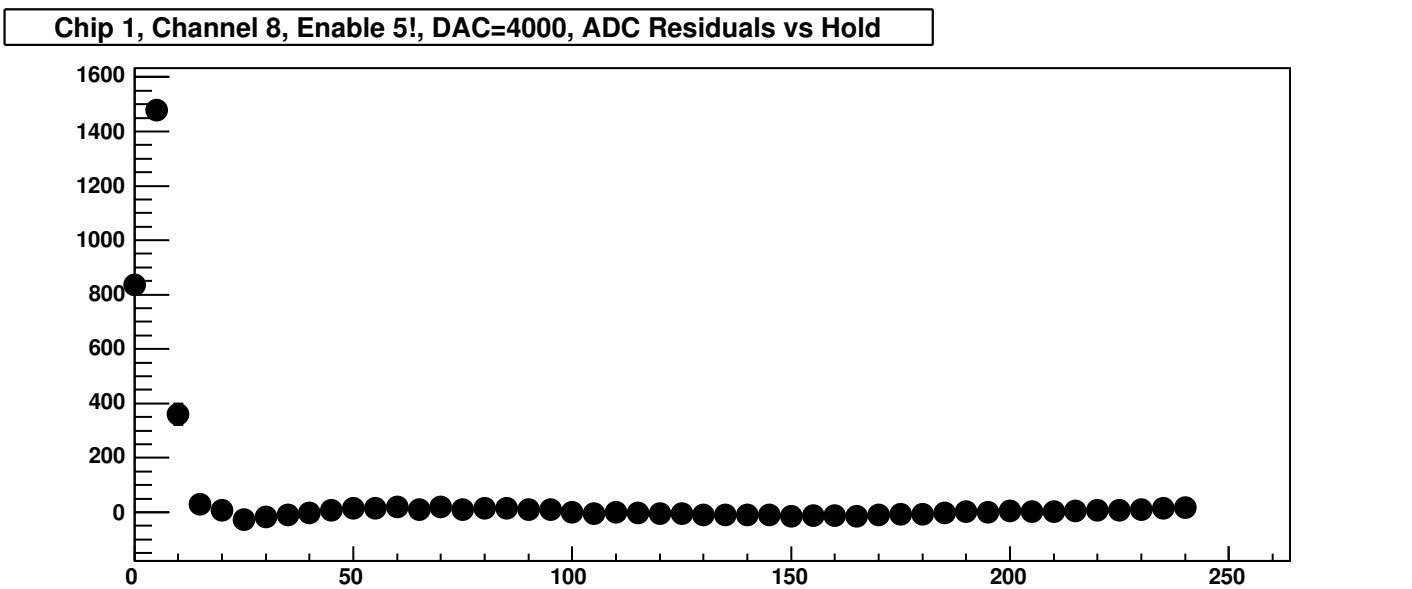

Chip 1, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 295.9 / 41
p0 422.3 ± 2.826
p1 8.594 ± 0.3964
p2 226.9 ± 2.933
p3 26.22 ± 0.4076
p4 -0.2864 ± 0.006975
640
620
600
580
560
540
520
500
480
460
0
50
100
150
200
250

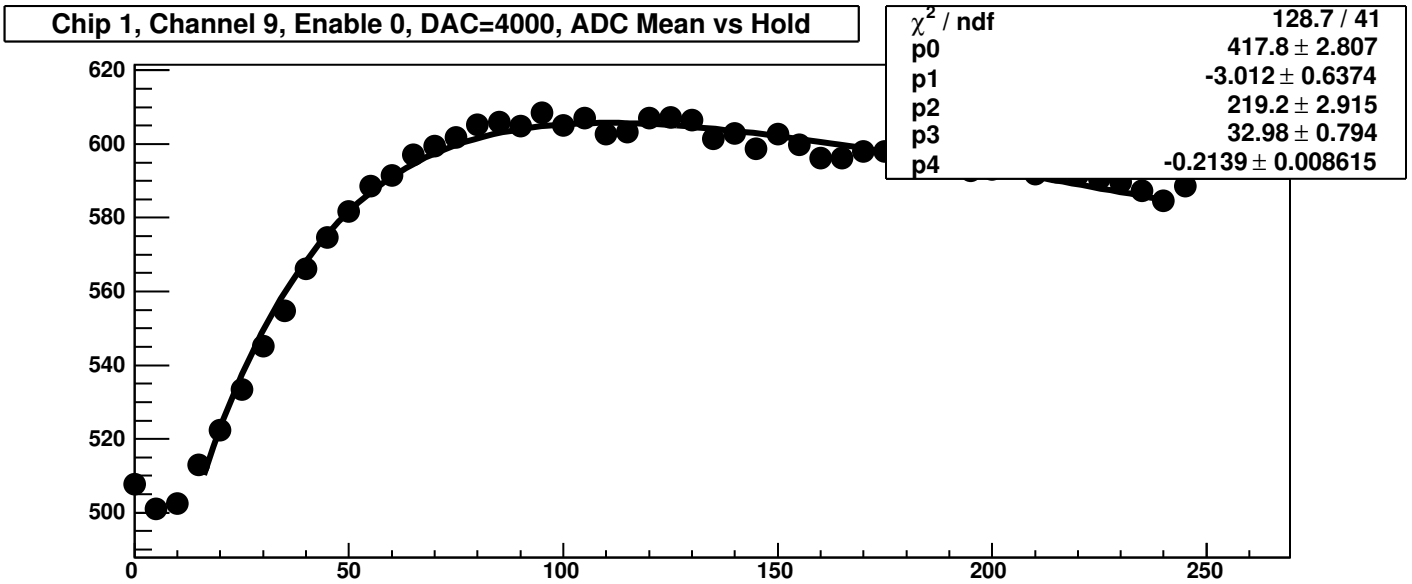

Chip 1, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250

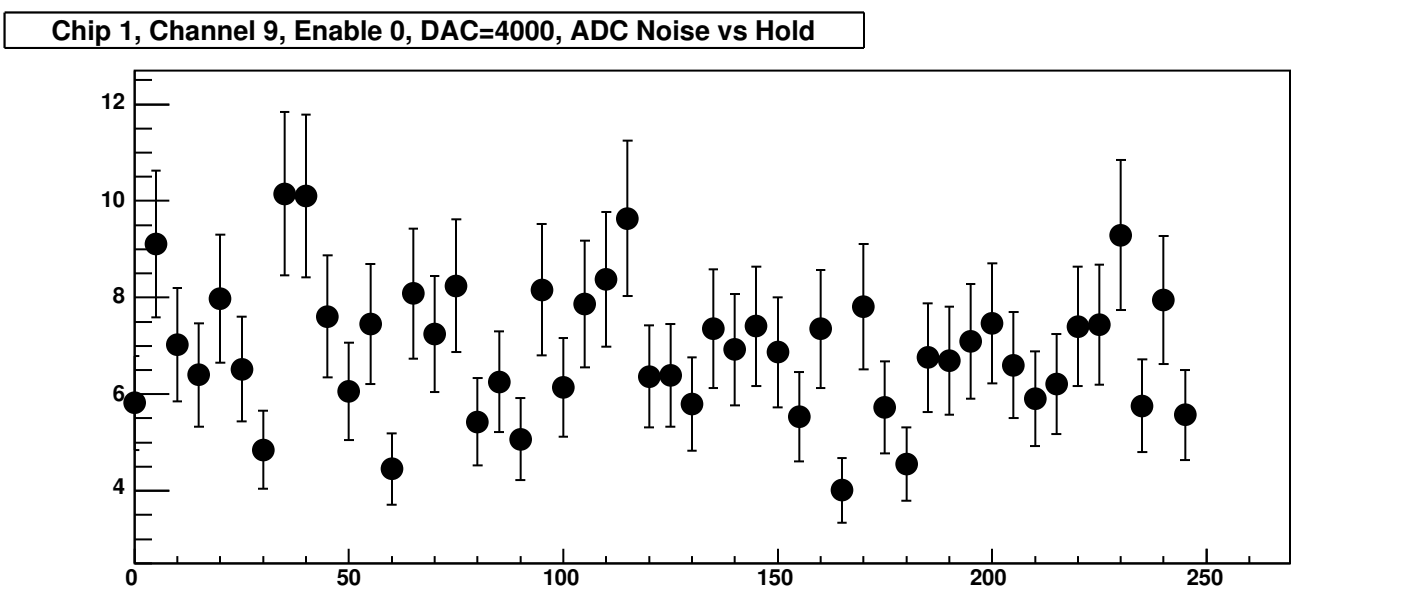

Chip 1, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

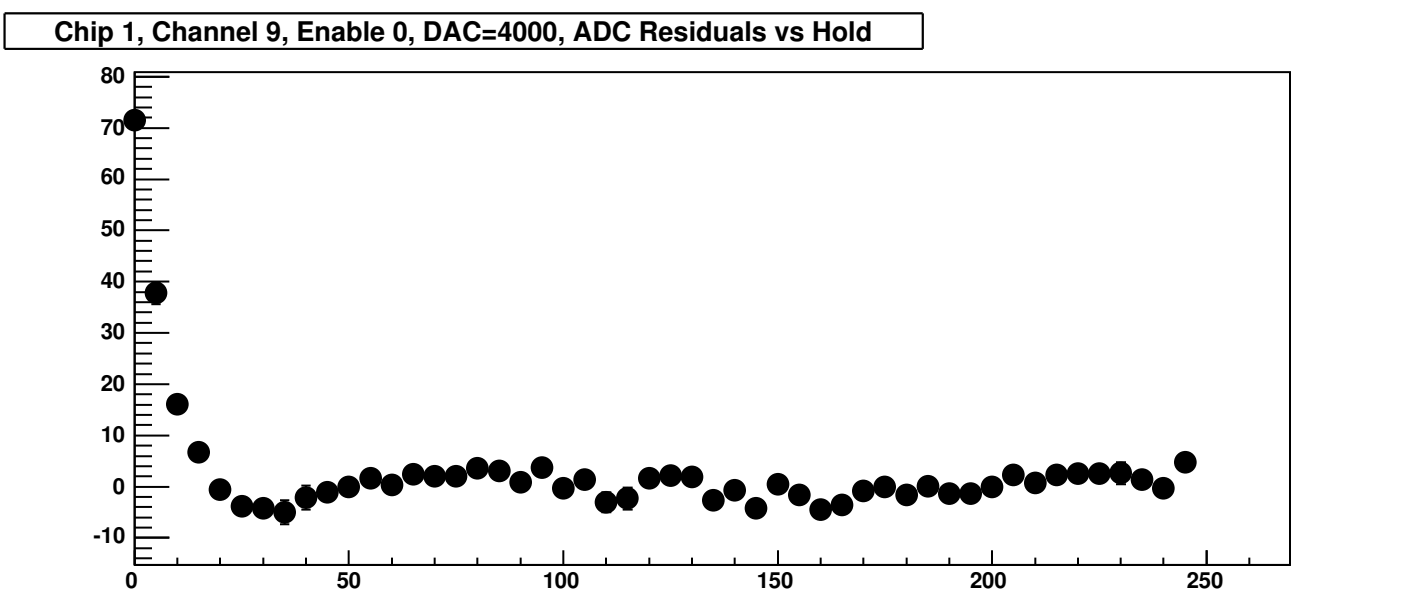

Chip 1, Channel 8, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1585 / 42
p0 -340.1 ± 0.5302
p1 4.391 ± 0.005863
p2 1.122e+04 ± 0.9658
p3 31.86 ± 0.004435
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 1, Channel 8, Enable 5!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

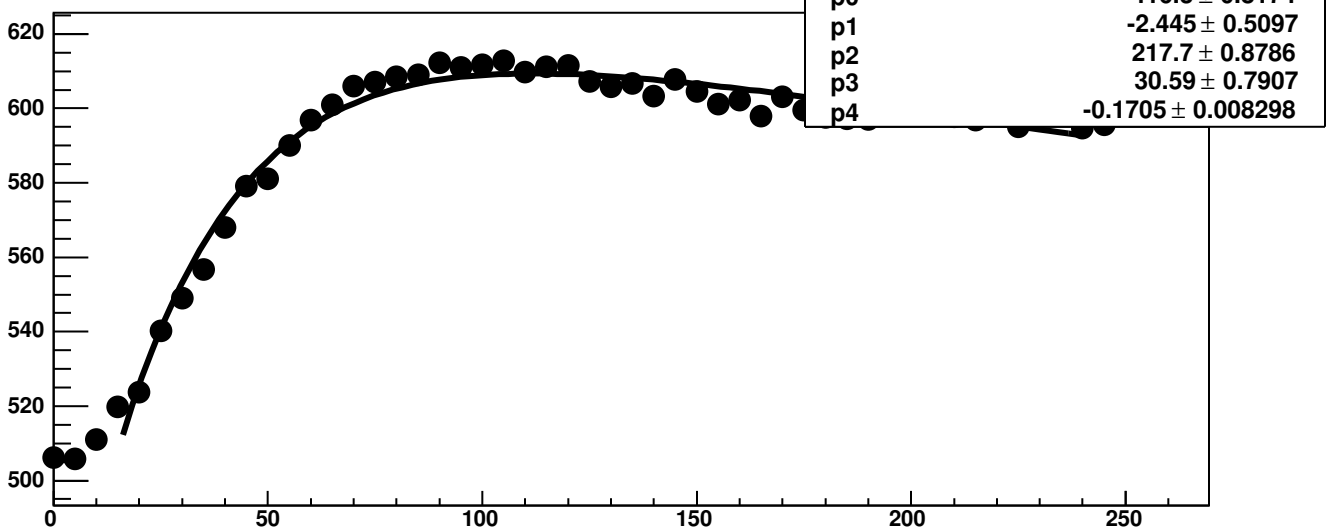

Chip 1, Channel 8, Enable 5!, DAC=4000, ADC Residuals vs Hold
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 1, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 128.7 / 41
p0 417.8 ± 2.807
p1 -3.012 ± 0.6374
p2 219.2 ± 2.915
p3 32.98 ± 0.794
p4 -0.2139 ± 0.008615
620
600
580
560
540
520
500
0
50
100
150
200
250

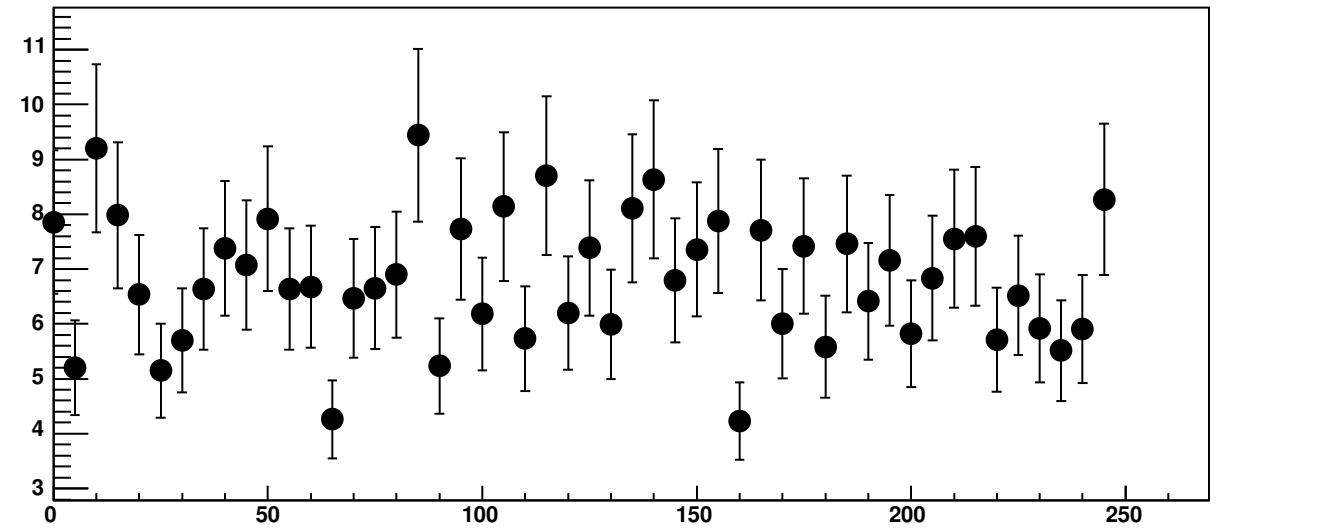

Chip 1, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 1, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold
80
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

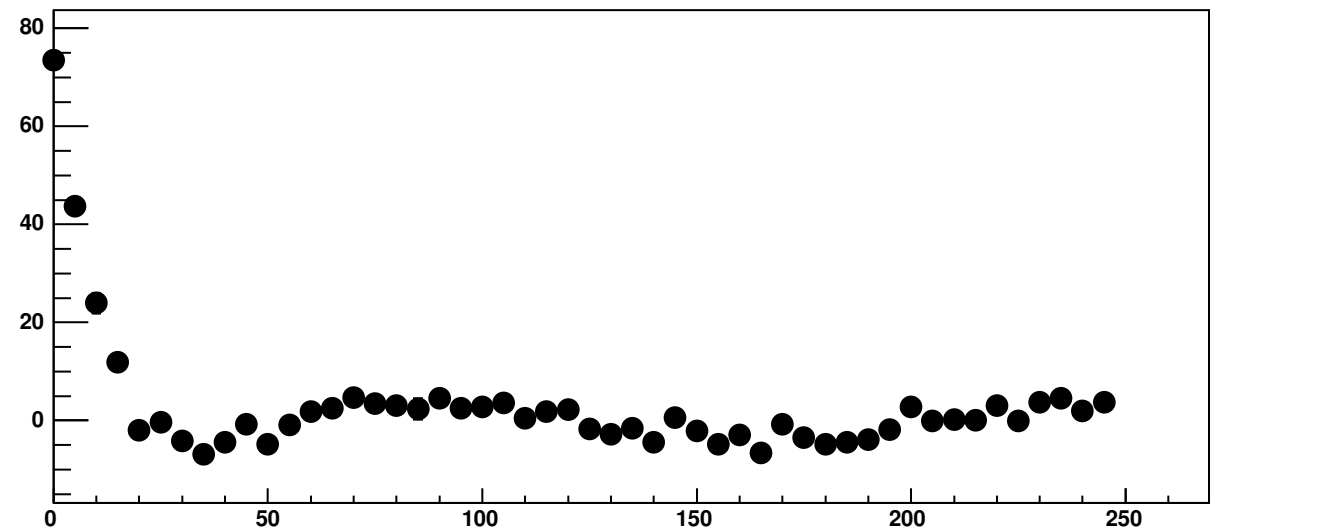
Chip 1, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold



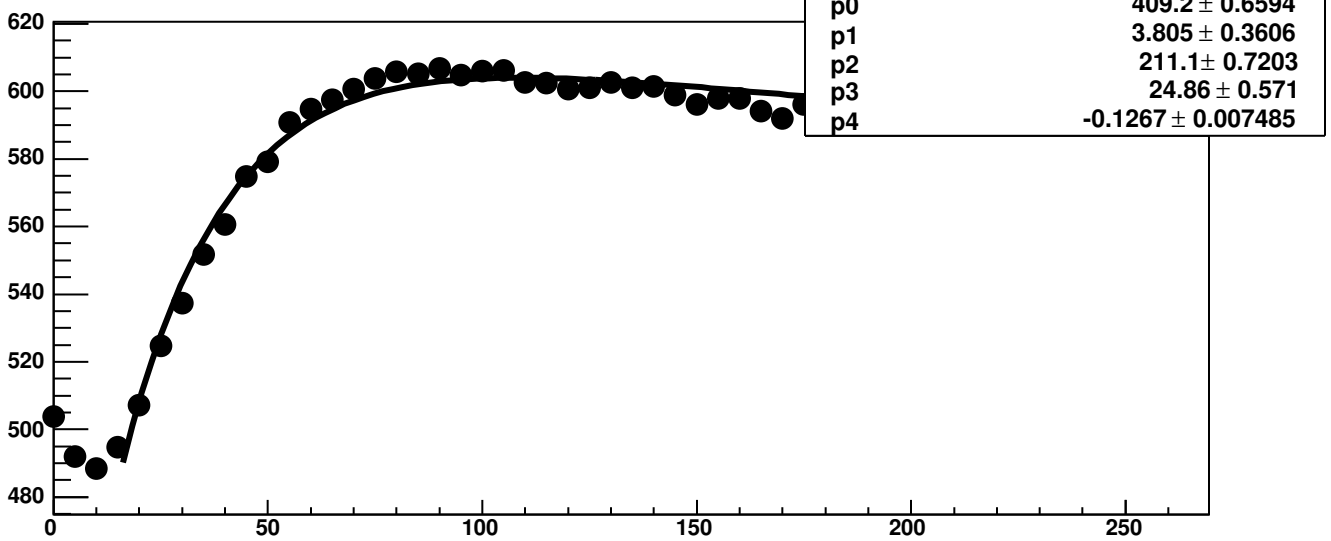
Chip 1, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



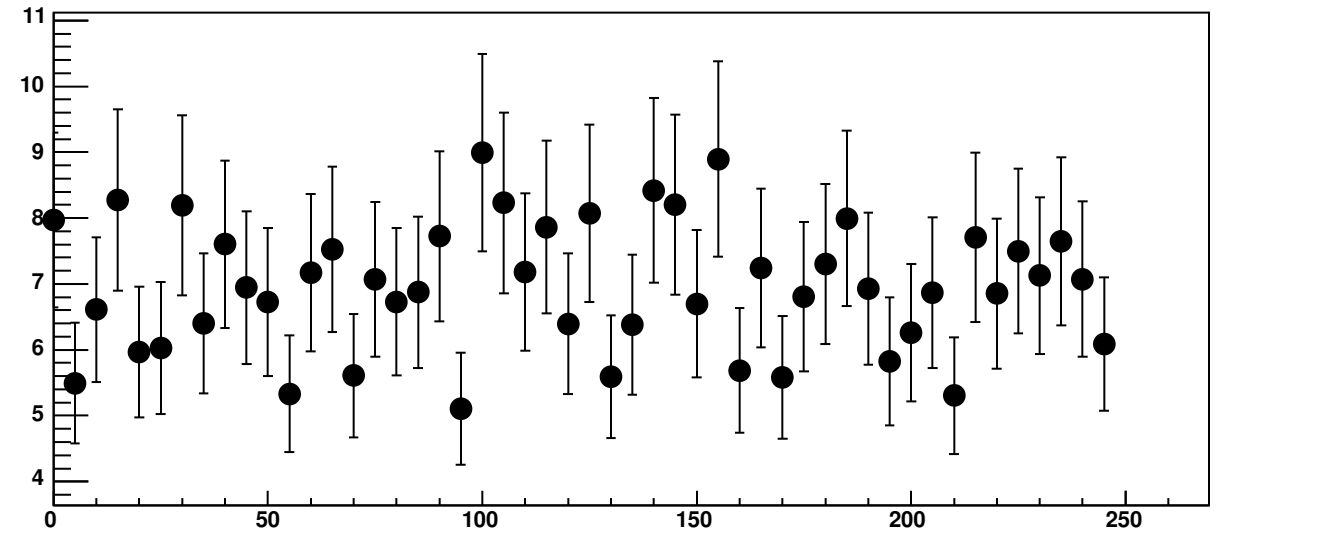
Chip 1, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold



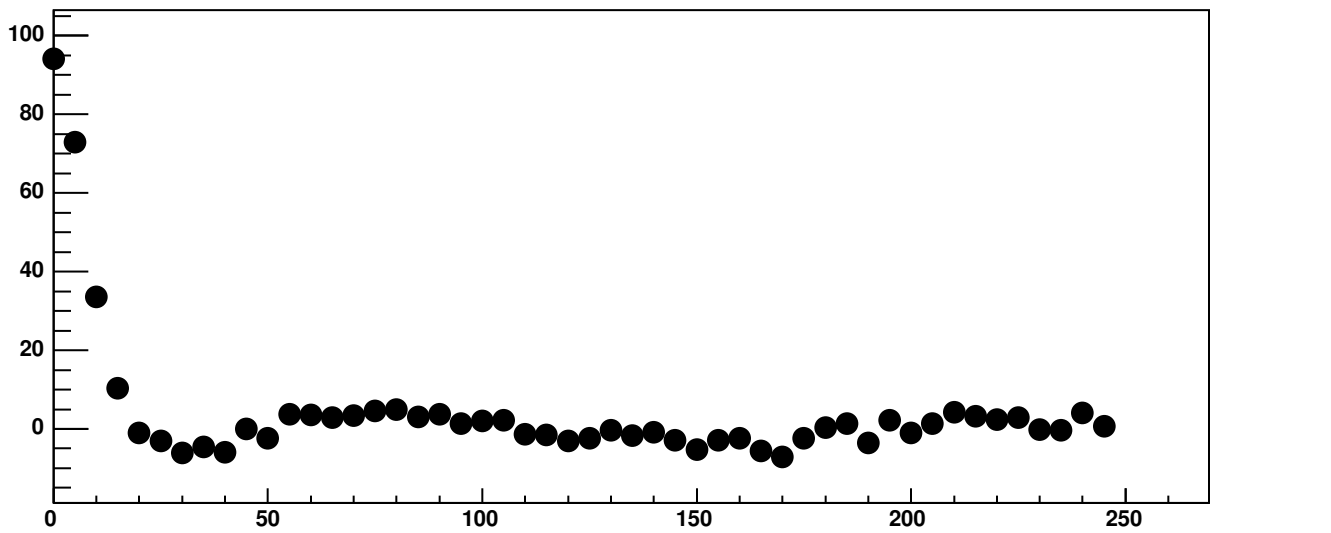
Chip 1, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold



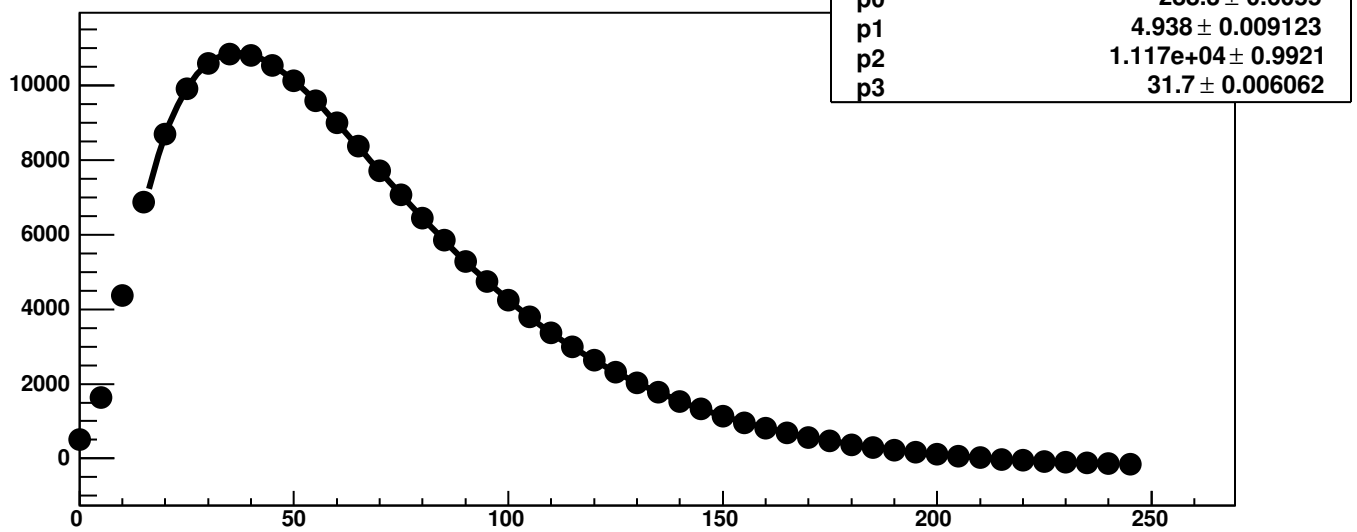
Chip 1, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



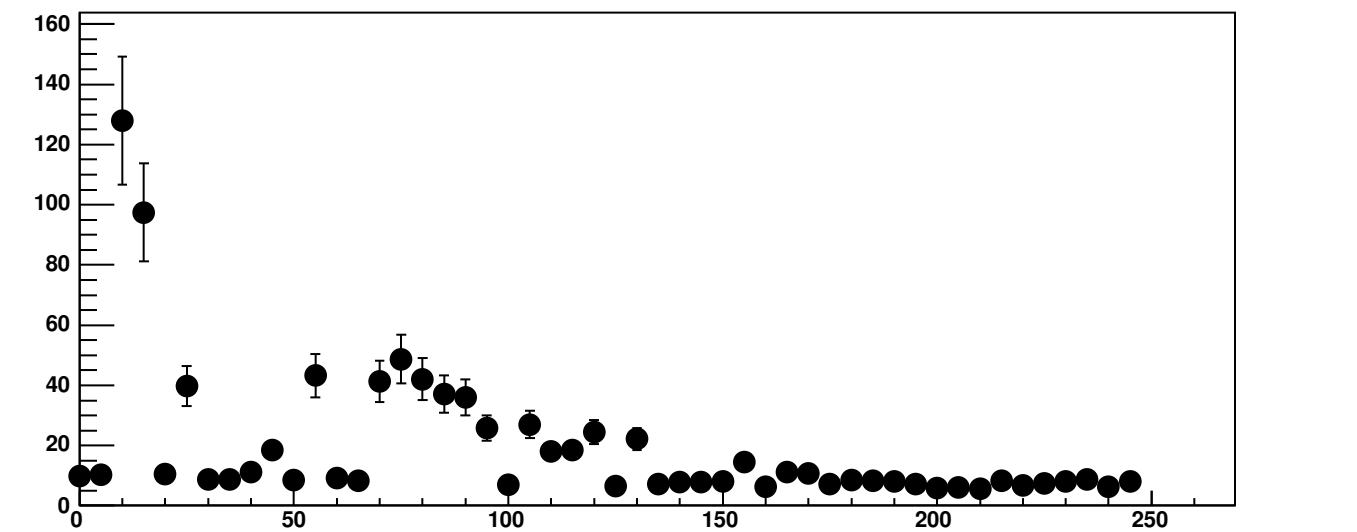
Chip 1, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold



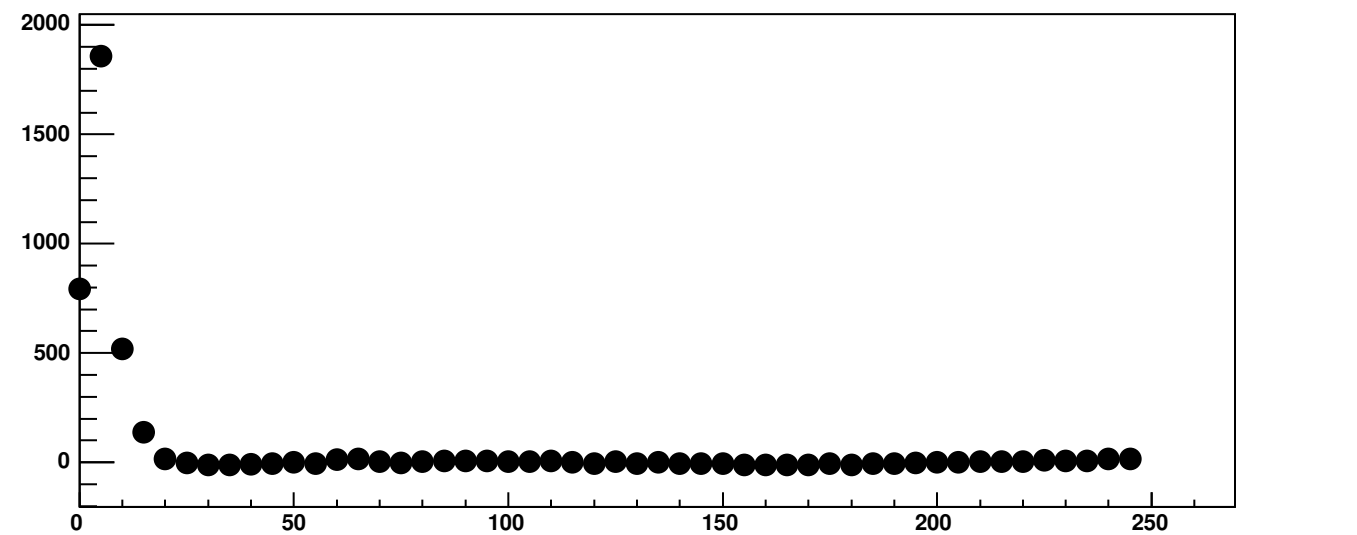
Chip 1, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold



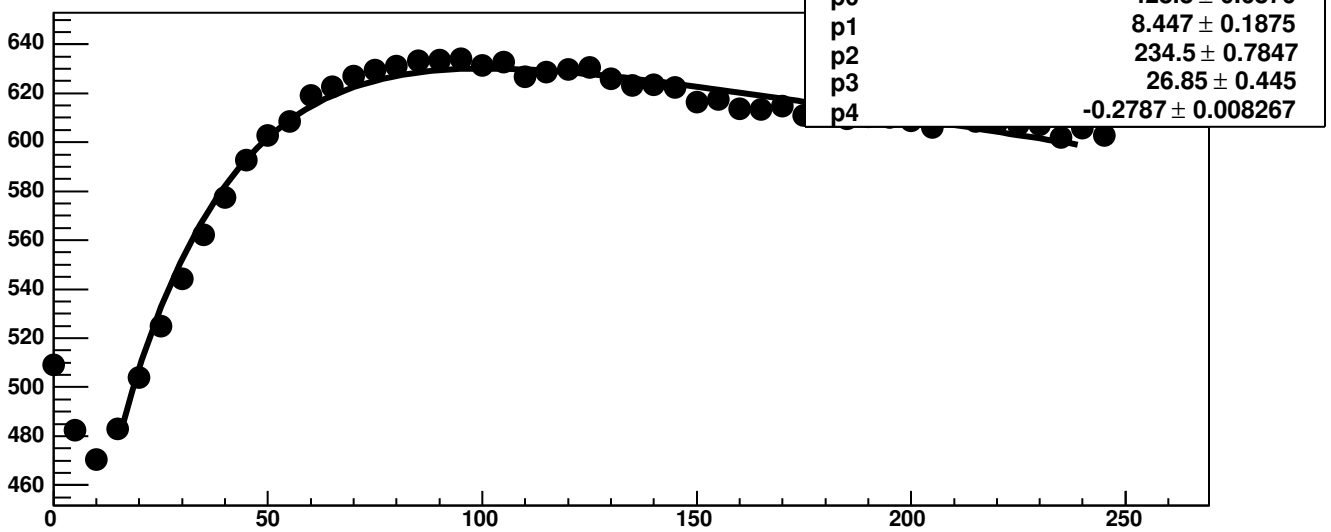
Chip 1, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold



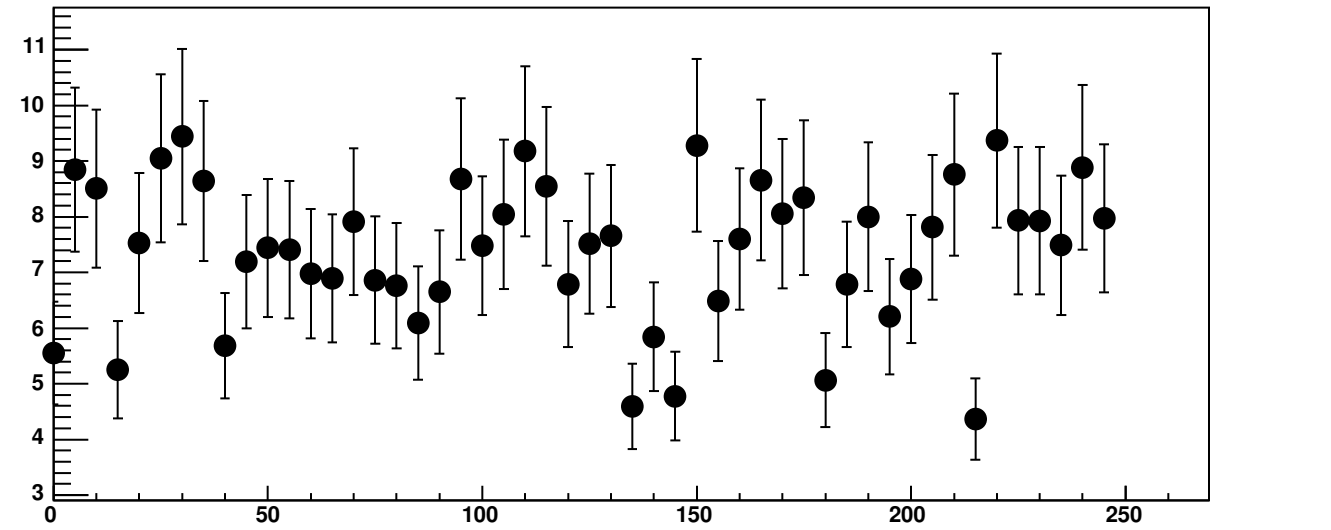
Chip 1, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold



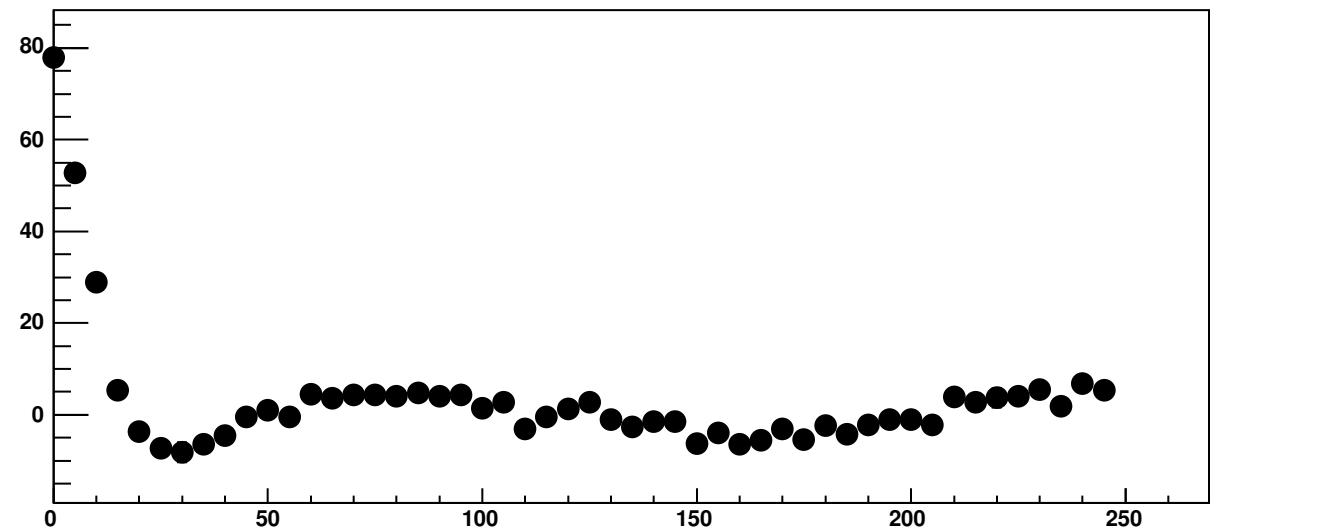
Chip 1, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold

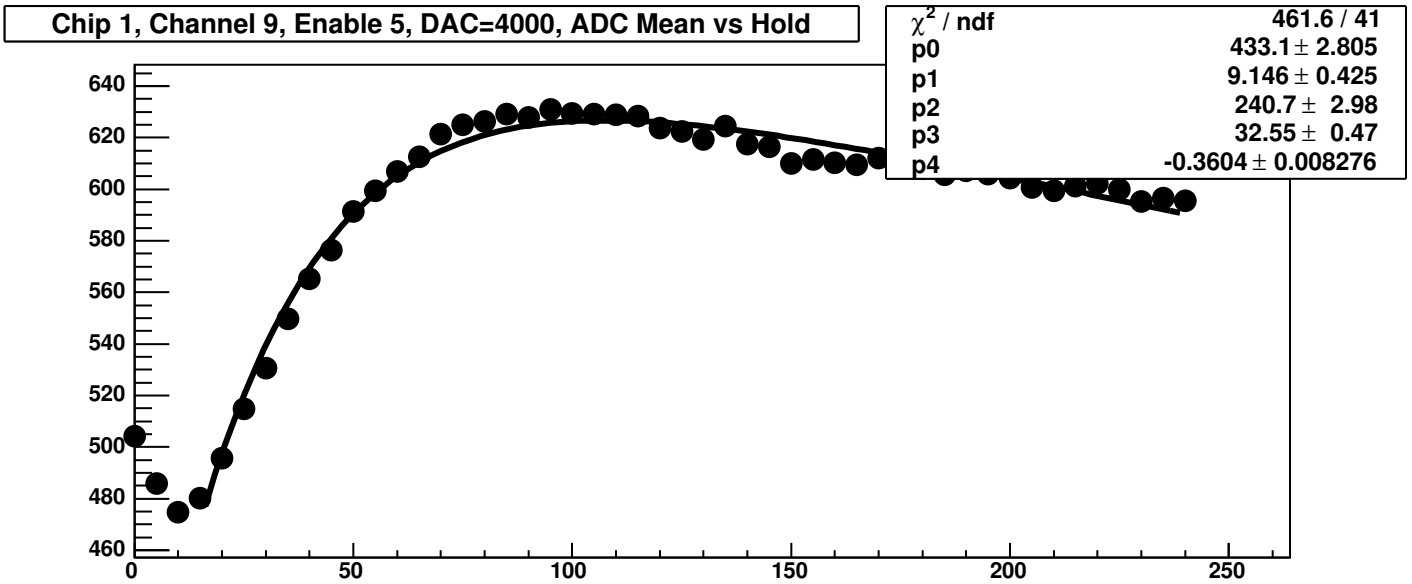


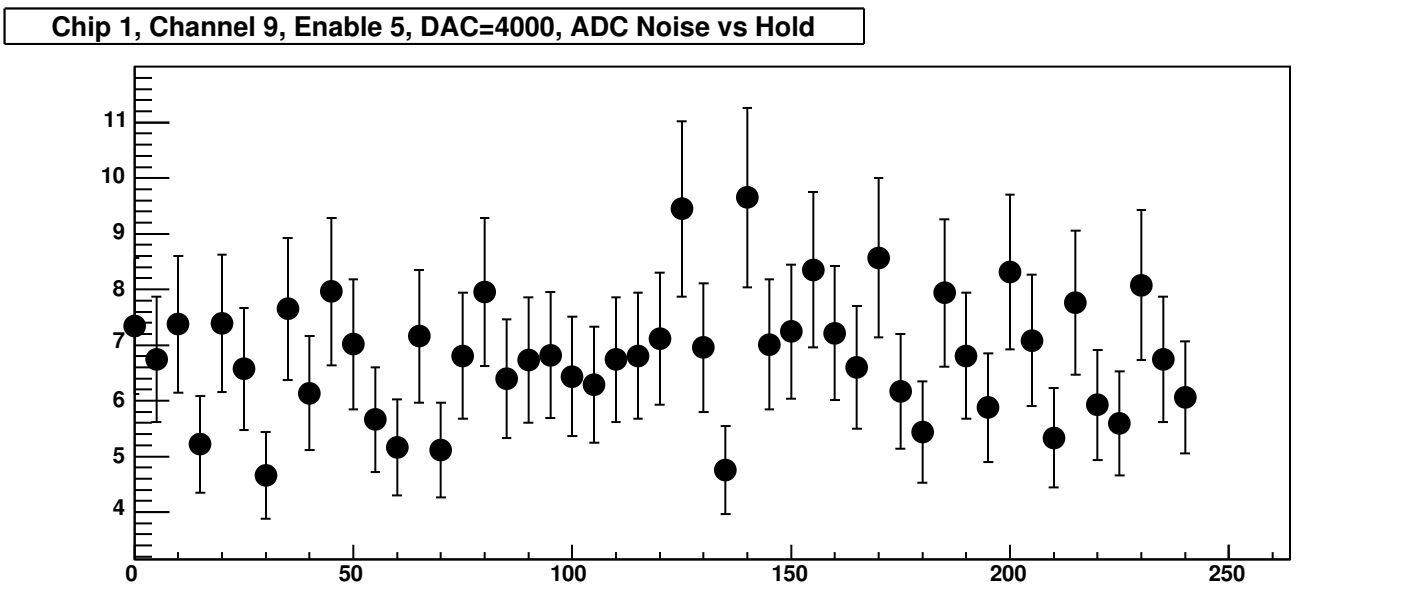
Chip 1, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold

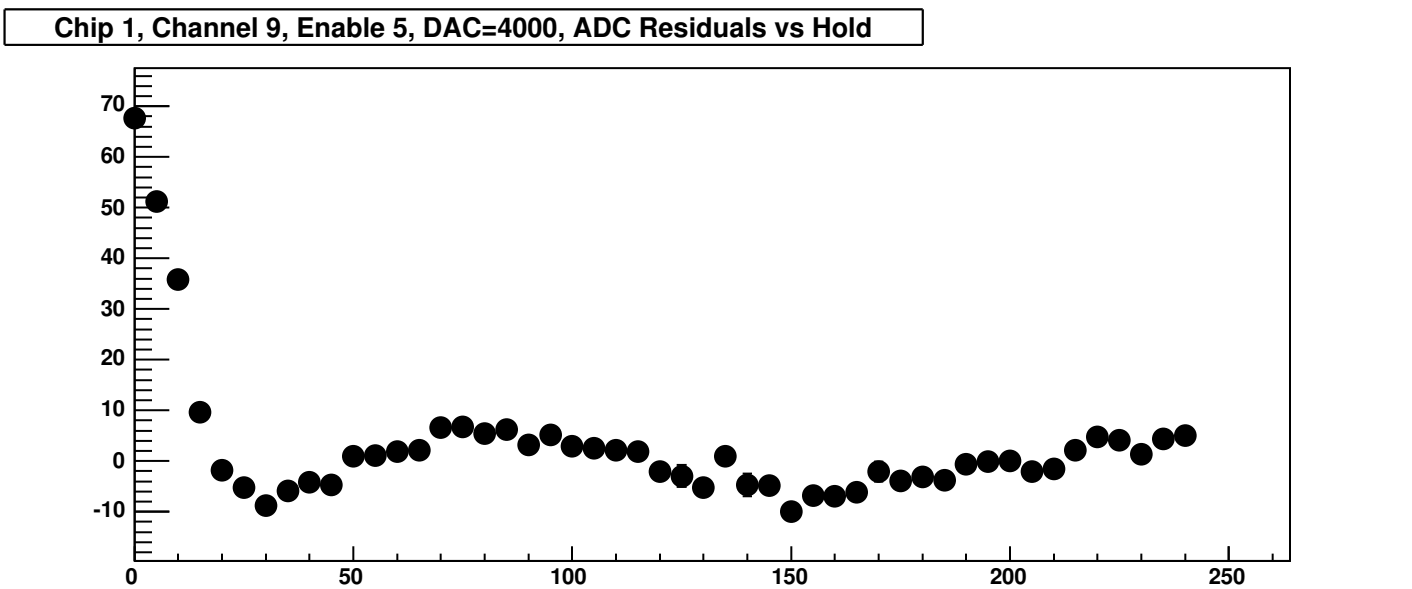


Chip 1, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold



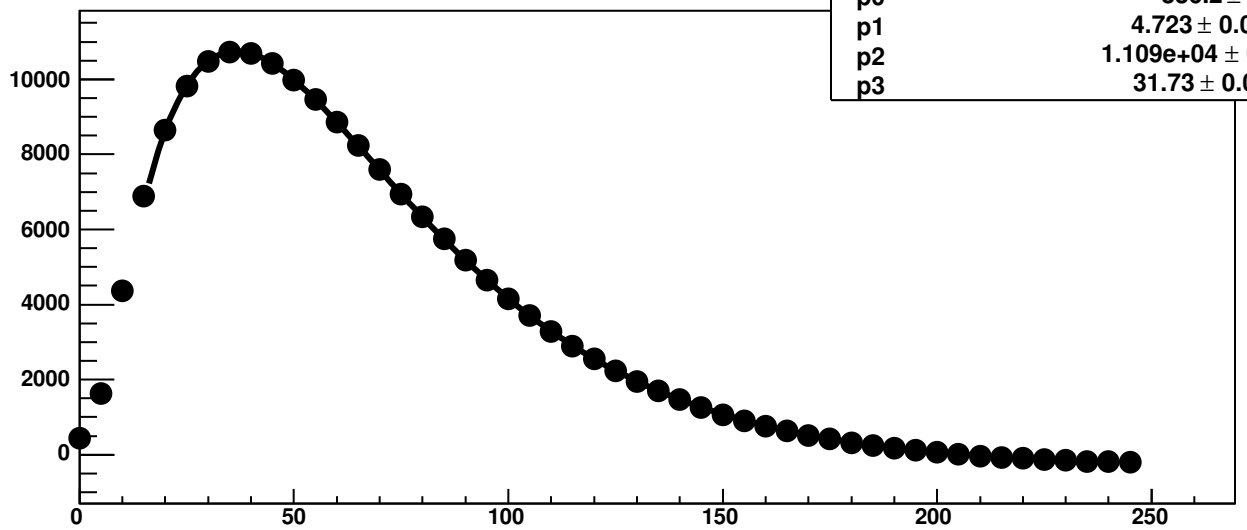

Chip 1, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 461.6 / 41
p0 433.1 ± 2.805
p1 9.146 ± 0.425
p2 240.7 ± 2.98
p3 32.55 ± 0.47
p4 -0.3604 ± 0.008276
640
620
600
580
560
540
520
500
480
460
0
50
100
150
200
250


Chip 1, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250

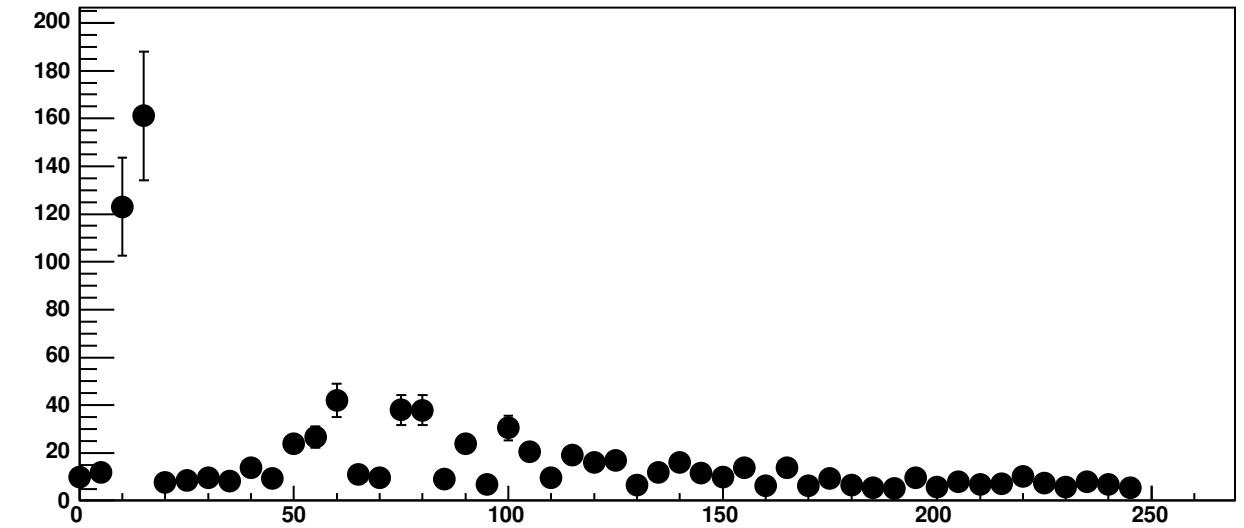

Chip 1, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

Chip 1, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold

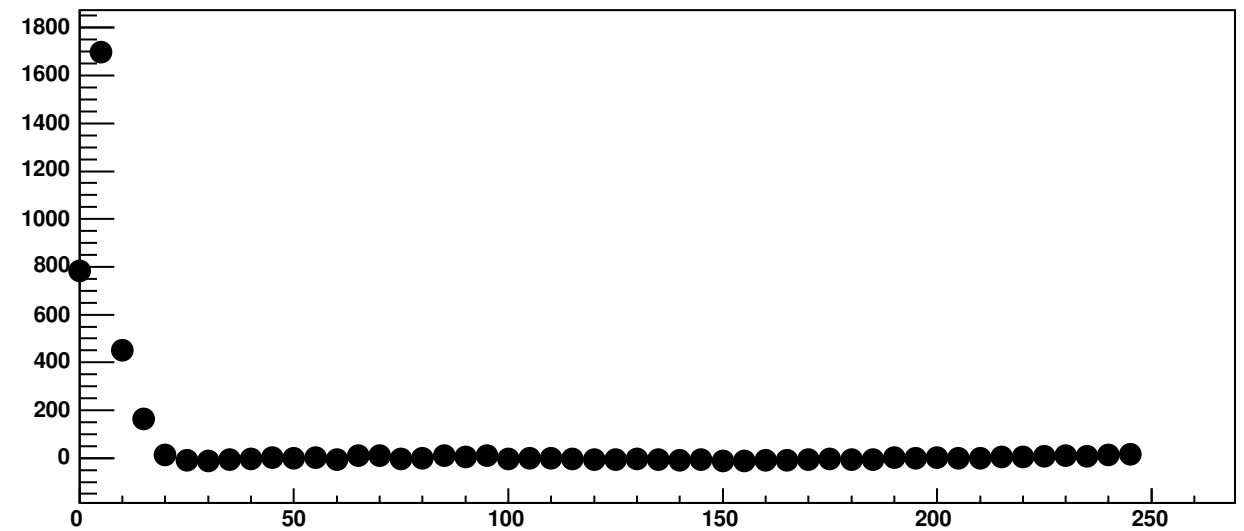
χ^2 / ndf	573.9 / 42
p0	-336.2 ± 0.5748
p1	4.723 ± 0.008145
p2	1.109e+04 ± 0.9904
p3	31.73 ± 0.005882



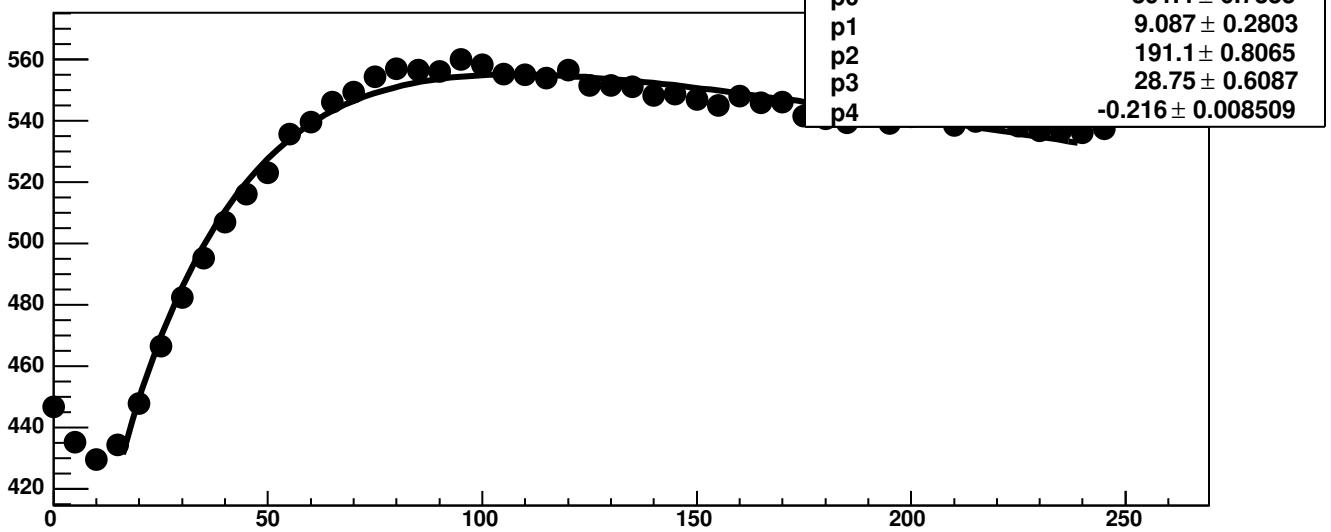
Chip 1, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold



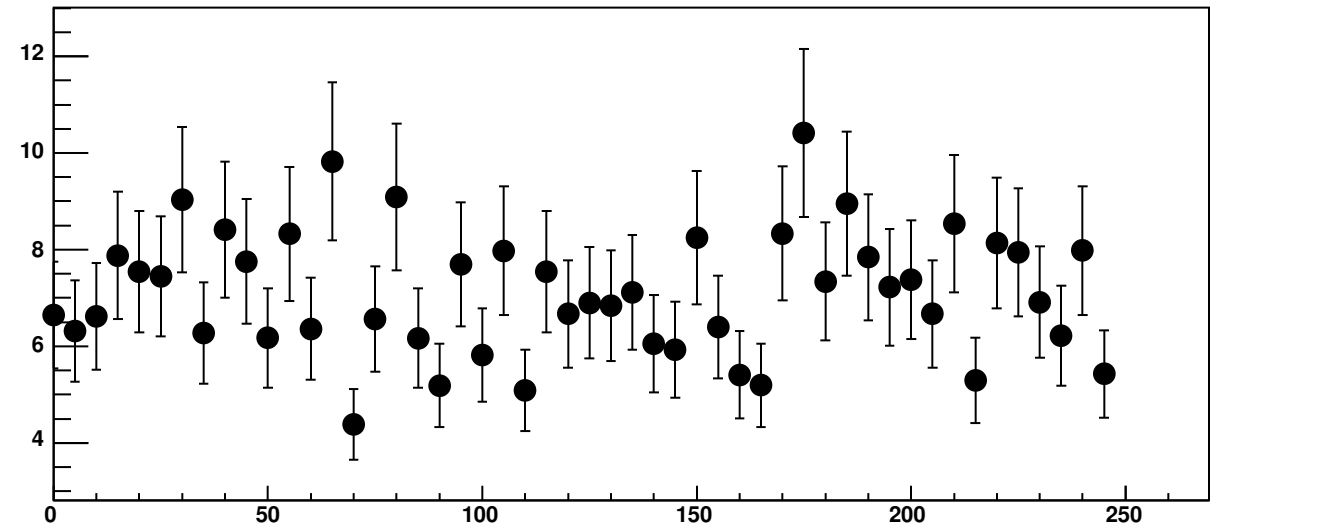
Chip 1, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold



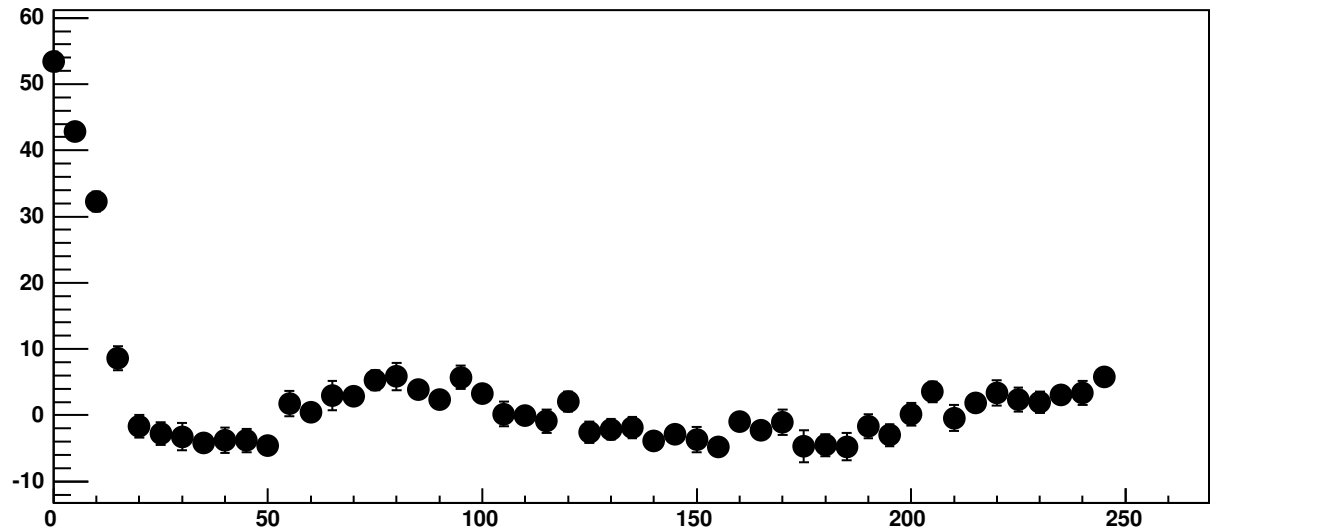
Chip 1, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold



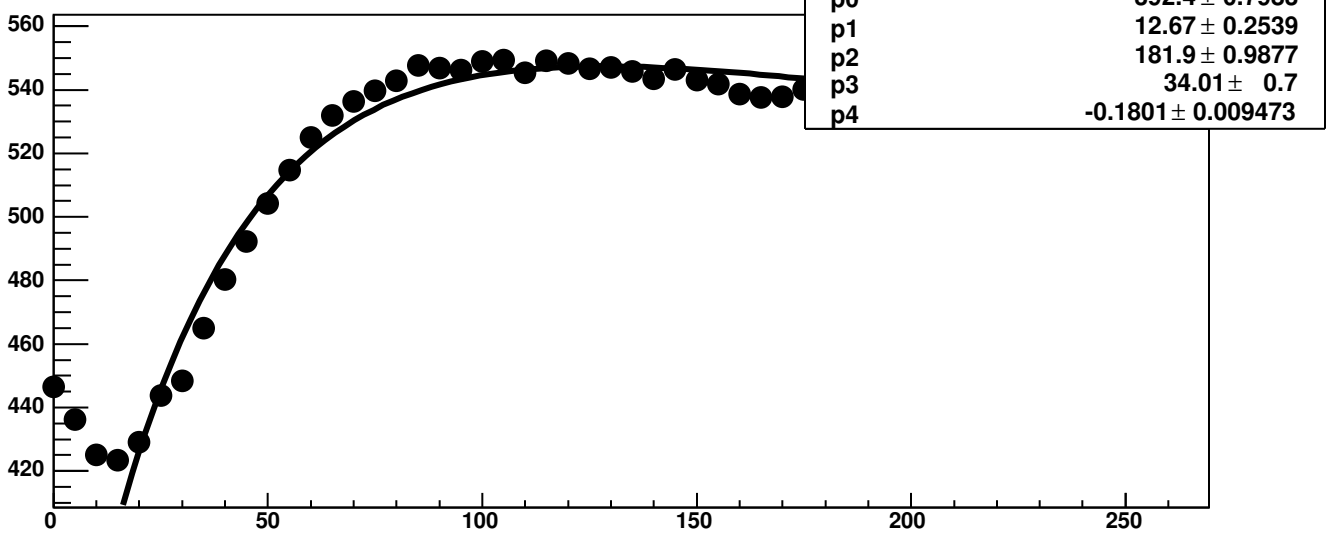
Chip 1, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



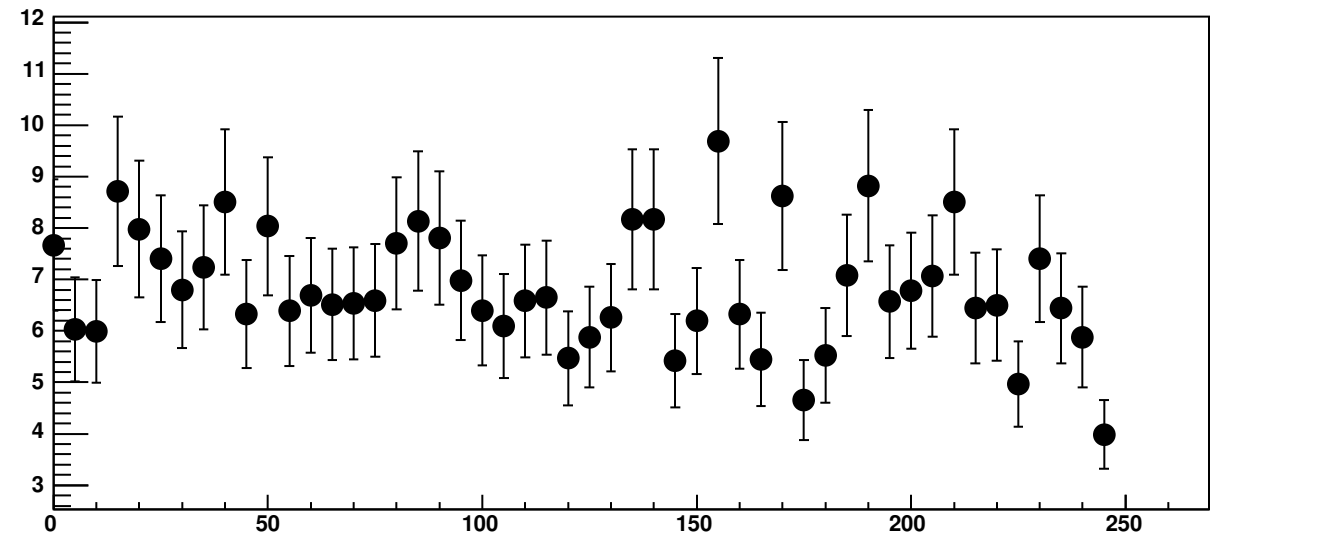
Chip 1, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold



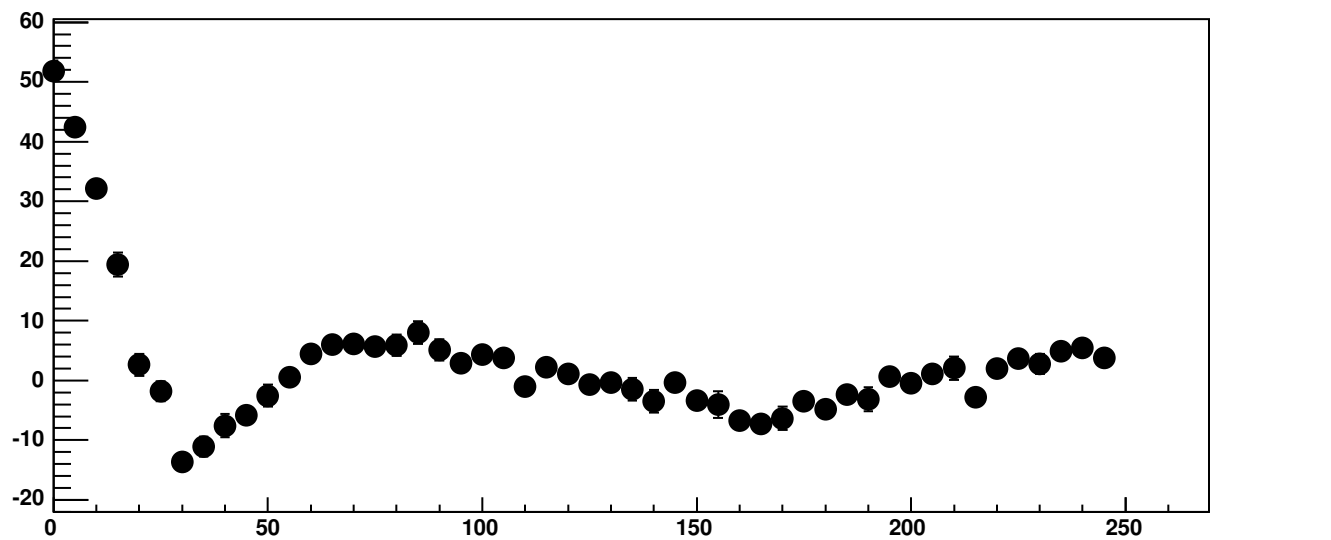
Chip 1, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold

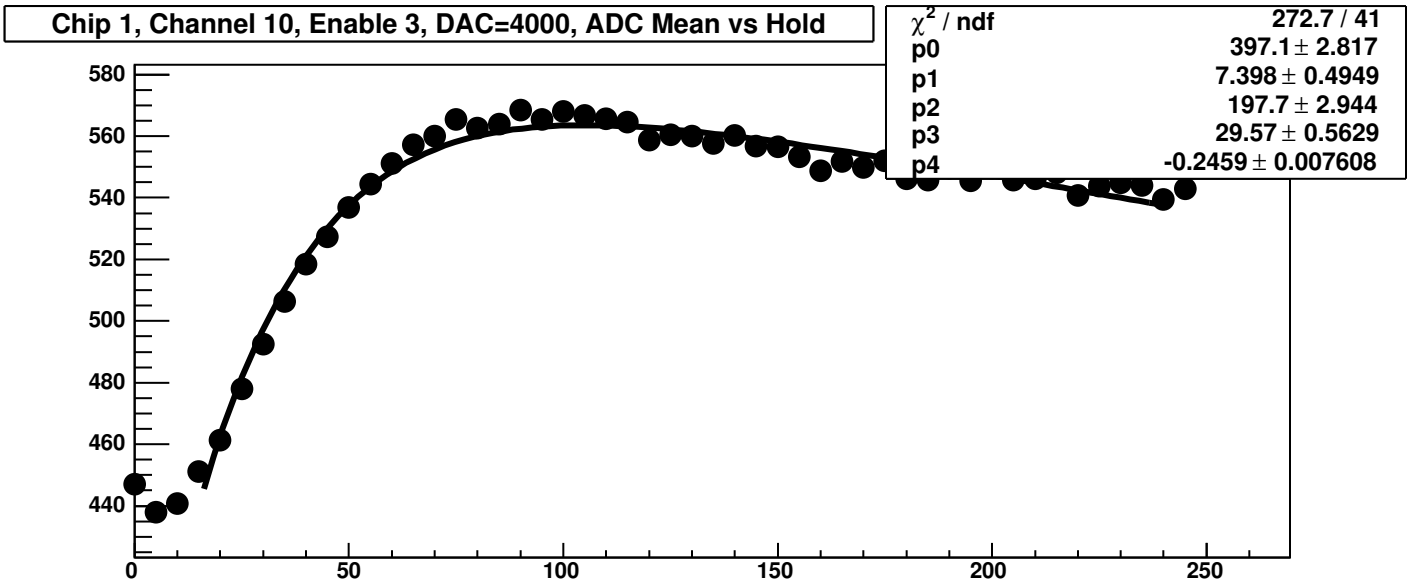


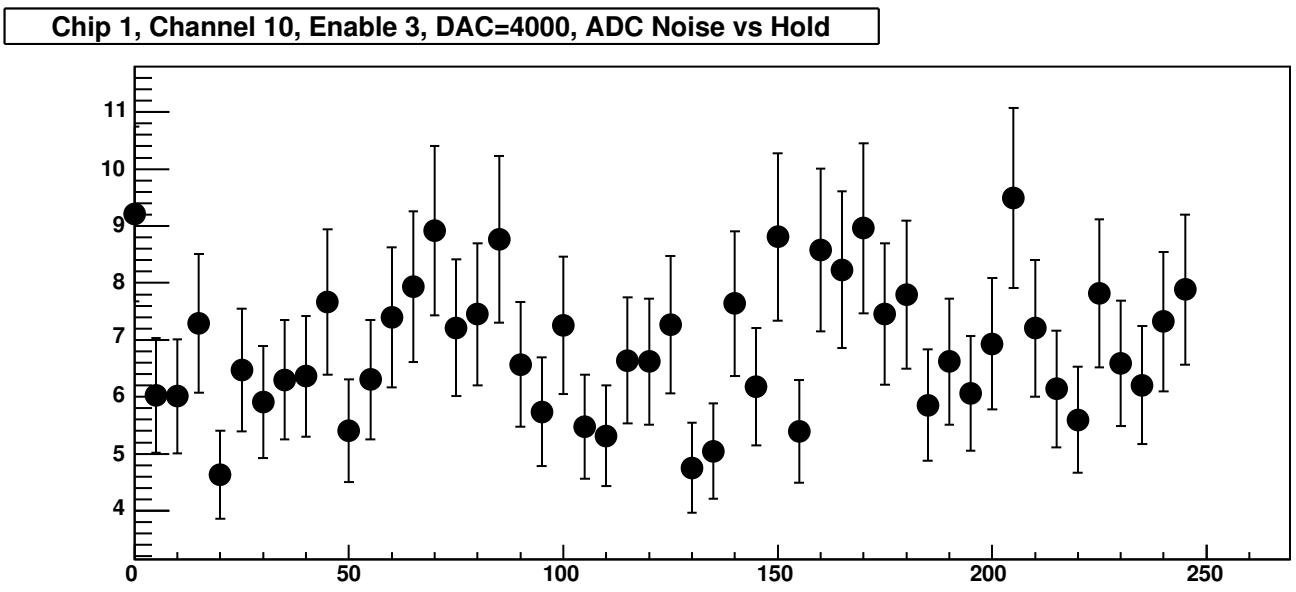
Chip 1, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold

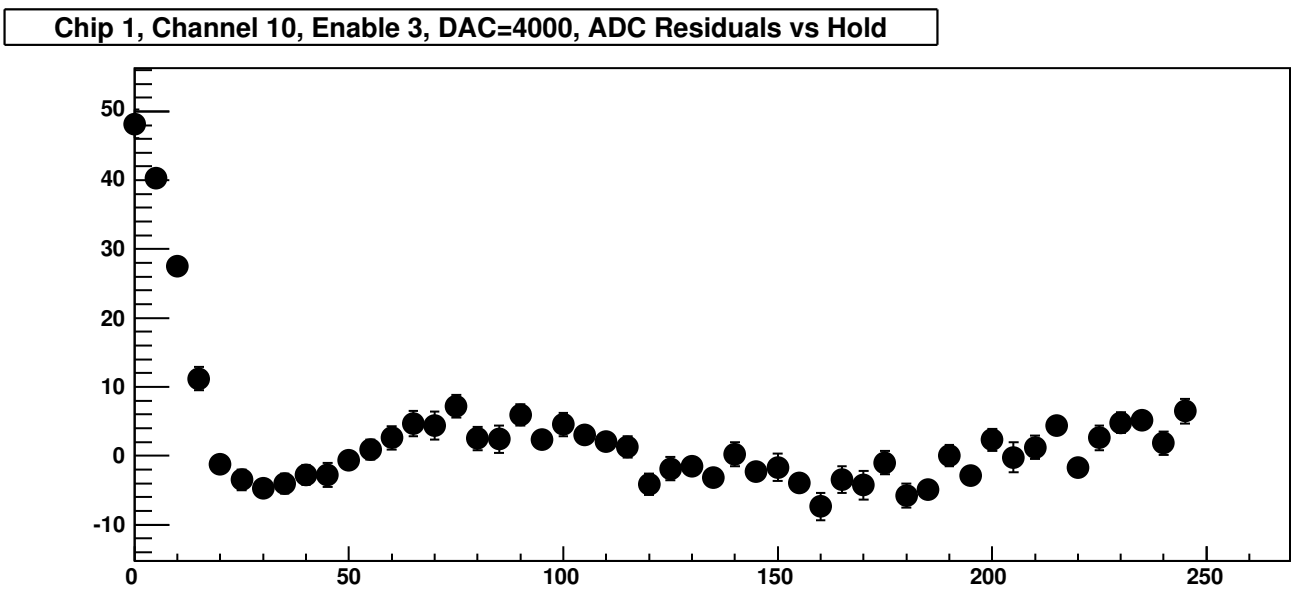


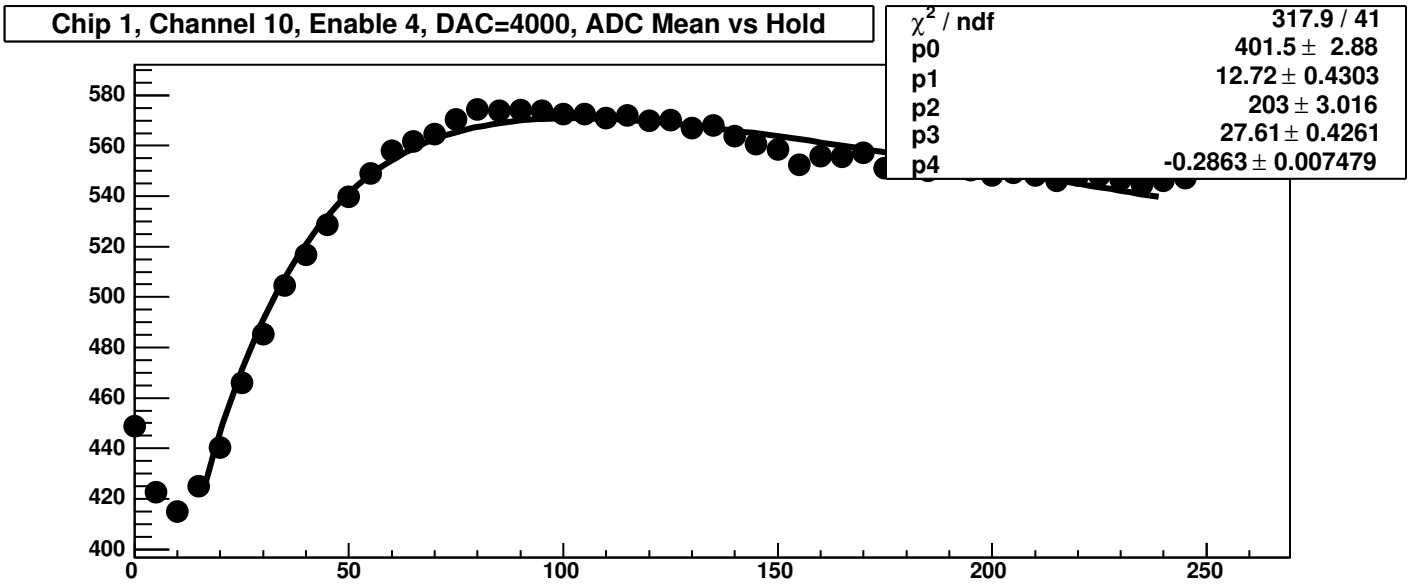
Chip 1, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

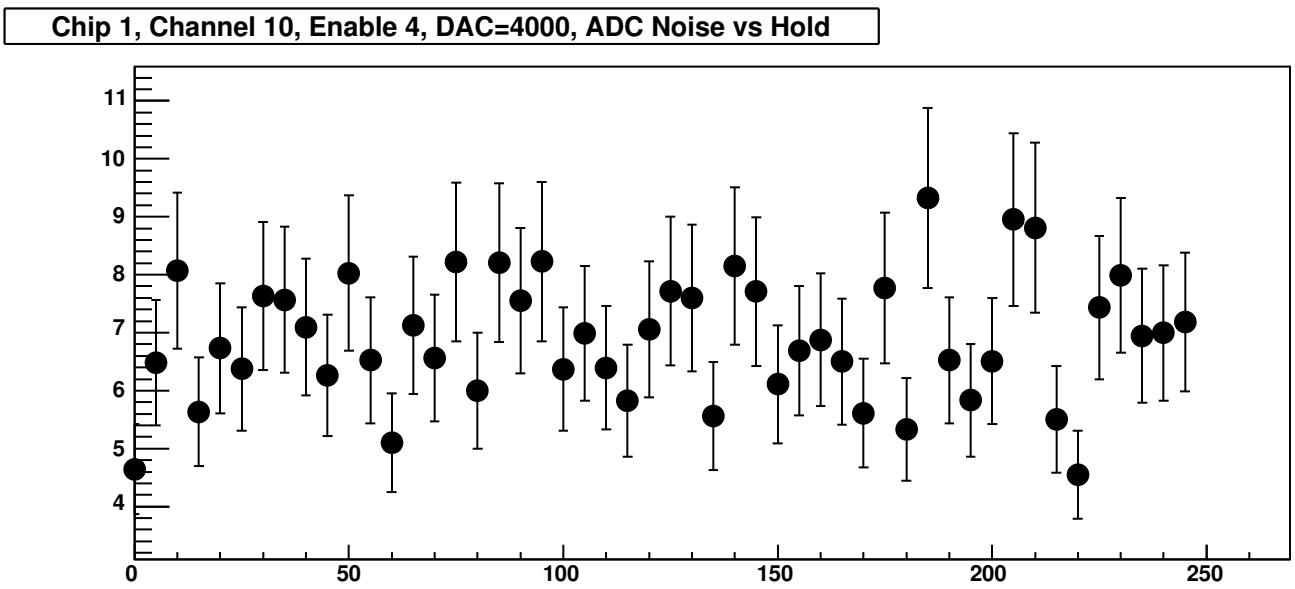


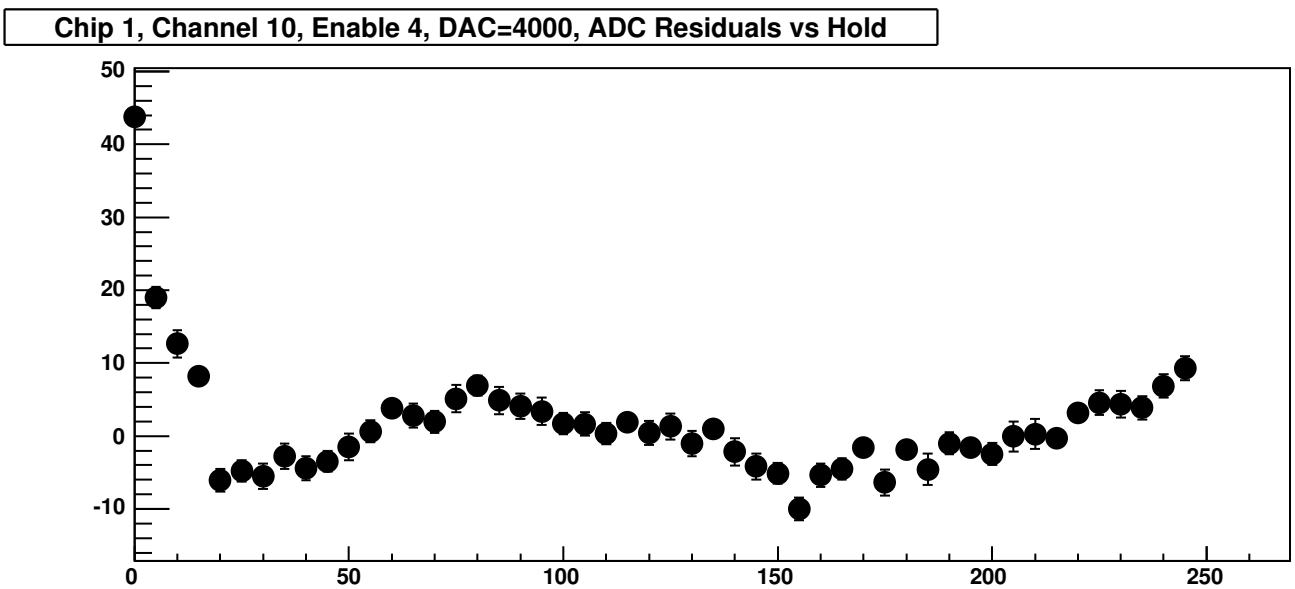

Chip 1, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 272.7 / 41
p0 397.1 ± 2.817
p1 7.398 ± 0.4949
p2 197.7 ± 2.944
p3 29.57 ± 0.5629
p4 -0.2459 ± 0.007608
580
560
540
520
500
480
460
440
0
50
100
150
200
250

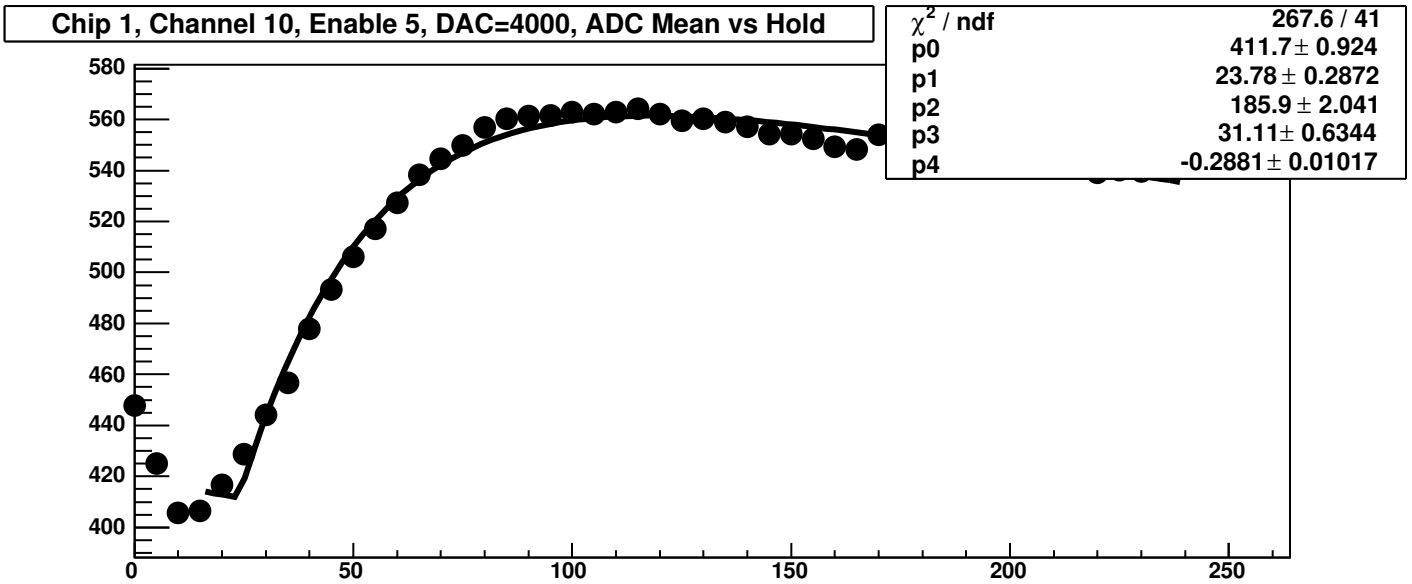

Chip 1, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250

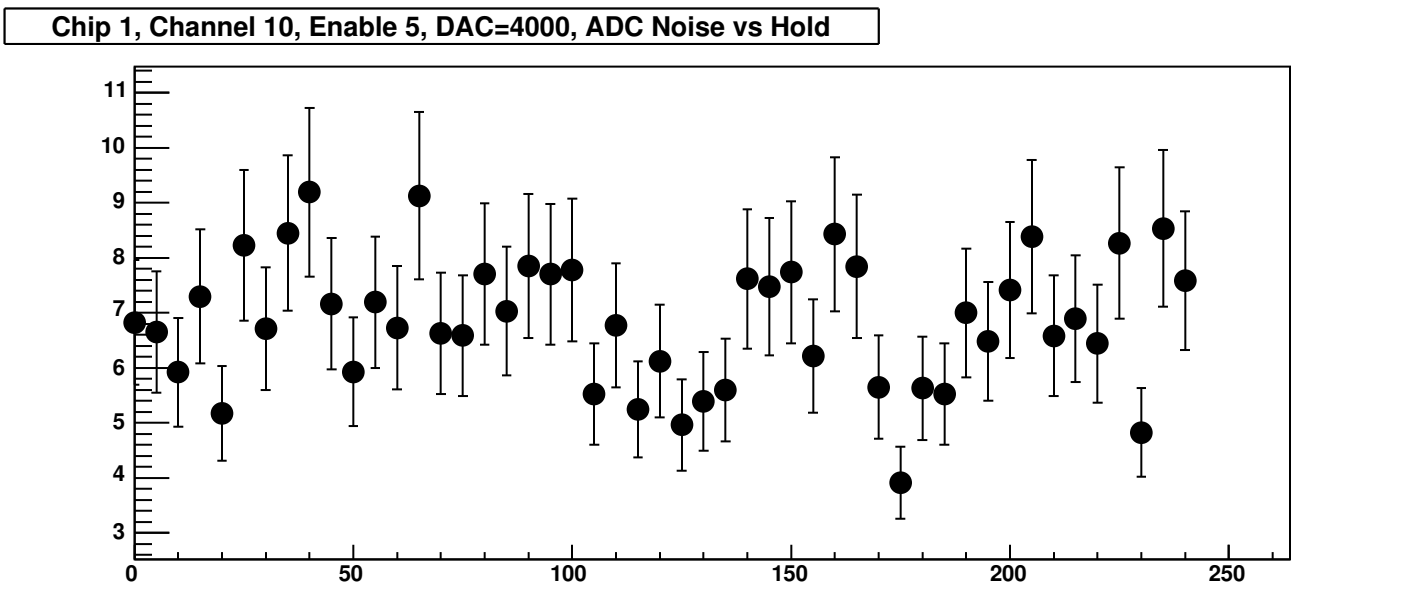

Chip 1, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250

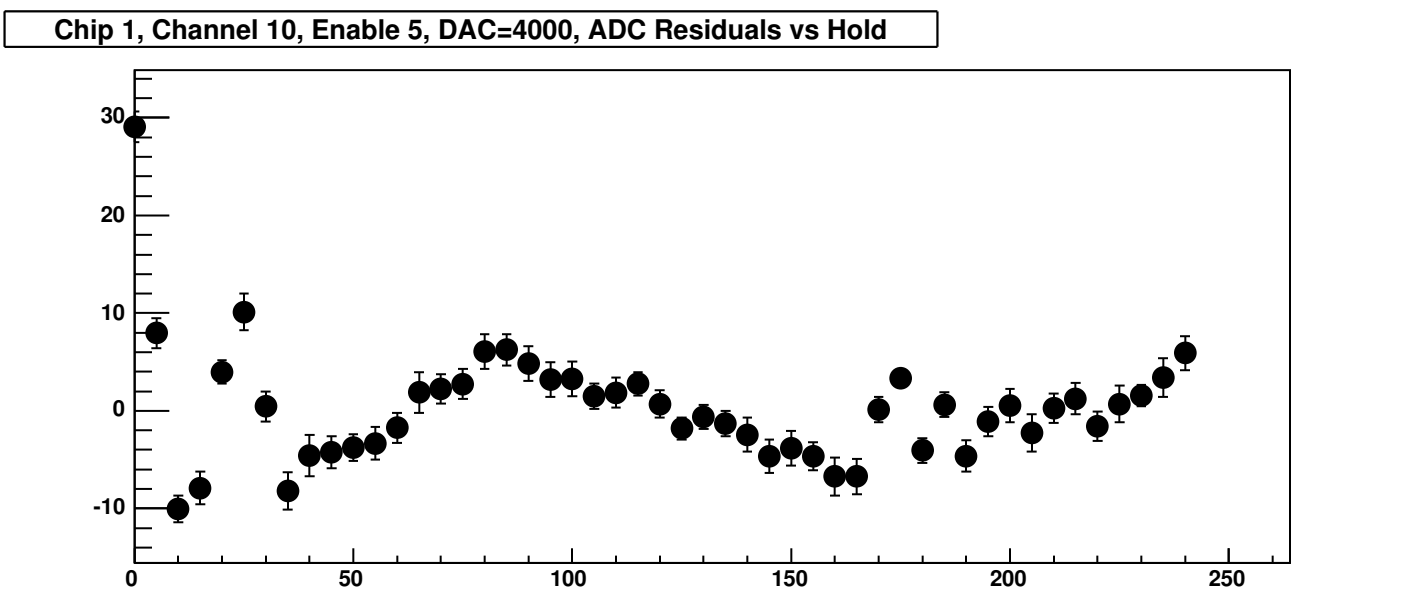

Chip 1, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 317.9 / 41
p0 401.5 ± 2.88
p1 12.72 ± 0.4303
p2 203 ± 3.016
p3 27.61 ± 0.4261
p4 -0.2863 ± 0.007479
580
560
540
520
500
480
460
440
420
400
0
50
100
150
200
250


Chip 1, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250

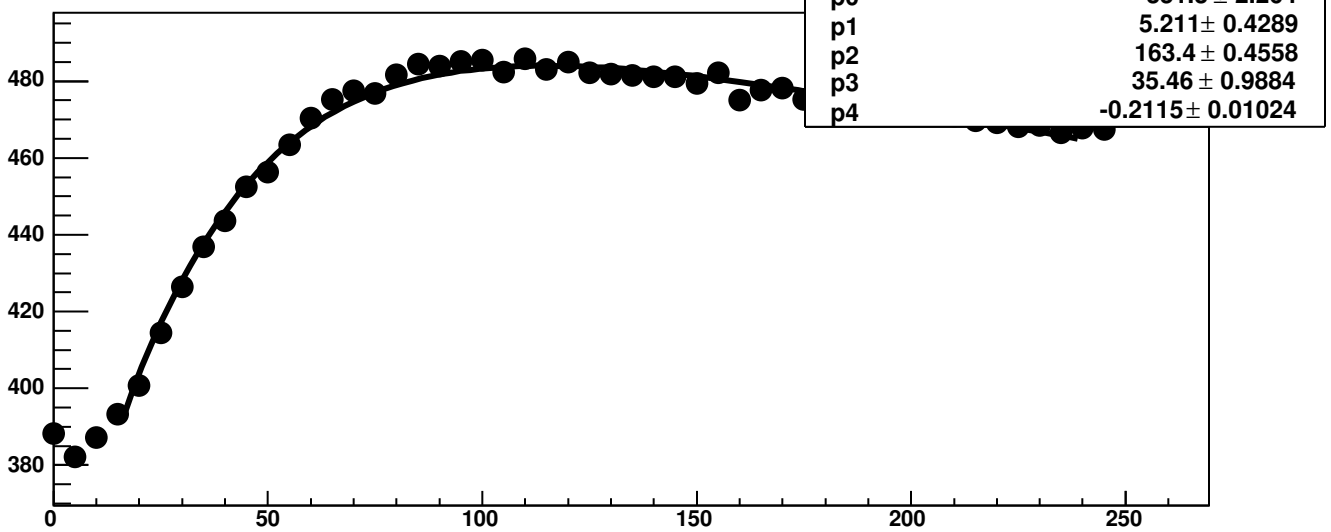

Chip 1, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250


Chip 1, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 267.6 / 41
p0 411.7 ± 0.924
p1 23.78 ± 0.2872
p2 185.9 ± 2.041
p3 31.11 ± 0.6344
p4 -0.2881 ± 0.01017
580
560
540
520
500
480
460
440
420
400
0
50
100
150
200
250

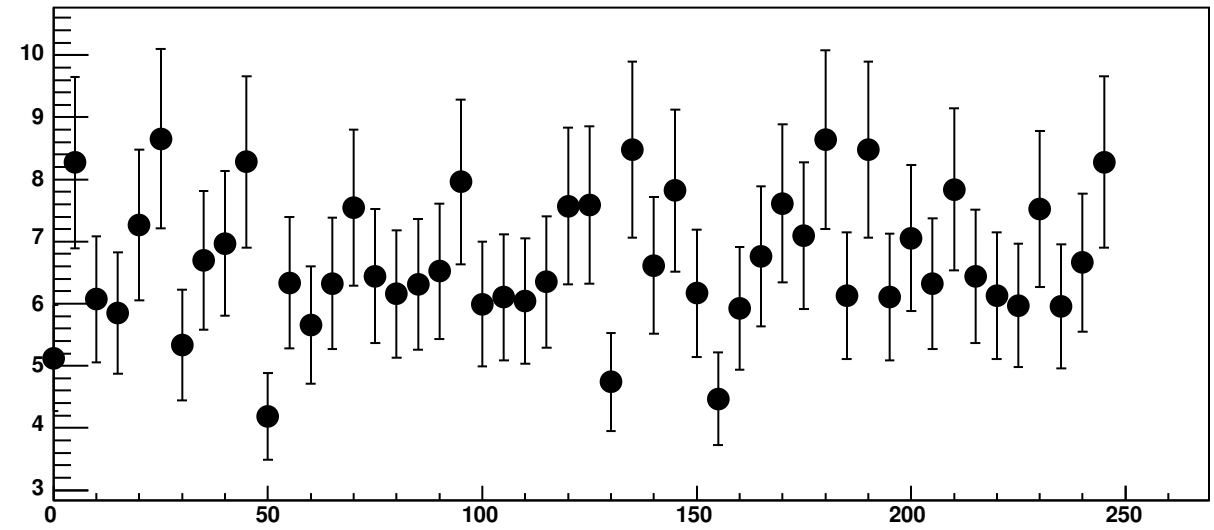

Chip 1, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 1, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
0
50
100
150
200
250

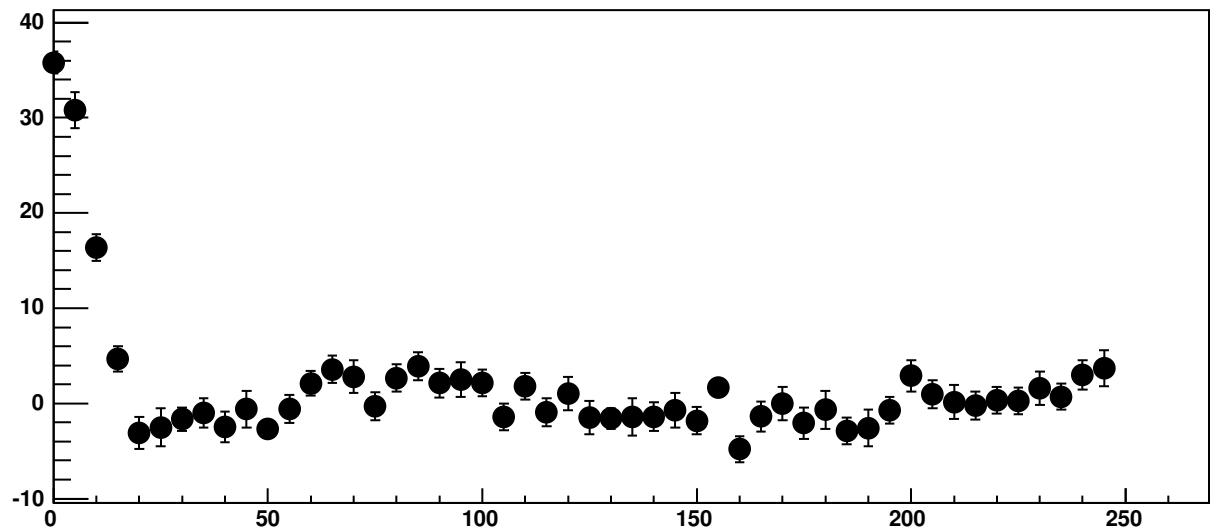
Chip 1, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold



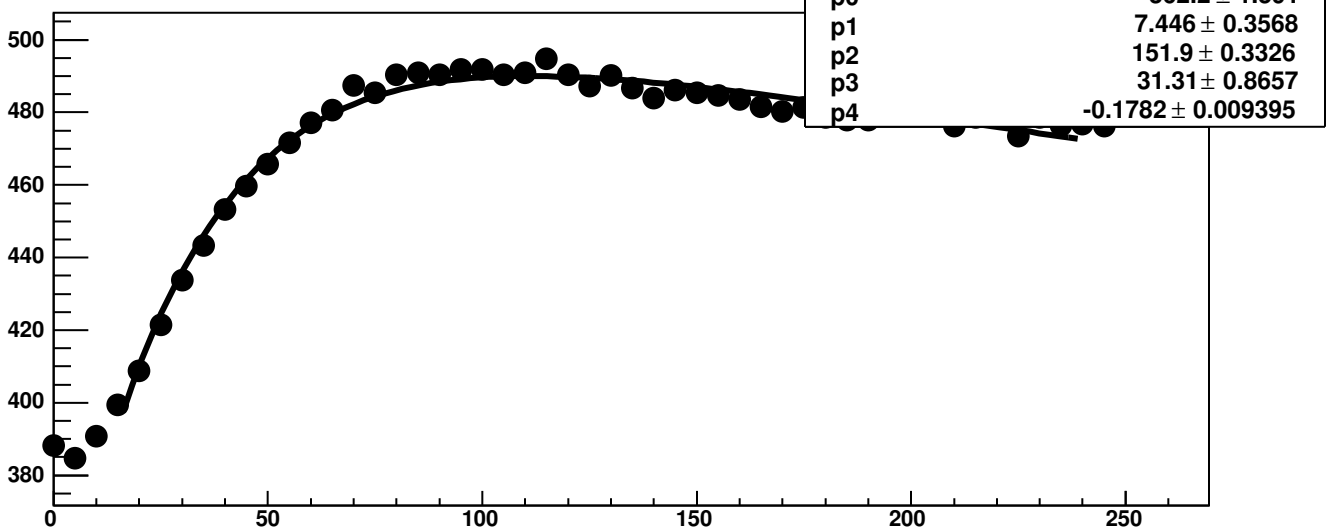
Chip 1, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold



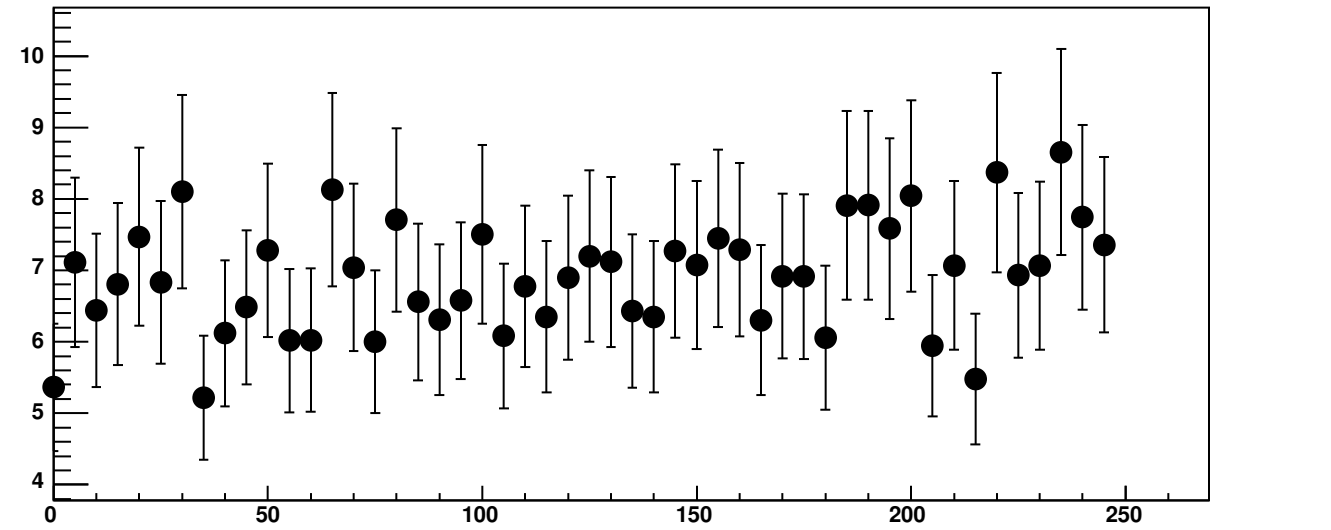
Chip 1, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold



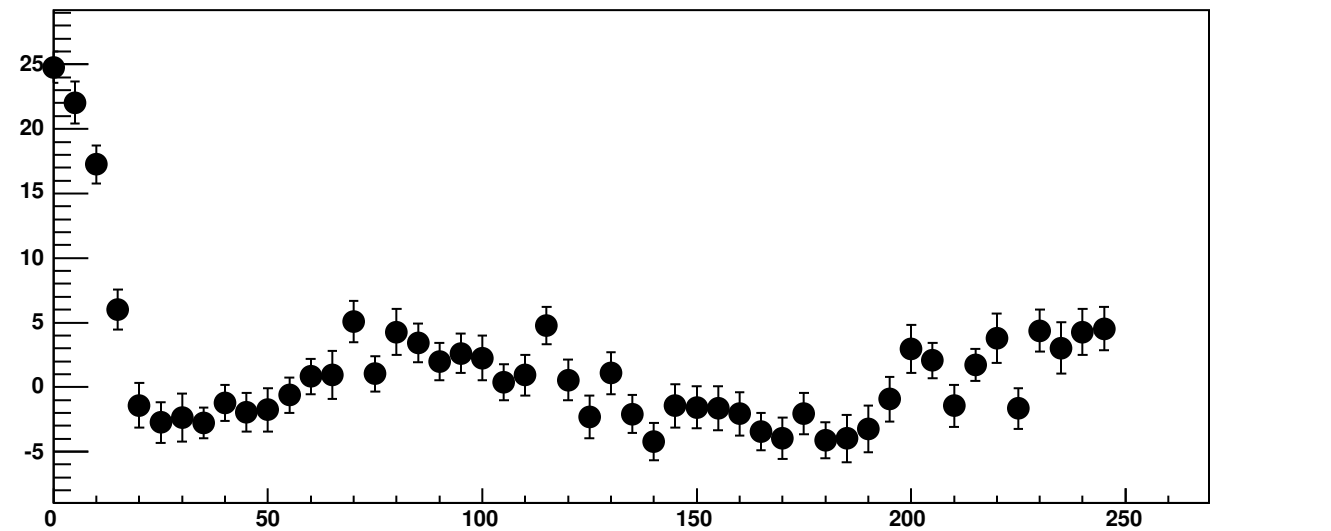
Chip 1, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold

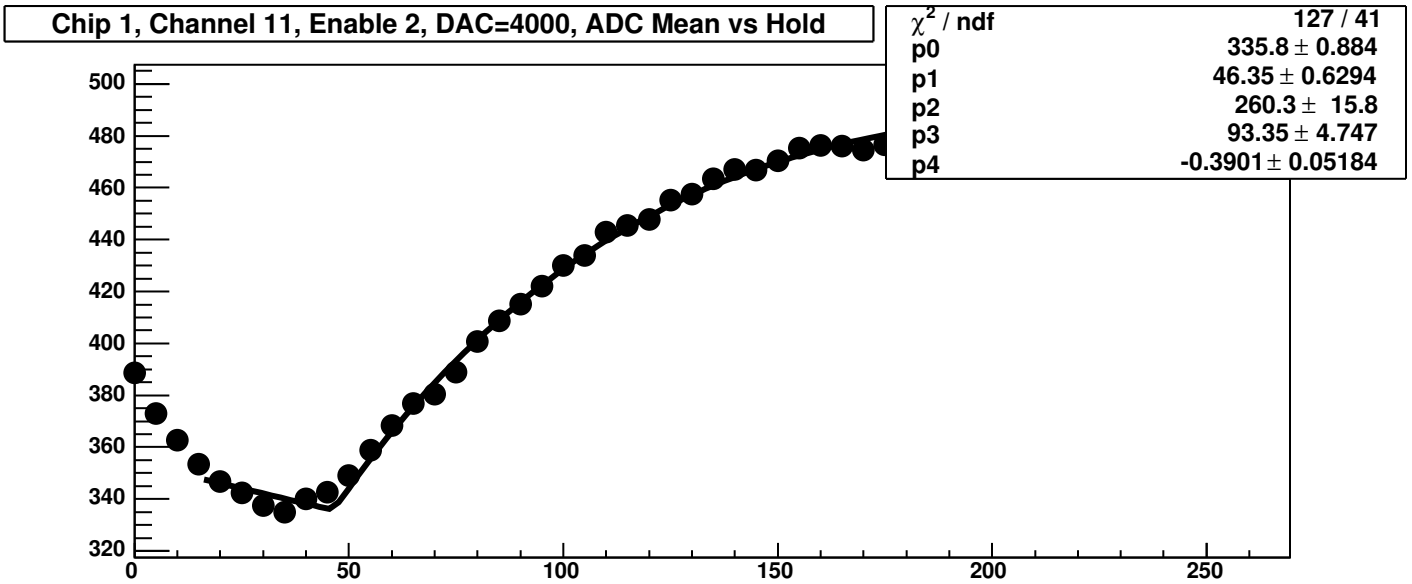


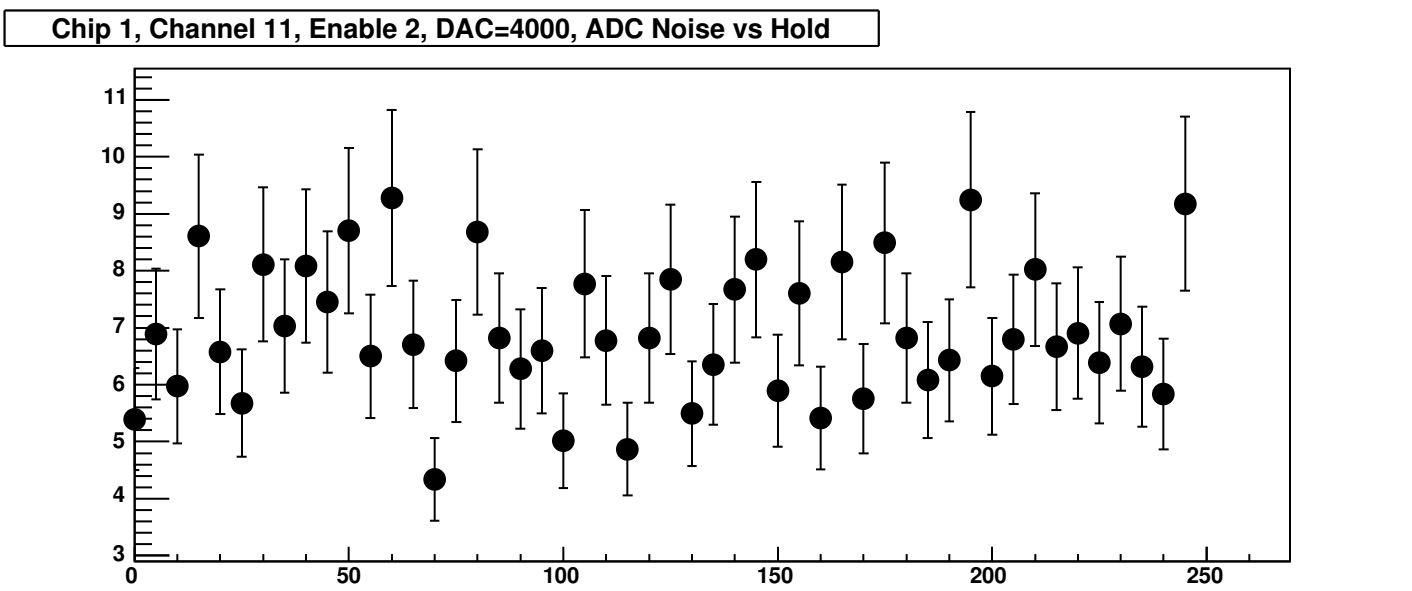
Chip 1, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold

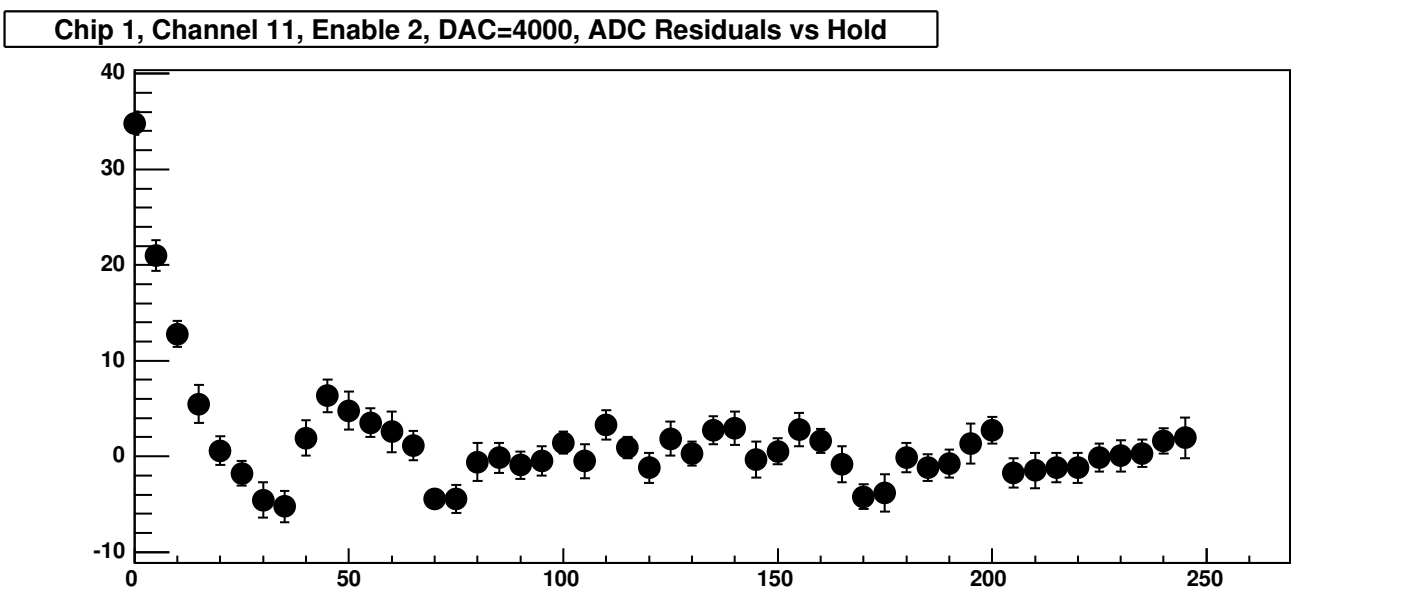


Chip 1, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold

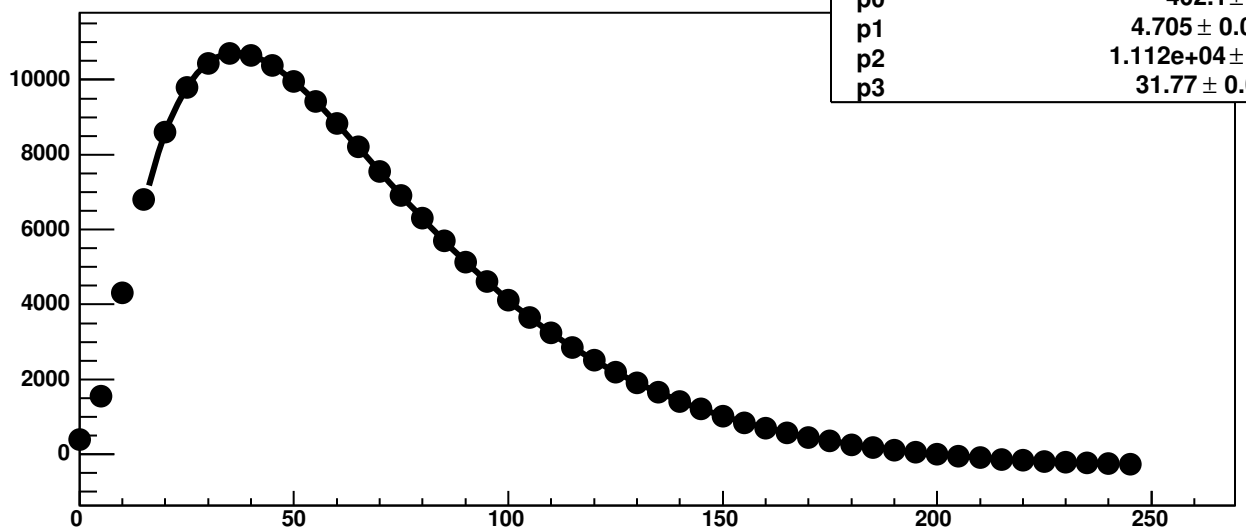



Chip 1, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 127 / 41
p0 335.8 ± 0.884
p1 46.35 ± 0.6294
p2 260.3 ± 15.8
p3 93.35 ± 4.747
p4 -0.3901 ± 0.05184
500
480
460
440
420
400
380
360
340
320
0
50
100
150
200
250

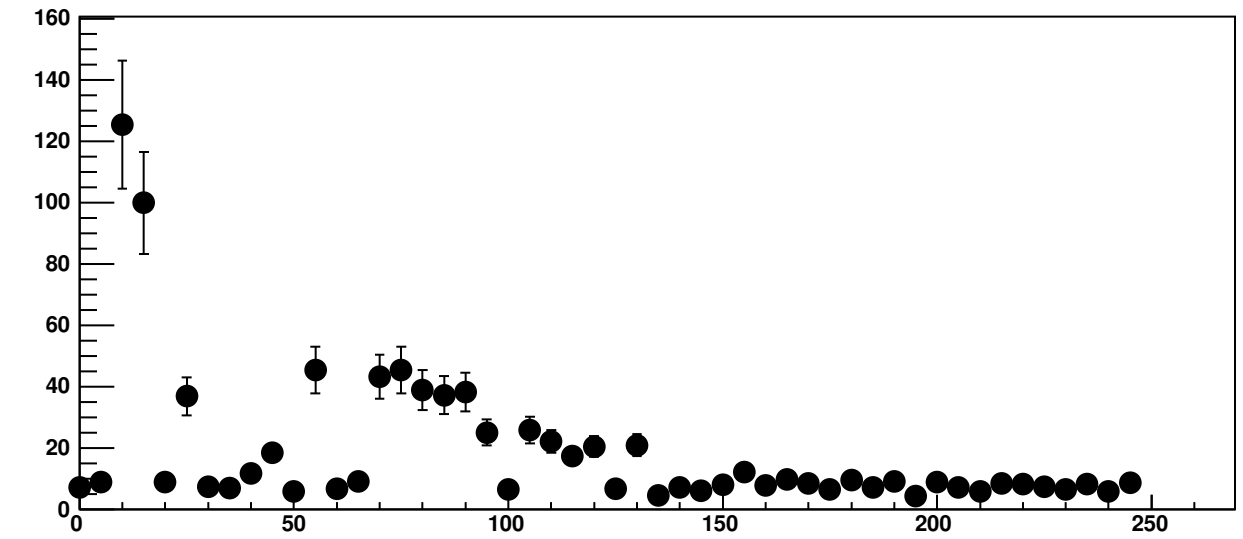

Chip 1, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 1, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

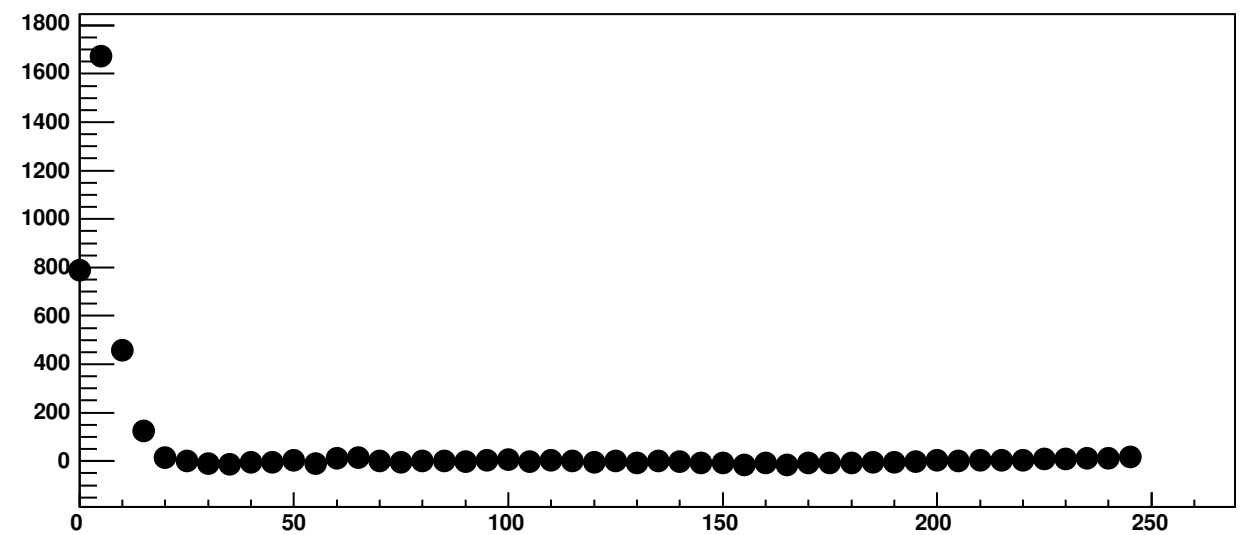
Chip 1, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold



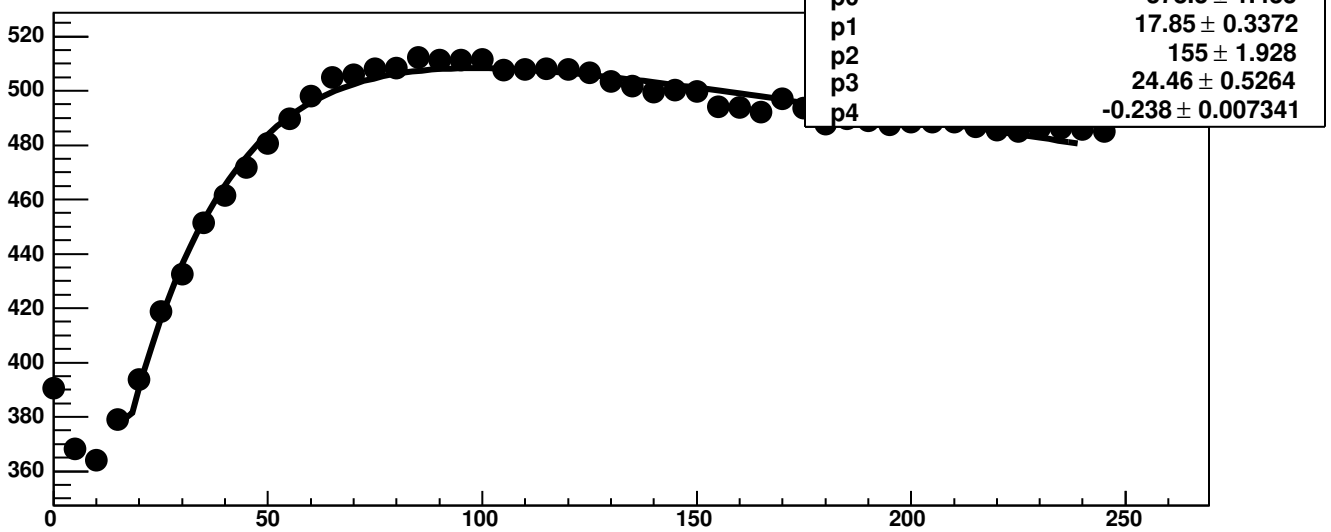
Chip 1, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold



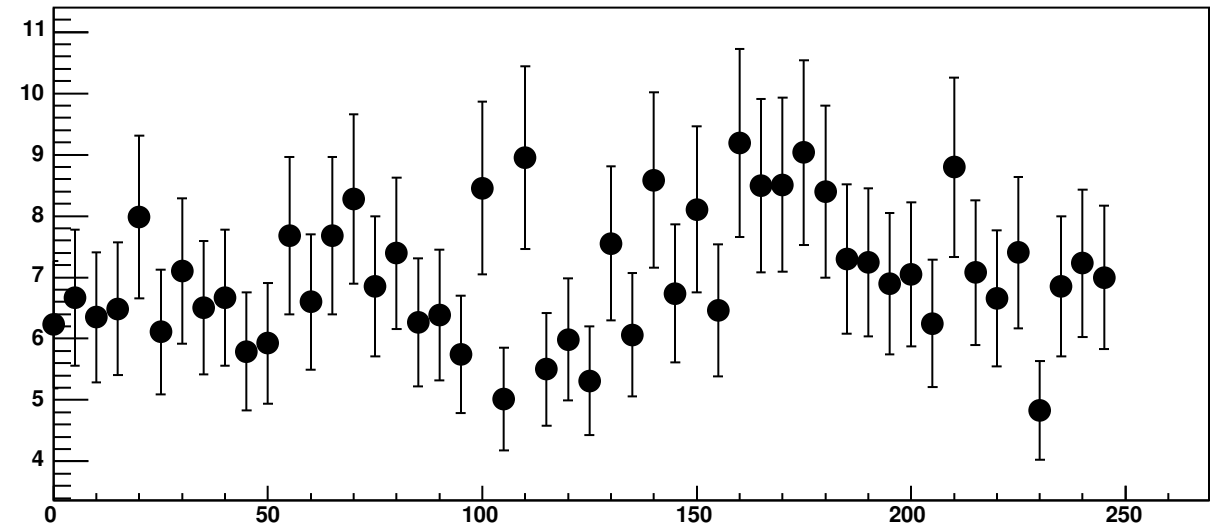
Chip 1, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold



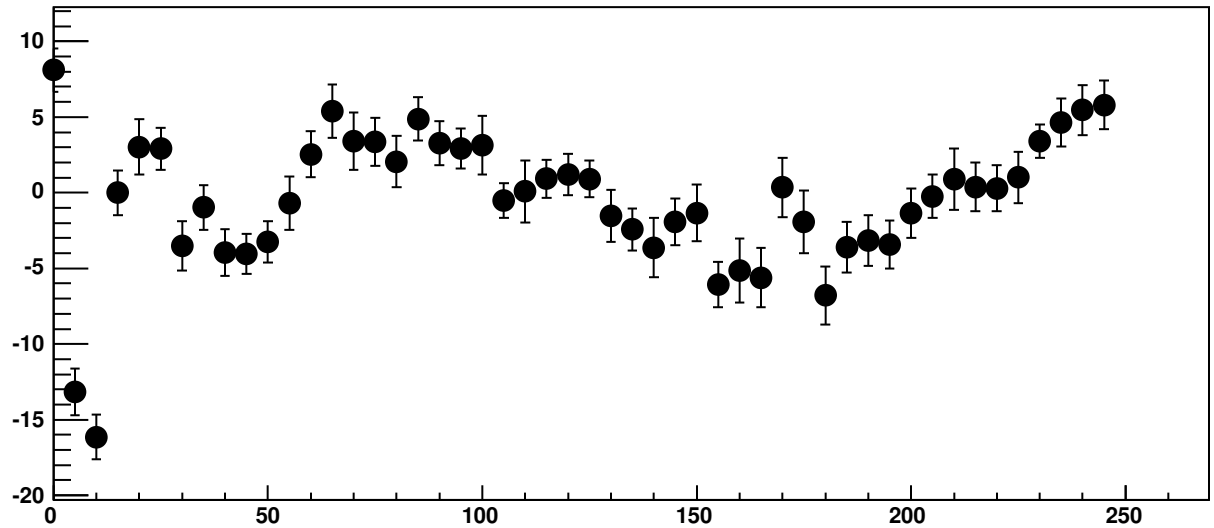
Chip 1, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

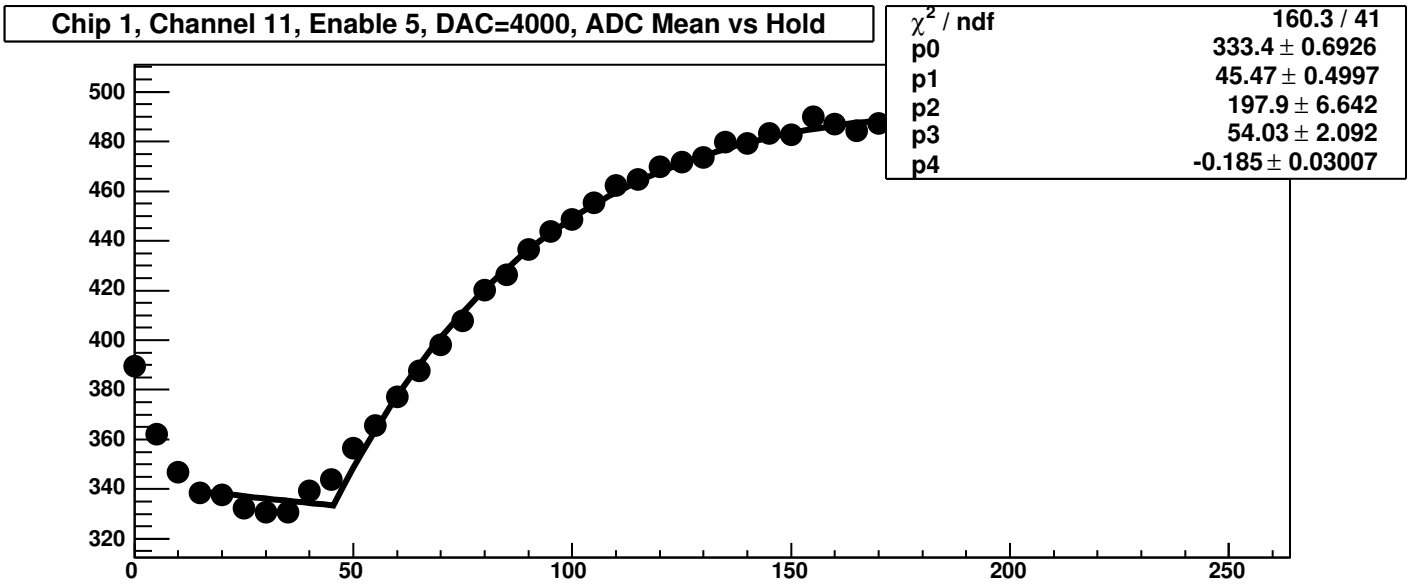


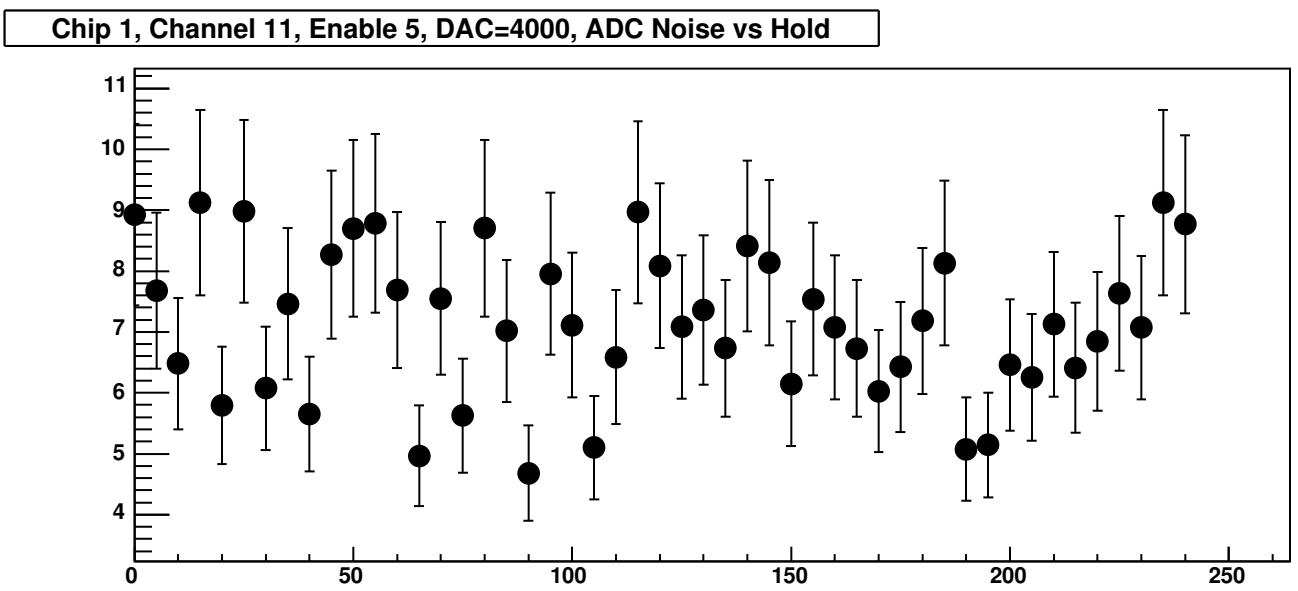
Chip 1, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold

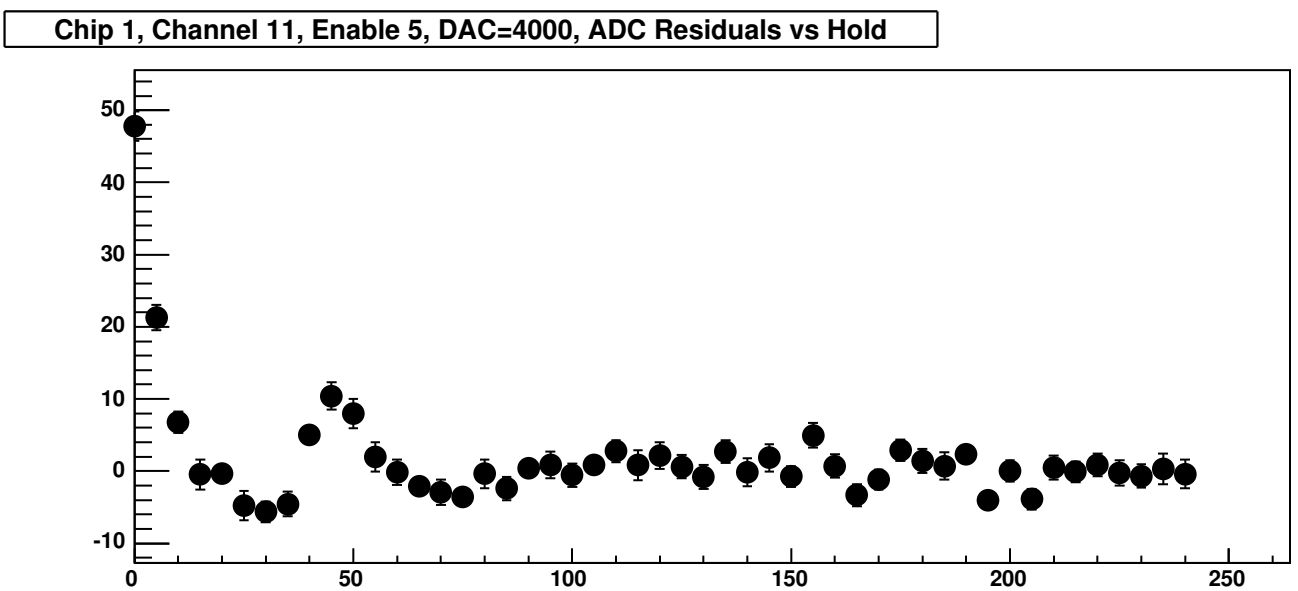


Chip 1, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

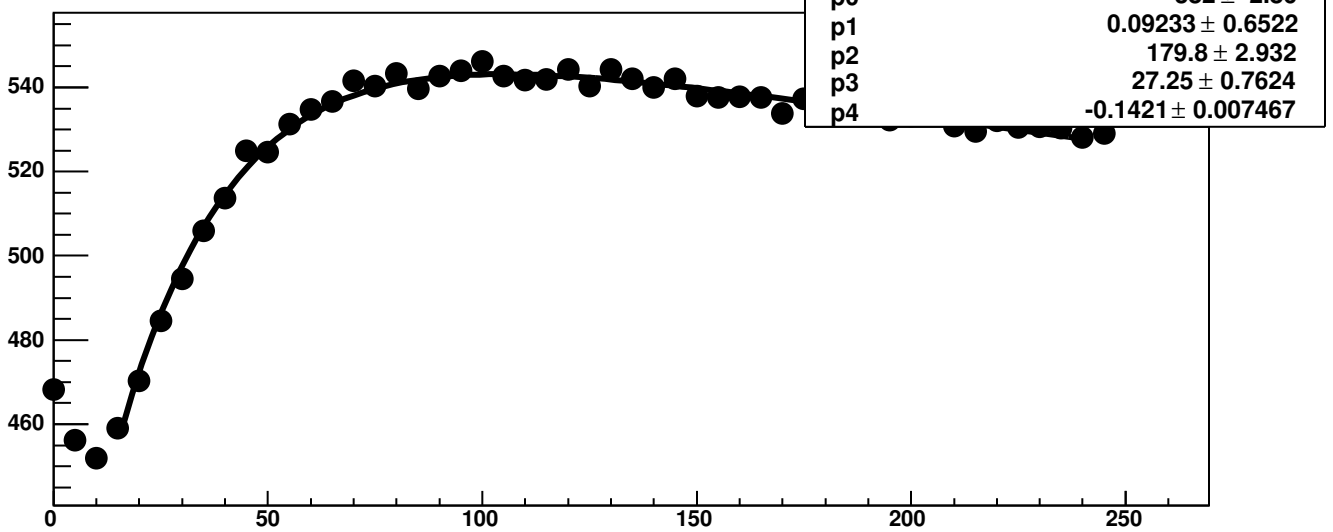



Chip 1, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 160.3 / 41
p0 333.4 ± 0.6926
p1 45.47 ± 0.4997
p2 197.9 ± 6.642
p3 54.03 ± 2.092
p4 -0.185 ± 0.03007
500
480
460
440
420
400
380
360
340
320
0
50
100
150
200
250

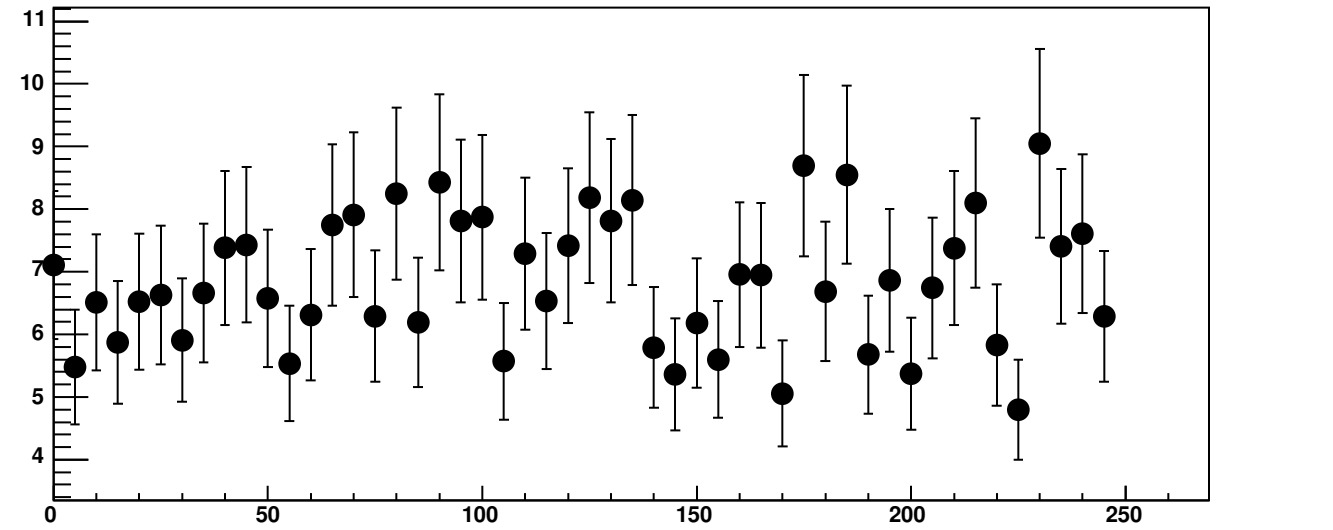

Chip 1, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 1, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250

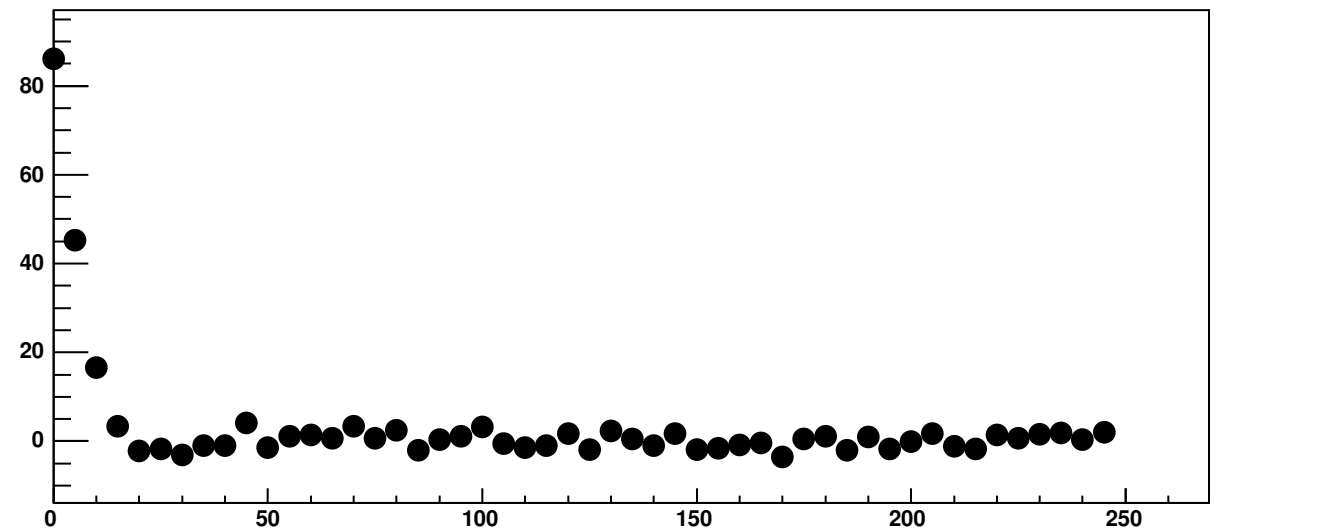
Chip 1, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold

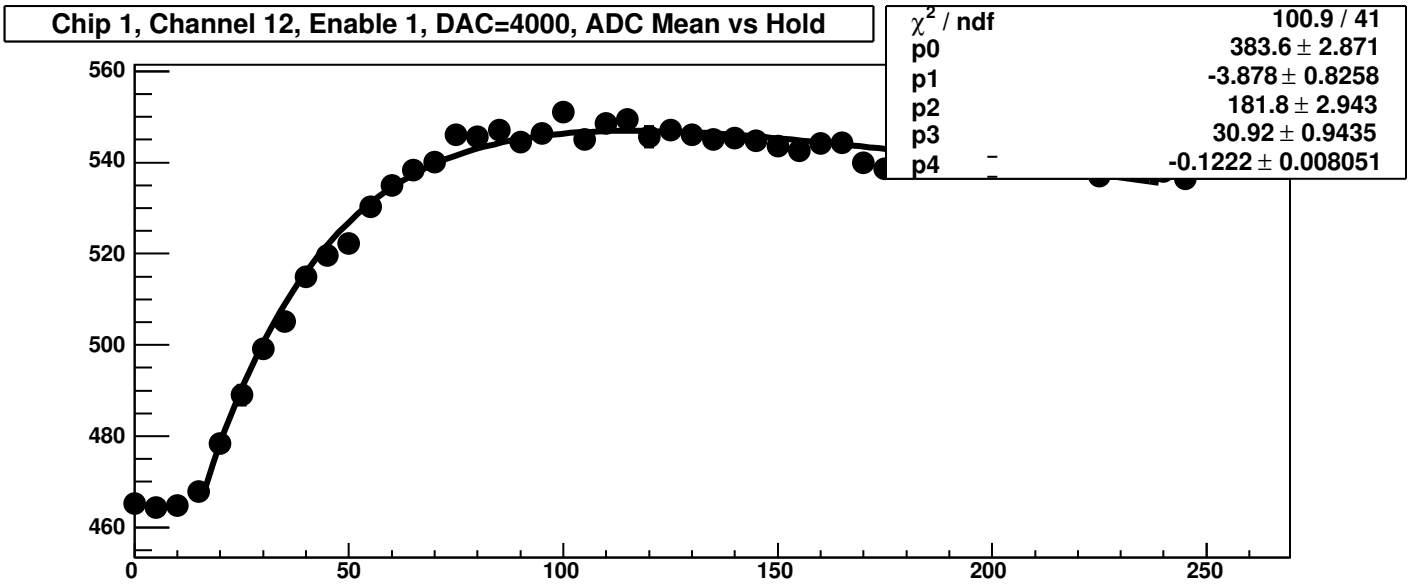


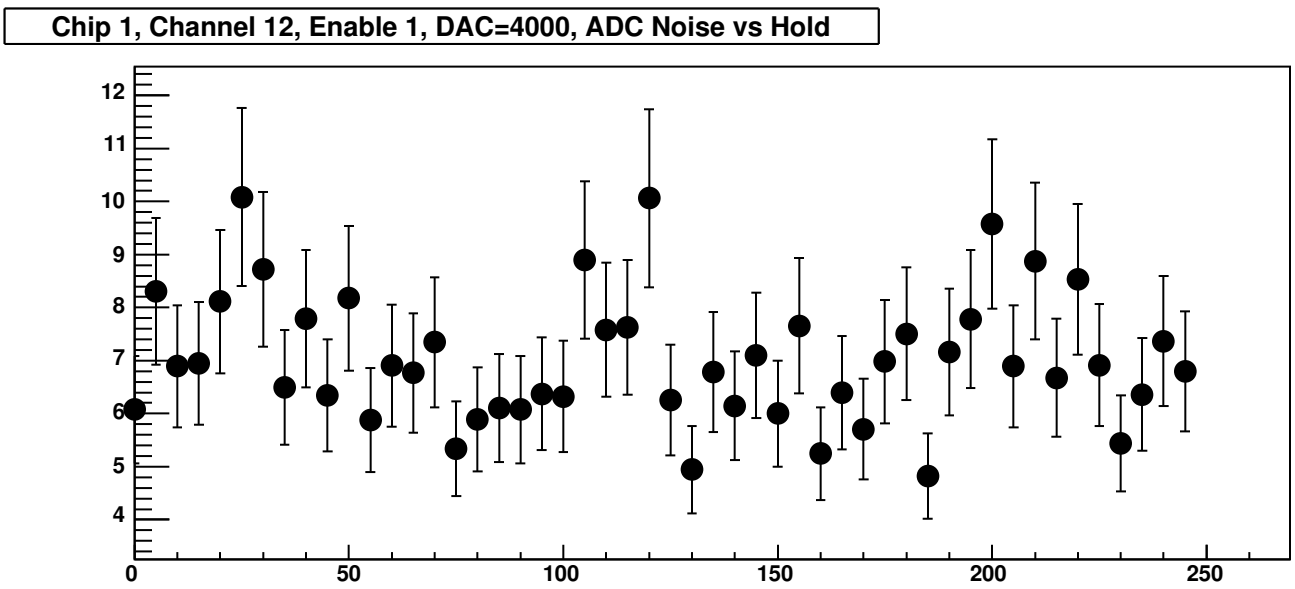
Chip 1, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold

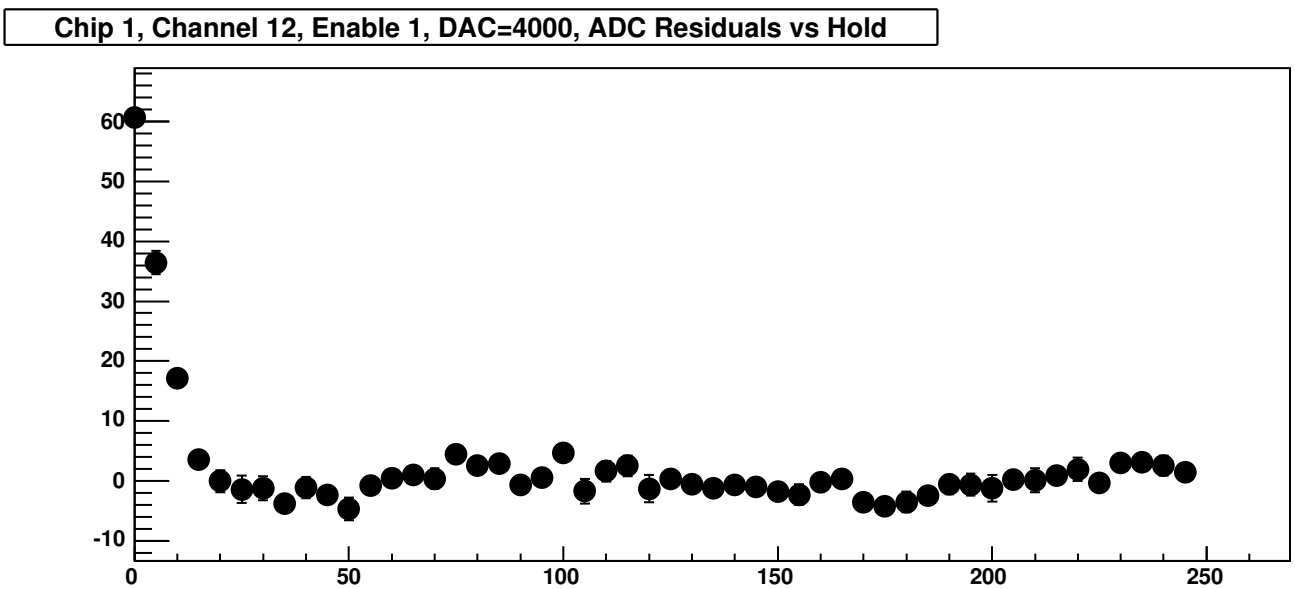


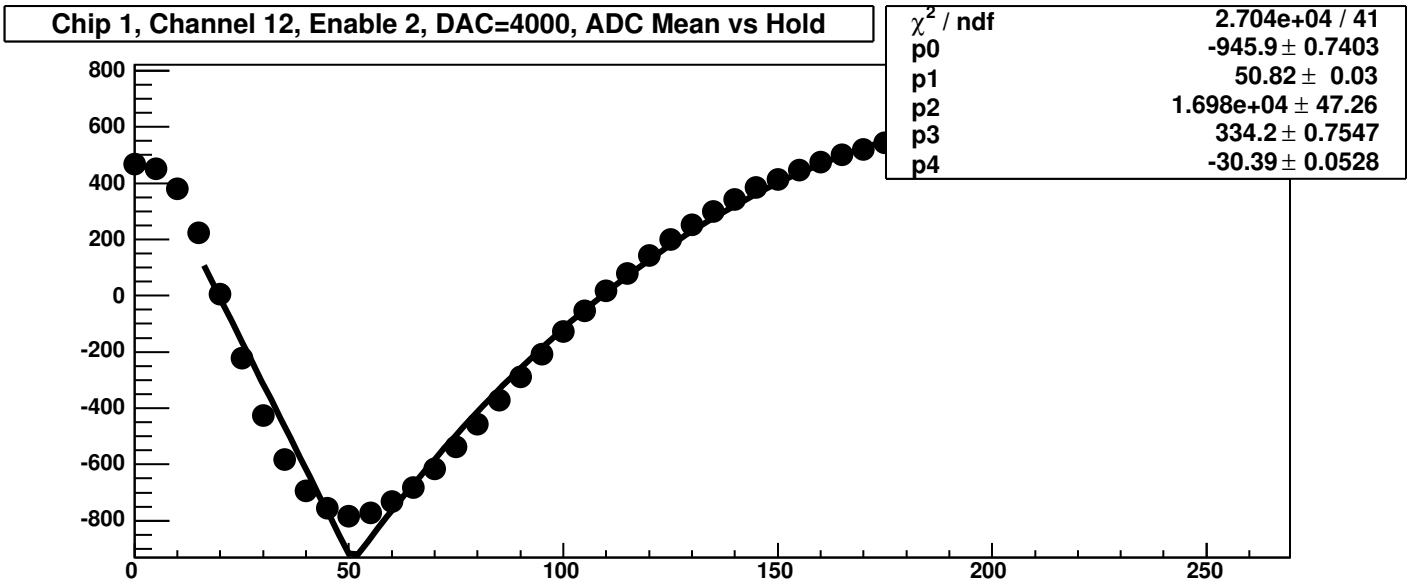
Chip 1, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

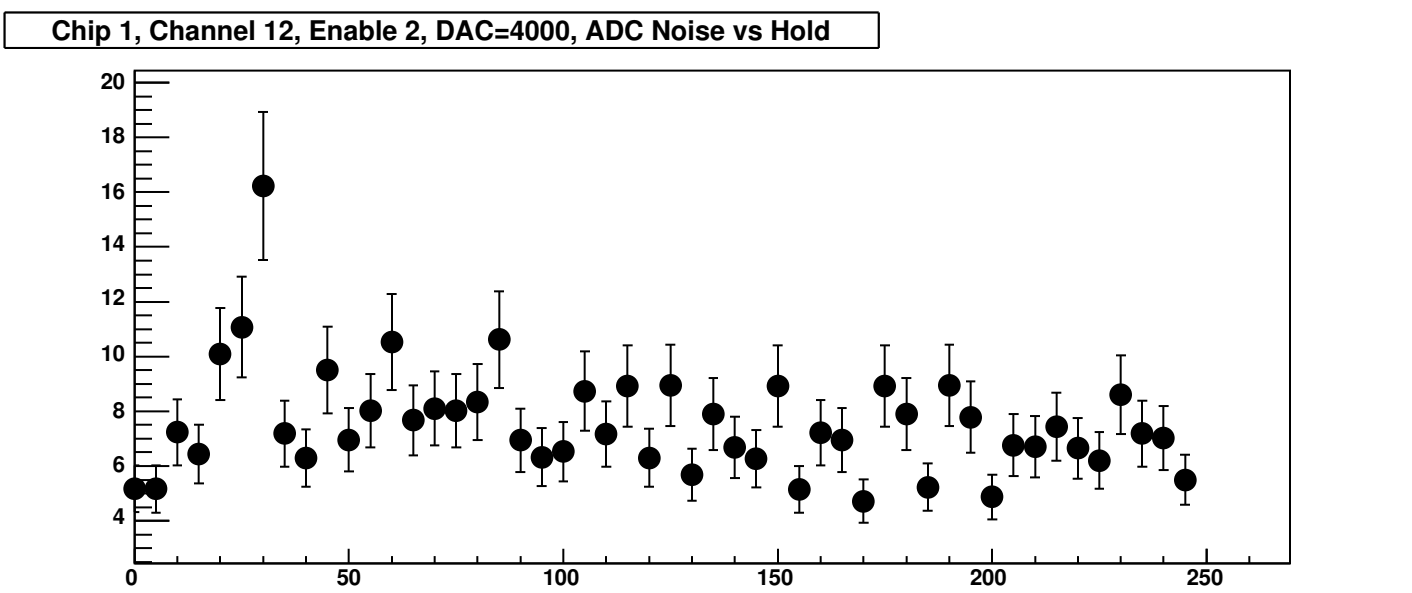


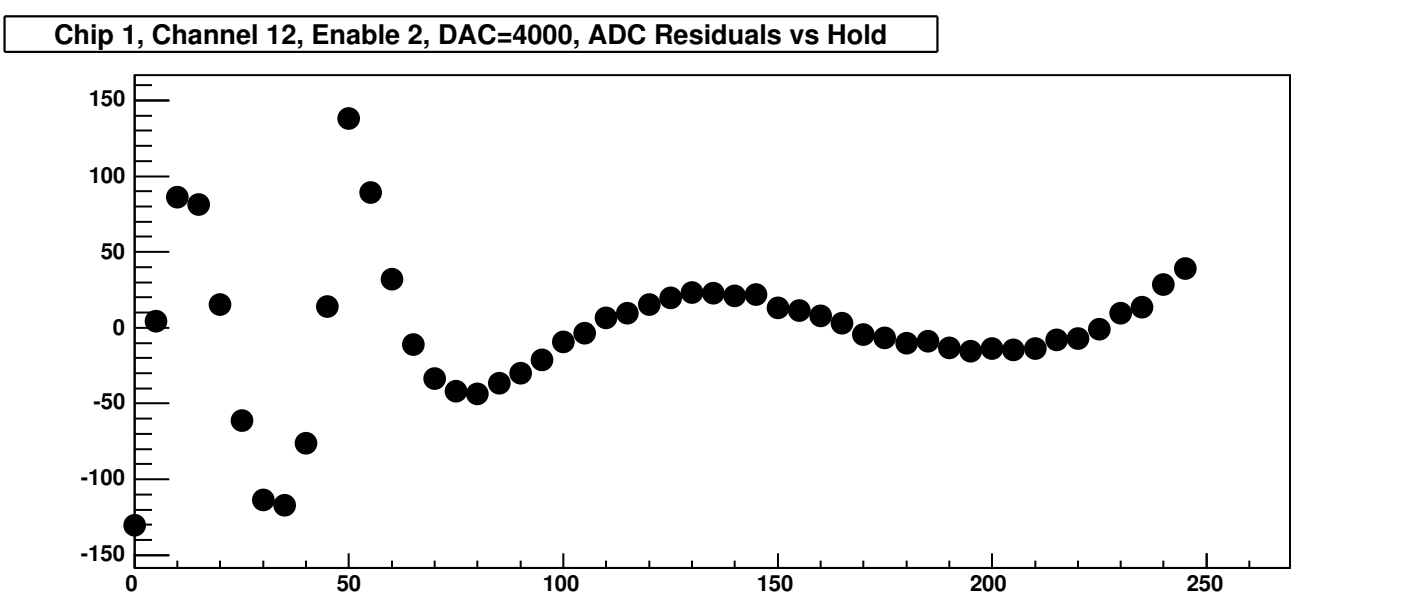

Chip 1, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 100.9 / 41
p0 383.6 ± 2.871
p1 -3.878 ± 0.8258
p2 181.8 ± 2.943
p3 30.92 ± 0.9435
p4 -0.1222 ± 0.008051
560
540
520
500
480
460
0
50
100
150
200
250


Chip 1, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

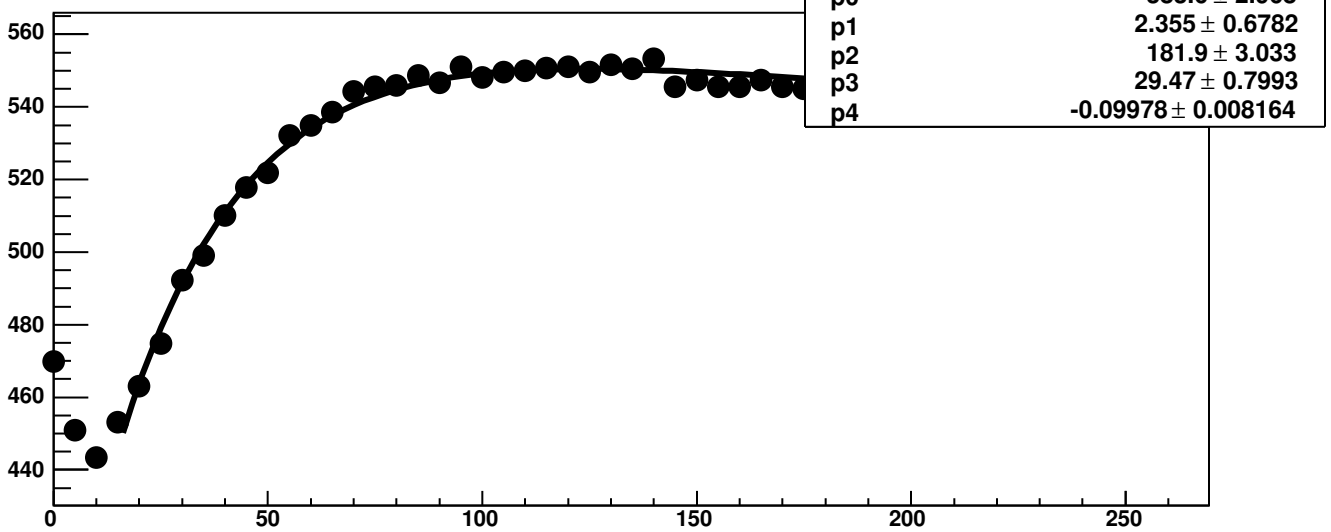

Chip 1, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold
60
50
40
30
20
10
0
-10
0
50
100
150
200
250


Chip 1, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.704e+04 / 41
p0 -945.9 ± 0.7403
p1 50.82 ± 0.03
p2 1.698e+04 ± 47.26
p3 334.2 ± 0.7547
p4 -30.39 ± 0.0528
800
600
400
200
0
-200
-400
-600
-800
0
50
100
150
200
250

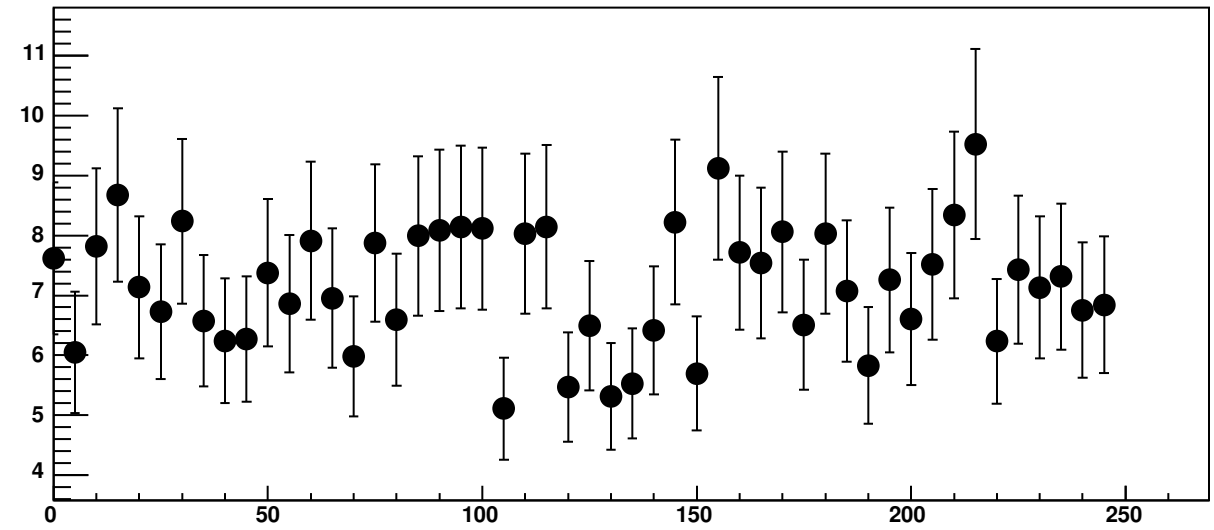

Chip 1, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold
20
18
16
14
12
10
8
6
4
0
50
100
150
200
250


Chip 1, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold
150
100
50
0
-50
-100
-150
0
50
100
150
200
250

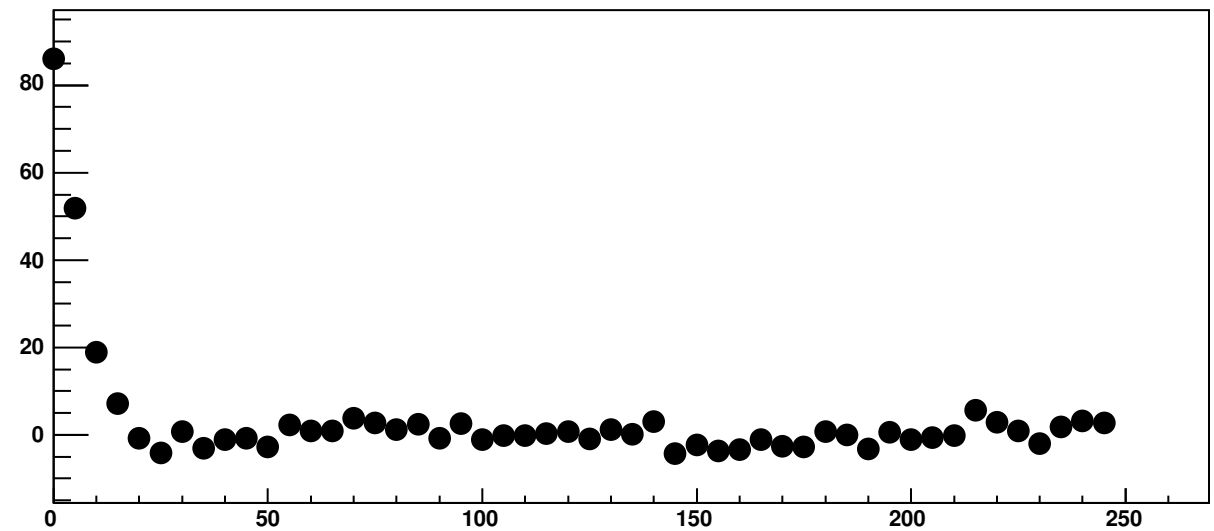
Chip 1, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold



Chip 1, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold

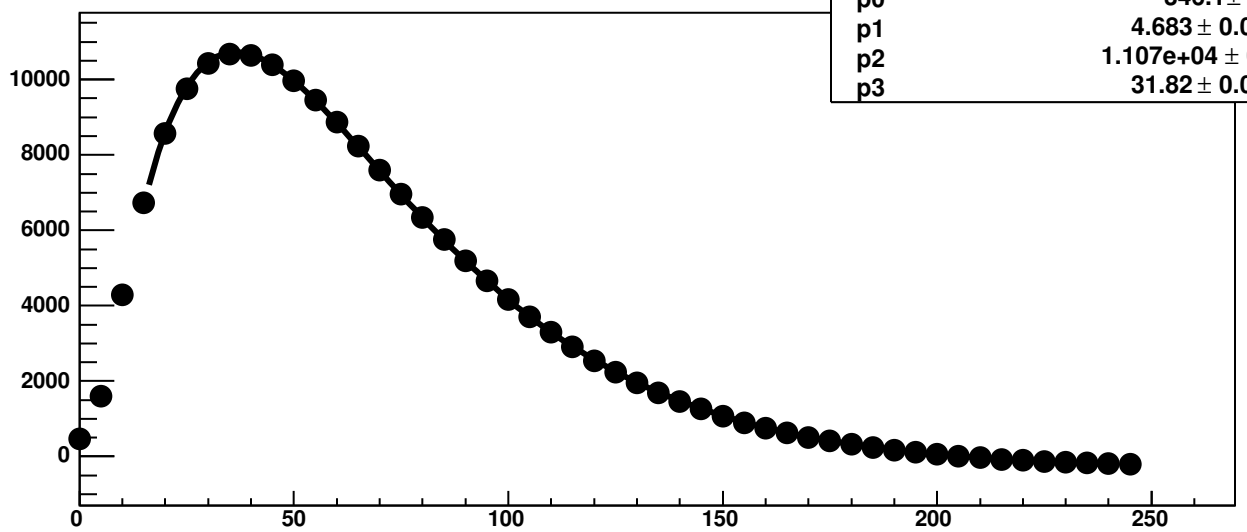


Chip 1, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

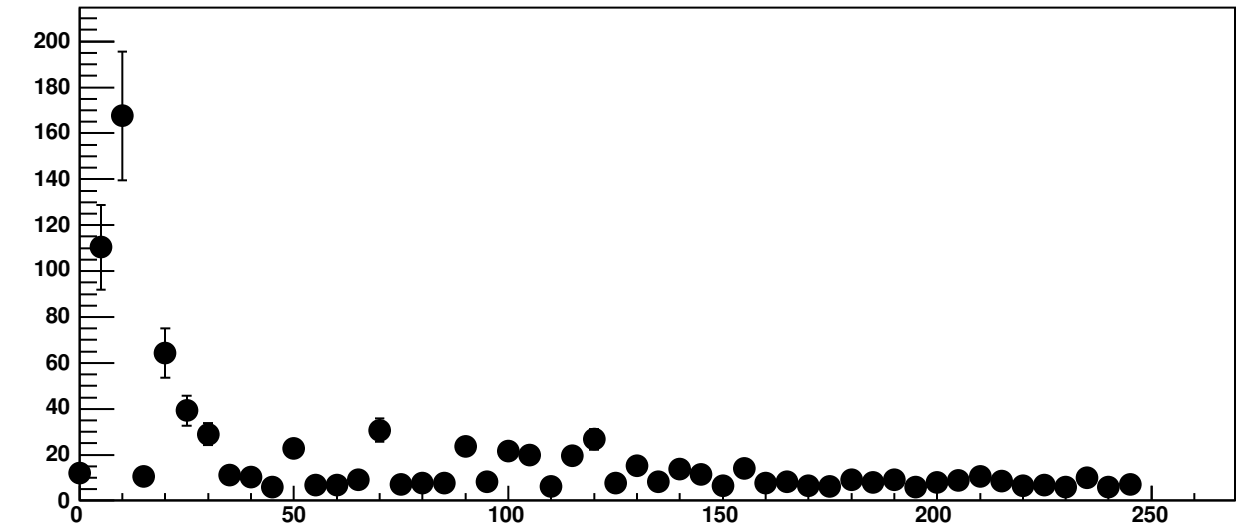


Chip 1, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold

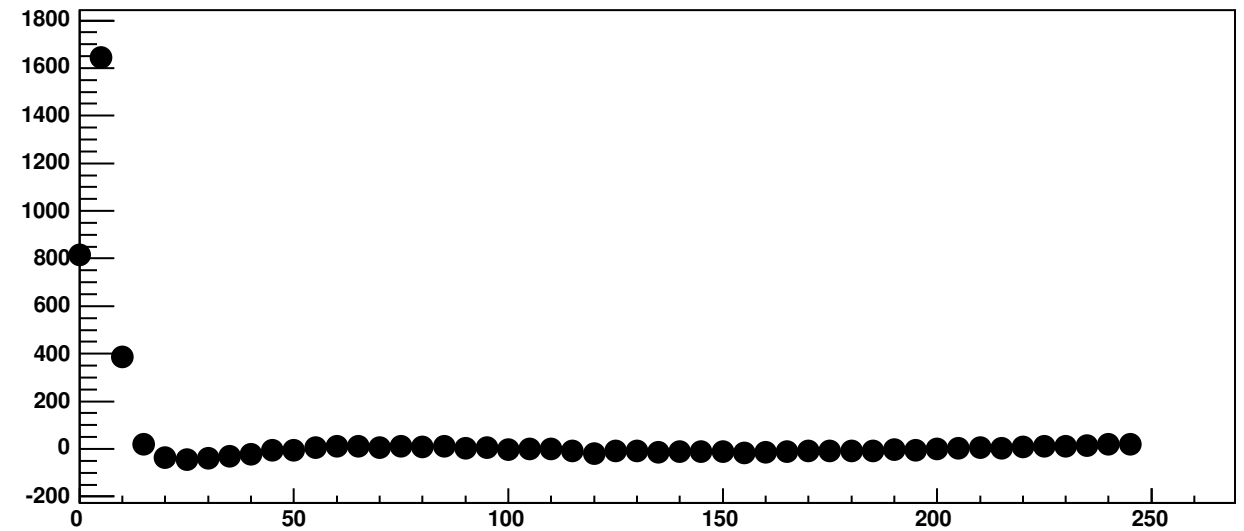
χ^2 / ndf	1372 / 42
p0	-346.1 ± 0.5687
p1	4.683 ± 0.006127
p2	1.107e+04 ± 0.9404
p3	31.82 ± 0.004825



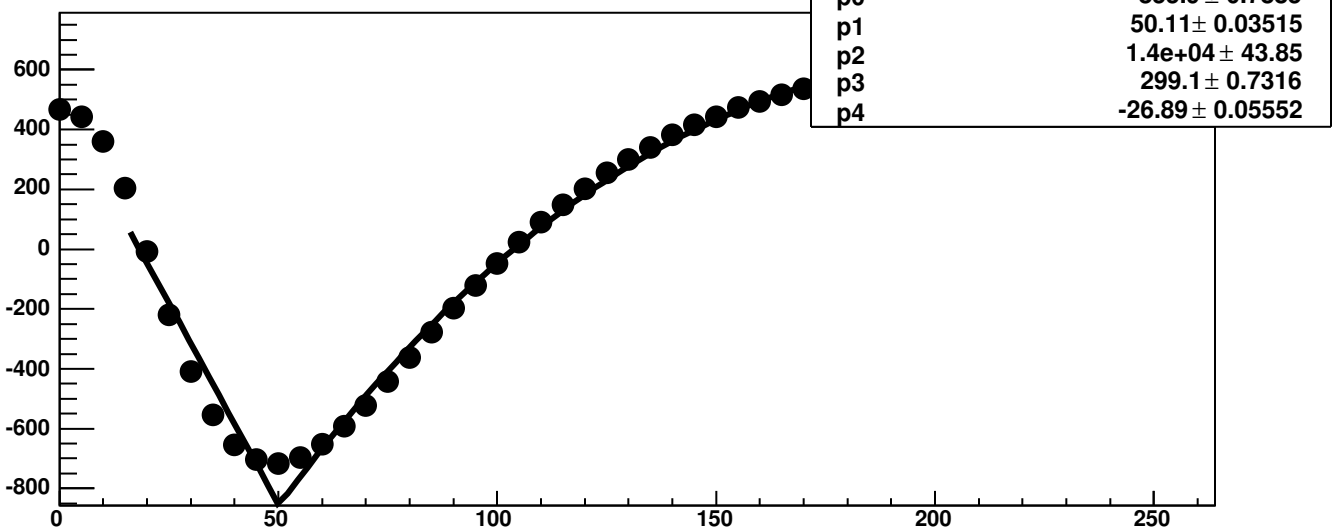
Chip 1, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold



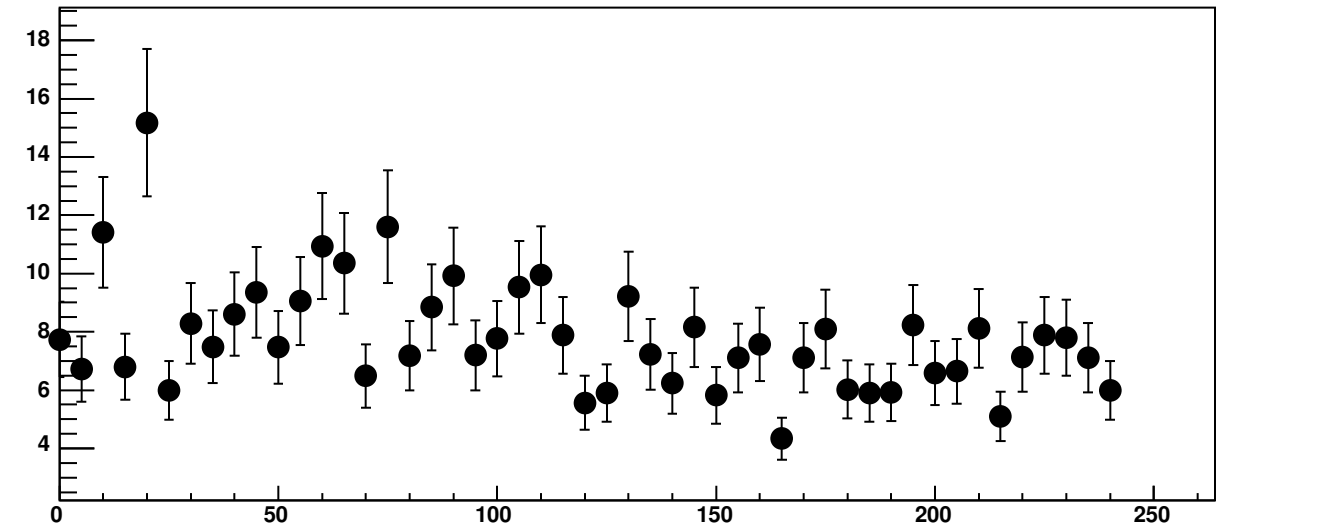
Chip 1, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold



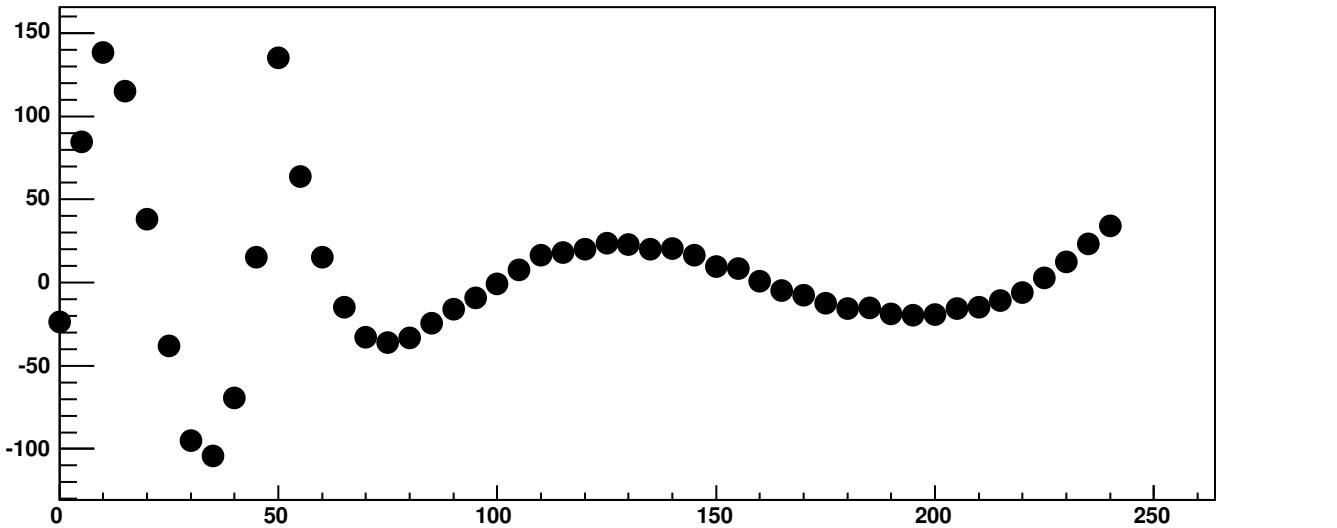
Chip 1, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold

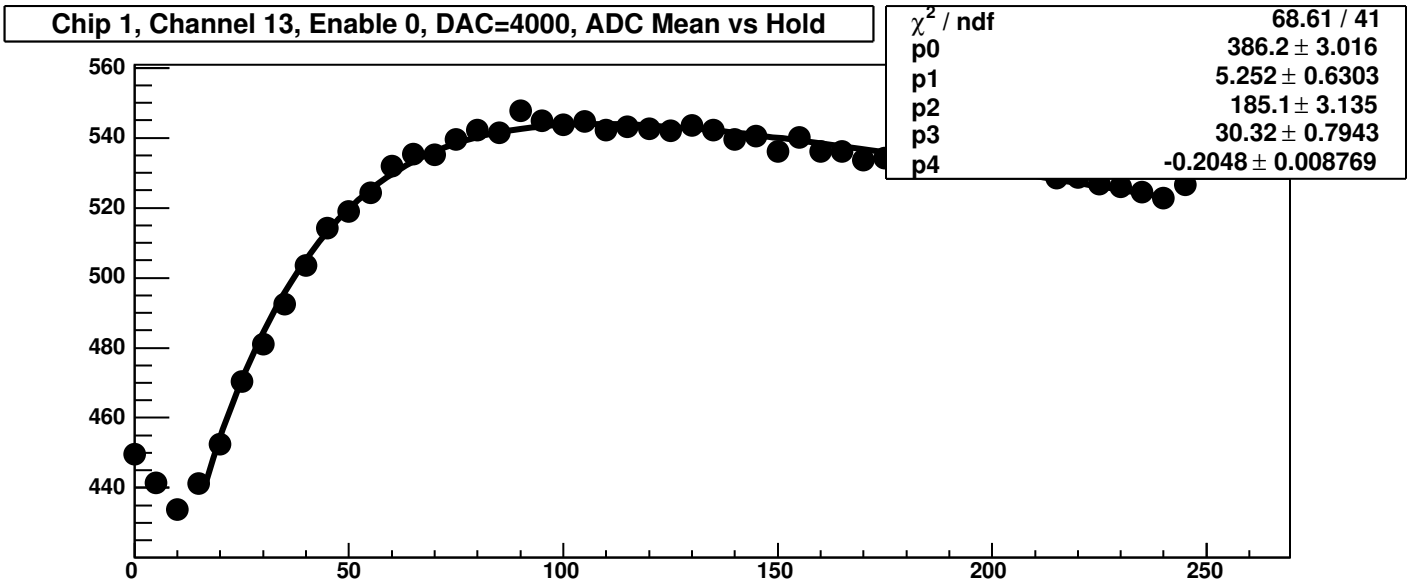


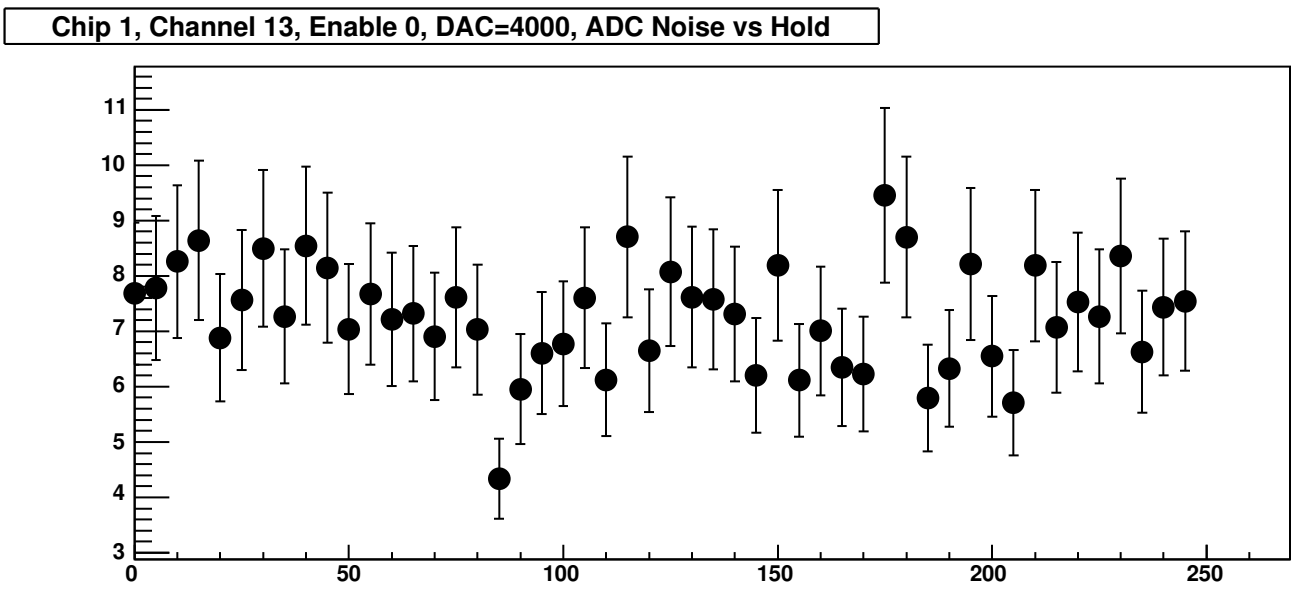
Chip 1, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold

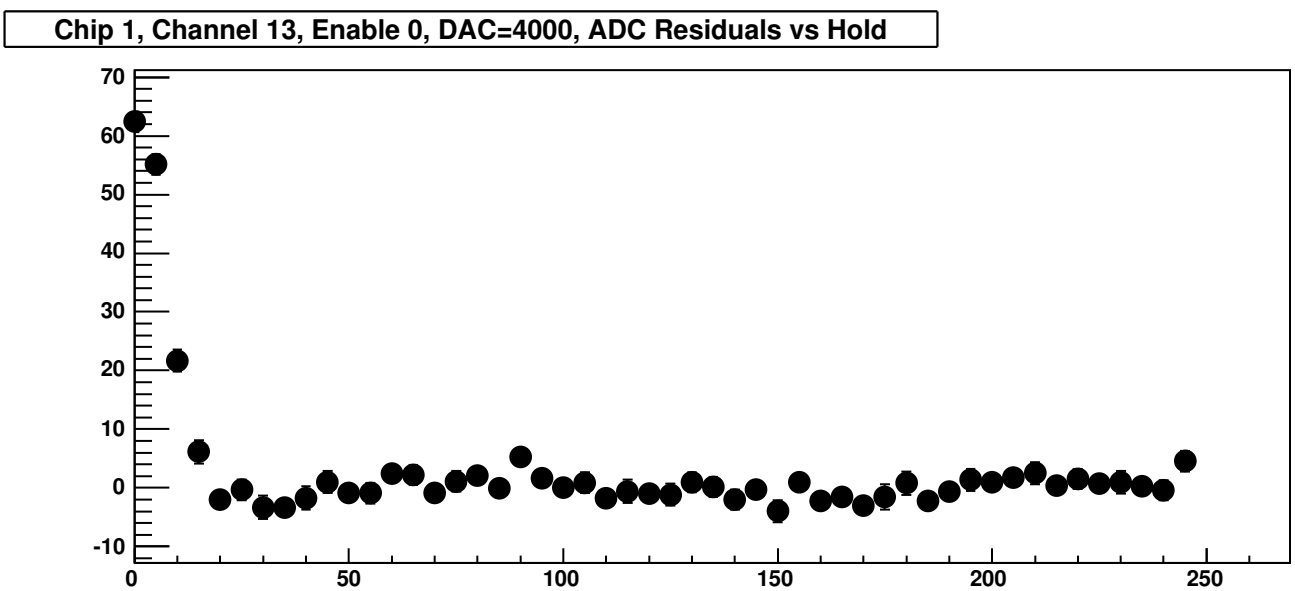


Chip 1, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold



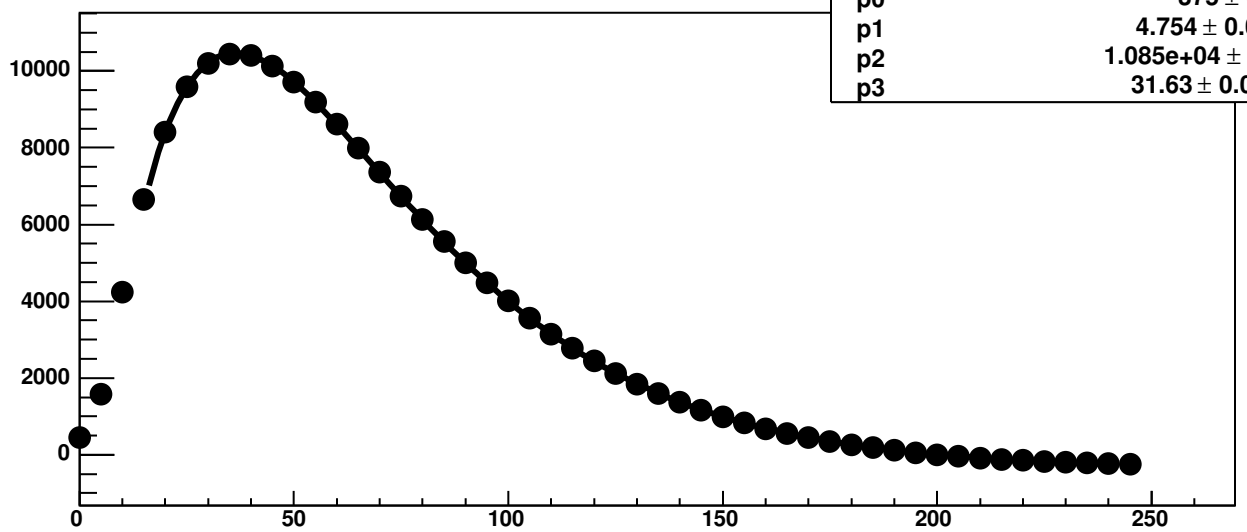

Chip 1, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 68.61 / 41
p0 386.2 ± 3.016
p1 5.252 ± 0.6303
p2 185.1 ± 3.135
p3 30.32 ± 0.7943
p4 -0.2048 ± 0.008769
560
540
520
500
480
460
440
0
50
100
150
200
250


Chip 1, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

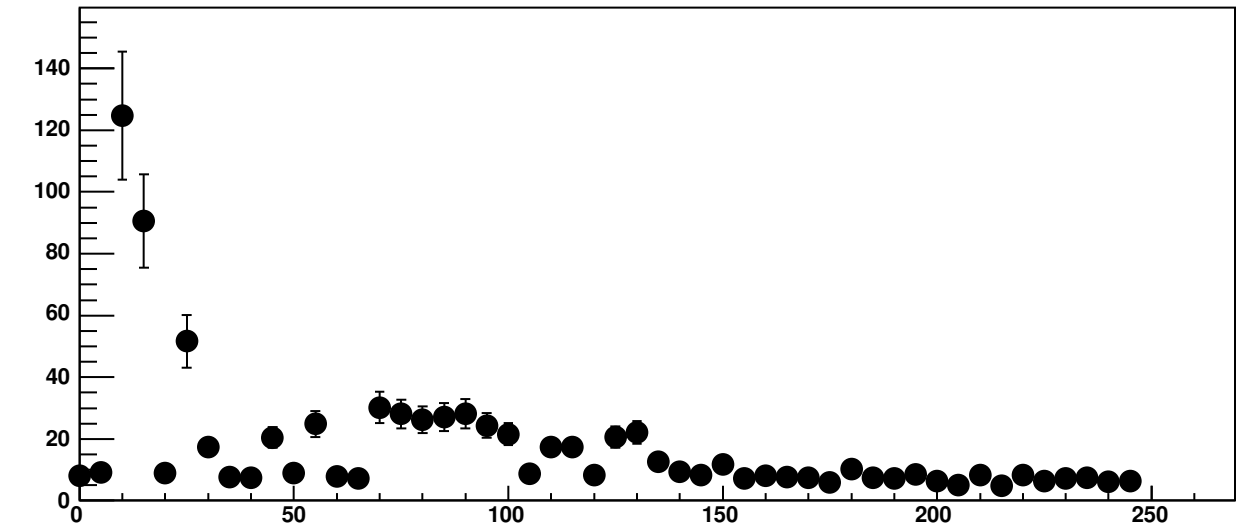

Chip 1, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

Chip 1, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold

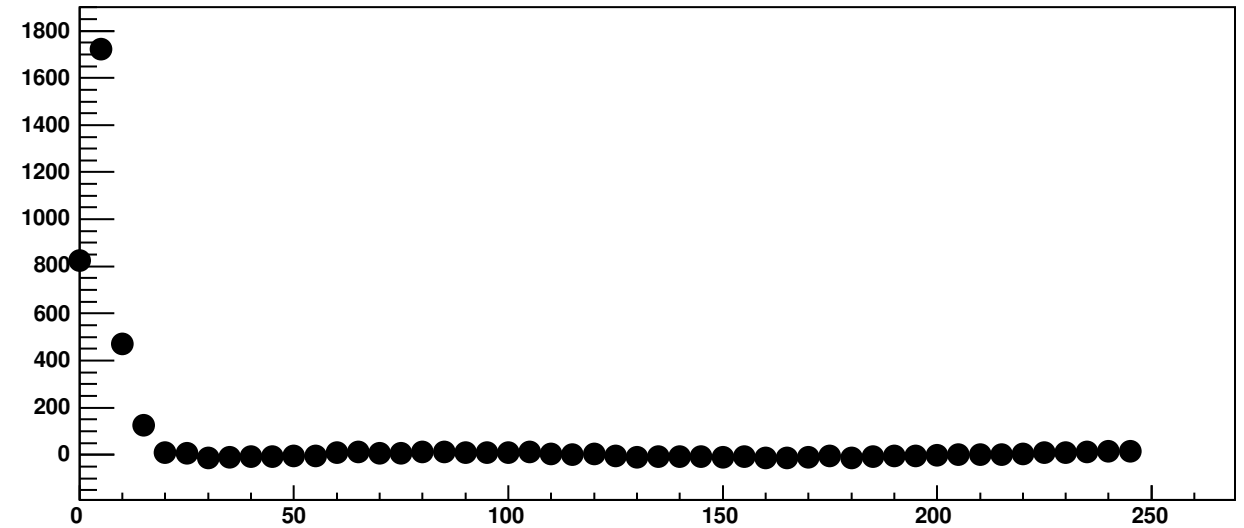
χ^2 / ndf	816.9 / 42
p0	-375 ± 0.5705
p1	4.754 ± 0.008411
p2	1.085e+04 ± 0.9323
p3	31.63 ± 0.006195



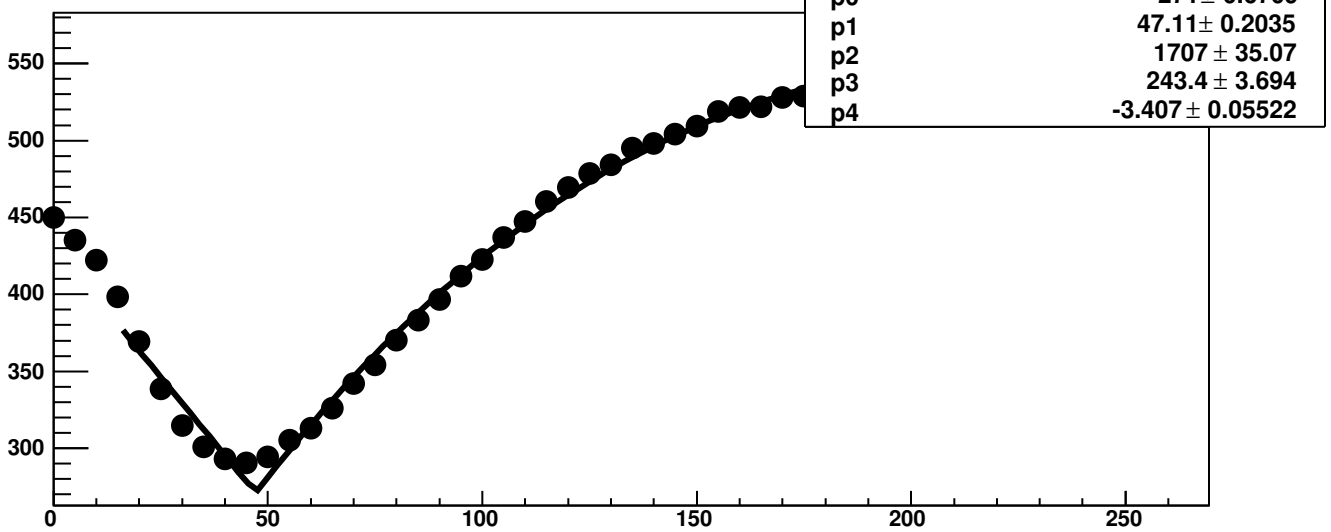
Chip 1, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold



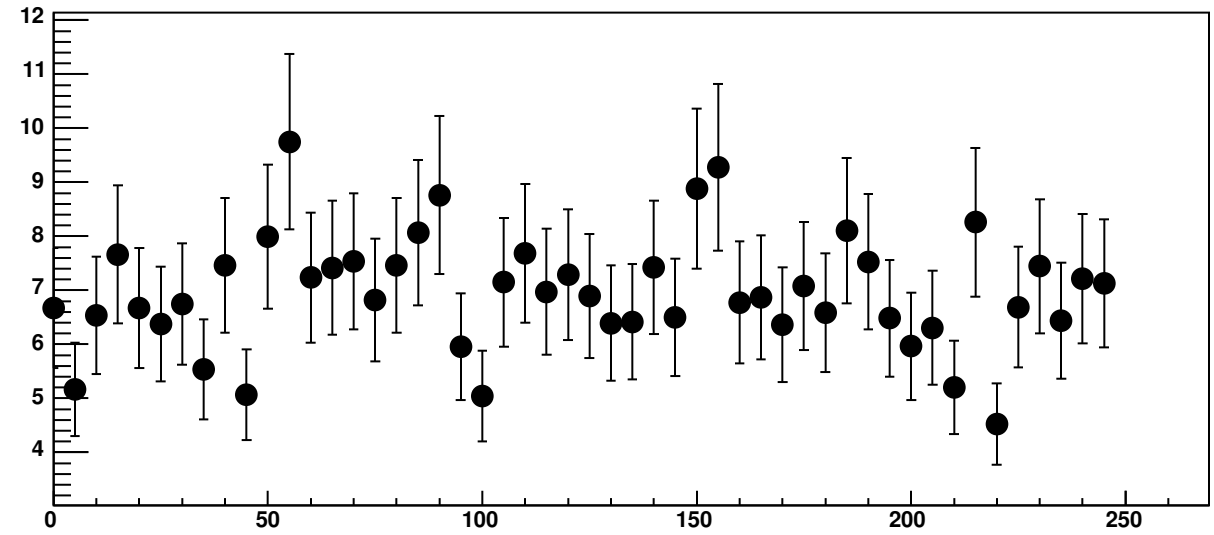
Chip 1, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold



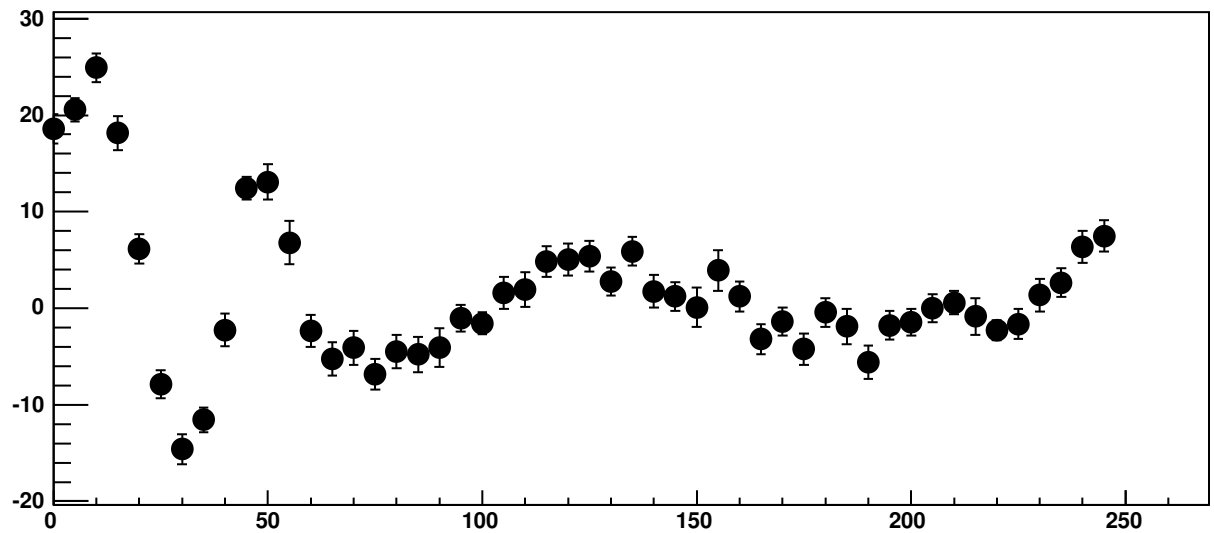
Chip 1, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold



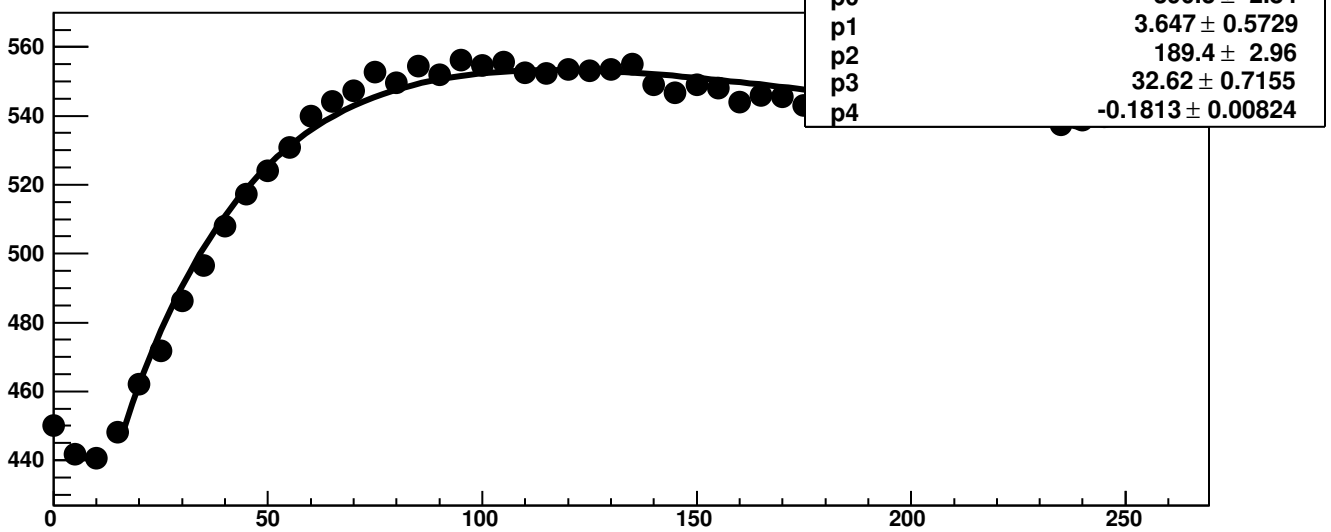
Chip 1, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



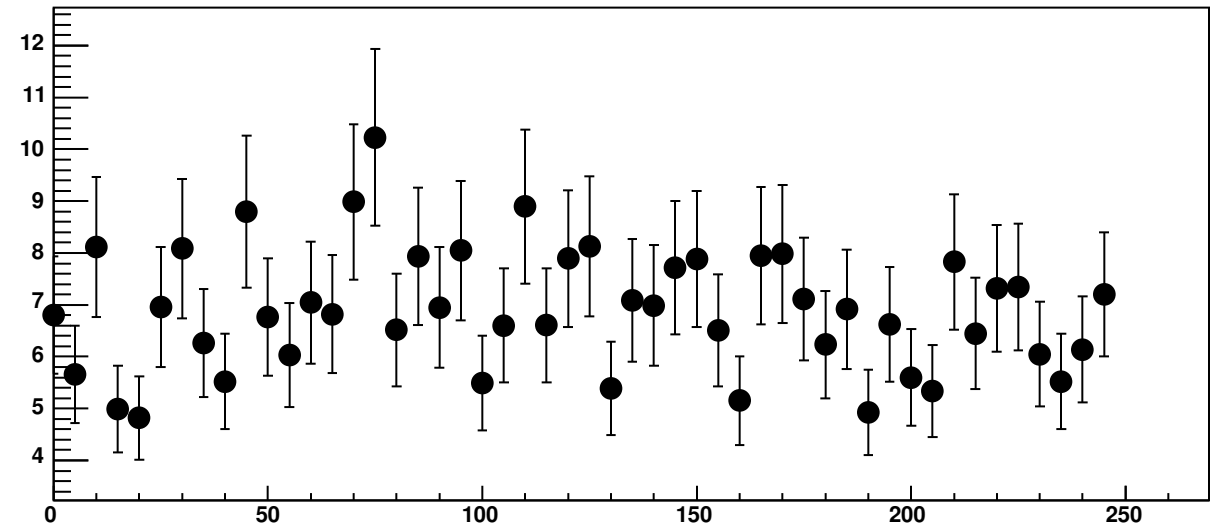
Chip 1, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold



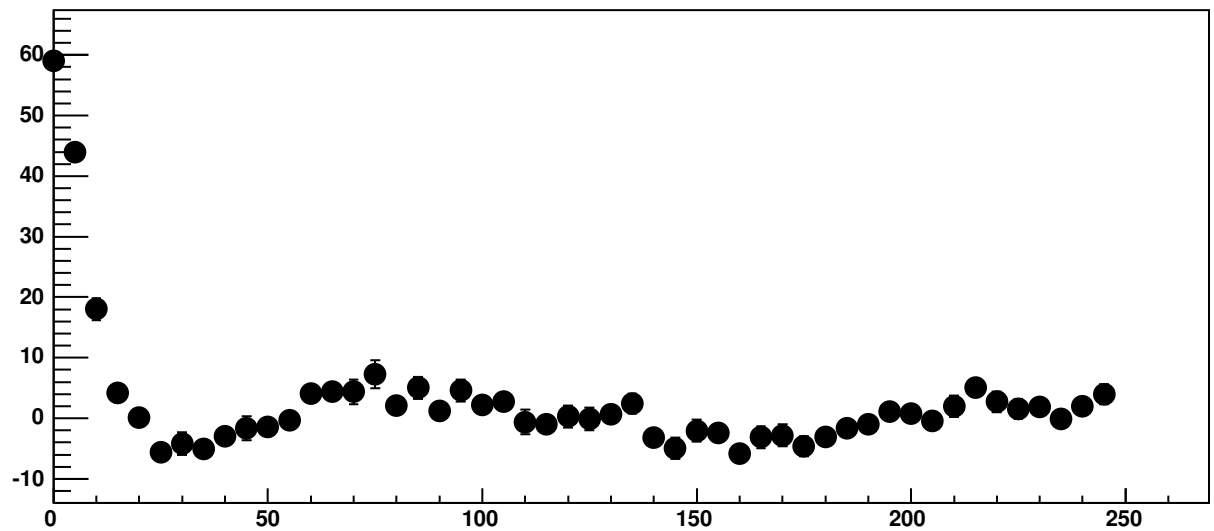
Chip 1, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

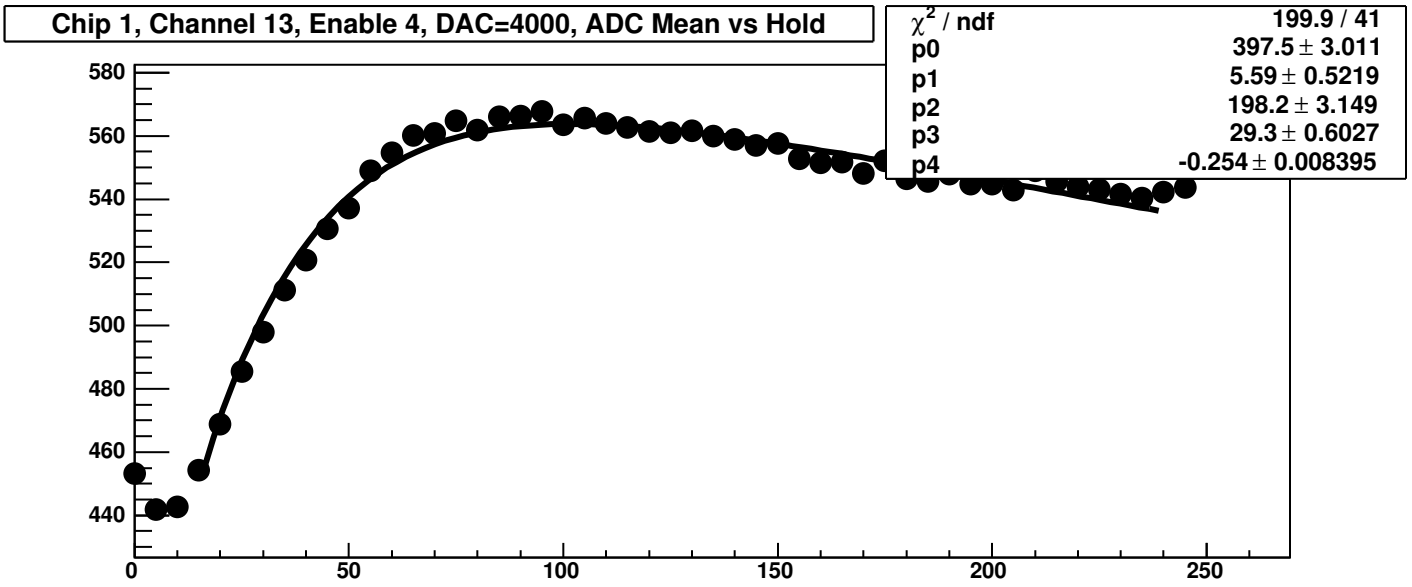


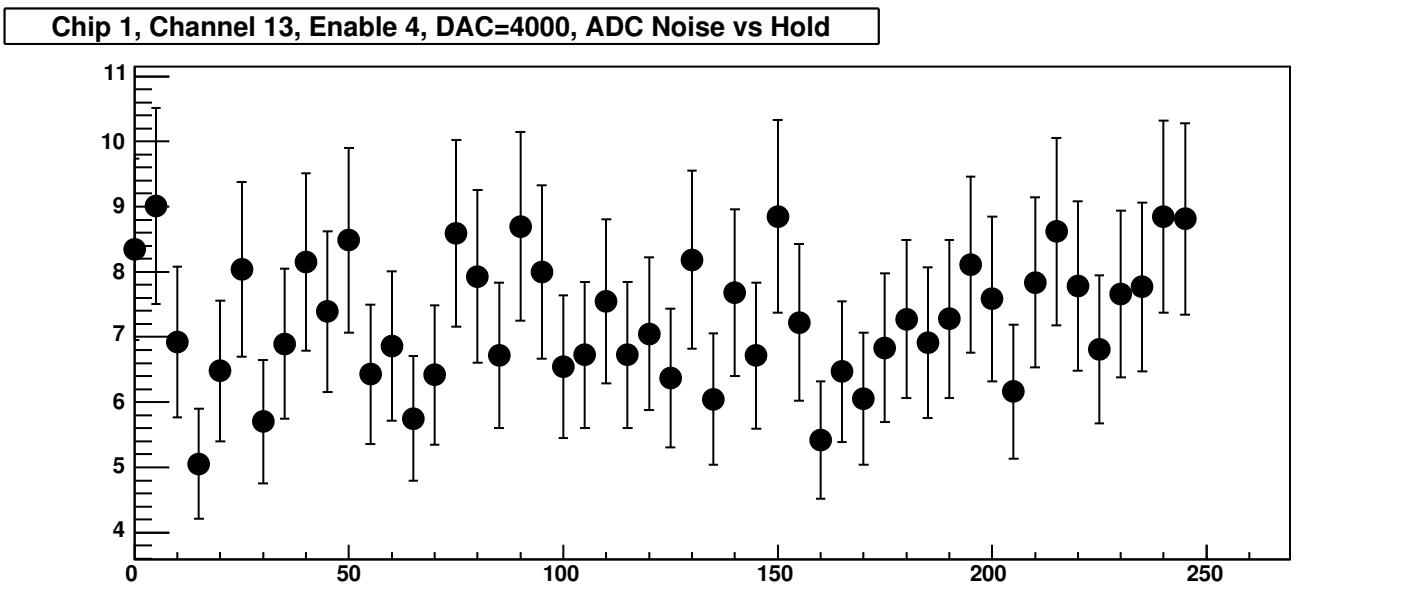
Chip 1, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold

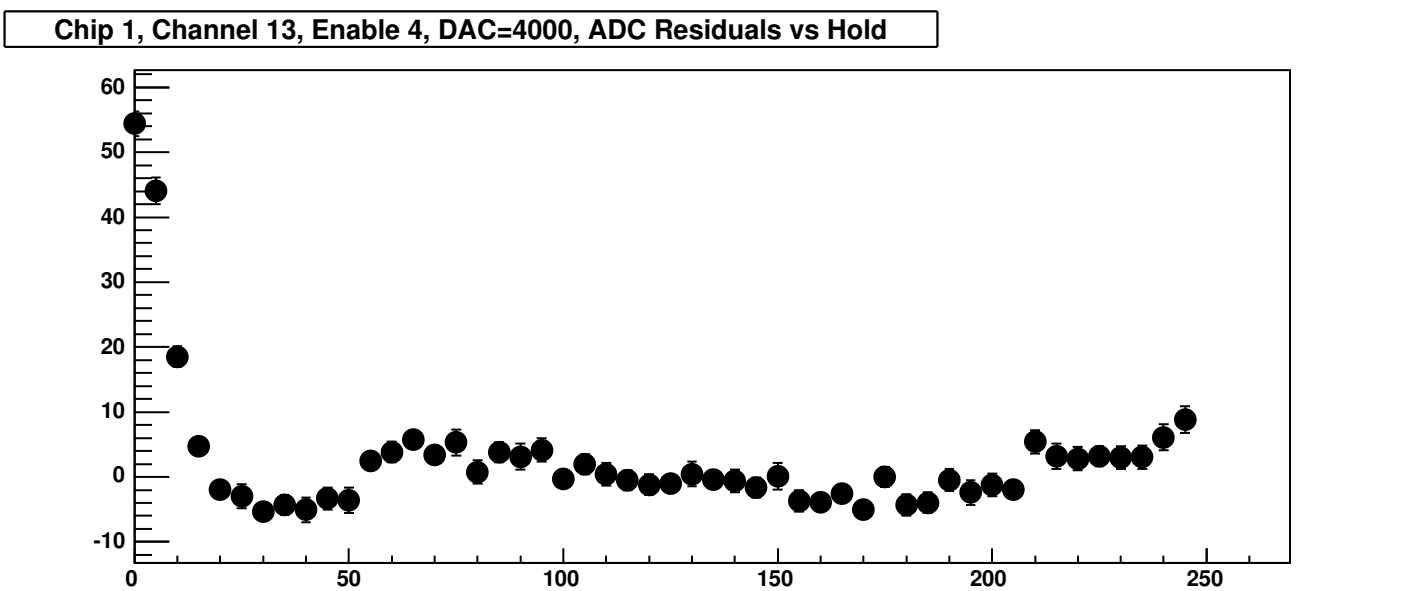


Chip 1, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

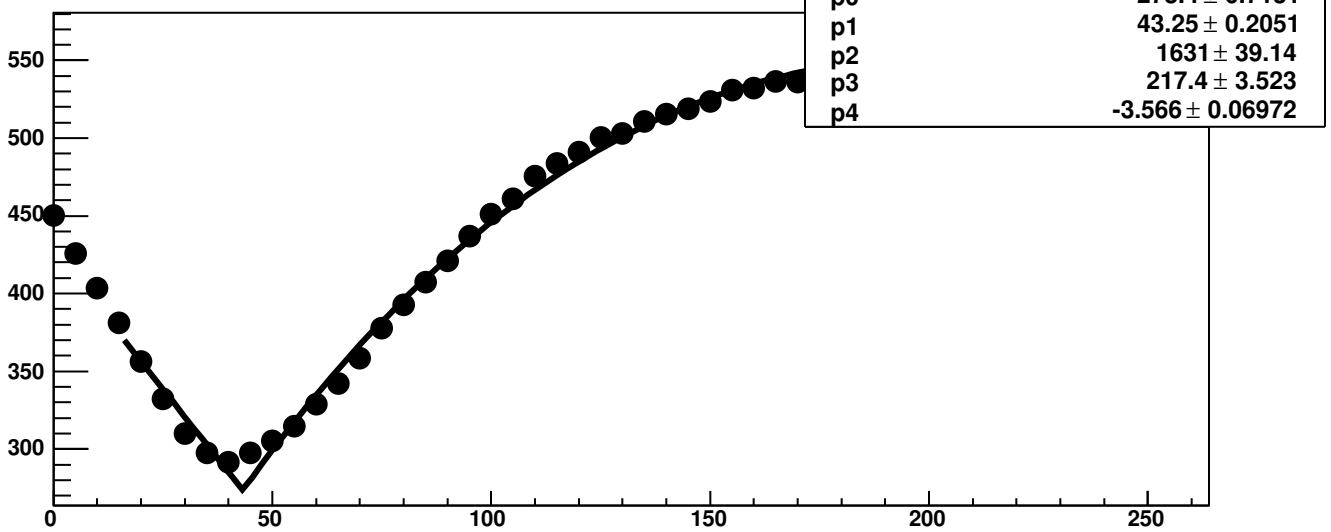



Chip 1, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 199.9 / 41
p0 397.5 ± 3.011
p1 5.59 ± 0.5219
p2 198.2 ± 3.149
p3 29.3 ± 0.6027
p4 -0.254 ± 0.008395
580
560
540
520
500
480
460
440
0
50
100
150
200
250

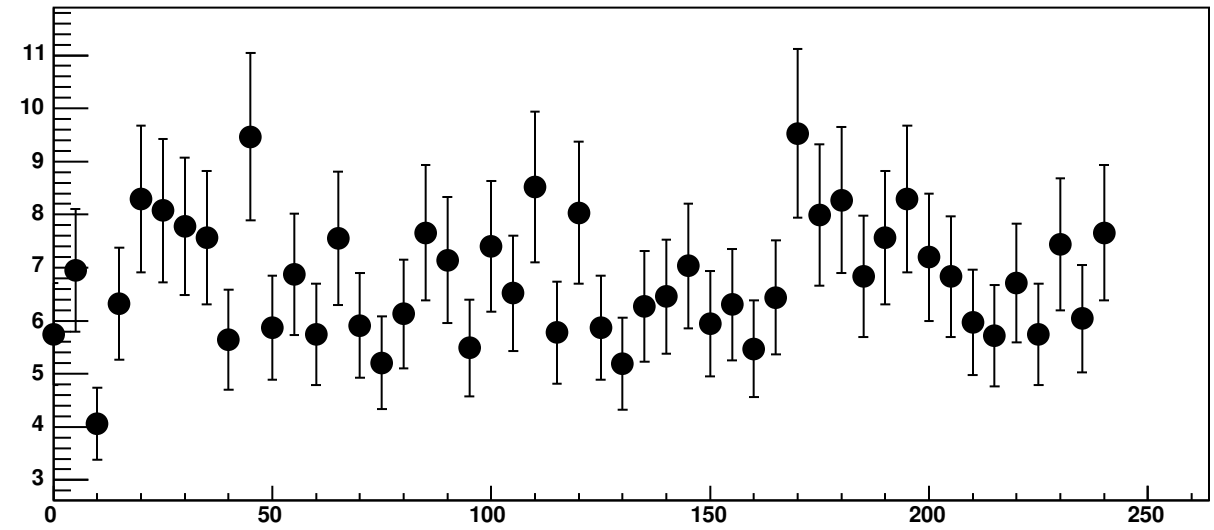

Chip 1, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 1, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

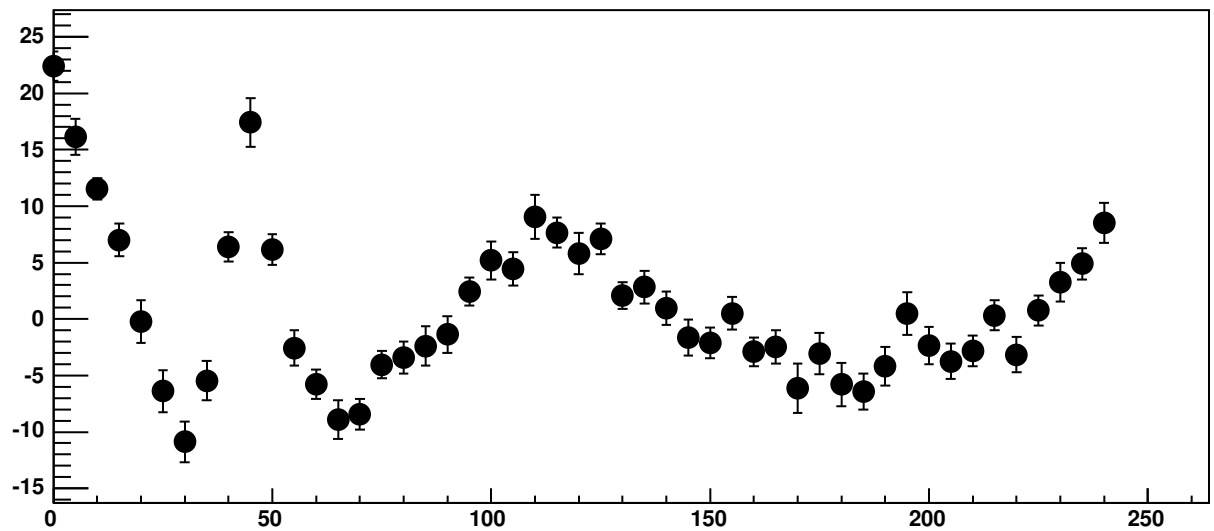
Chip 1, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

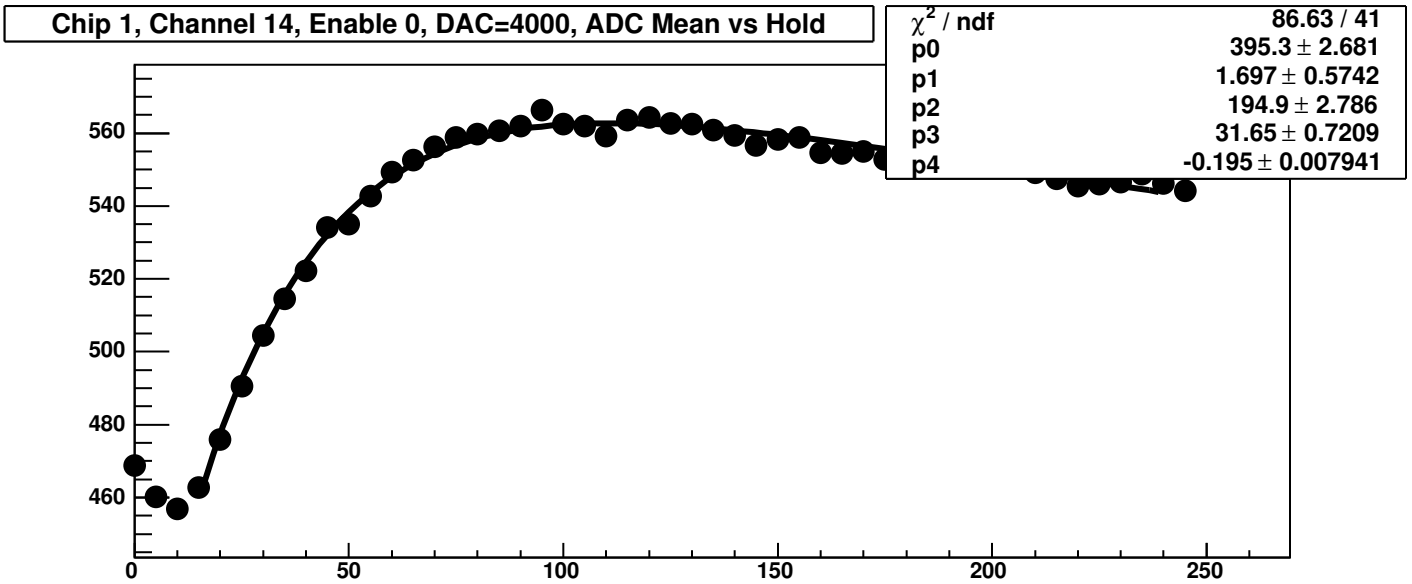


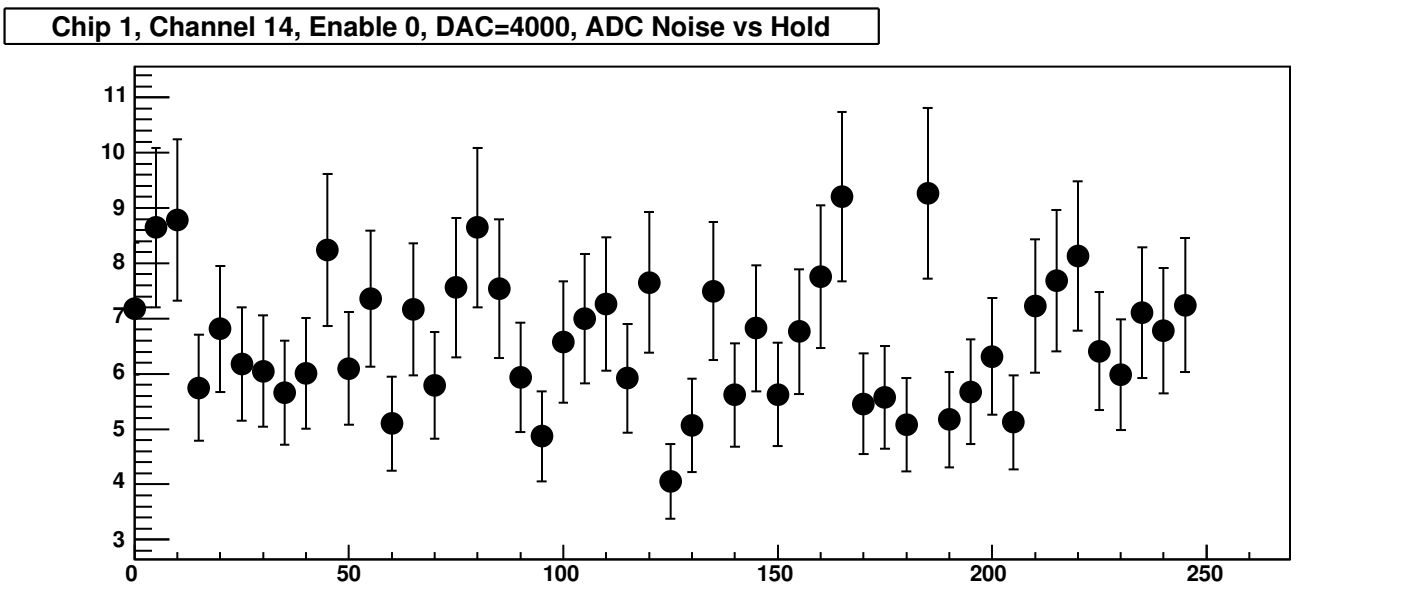
Chip 1, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold

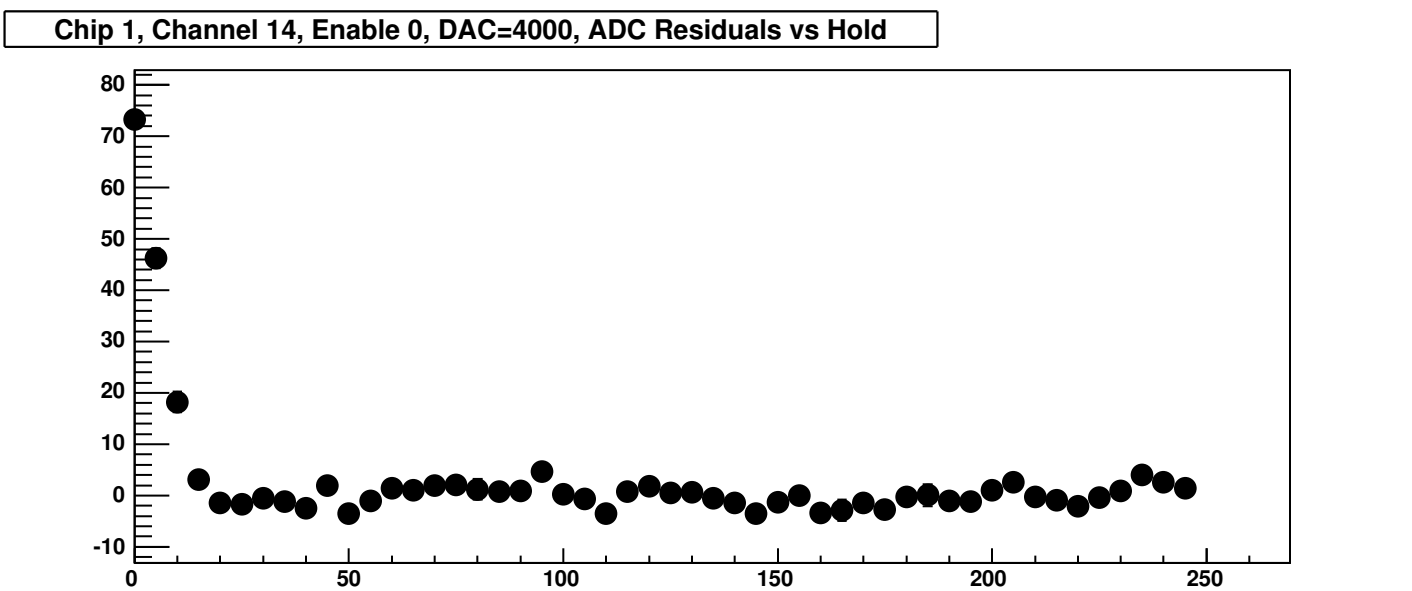


Chip 1, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold

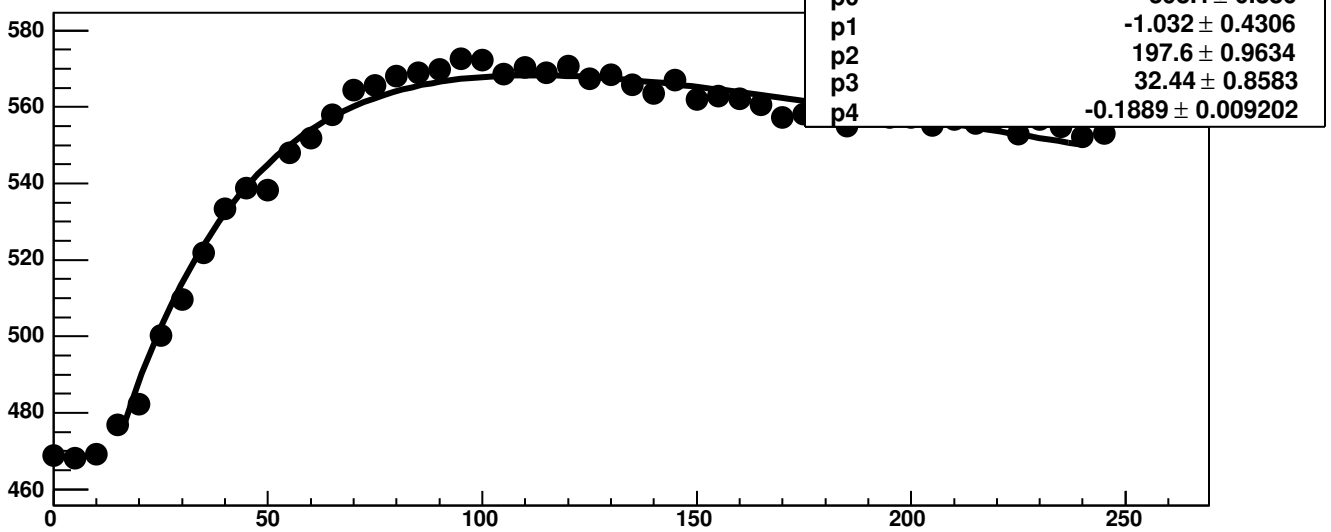



Chip 1, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 86.63 / 41
p0 395.3 ± 2.681
p1 1.697 ± 0.5742
p2 194.9 ± 2.786
p3 31.65 ± 0.7209
p4 -0.195 ± 0.007941
560
540
520
500
480
460
0
50
100
150
200
250

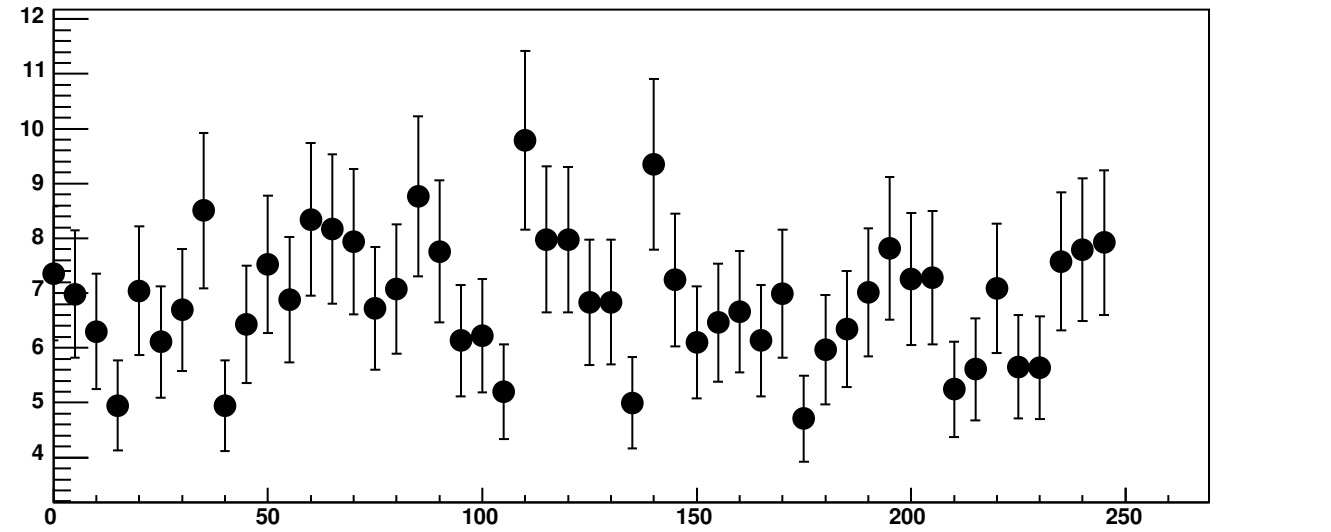

Chip 1, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 1, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold
80
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

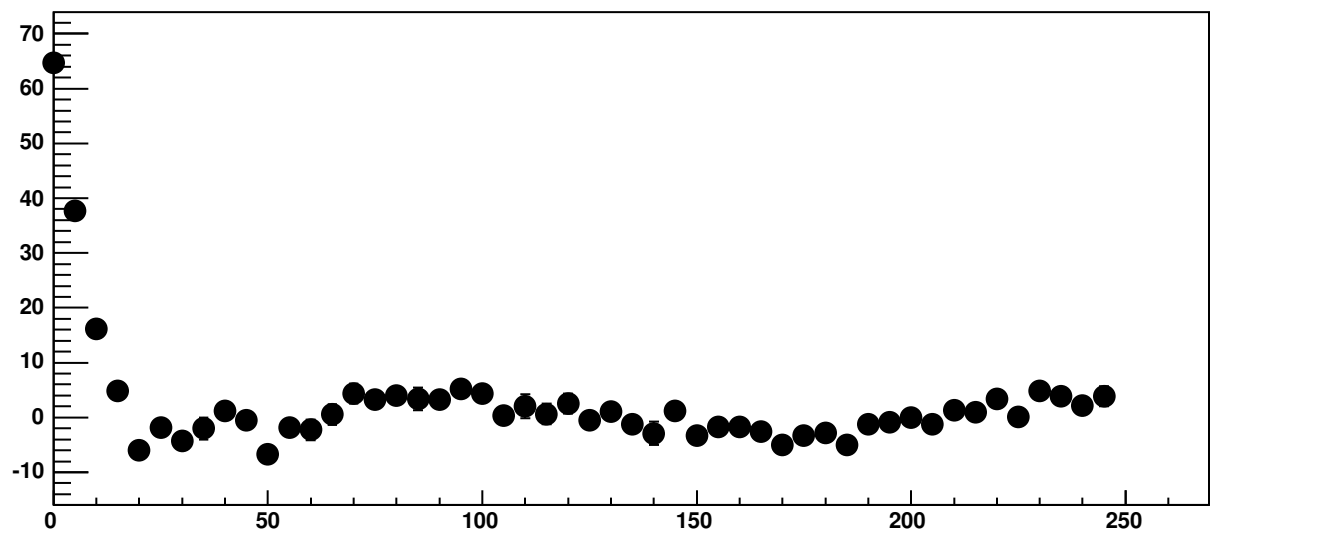
Chip 1, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold

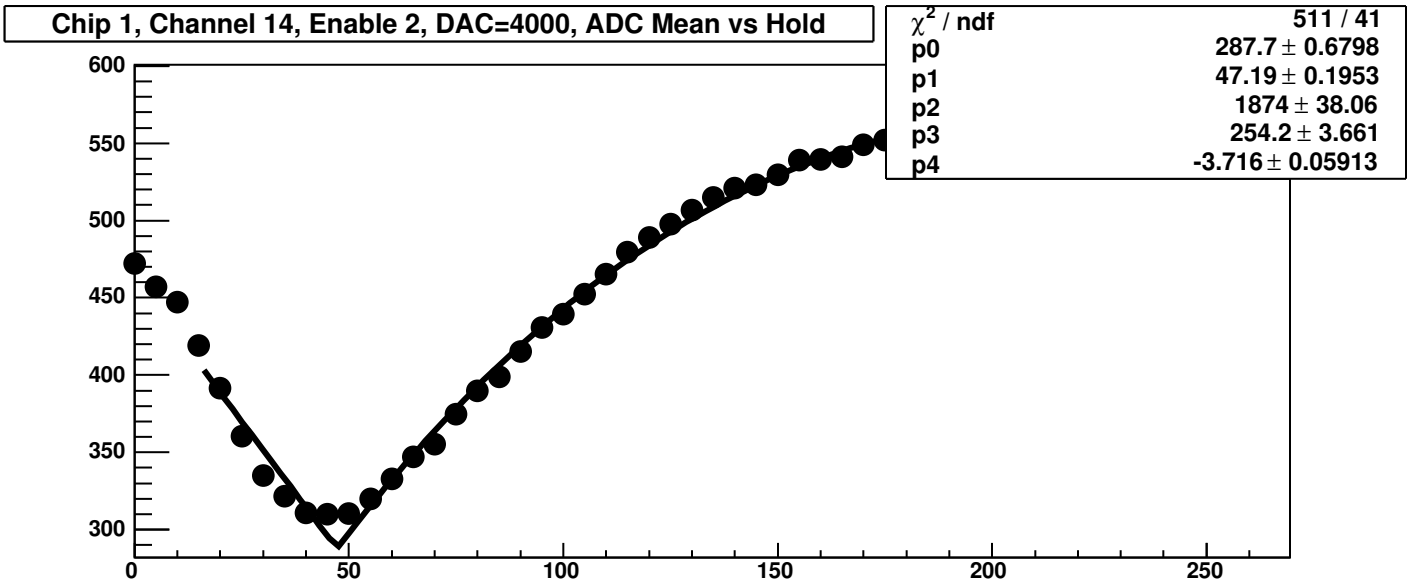


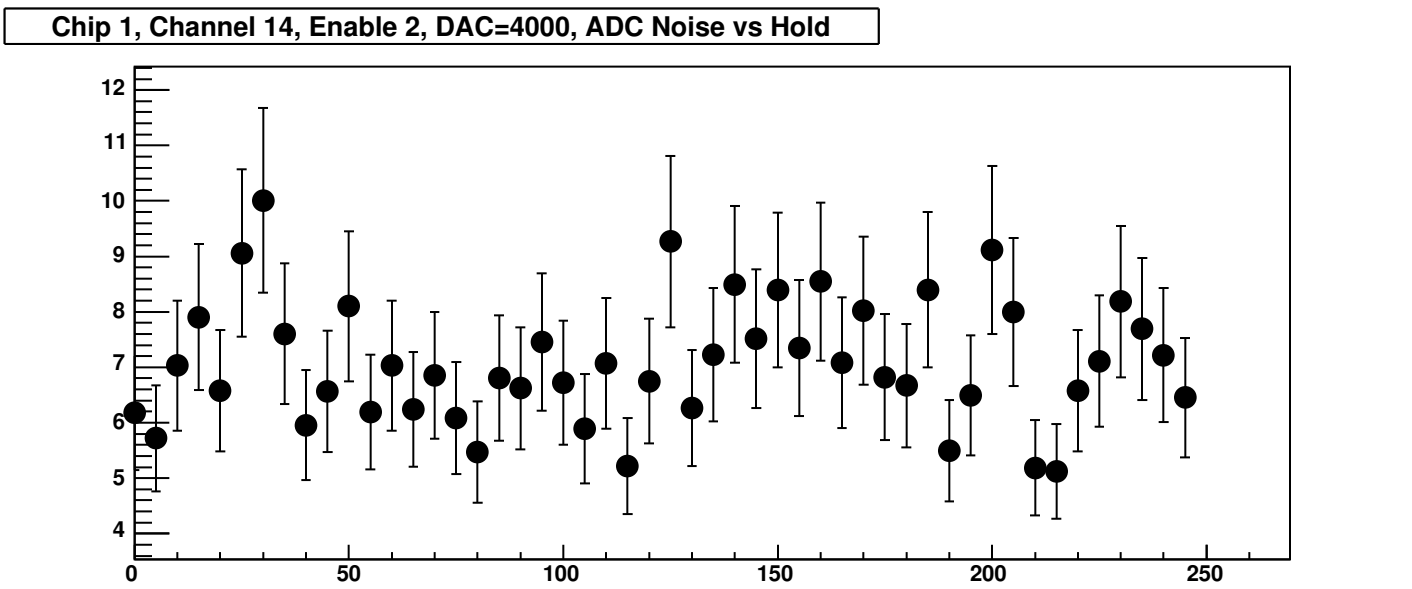
Chip 1, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold

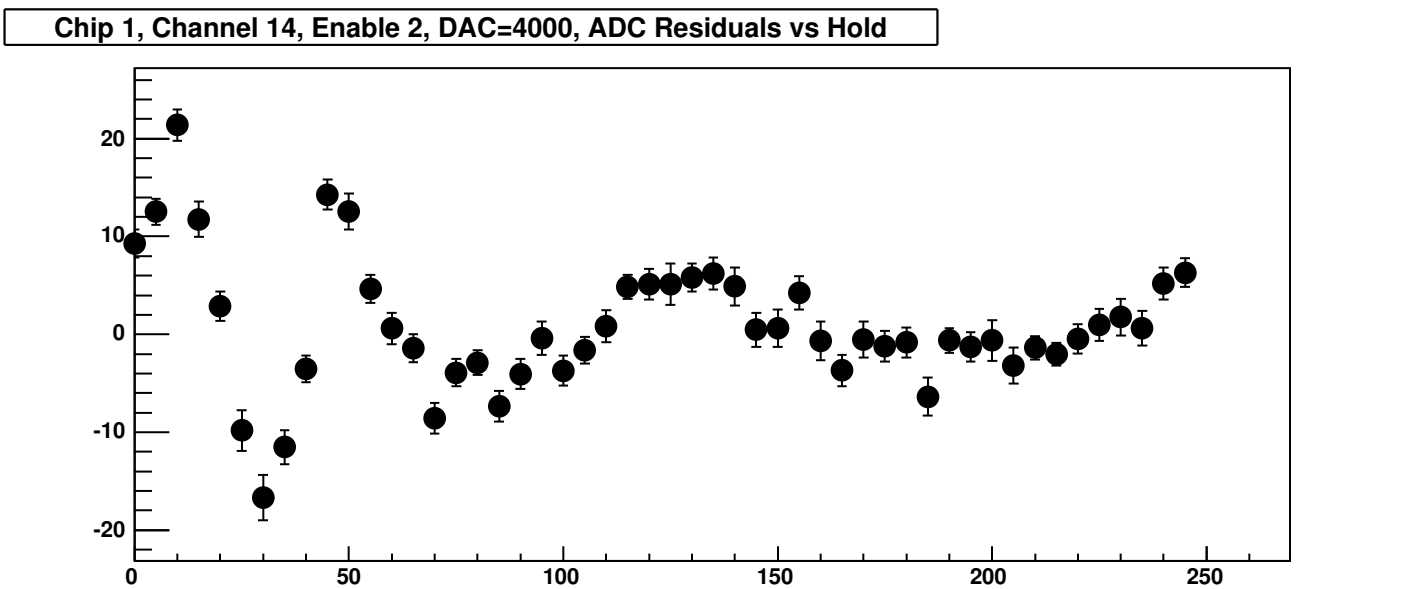


Chip 1, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold

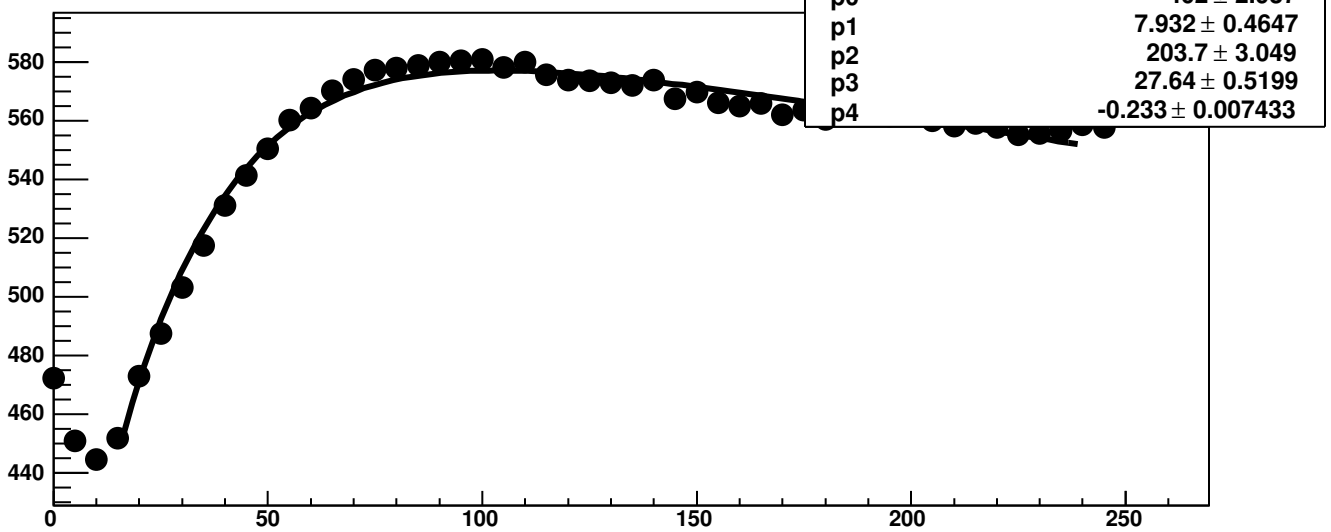



Chip 1, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 511 / 41
p0 287.7 ± 0.6798
p1 47.19 ± 0.1953
p2 1874 ± 38.06
p3 254.2 ± 3.661
p4 -3.716 ± 0.05913
600
550
500
450
400
350
300
0
50
100
150
200
250

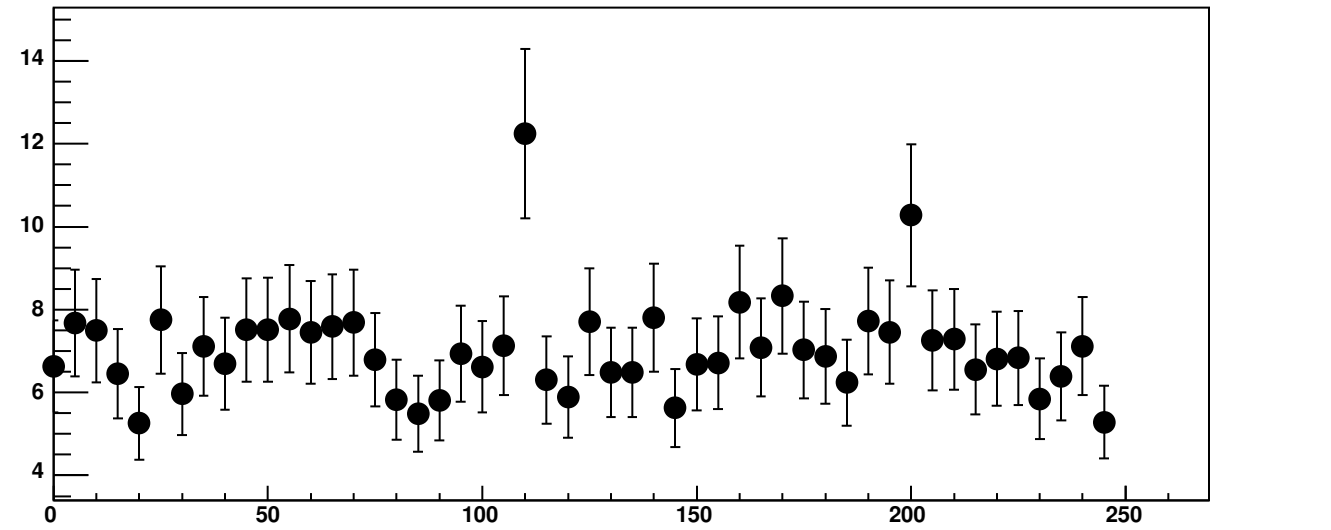

Chip 1, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 1, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
0
50
100
150
200
250

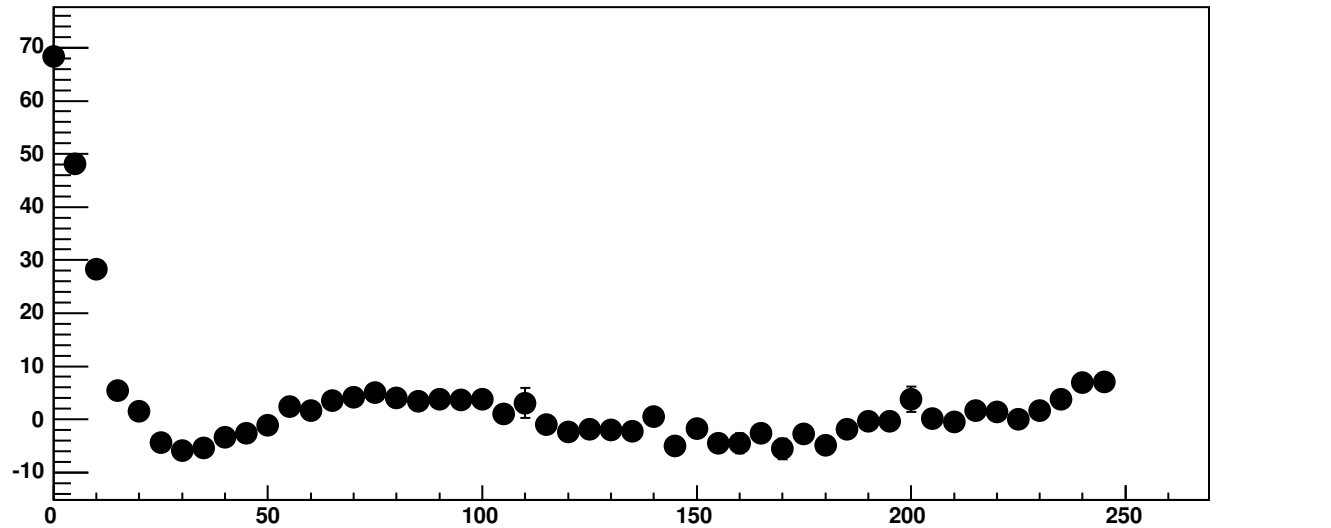
Chip 1, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

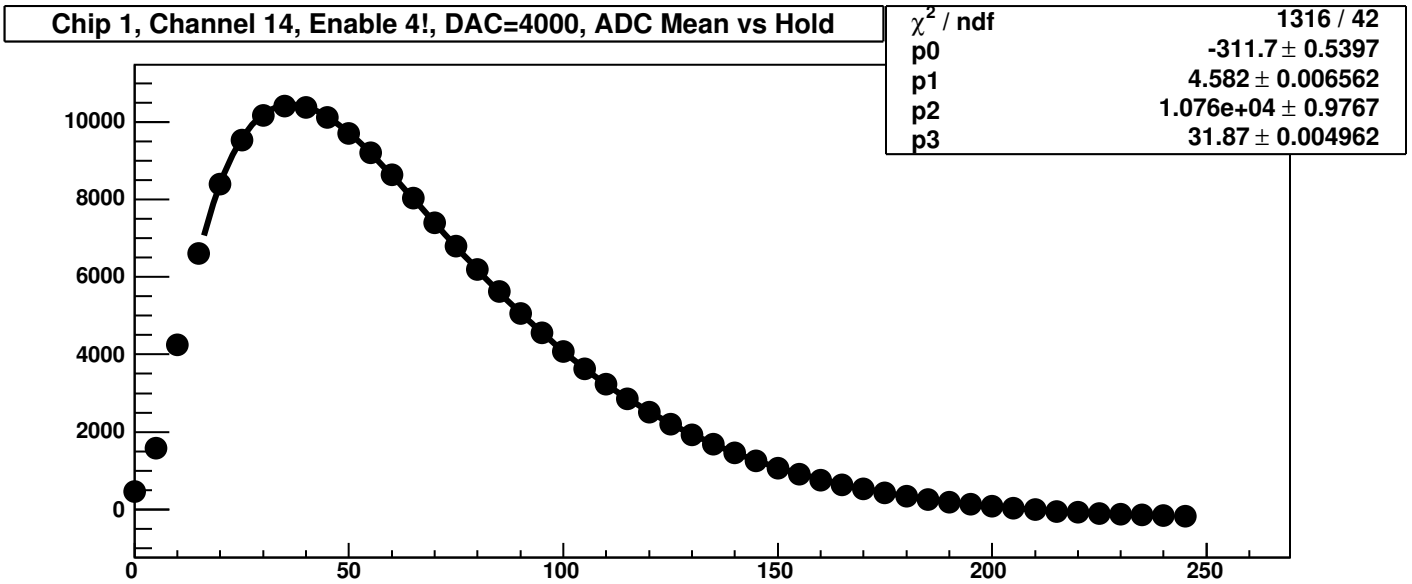


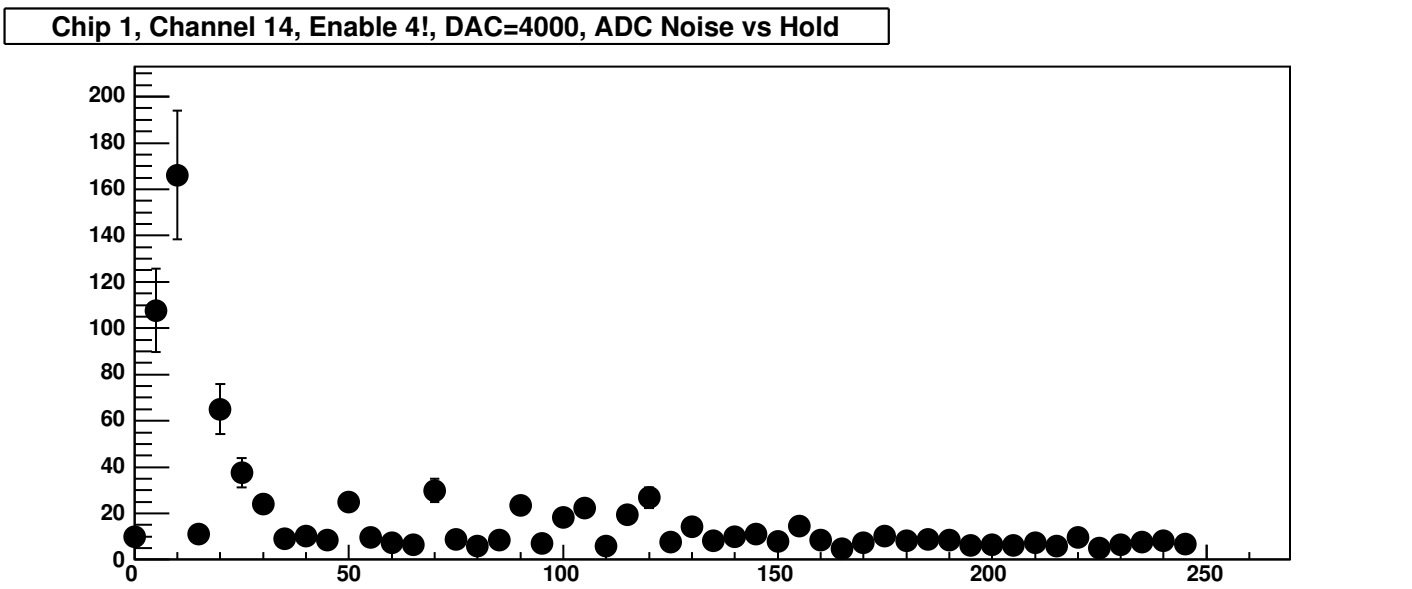
Chip 1, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold

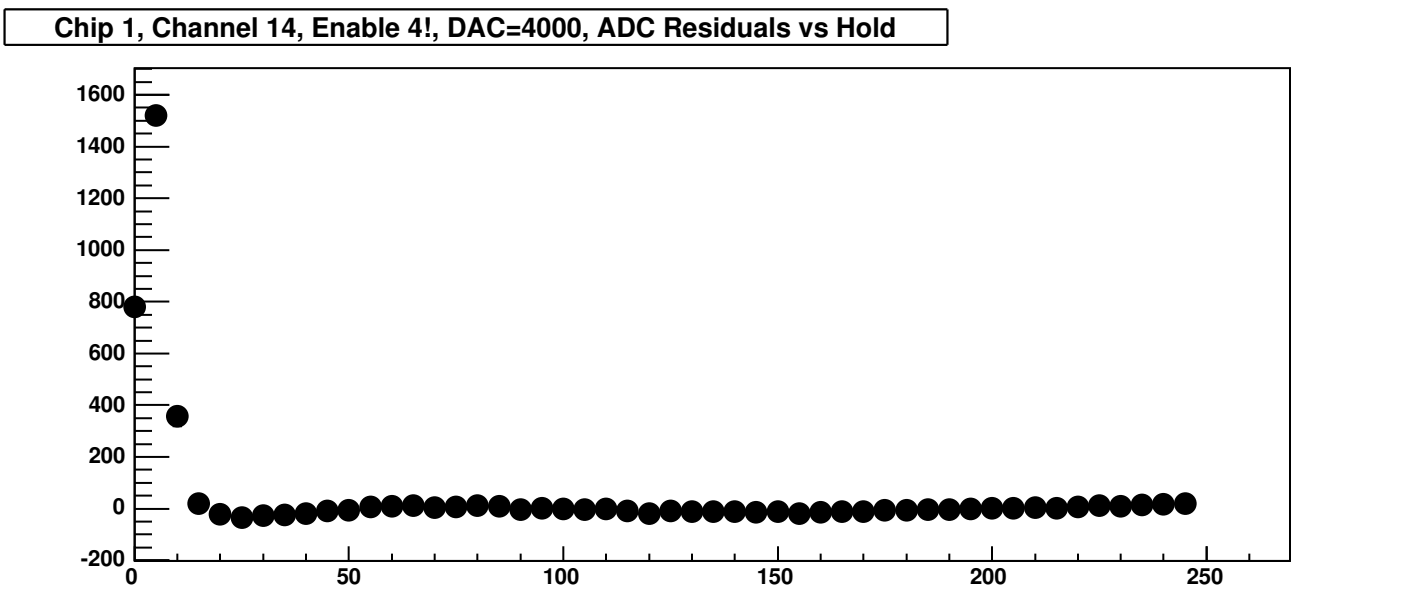


Chip 1, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

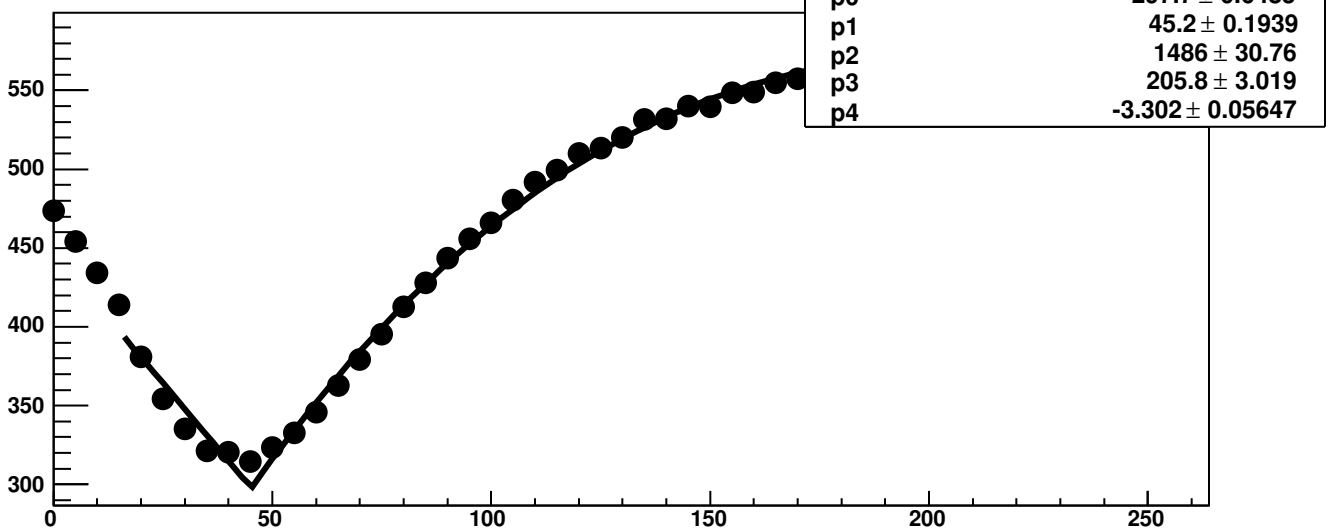



Chip 1, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1316 / 42
p0 -311.7 ± 0.5397
p1 4.582 ± 0.006562
p2 1.076e+04 ± 0.9767
p3 31.87 ± 0.004962
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

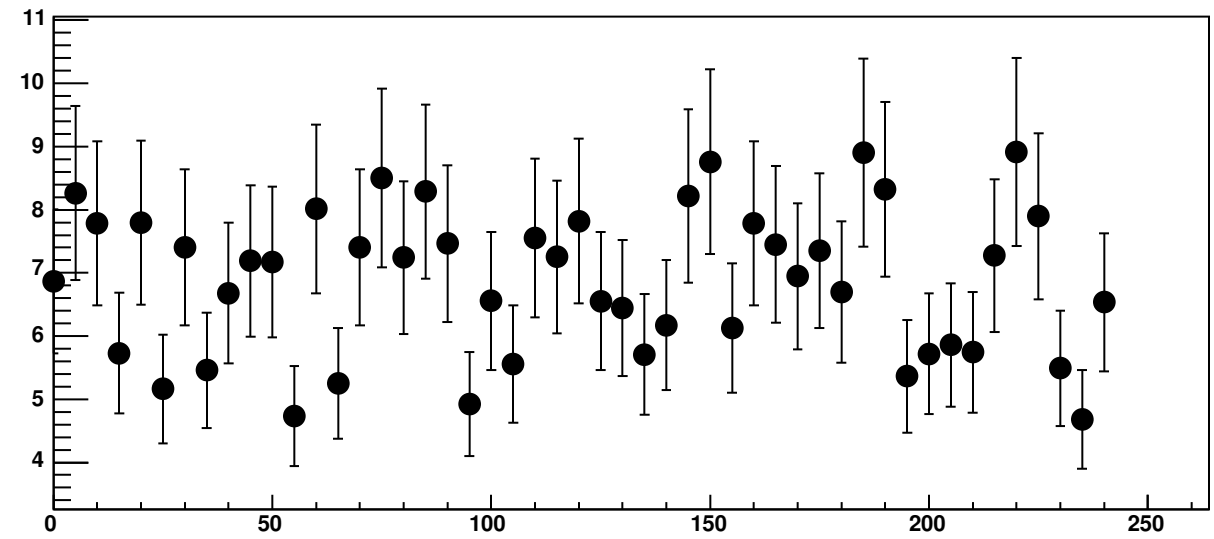

Chip 1, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 1, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

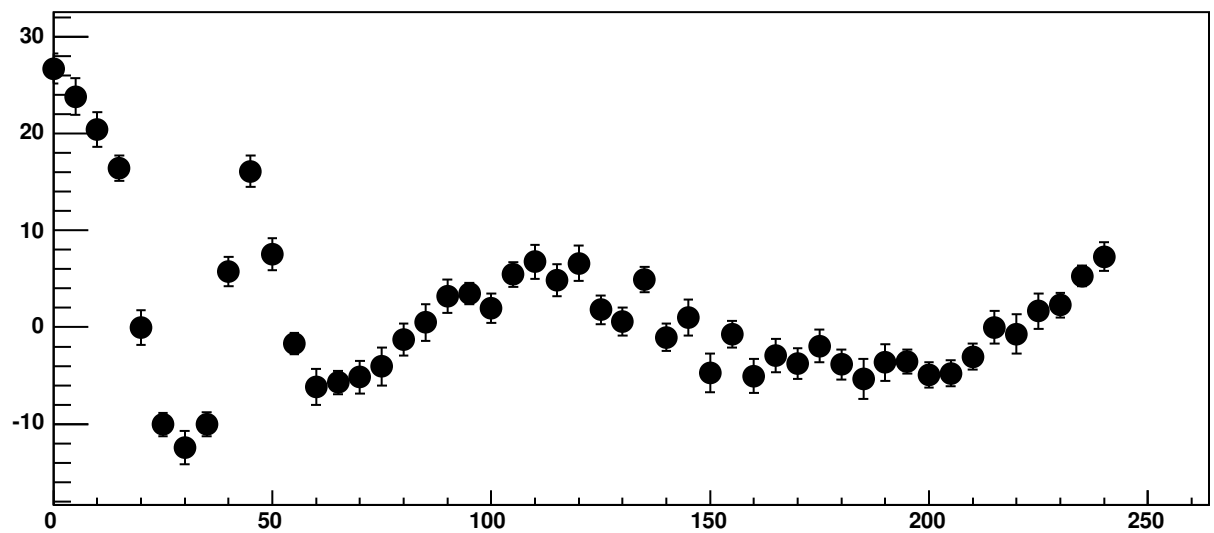
Chip 1, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

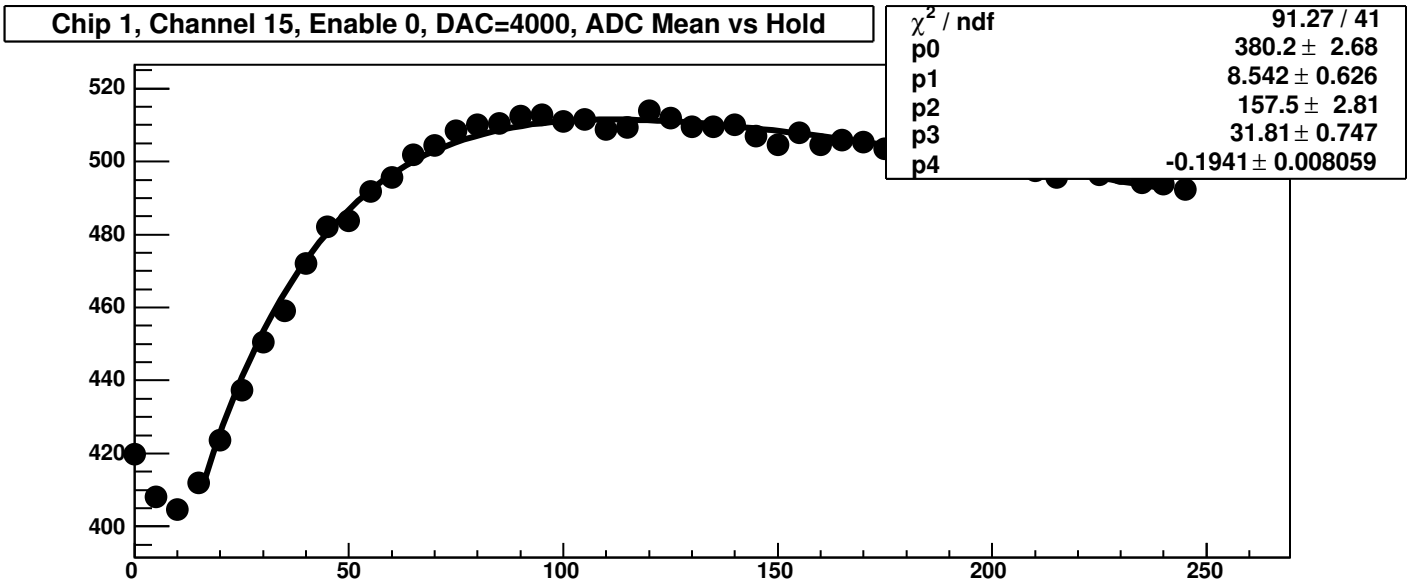


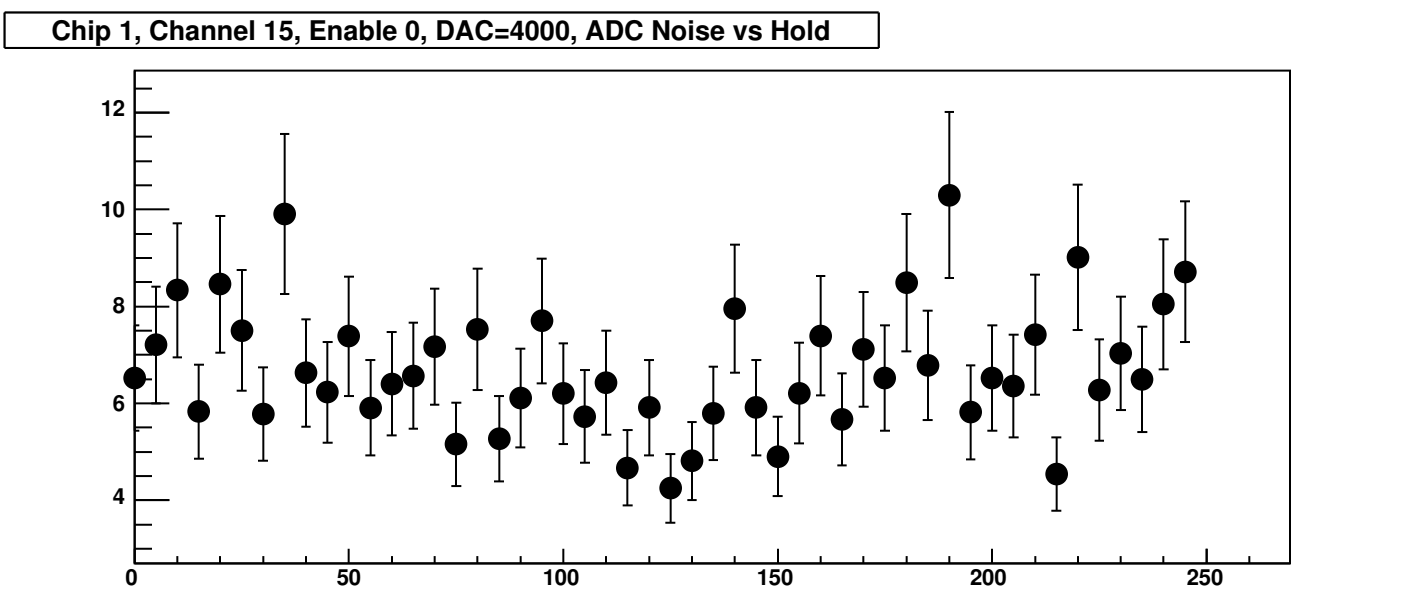
Chip 1, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold

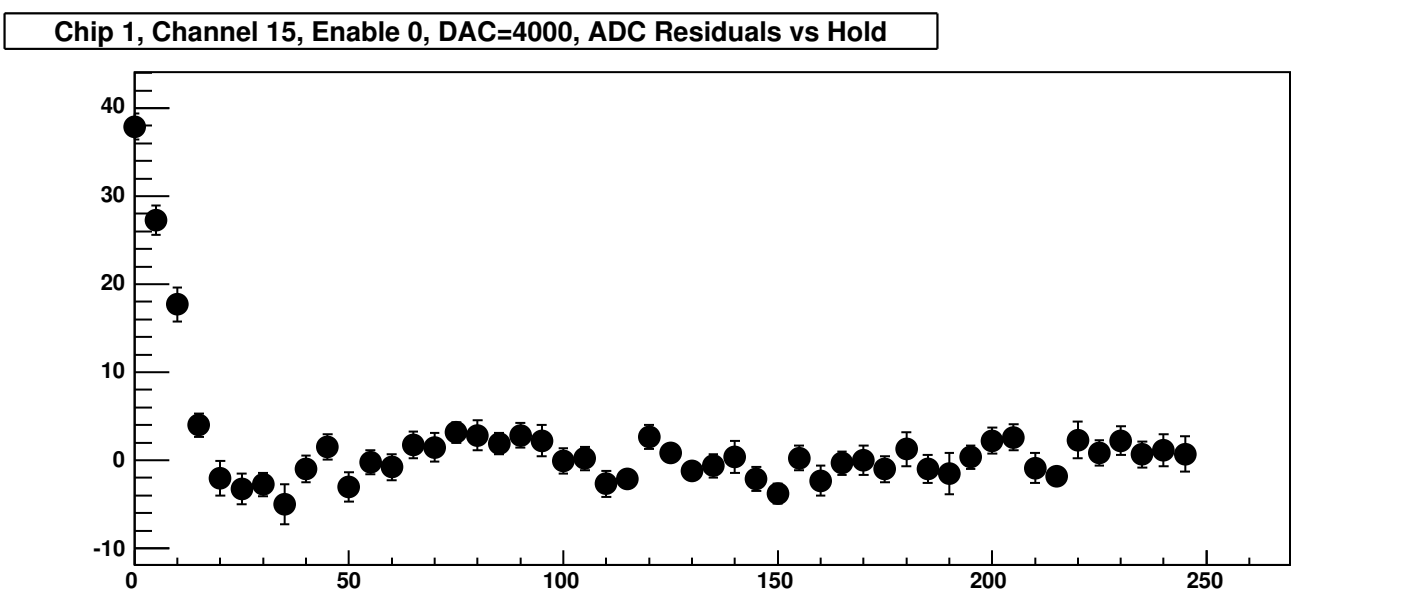


Chip 1, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold

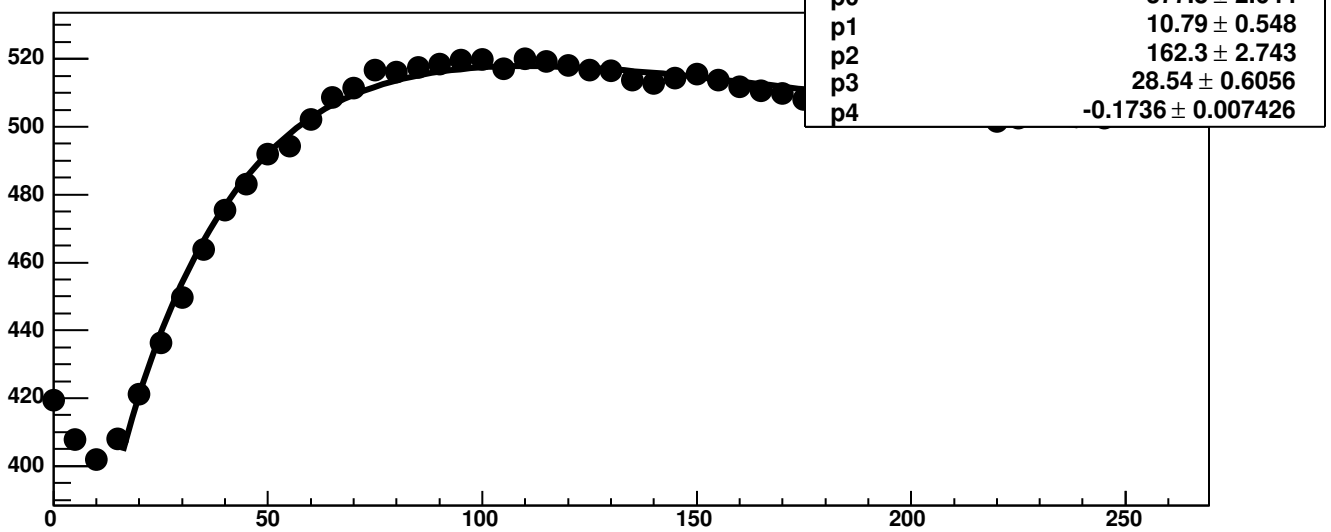



Chip 1, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 91.27 / 41
p0 380.2 ± 2.68
p1 8.542 ± 0.626
p2 157.5 ± 2.81
p3 31.81 ± 0.747
p4 -0.1941 ± 0.008059
520
500
480
460
440
420
400
0
50
100
150
200
250

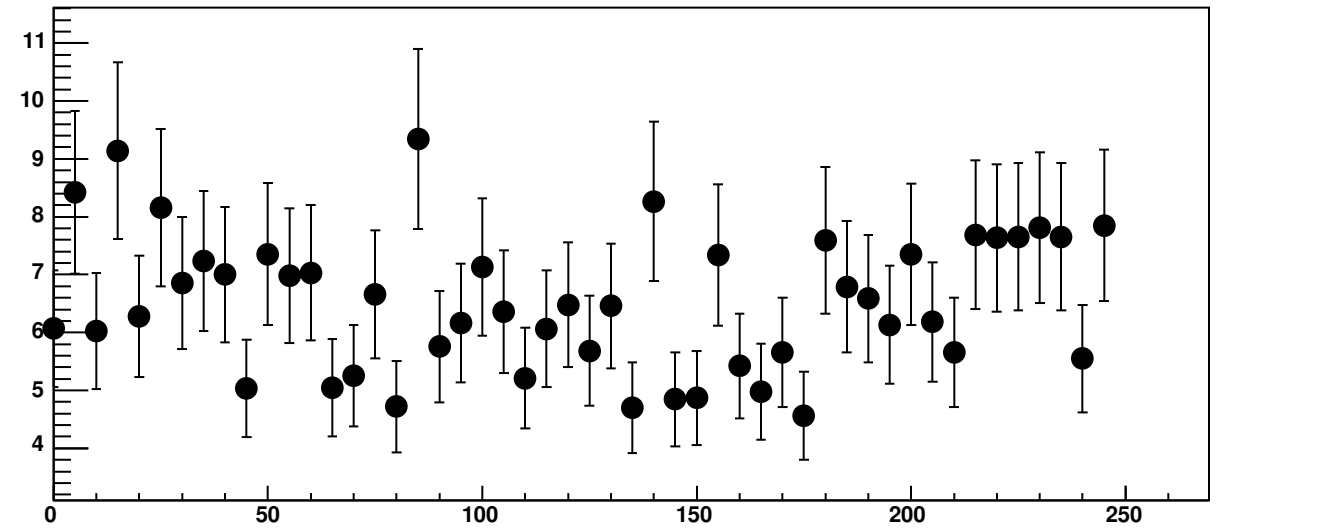

Chip 1, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 1, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

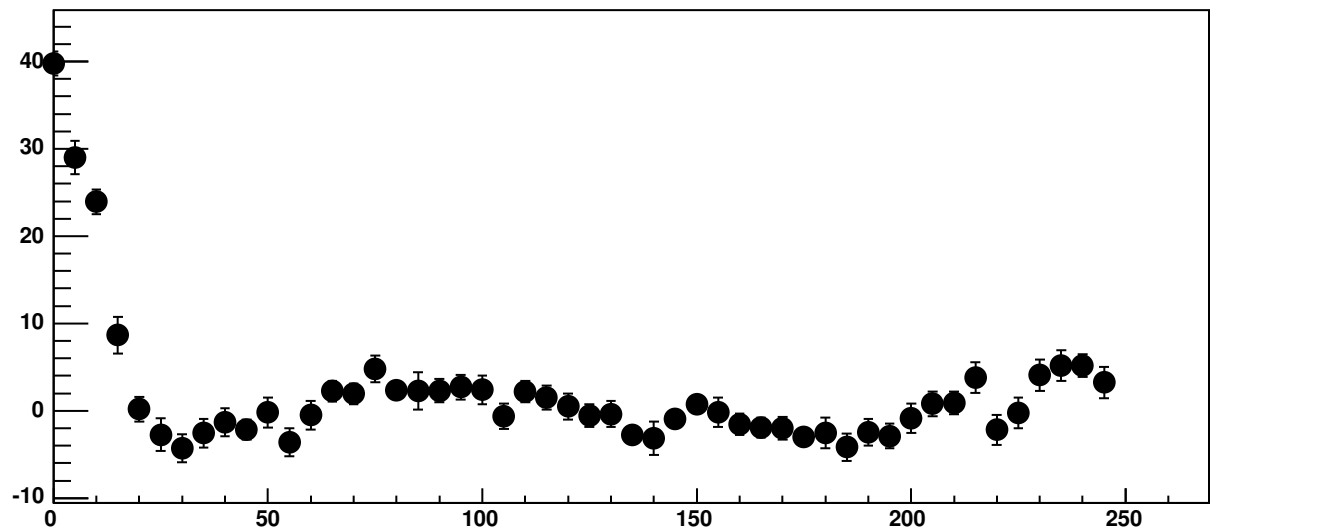
Chip 1, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold



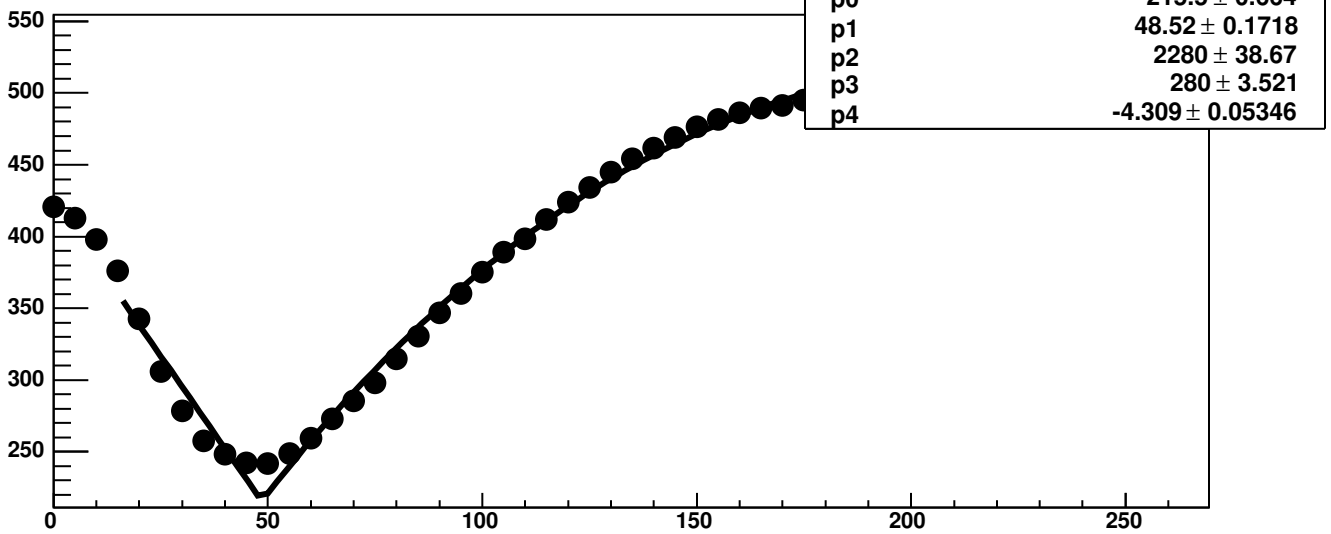
Chip 1, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



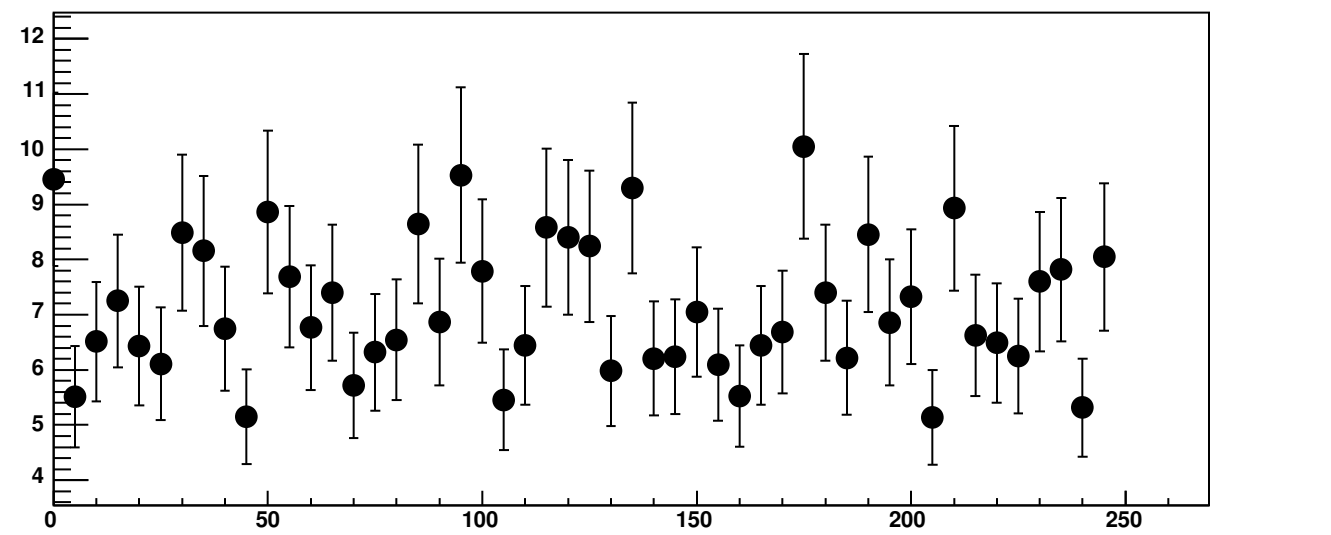
Chip 1, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold



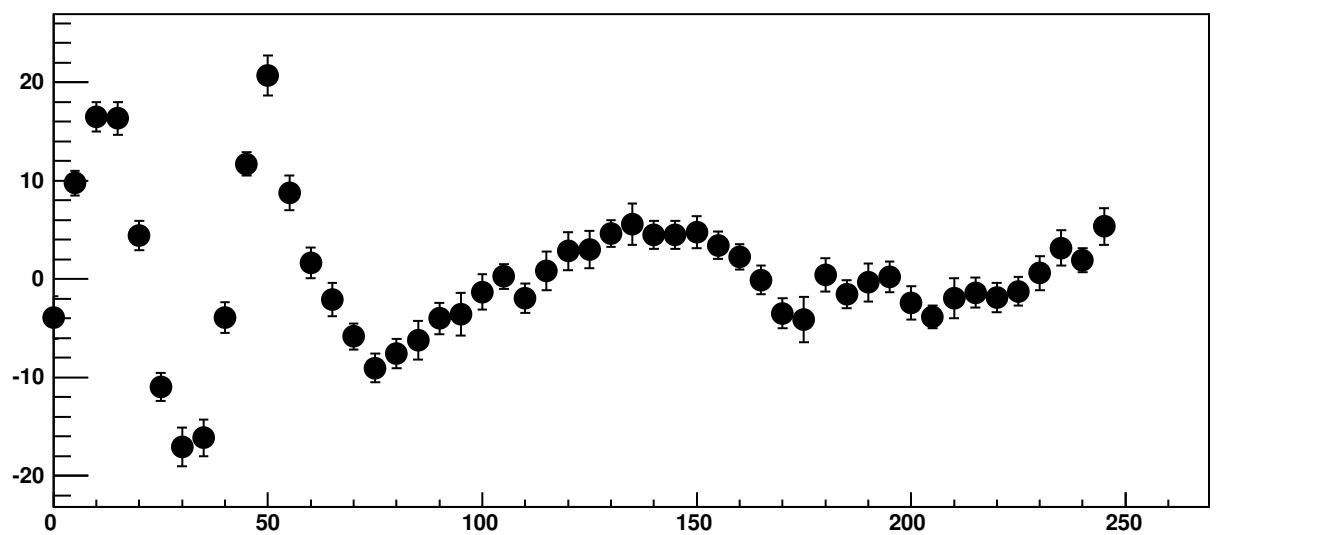
Chip 1, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold

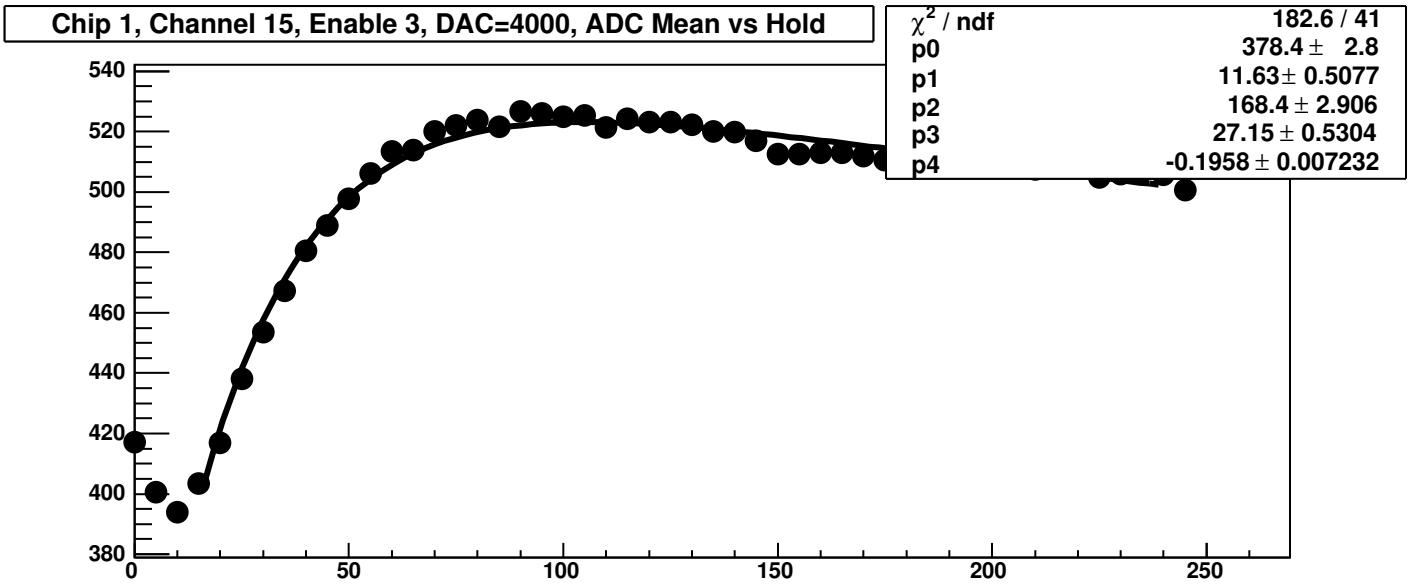


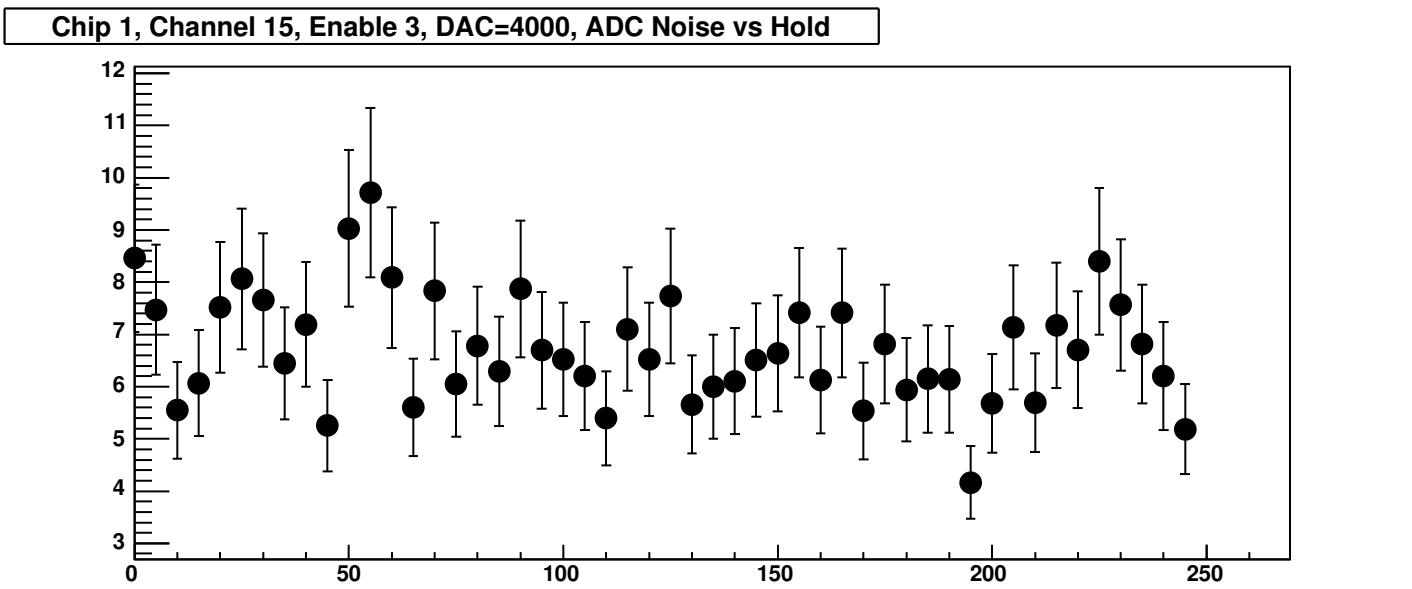
Chip 1, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold

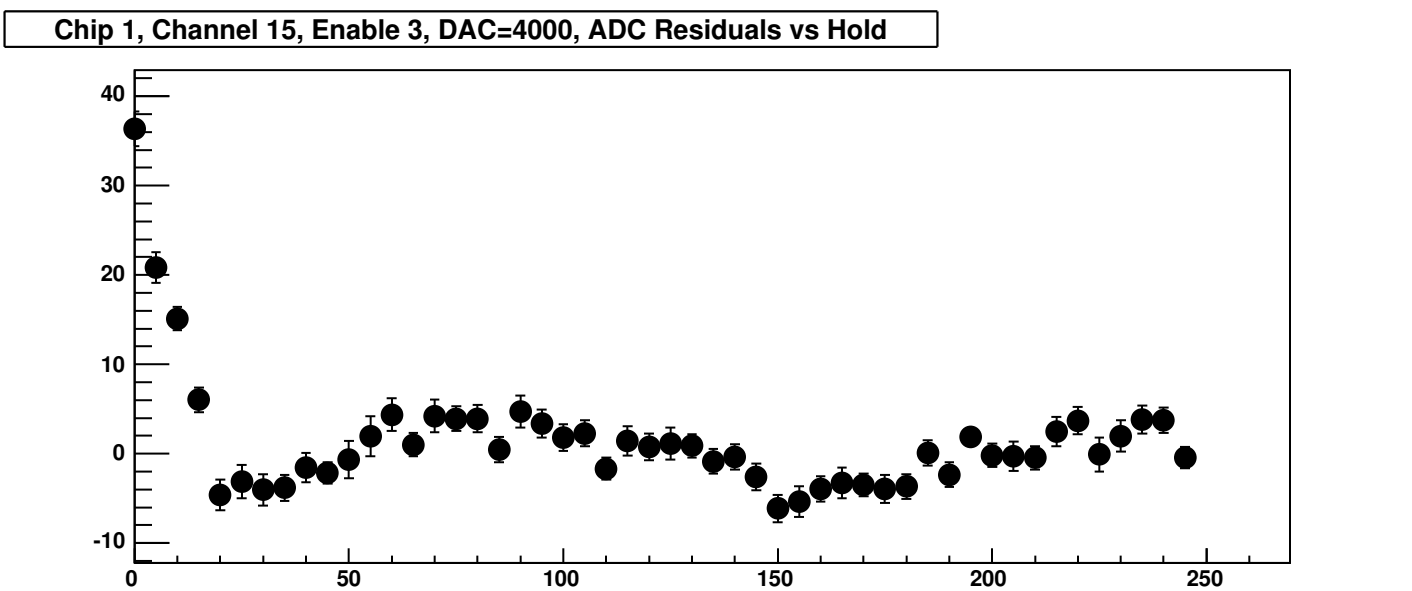


Chip 1, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold

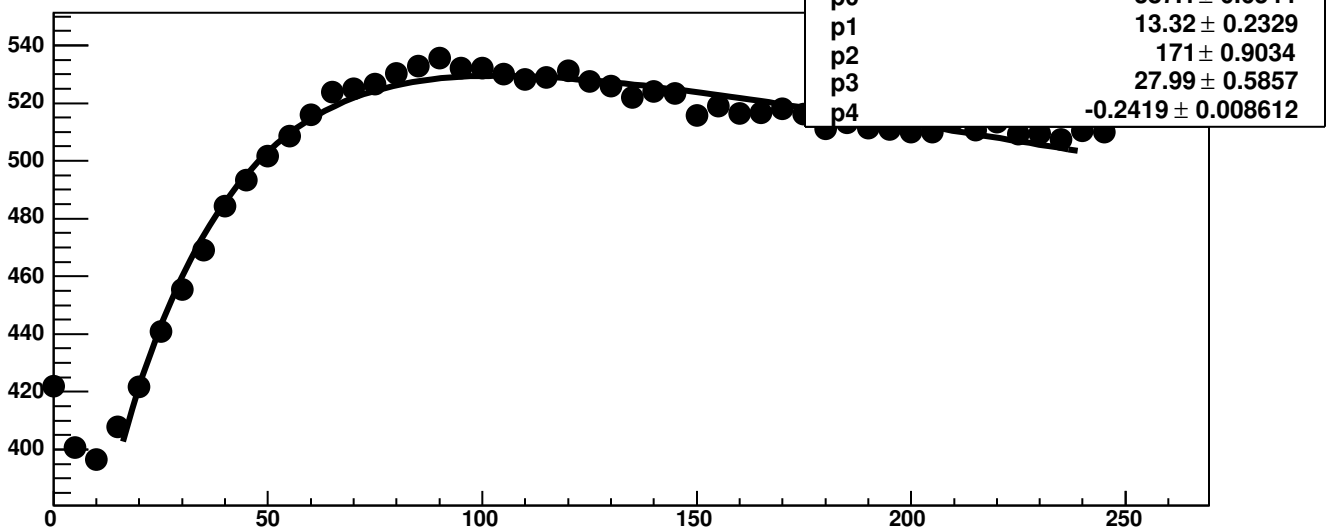



Chip 1, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 182.6 / 41
p0 378.4 ± 2.8
p1 11.63 ± 0.5077
p2 168.4 ± 2.906
p3 27.15 ± 0.5304
p4 -0.1958 ± 0.007232
540
520
500
480
460
440
420
400
380
0
50
100
150
200
250

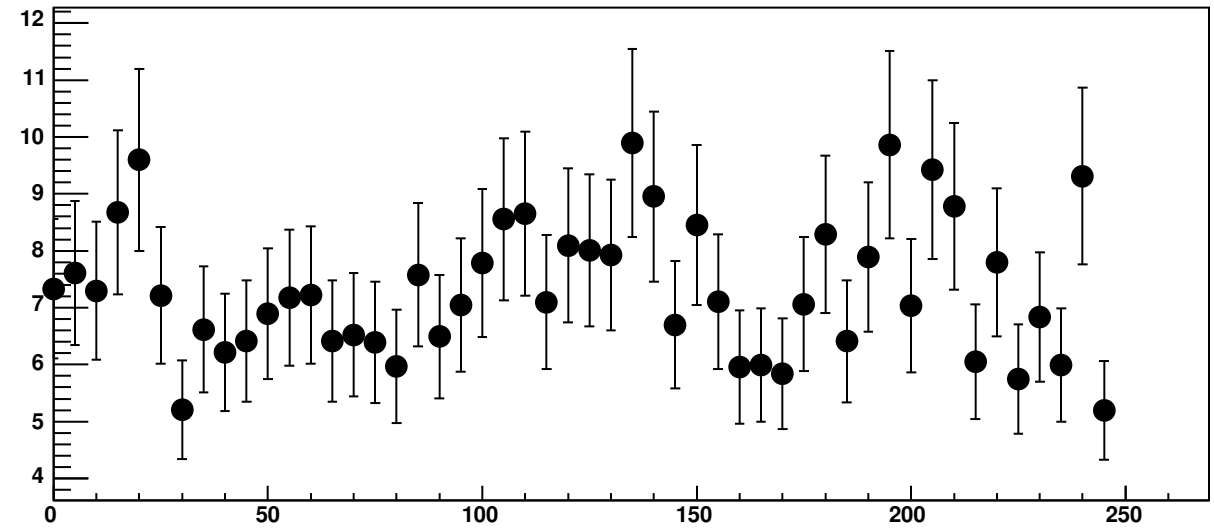

Chip 1, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 1, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

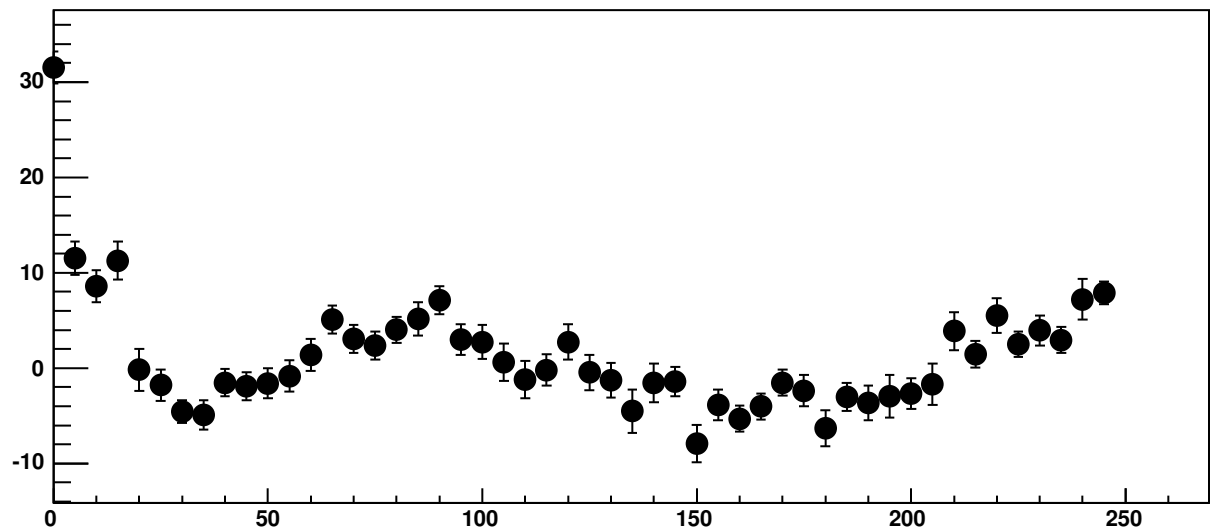
Chip 1, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold

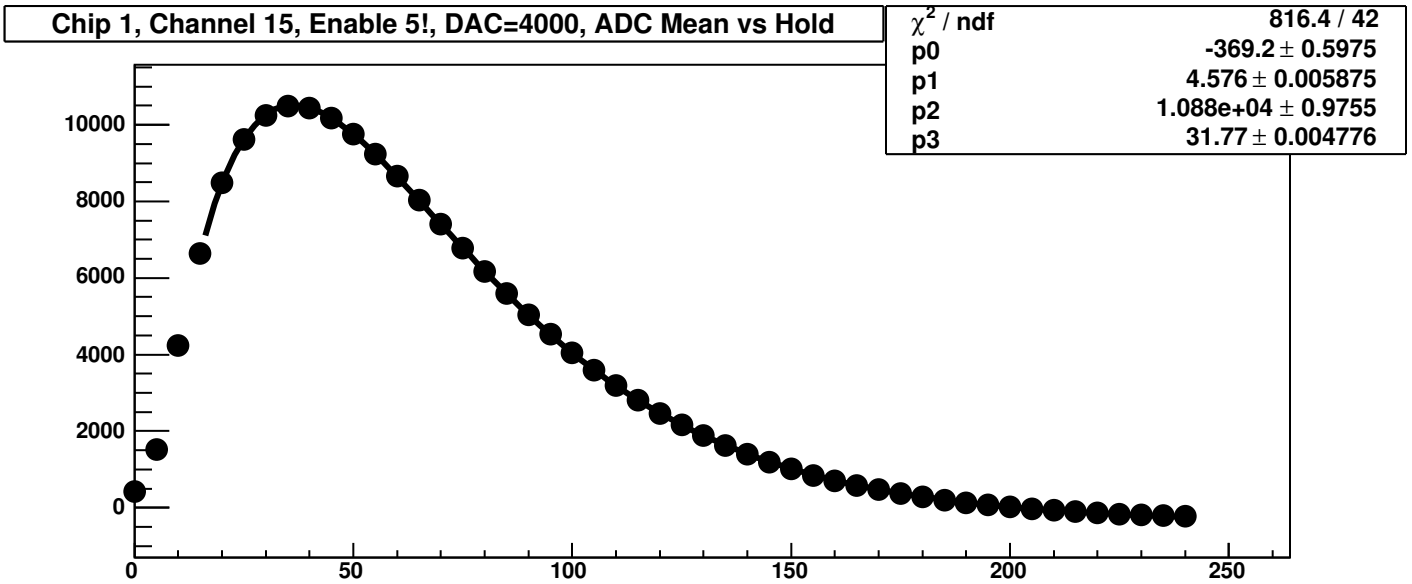


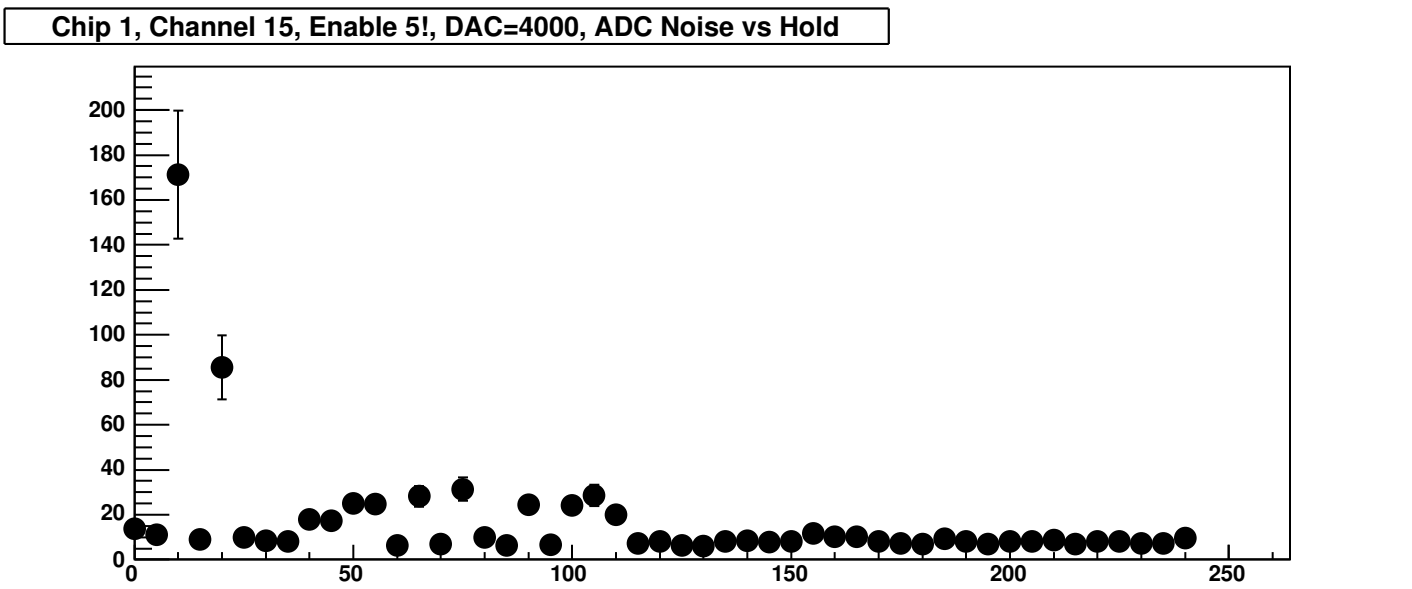
Chip 1, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold

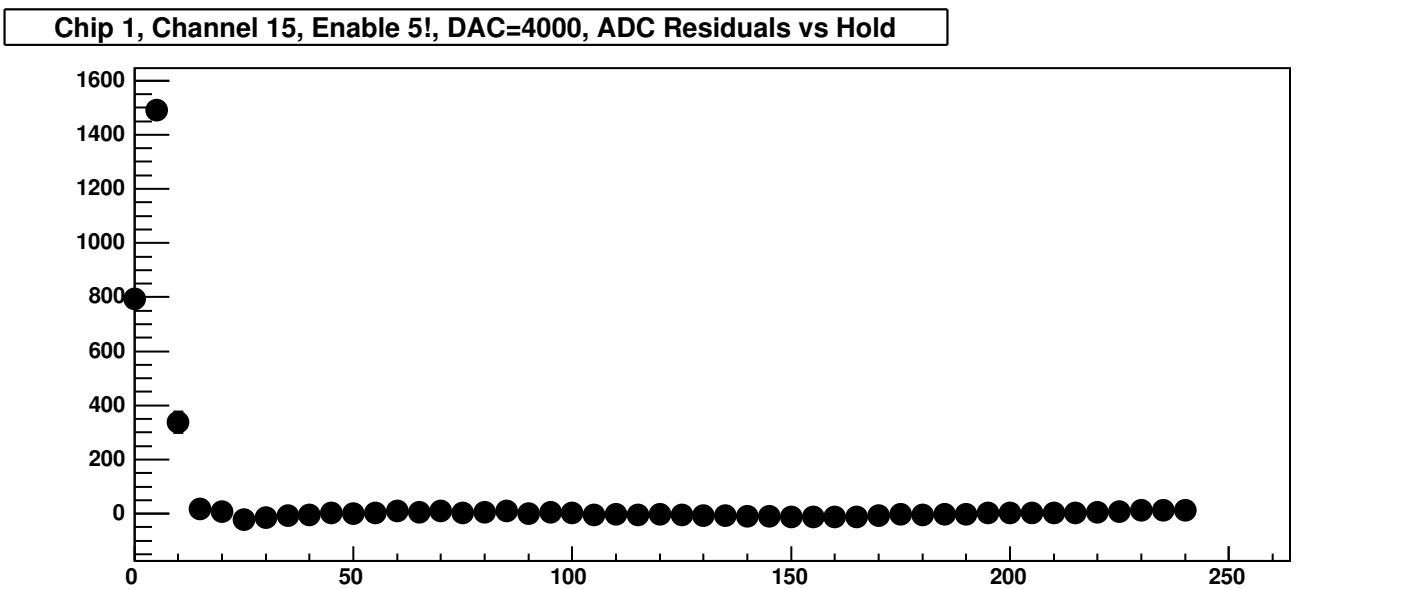


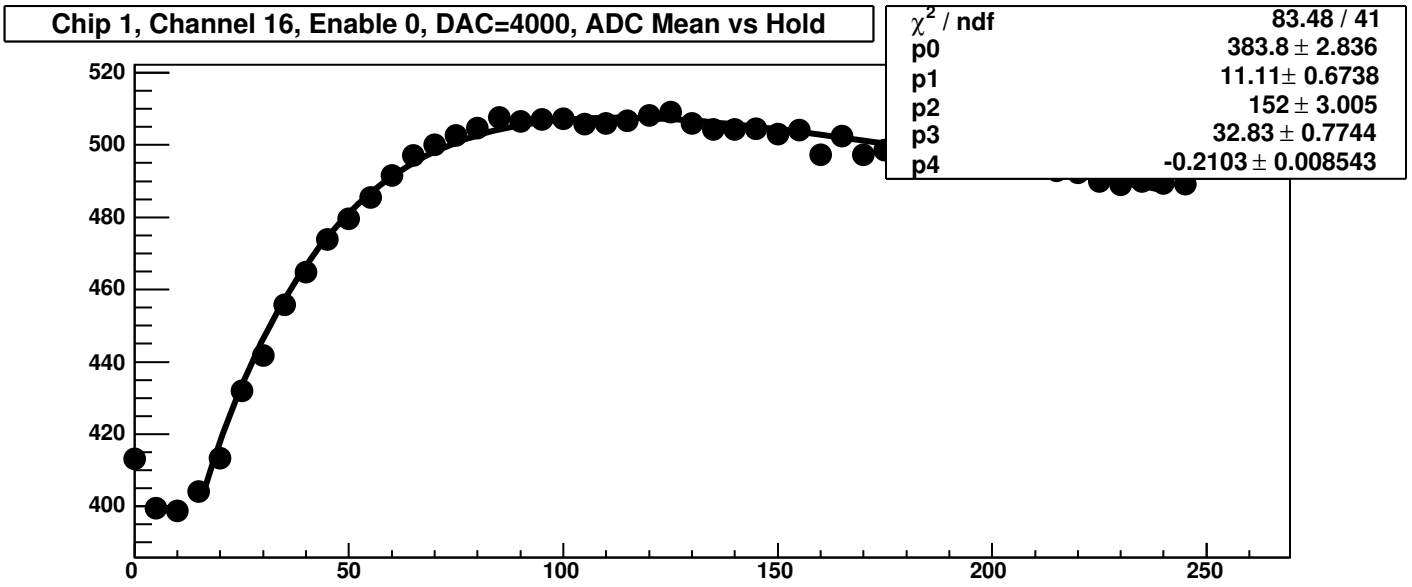
Chip 1, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

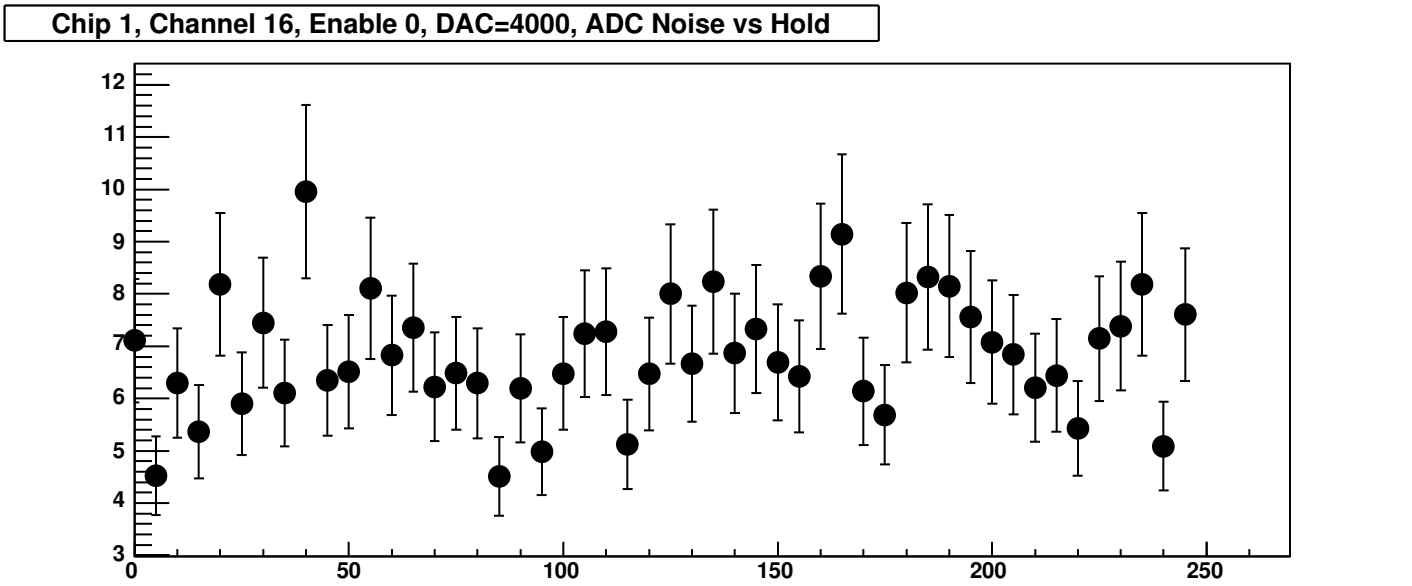


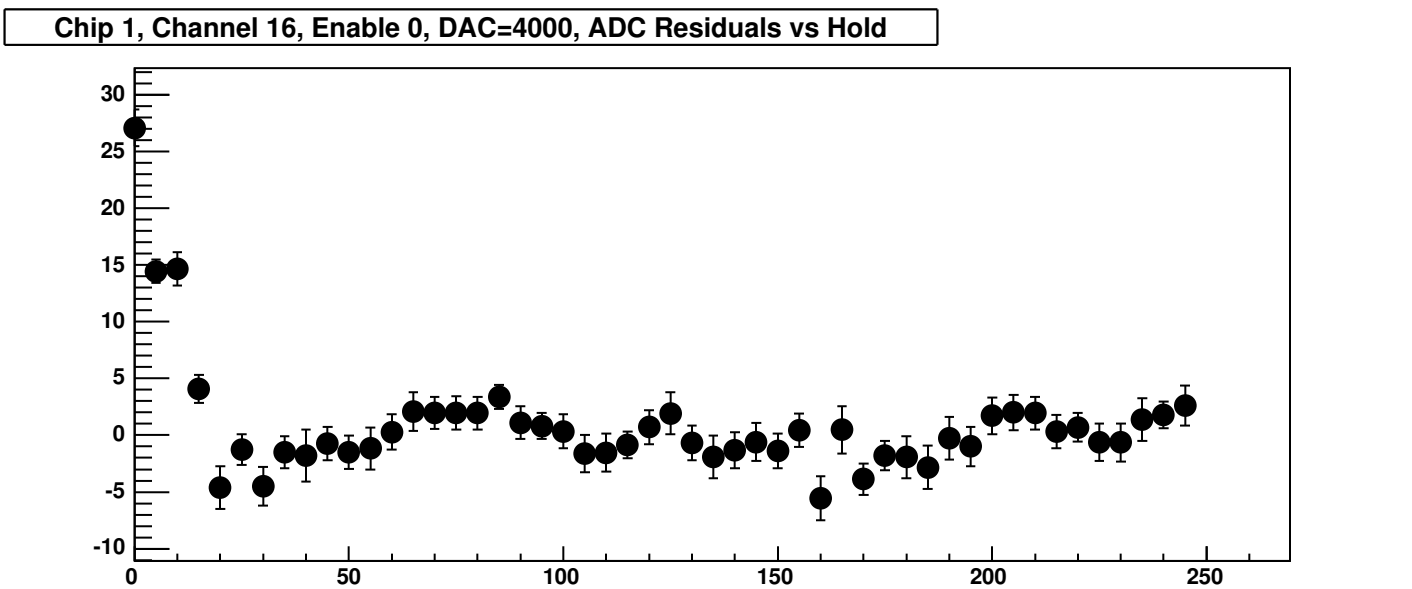

Chip 1, Channel 15, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 816.4 / 42
p0 -369.2 ± 0.5975
p1 4.576 ± 0.005875
p2 1.088e+04 ± 0.9755
p3 31.77 ± 0.004776
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

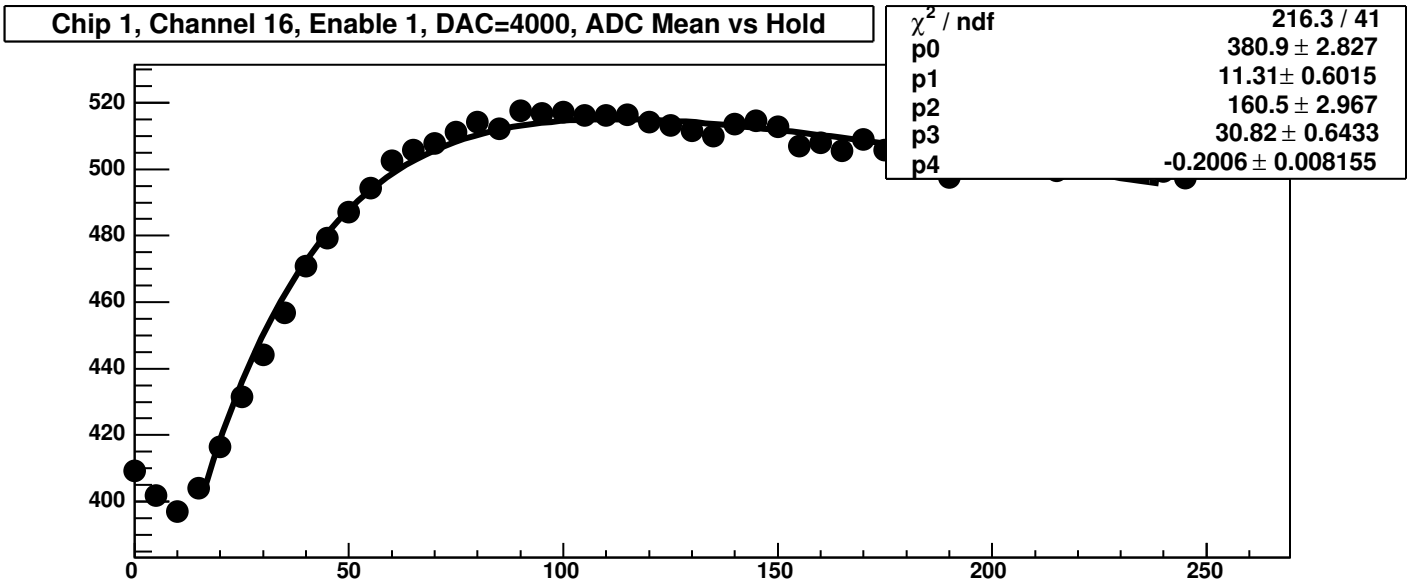

Chip 1, Channel 15, Enable 5!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

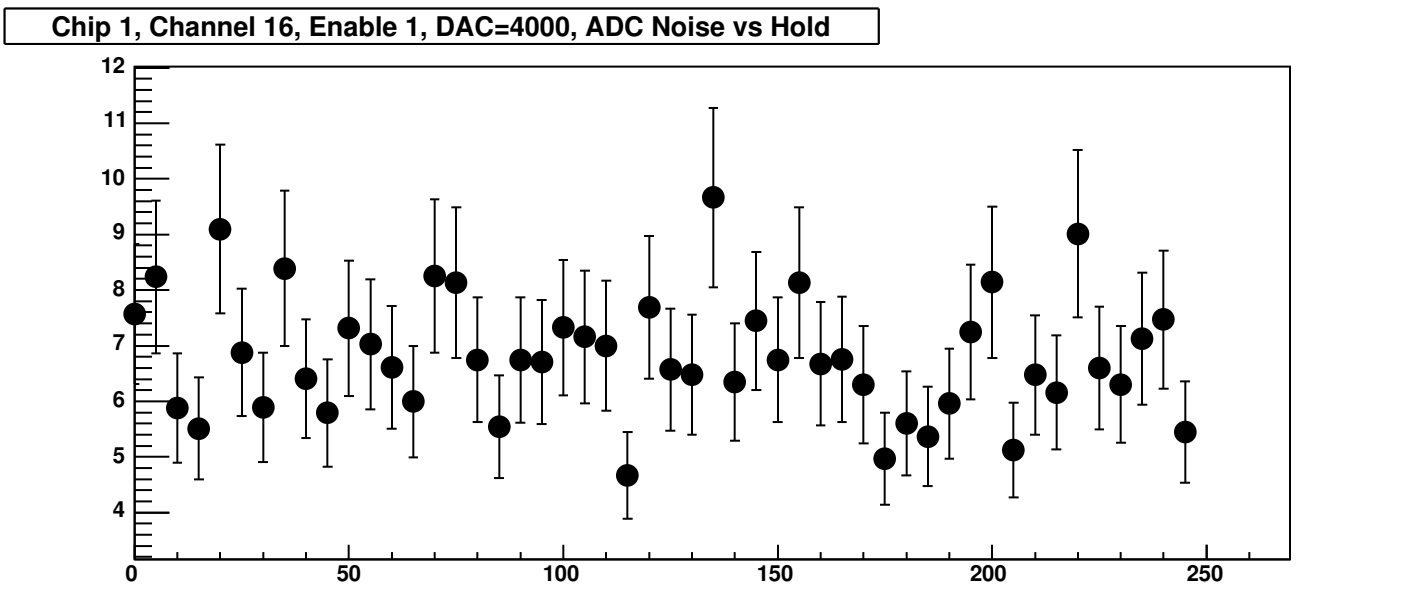

Chip 1, Channel 15, Enable 5!, DAC=4000, ADC Residuals vs Hold
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

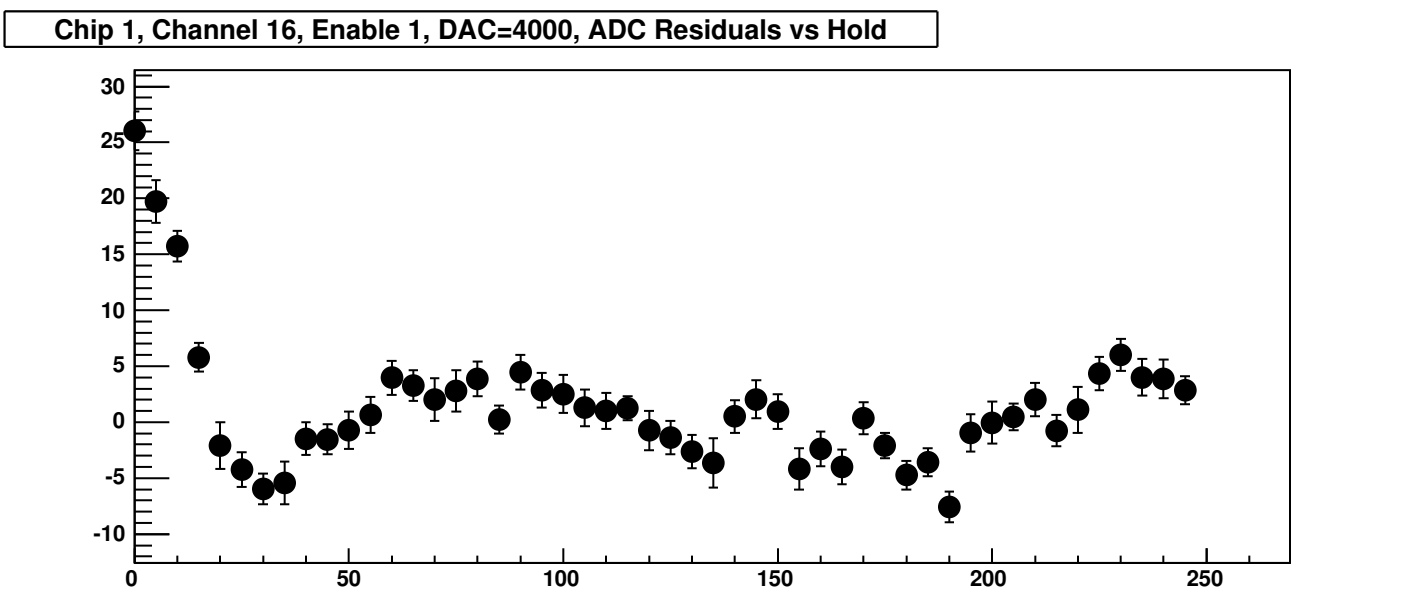

Chip 1, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 83.48 / 41
p0 383.8 ± 2.836
p1 11.11 ± 0.6738
p2 152 ± 3.005
p3 32.83 ± 0.7744
p4 -0.2103 ± 0.008543
520
500
480
460
440
420
400
0
50
100
150
200
250

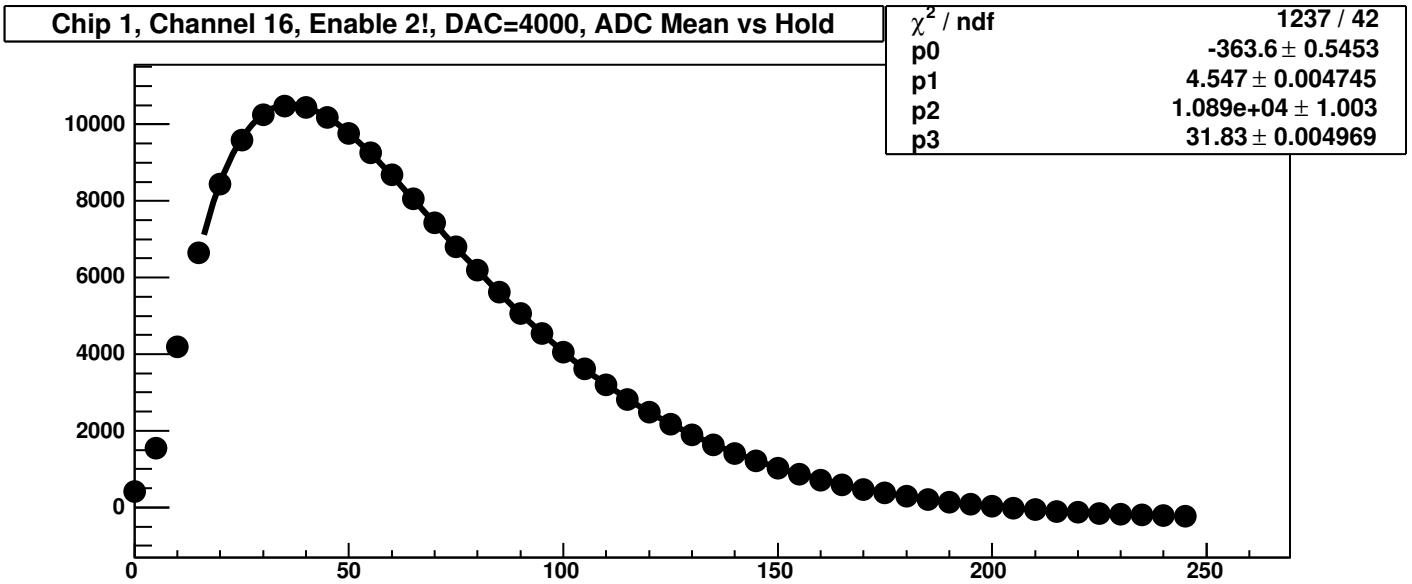

Chip 1, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

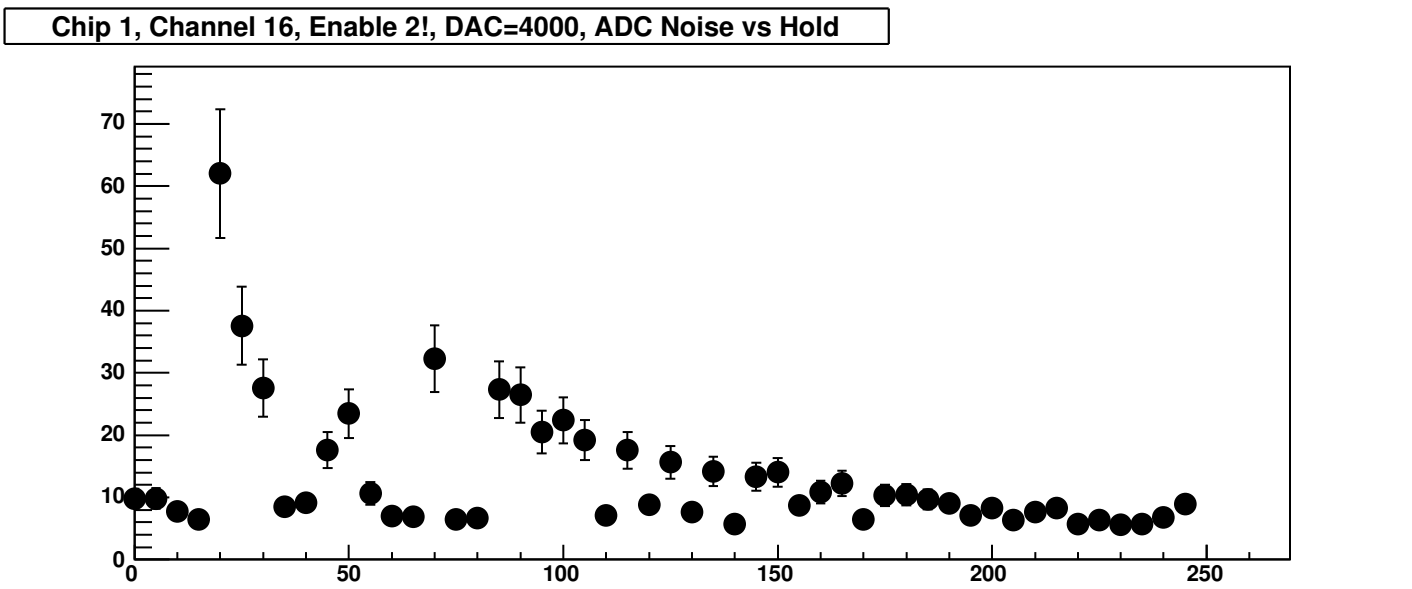

Chip 1, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
0
50
100
150
200
250

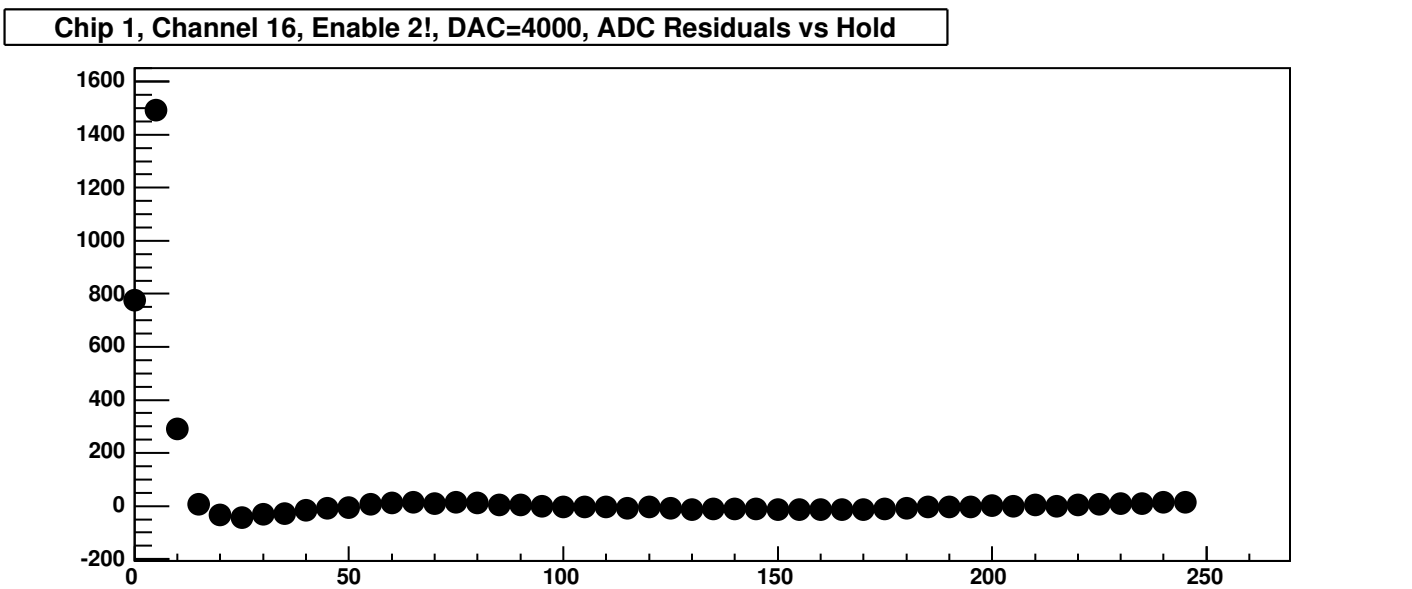

Chip 1, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 216.3 / 41
p0 380.9 ± 2.827
p1 11.31 ± 0.6015
p2 160.5 ± 2.967
p3 30.82 ± 0.6433
p4 -0.2006 ± 0.008155
520
500
480
460
440
420
400
0
50
100
150
200
250

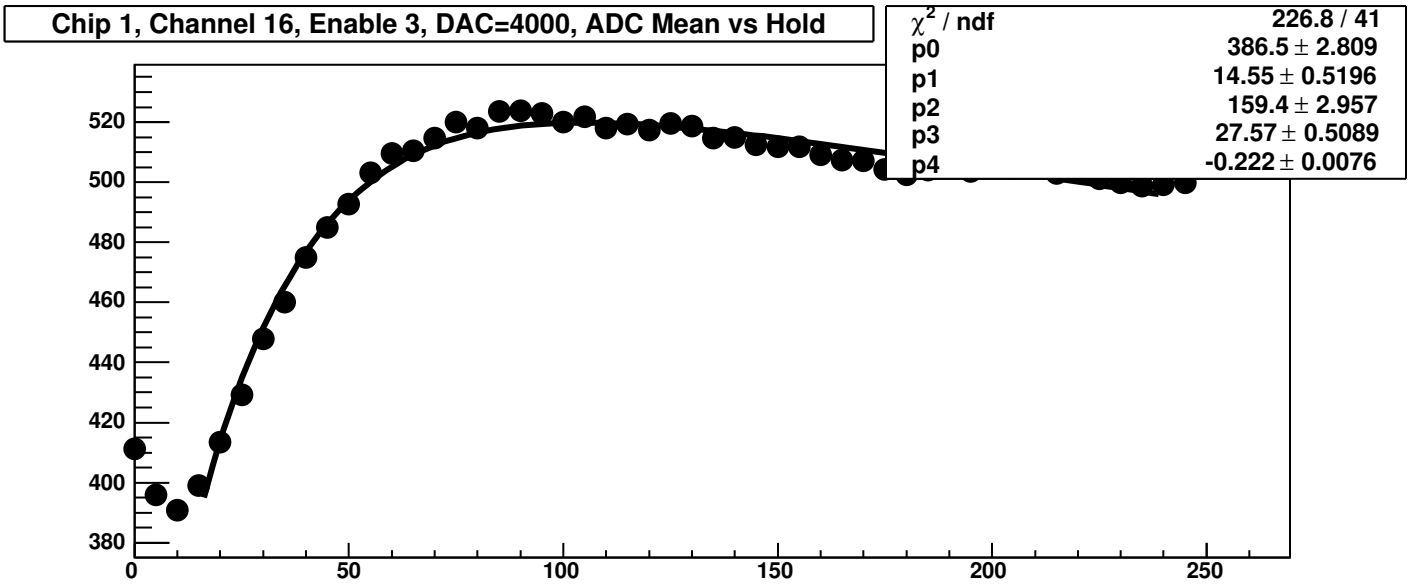

Chip 1, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

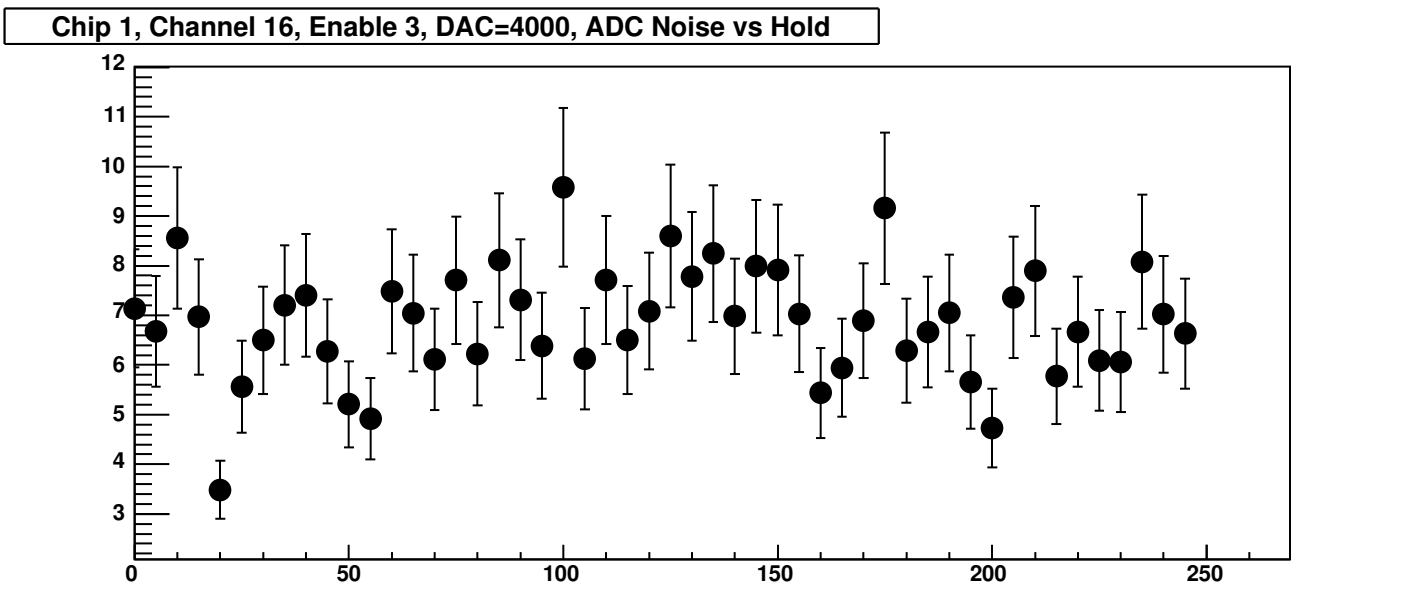

Chip 1, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
0
50
100
150
200
250

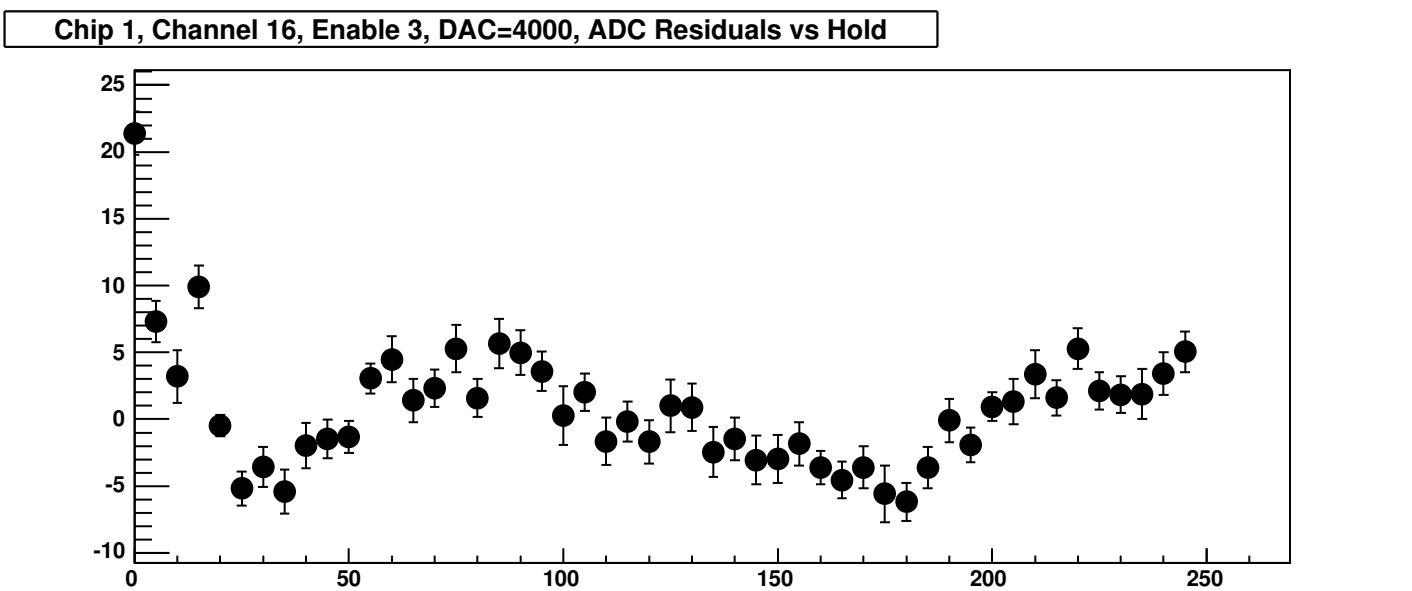

Chip 1, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1237 / 42
p0 -363.6 ± 0.5453
p1 4.547 ± 0.004745
p2 1.089e+04 ± 1.003
p3 31.83 ± 0.004969
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 1, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold
70
60
50
40
30
20
10
0
0
50
100
150
200
250

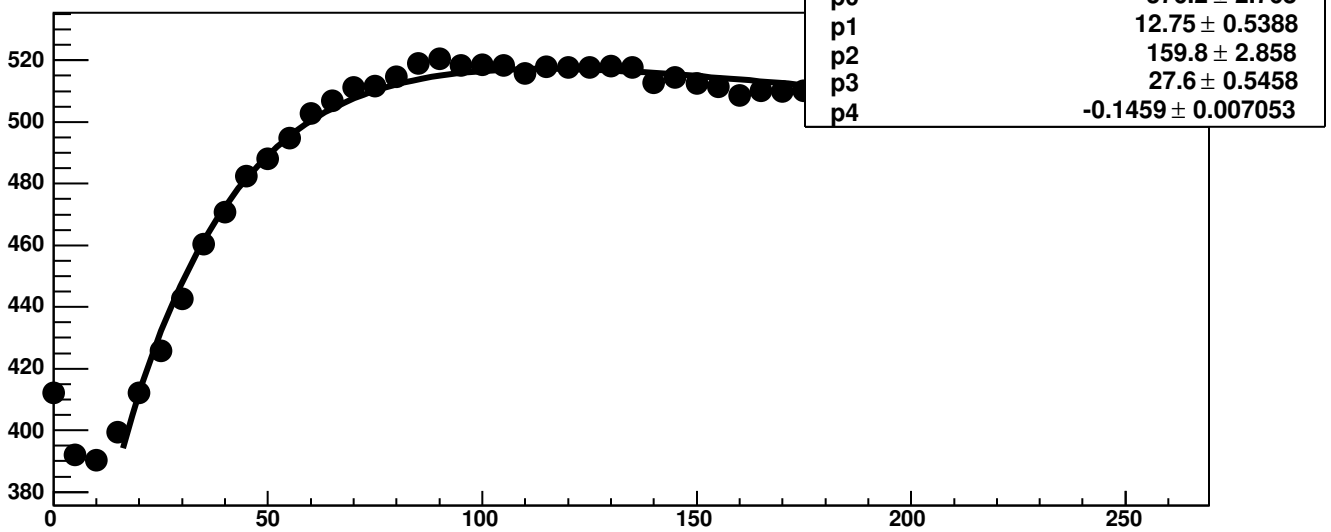

Chip 1, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250


Chip 1, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 226.8 / 41
p0 386.5 ± 2.809
p1 14.55 ± 0.5196
p2 159.4 ± 2.957
p3 27.57 ± 0.5089
p4 -0.222 ± 0.0076
520
500
480
460
440
420
400
380
0
50
100
150
200
250

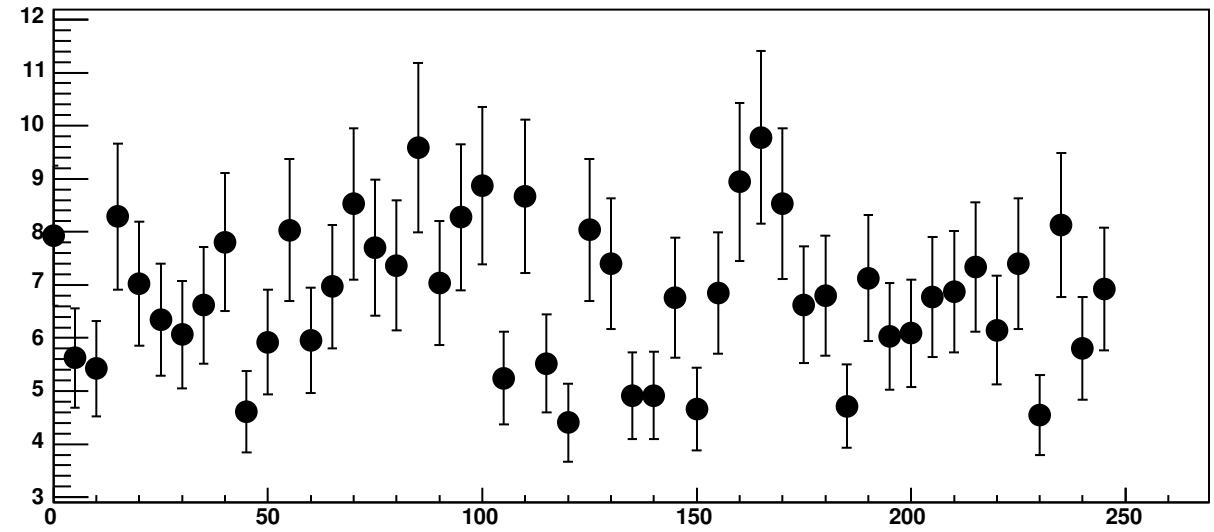

Chip 1, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 1, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
0
50
100
150
200
250

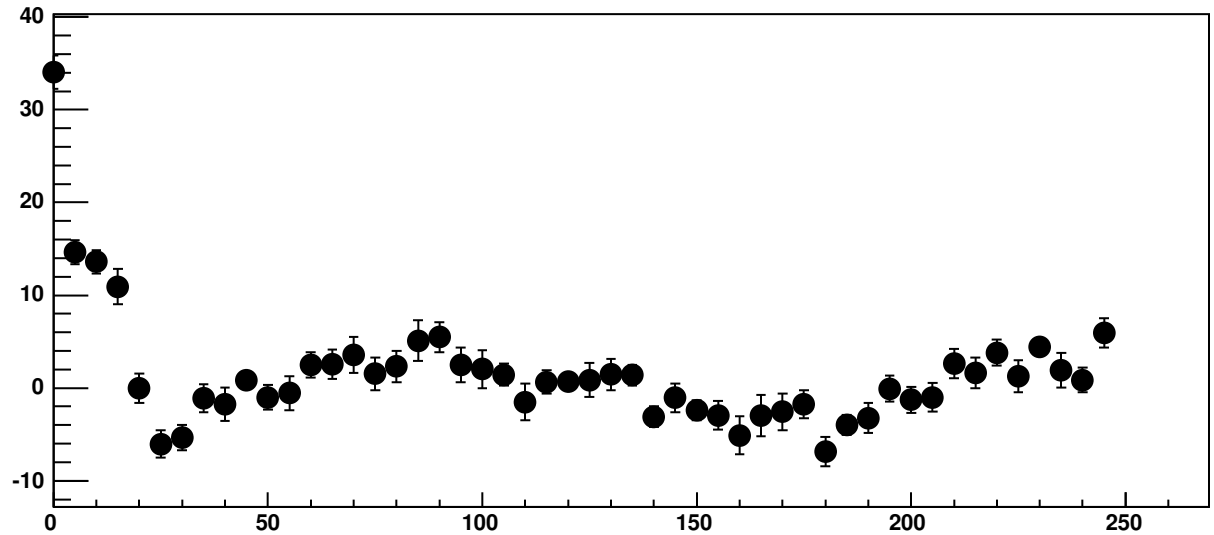
Chip 1, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold



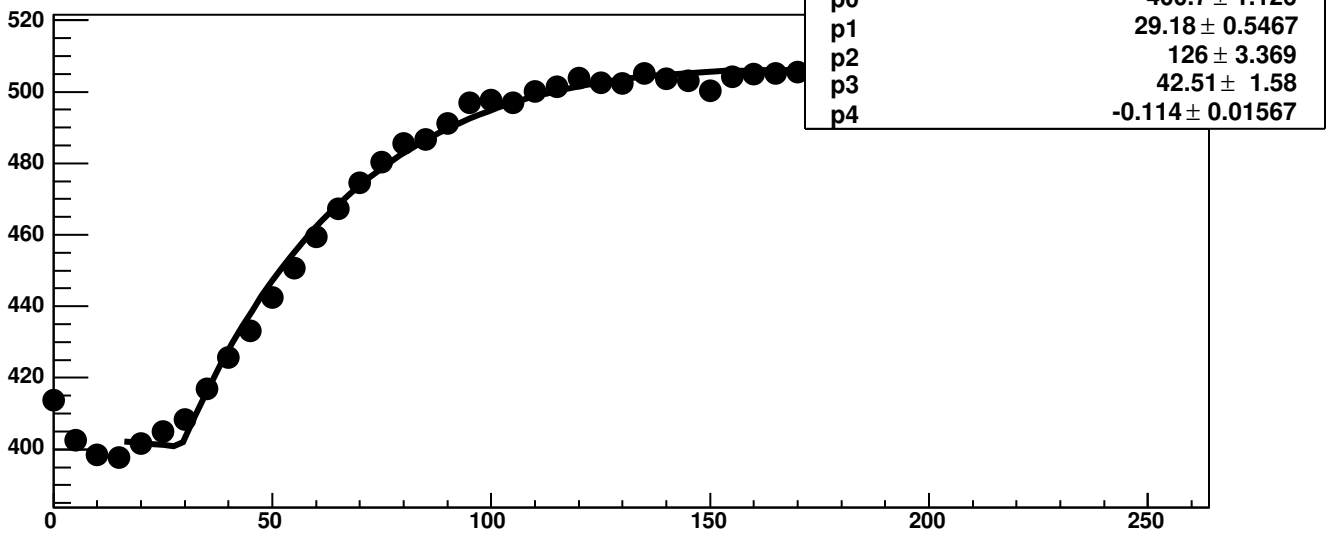
Chip 1, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



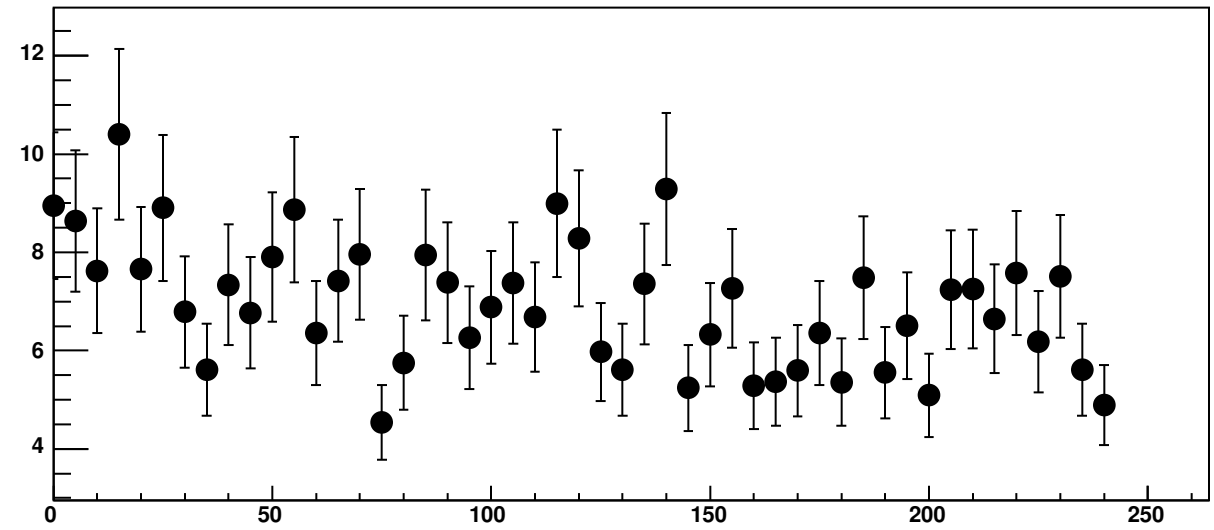
Chip 1, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold



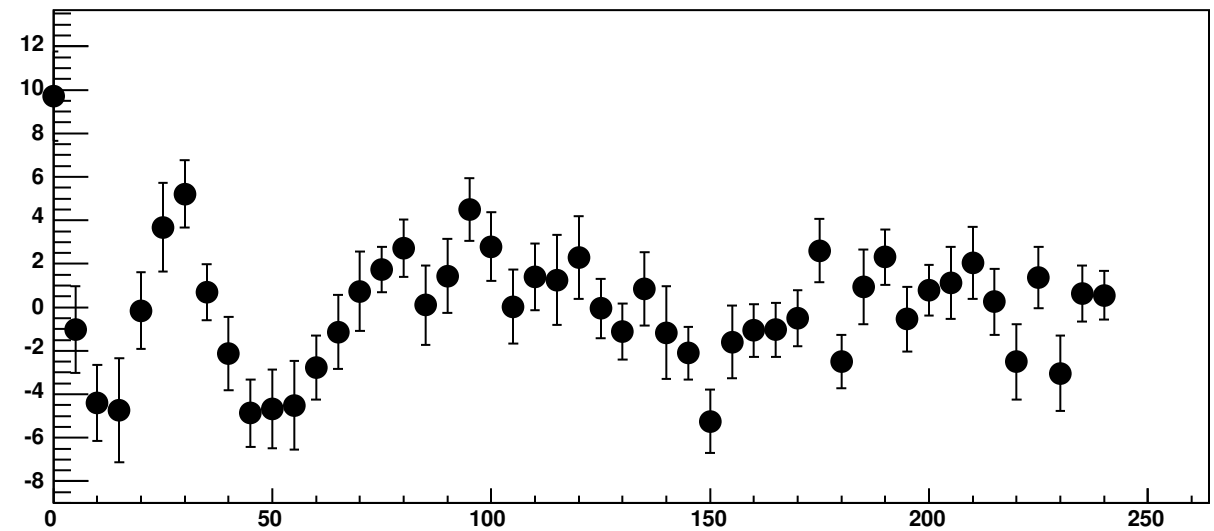
Chip 1, Channel 16, Enable 5, DAC=4000, ADC Mean vs Hold



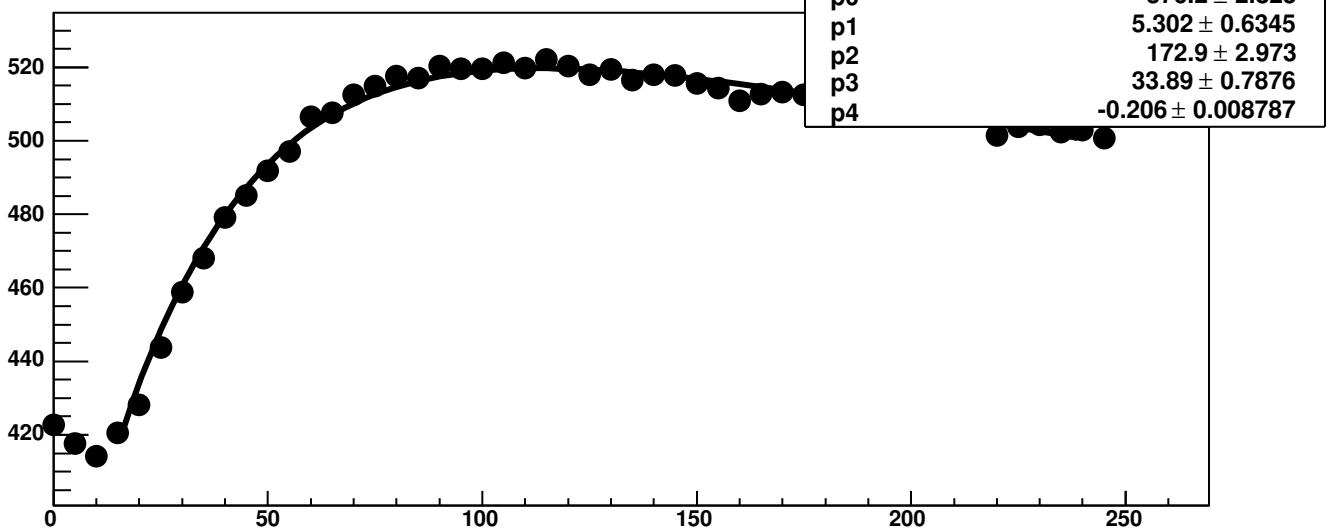
Chip 1, Channel 16, Enable 5, DAC=4000, ADC Noise vs Hold



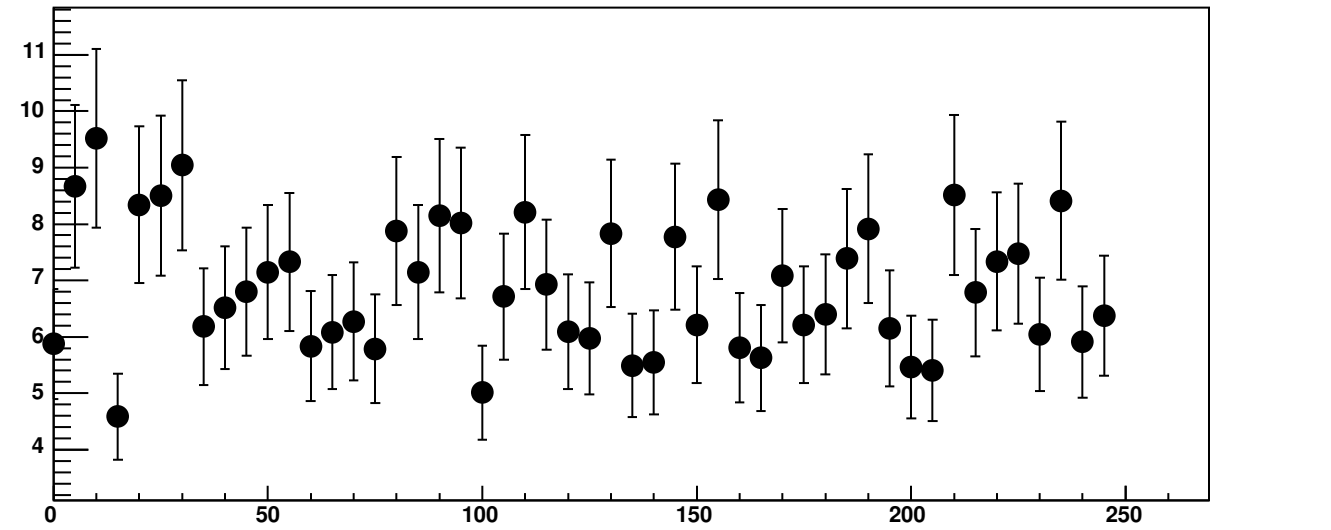
Chip 1, Channel 16, Enable 5, DAC=4000, ADC Residuals vs Hold



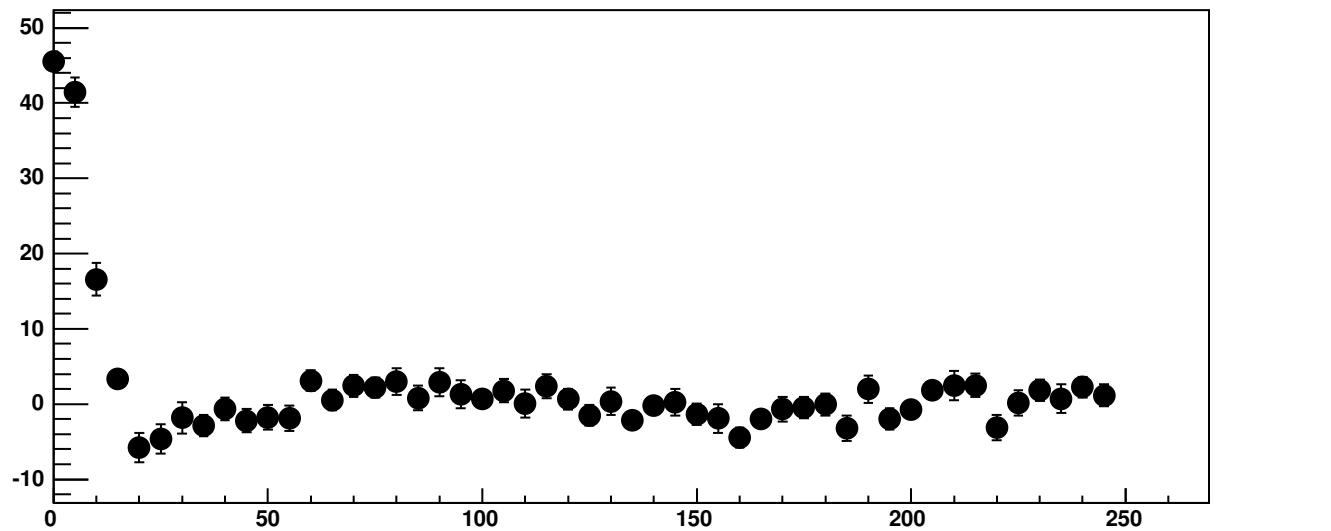
Chip 1, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold



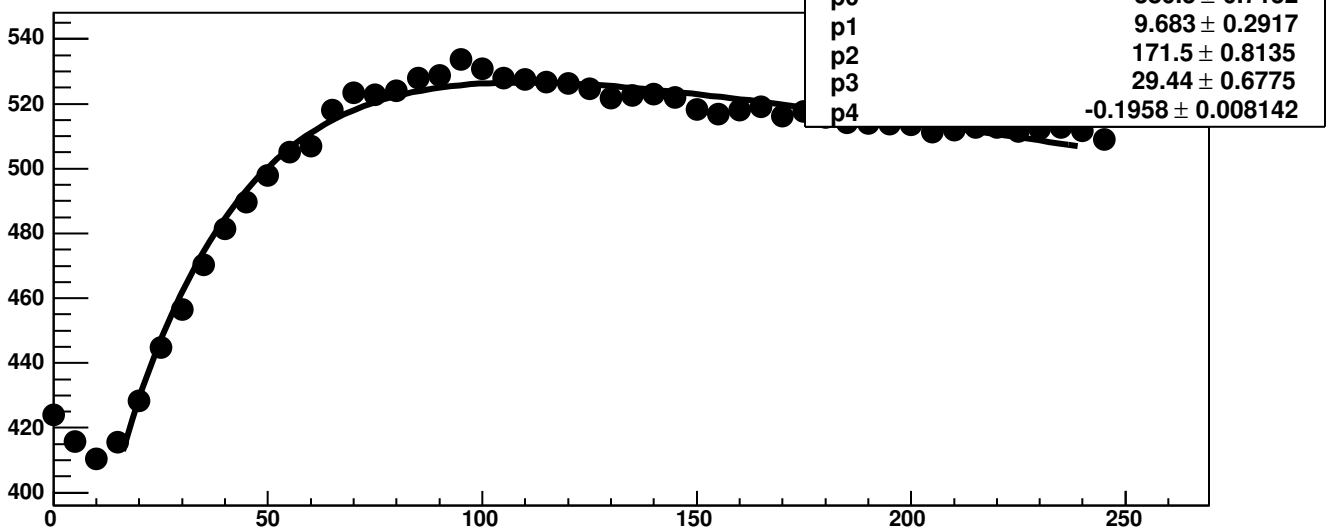
Chip 1, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



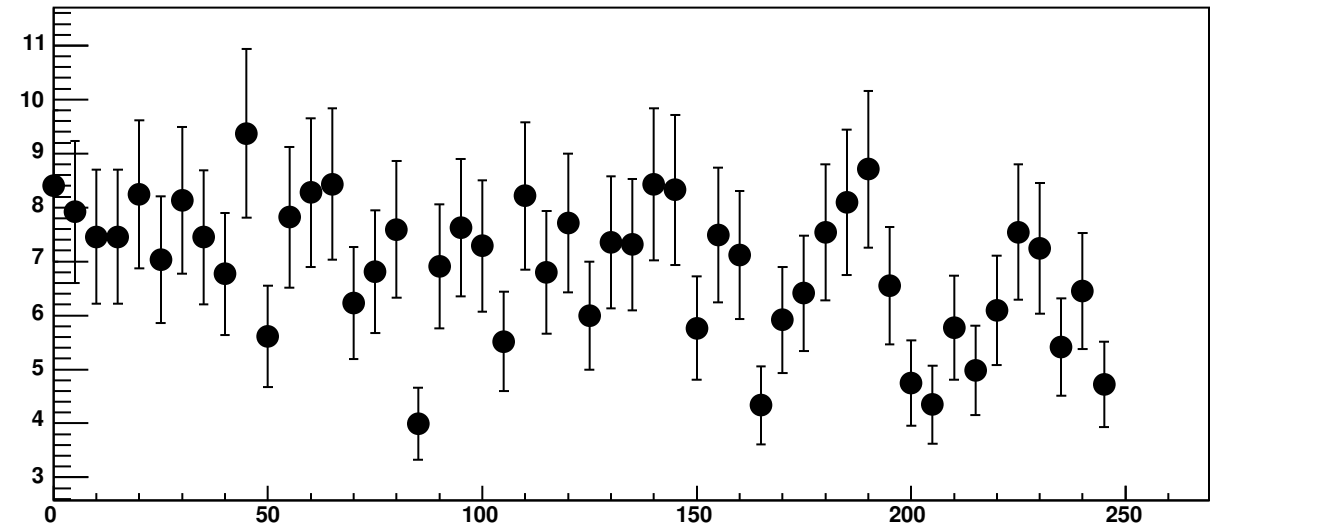
Chip 1, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold



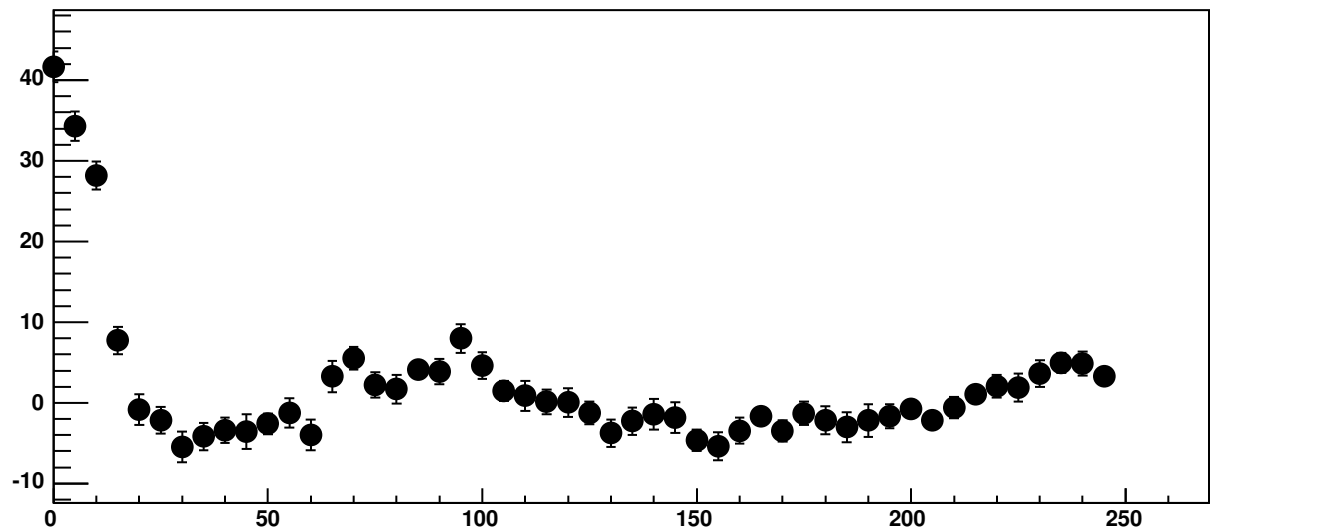
Chip 1, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold

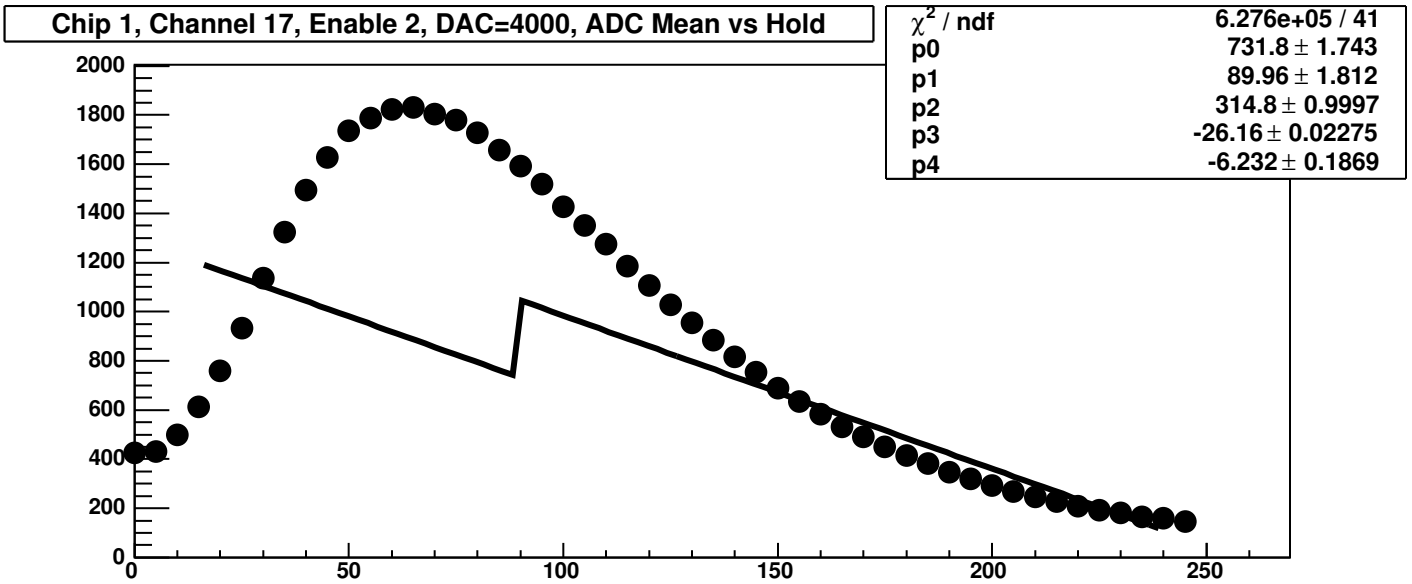


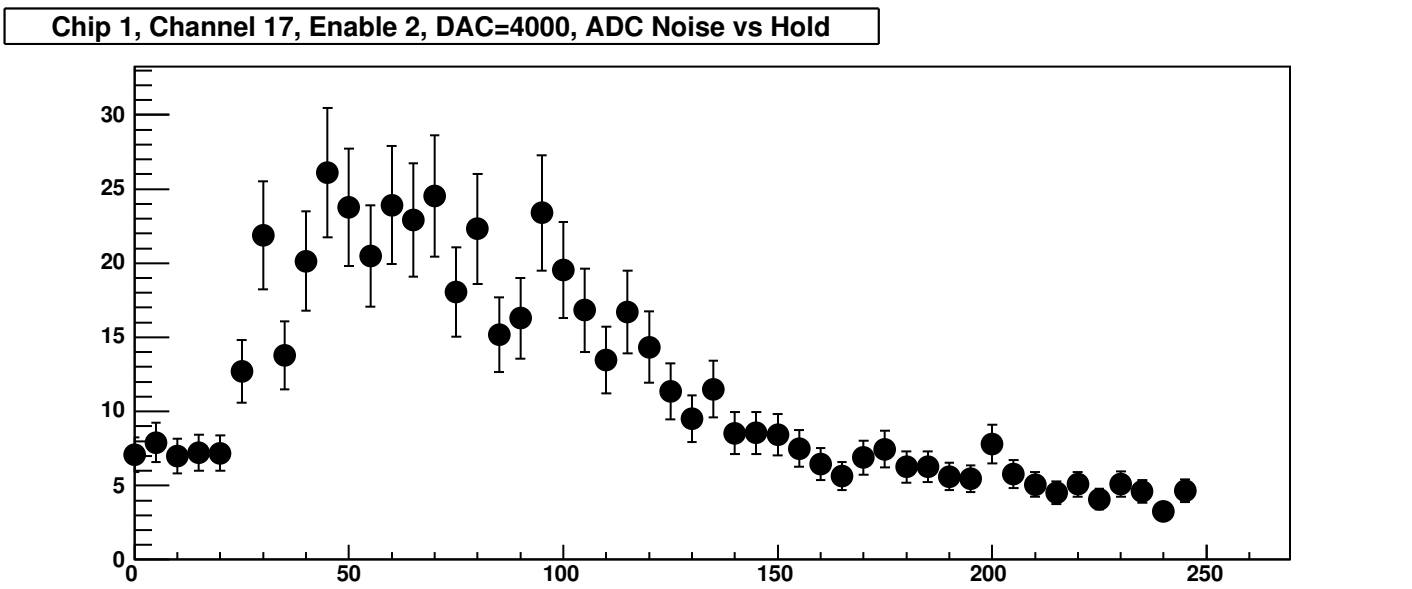
Chip 1, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold

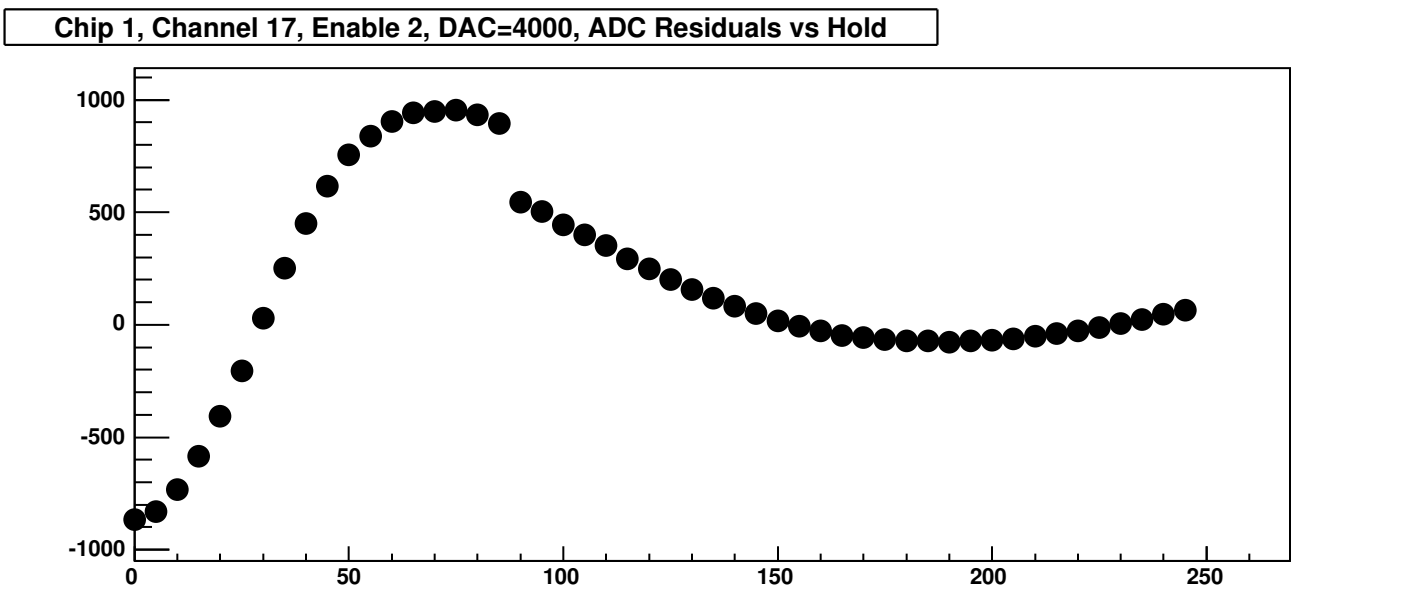


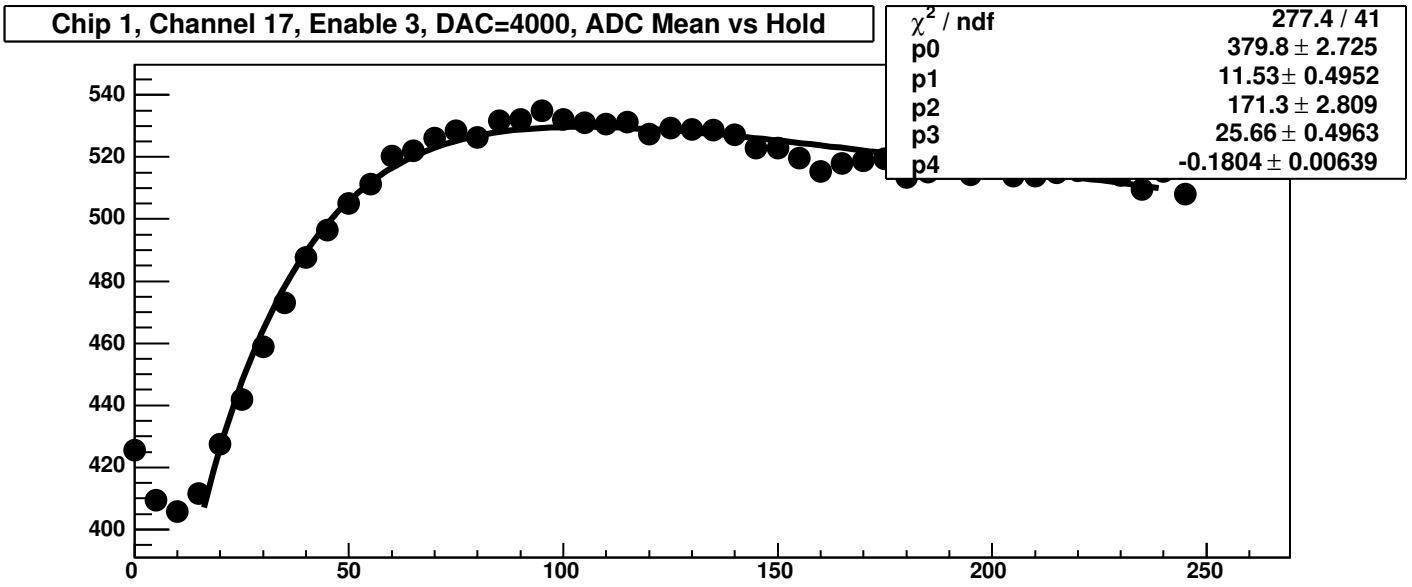
Chip 1, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

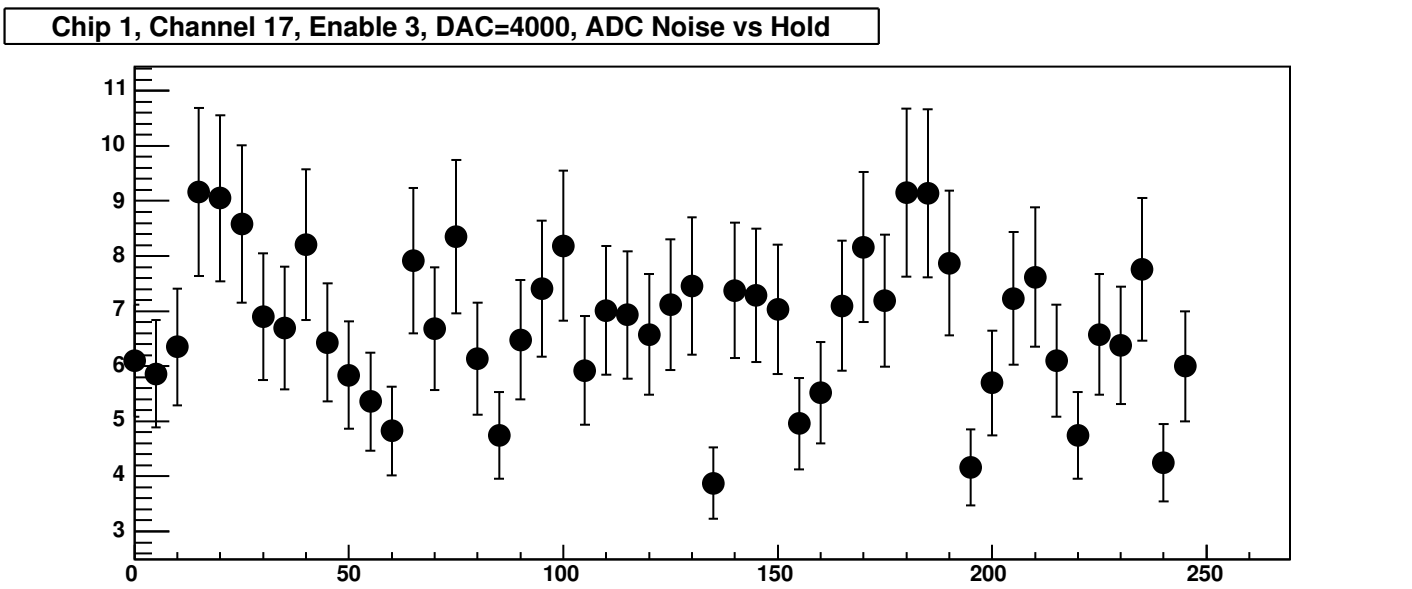


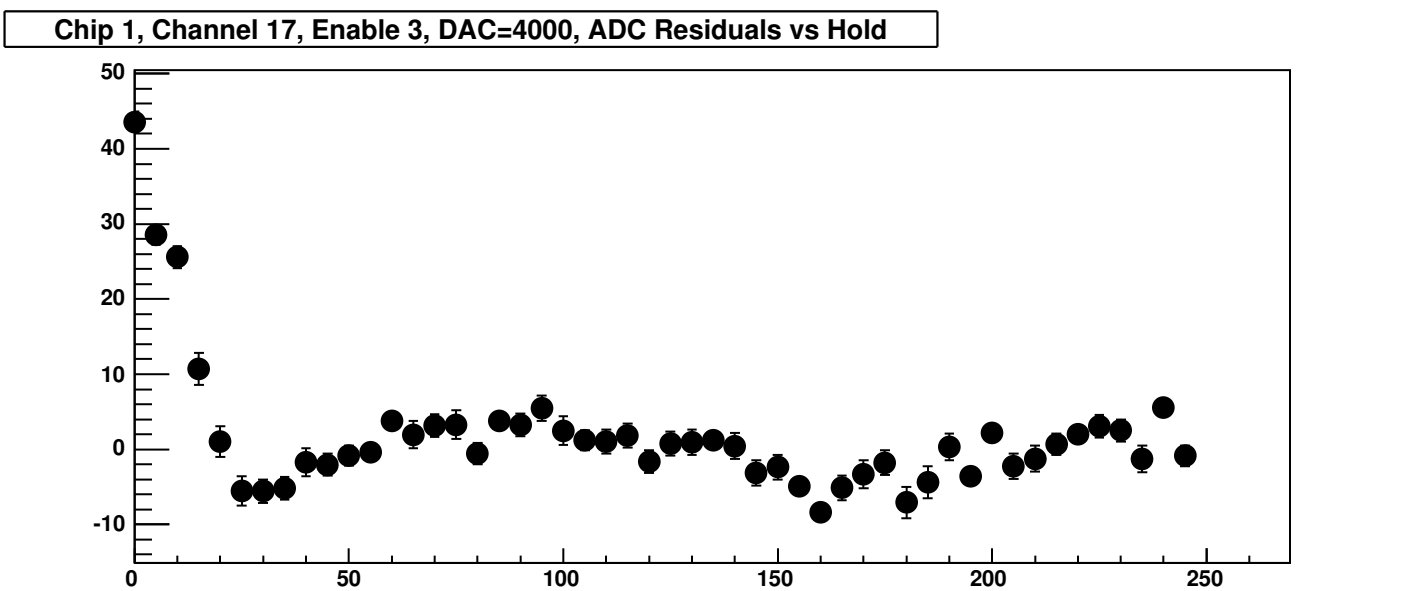

Chip 1, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 6.276e+05 / 41
p0 731.8 ± 1.743
p1 89.96 ± 1.812
p2 314.8 ± 0.9997
p3 -26.16 ± 0.02275
p4 -6.232 ± 0.1869
2000
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 1, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold
30
25
20
15
10
5
0
0
50
100
150
200
250

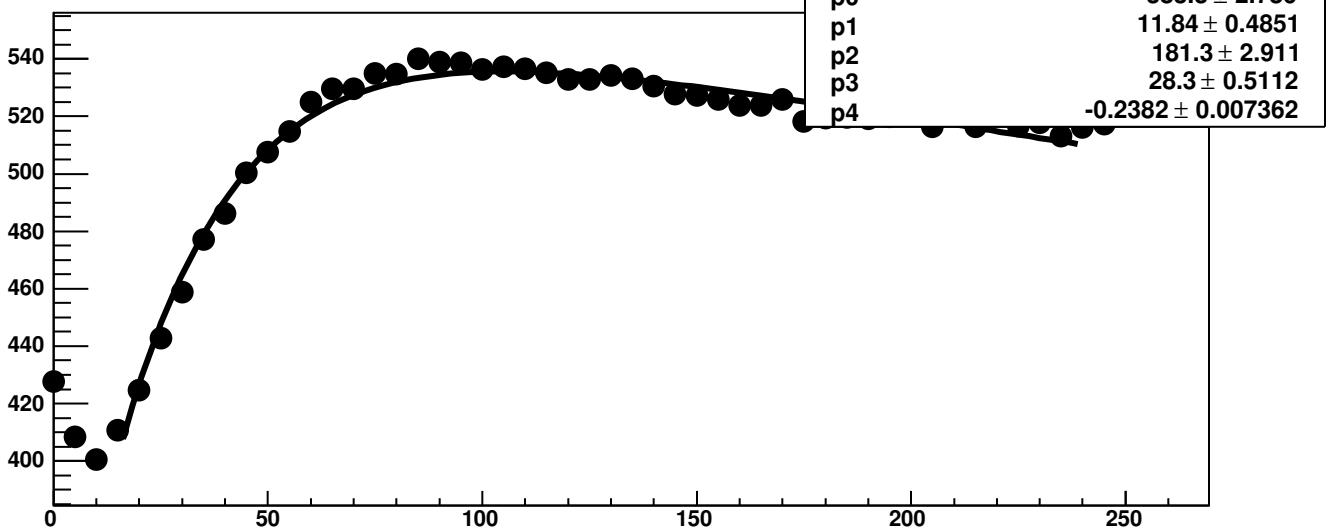

Chip 1, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold
1000
500
0
-500
-1000
0
50
100
150
200
250


Chip 1, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 277.4 / 41
p0 379.8 ± 2.725
p1 11.53 ± 0.4952
p2 171.3 ± 2.809
p3 25.66 ± 0.4963
p4 -0.1804 ± 0.00639
540
520
500
480
460
440
420
400
0
50
100
150
200
250

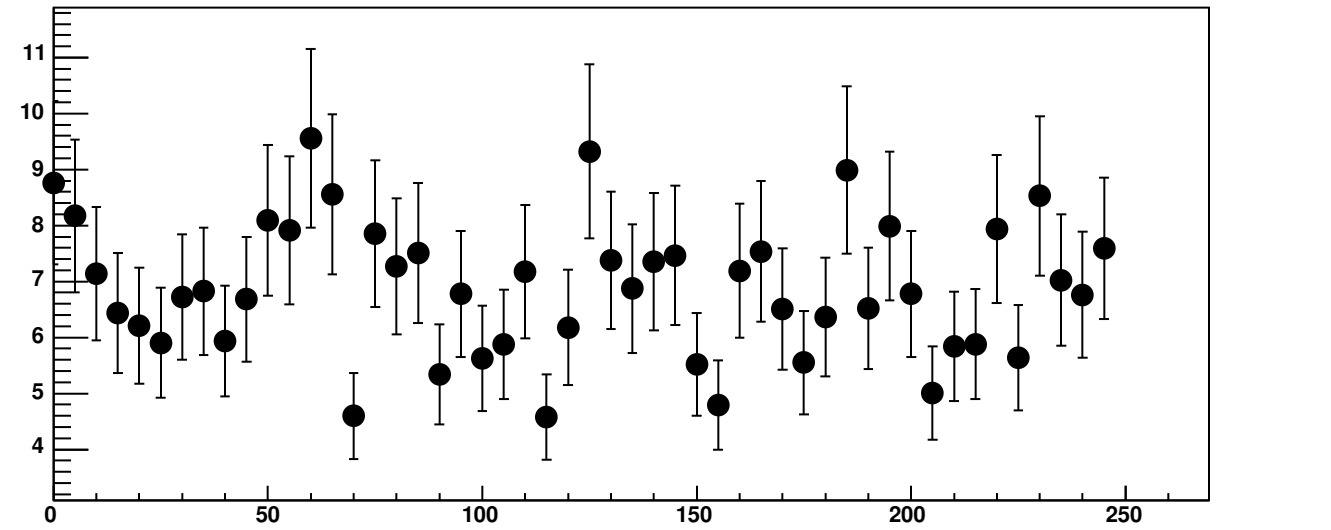

Chip 1, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 1, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250

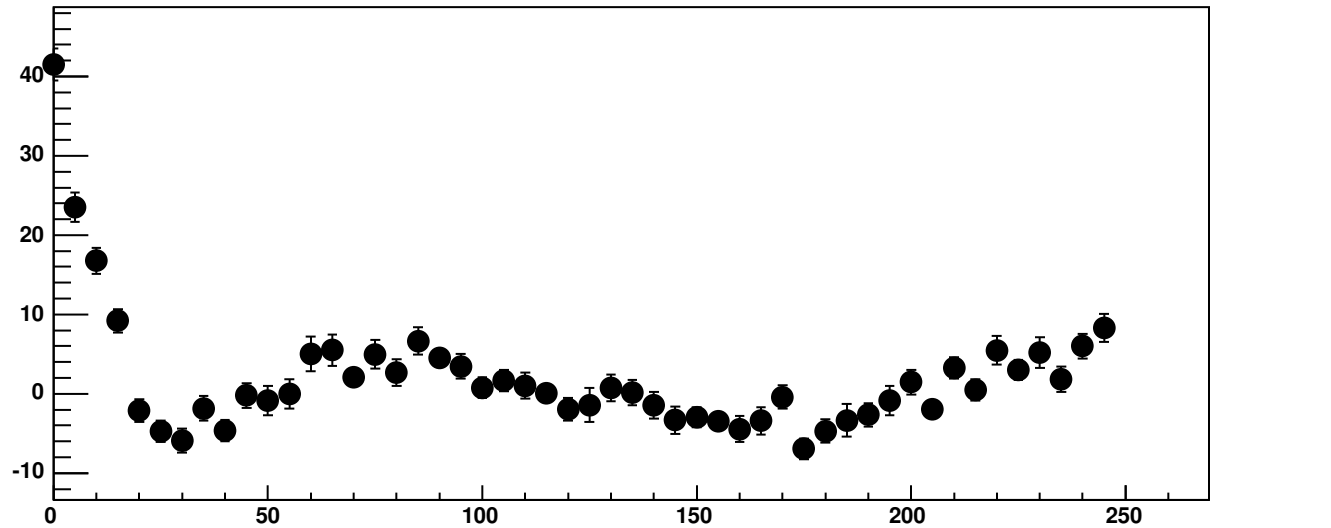
Chip 1, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold

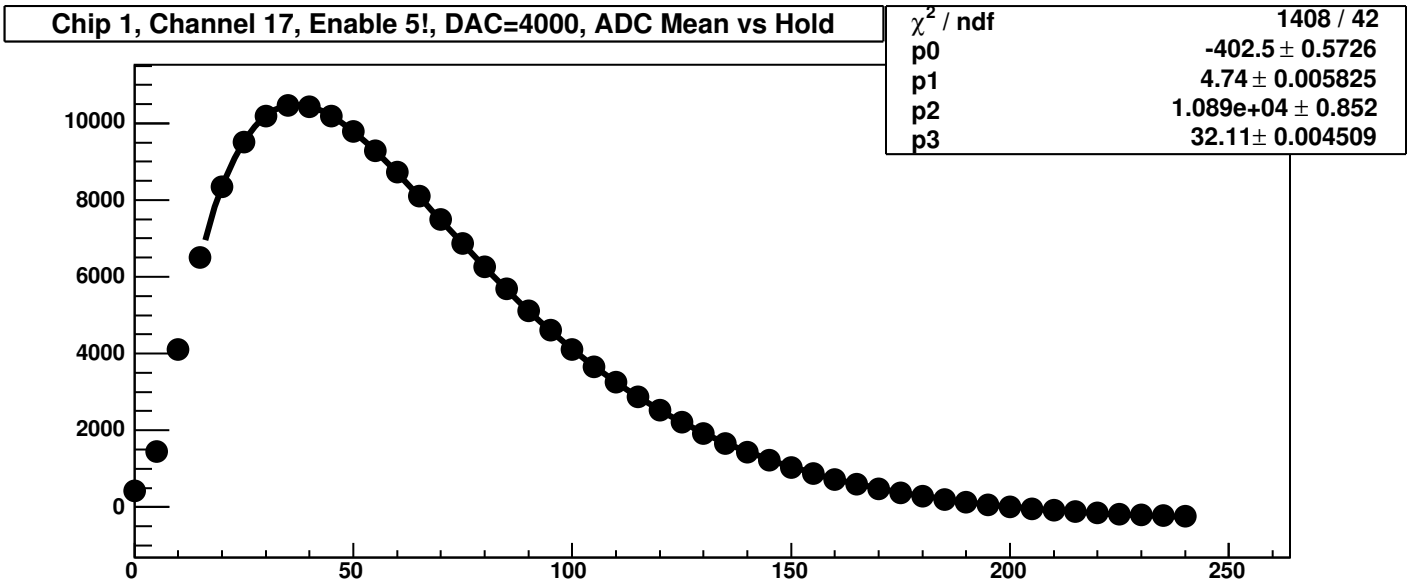


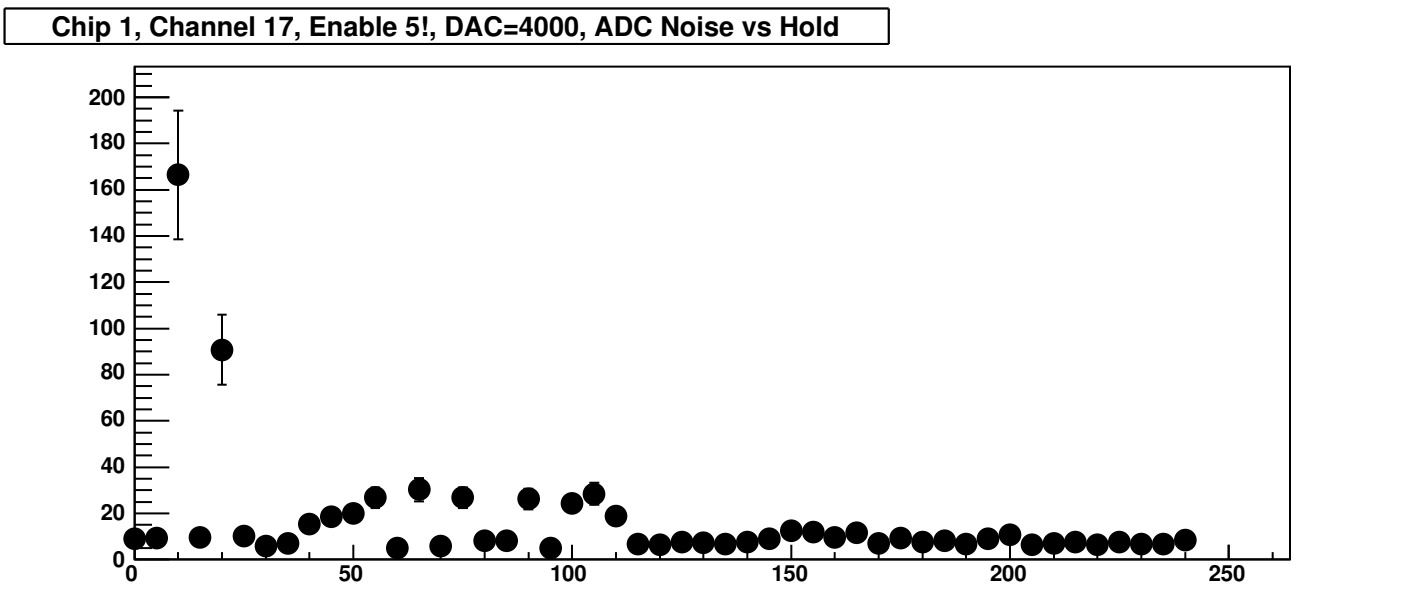
Chip 1, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold

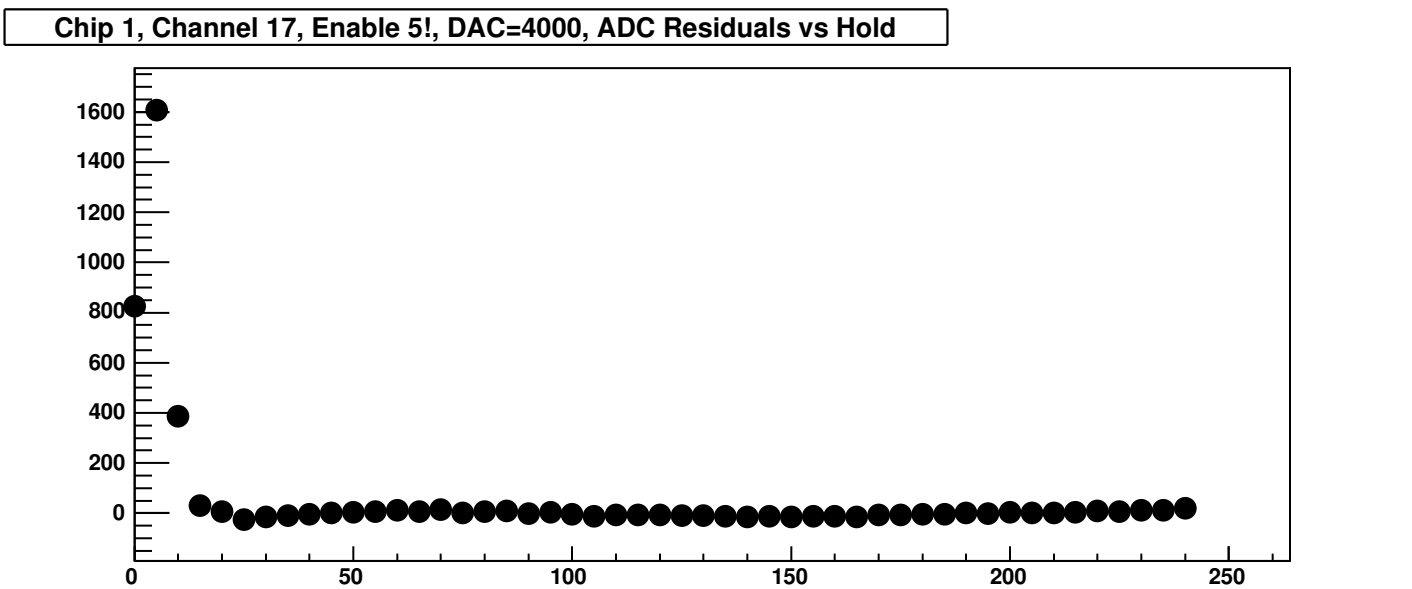


Chip 1, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

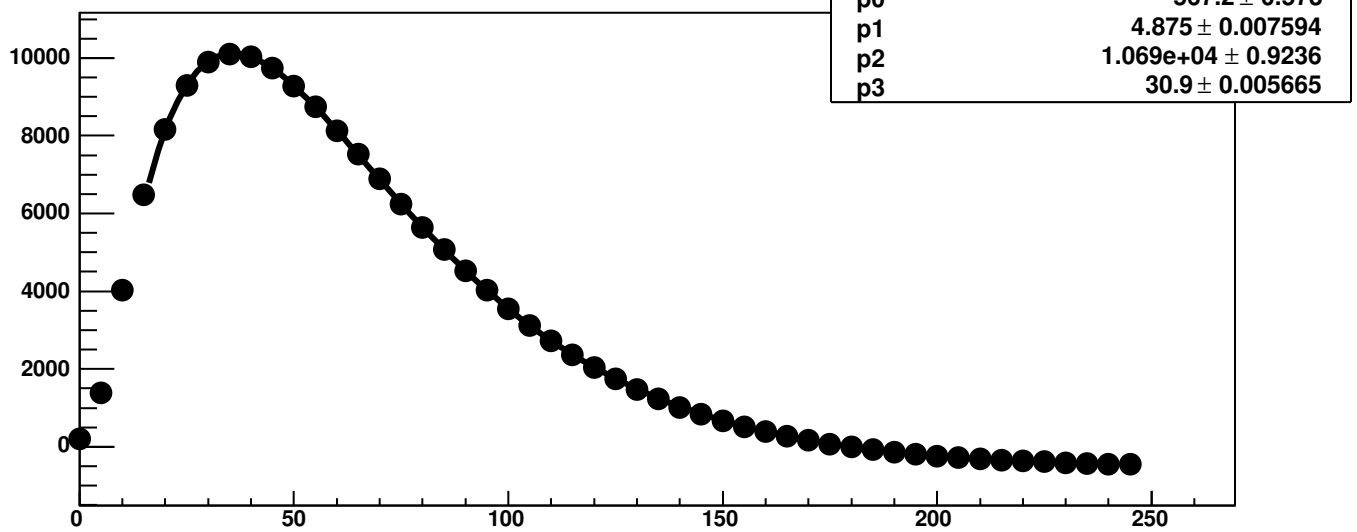



Chip 1, Channel 17, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1408 / 42
p0 -402.5 ± 0.5726
p1 4.74 ± 0.005825
p2 1.089e+04 ± 0.852
p3 32.11 ± 0.004509
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

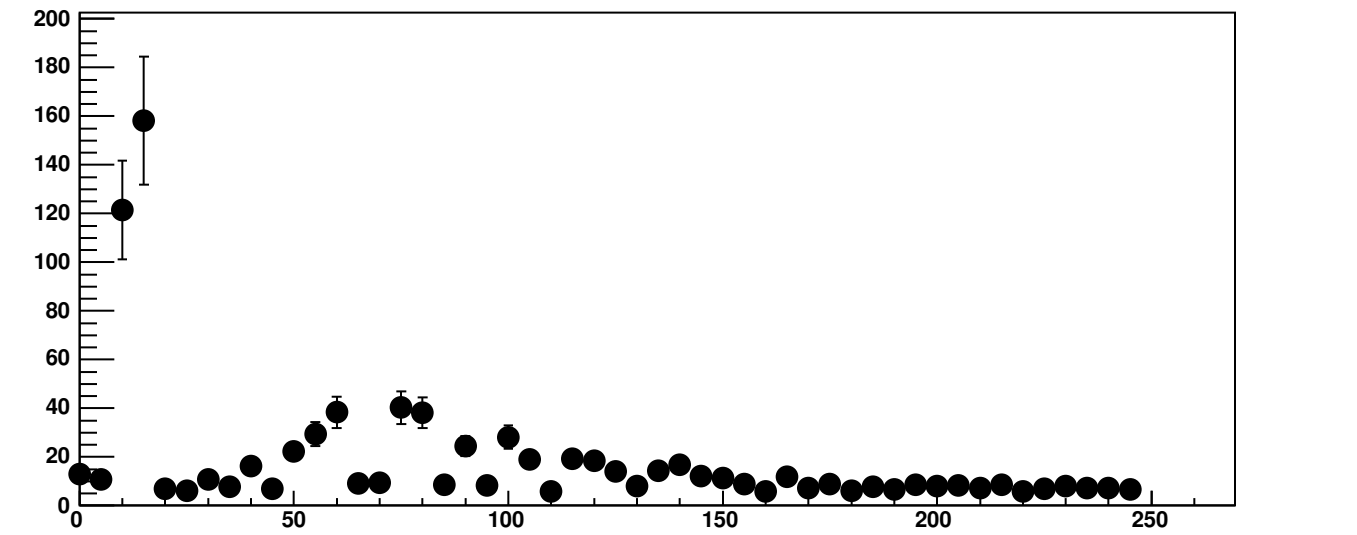

Chip 1, Channel 17, Enable 5!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 1, Channel 17, Enable 5!, DAC=4000, ADC Residuals vs Hold
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

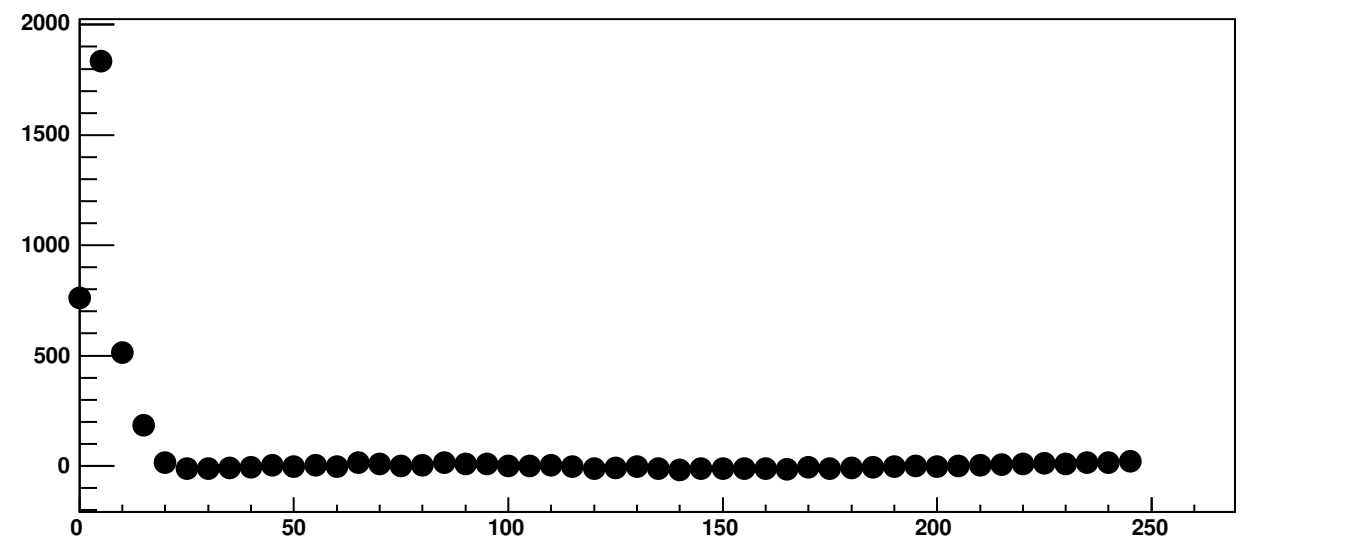
Chip 2, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold



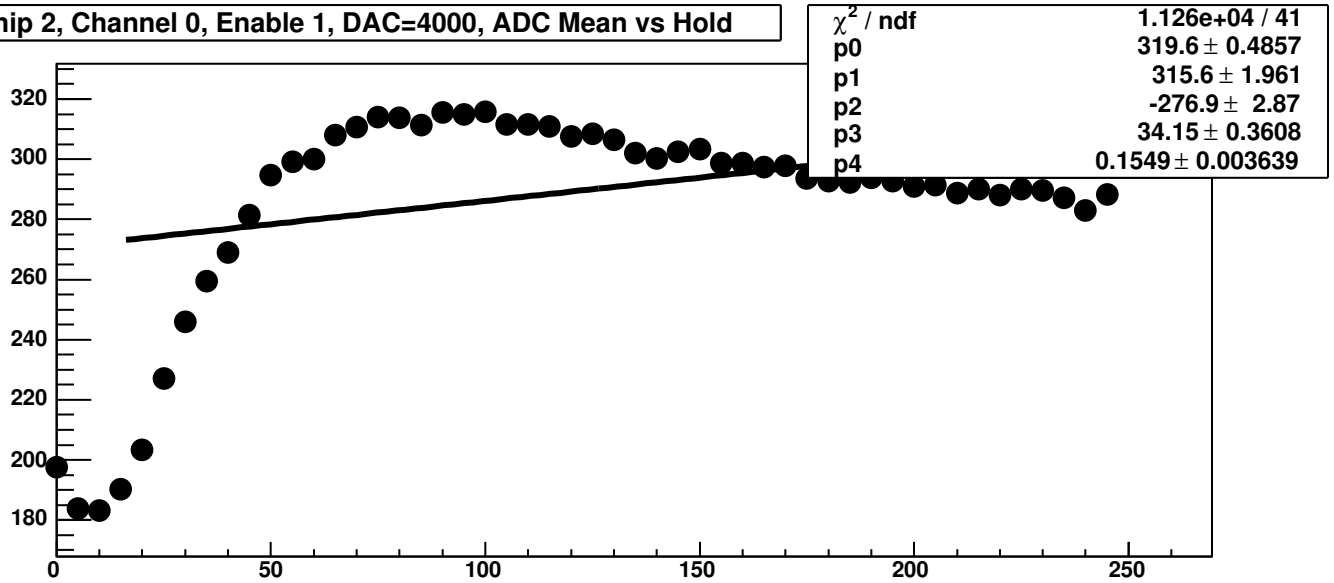
Chip 2, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold



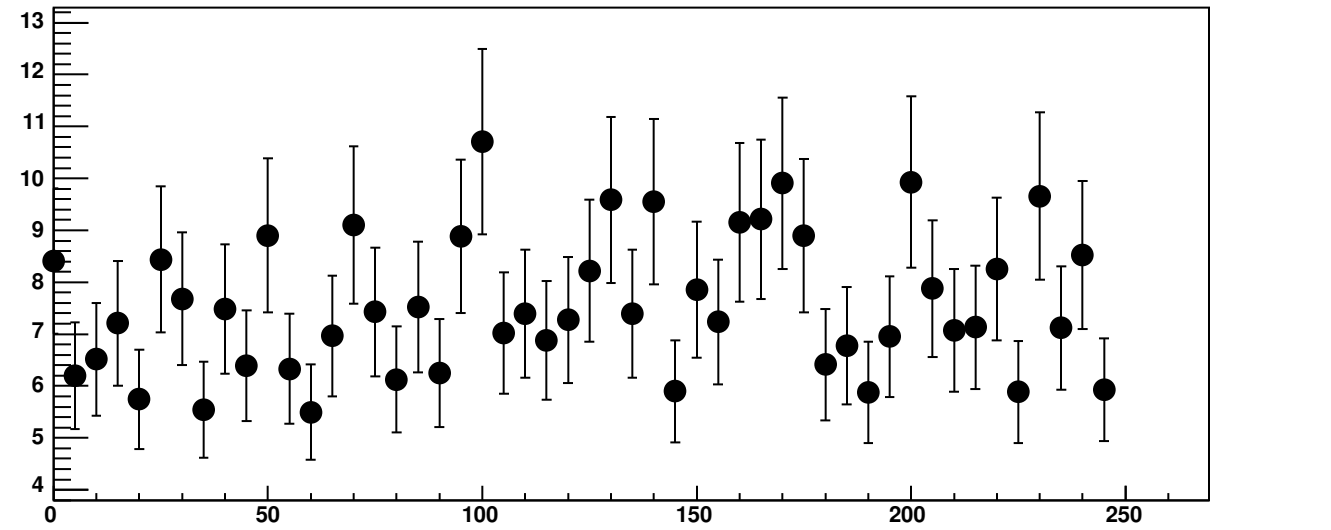
Chip 2, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold



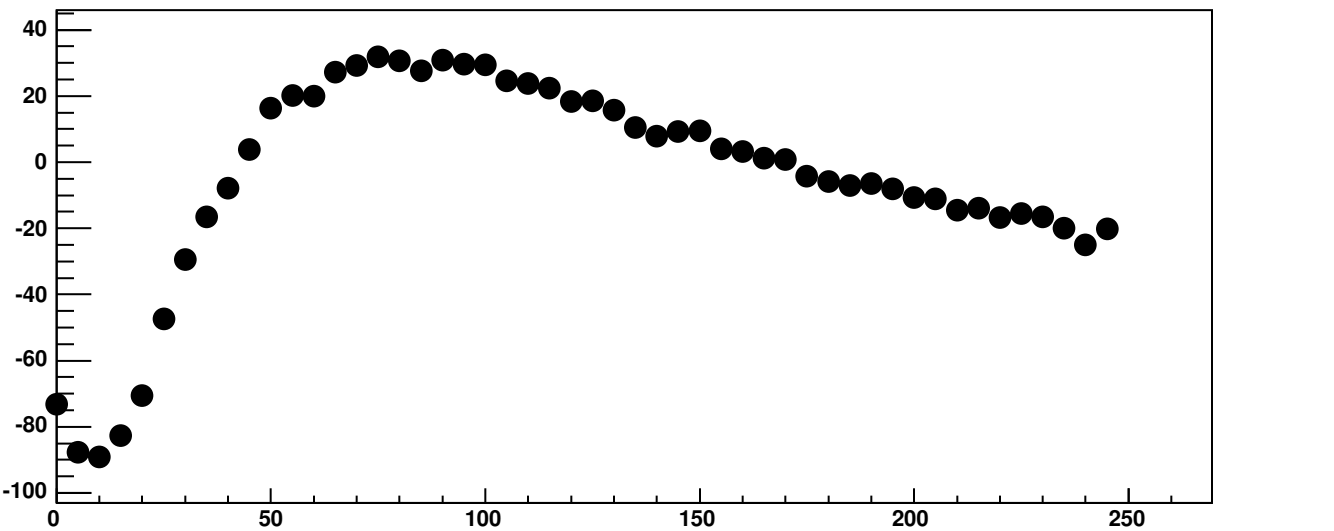
Chip 2, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold

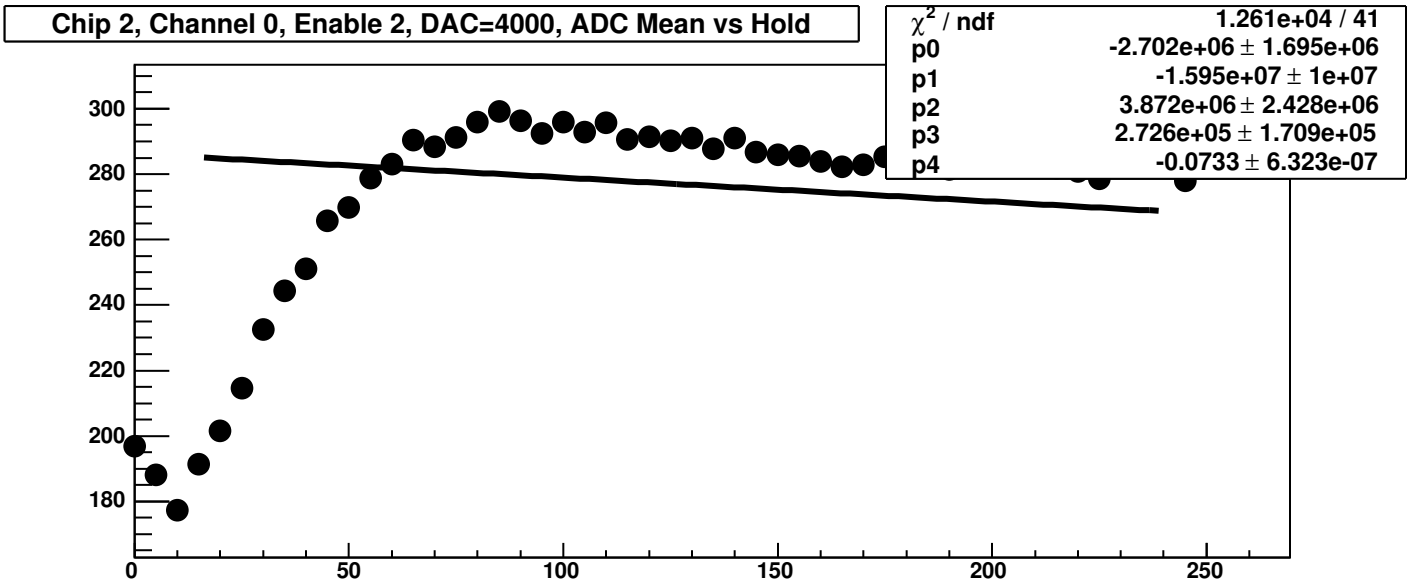


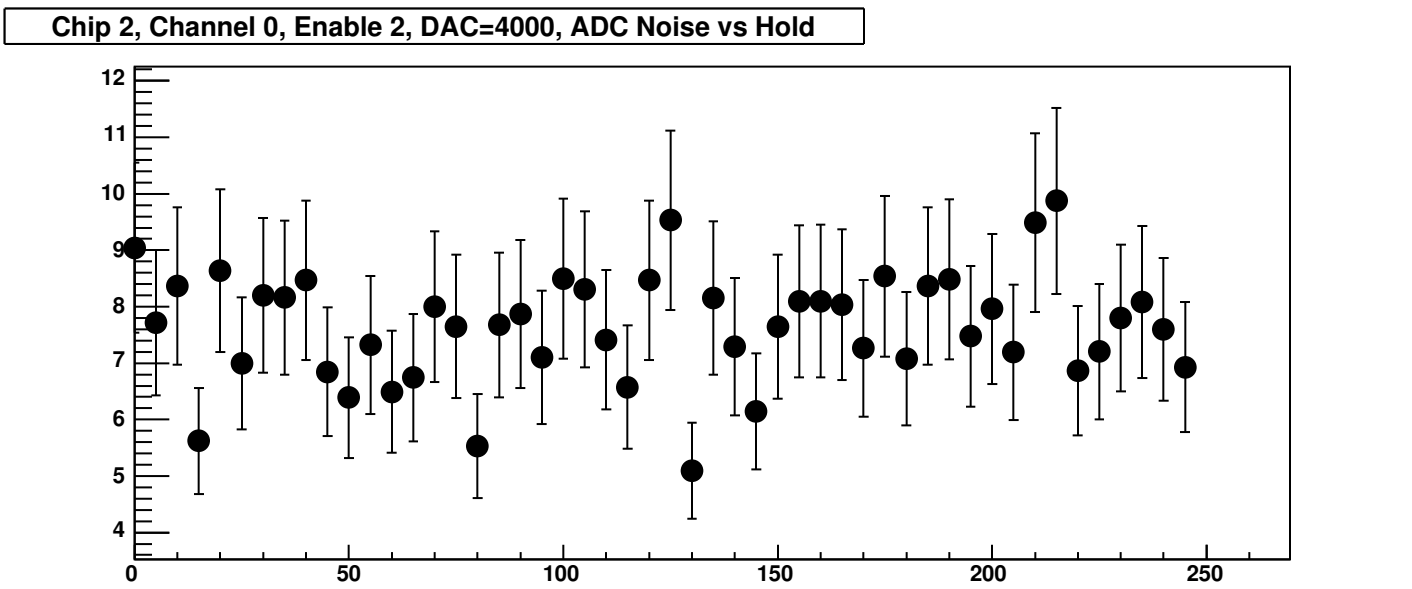
Chip 2, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold

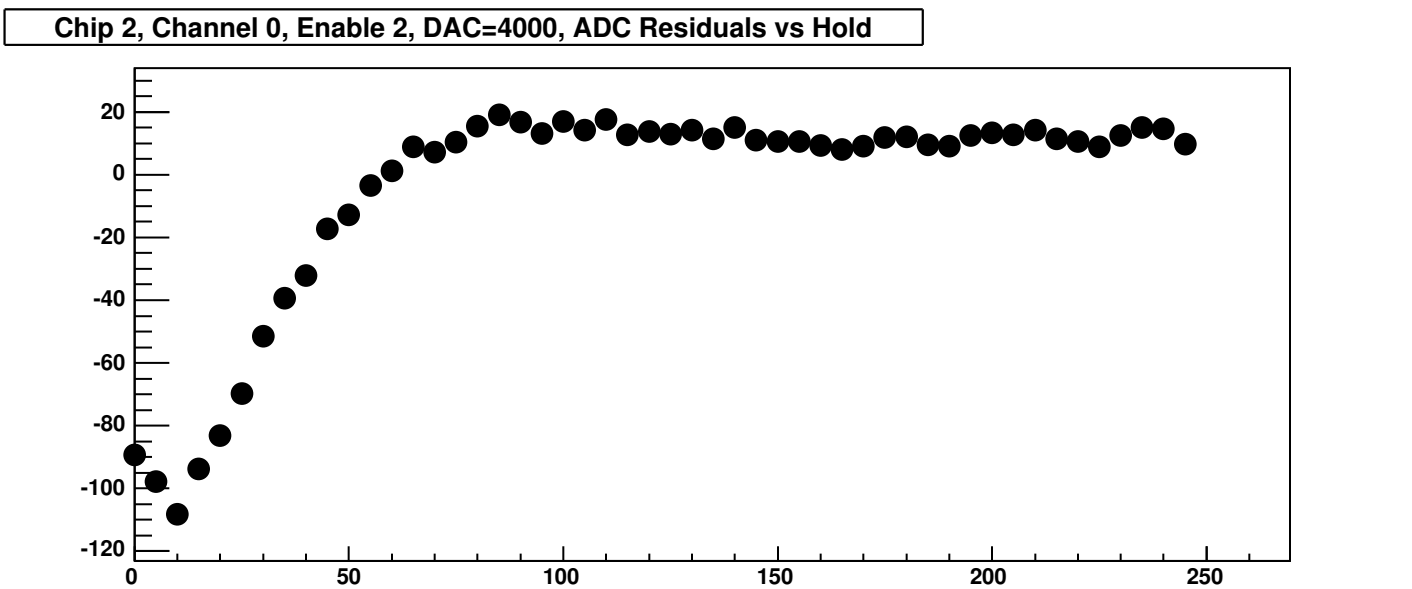


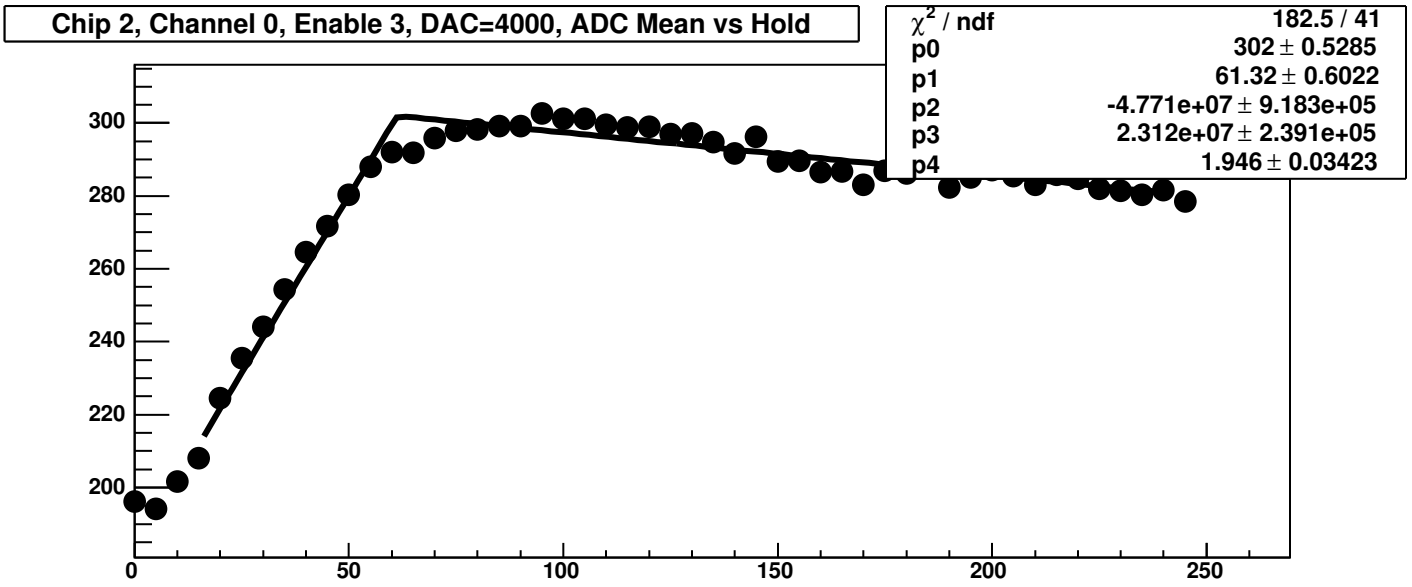
Chip 2, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold

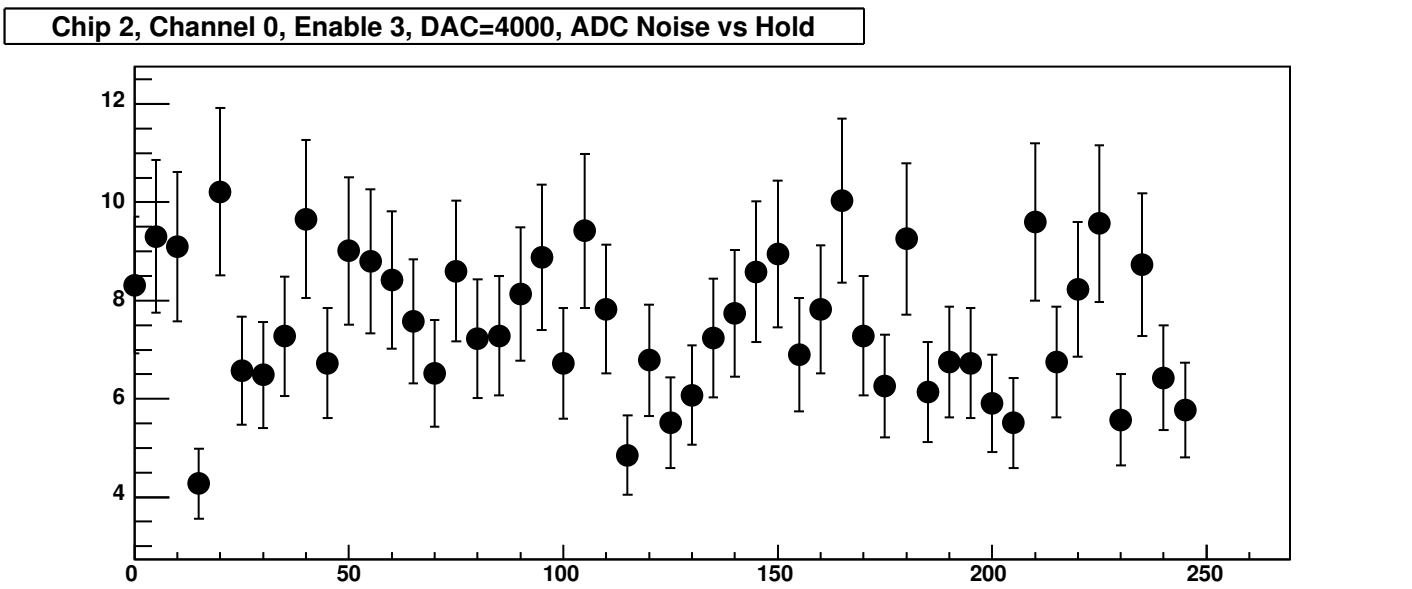


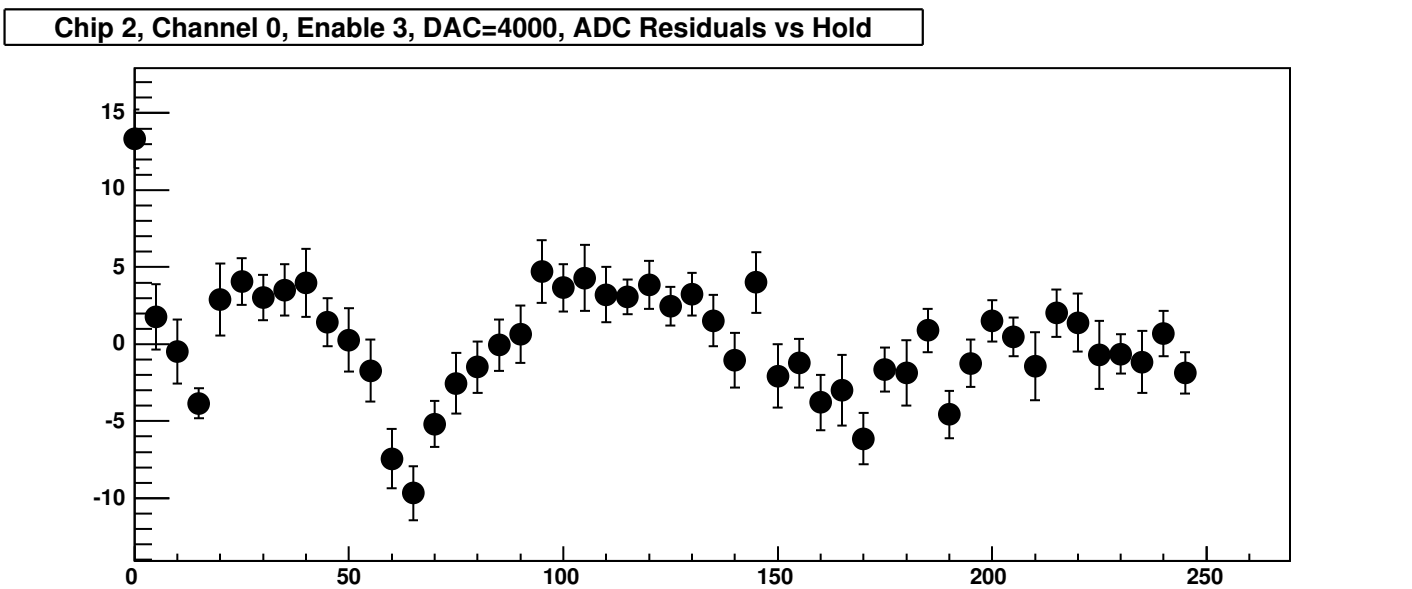

Chip 2, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.261e+04 / 41
p0 -2.702e+06 ± 1.695e+06
p1 -1.595e+07 ± 1e+07
p2 3.872e+06 ± 2.428e+06
p3 2.726e+05 ± 1.709e+05
p4 -0.0733 ± 6.323e-07
300
280
260
240
220
200
180
0
50
100
150
200
250

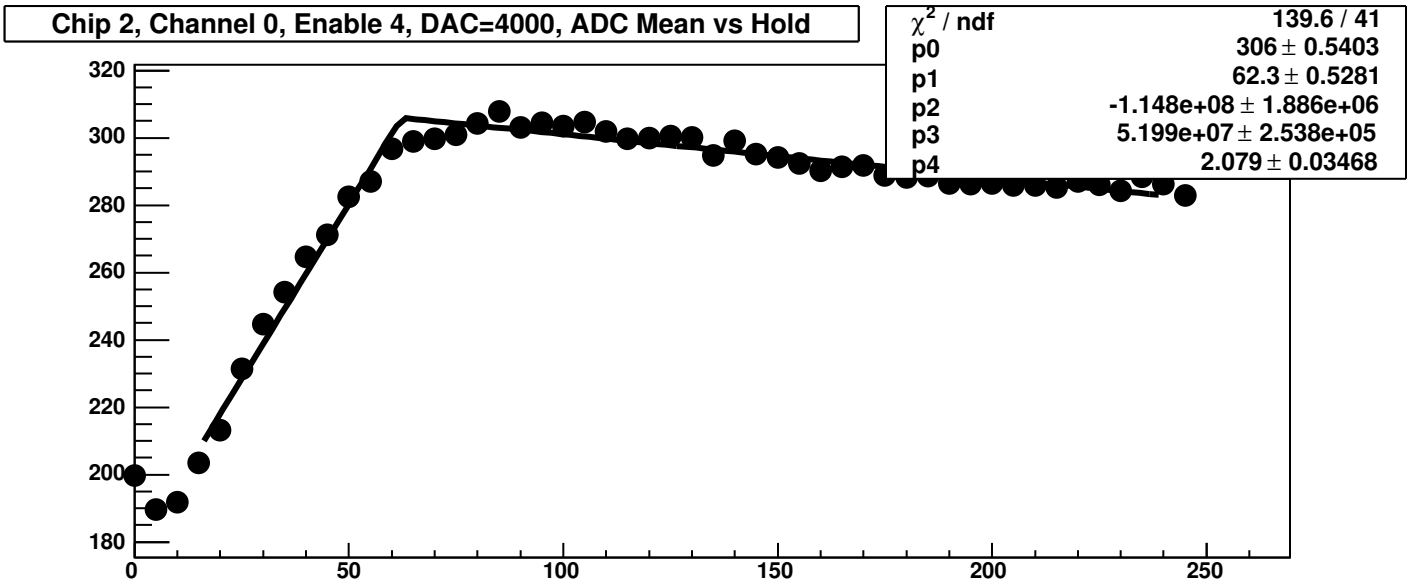

Chip 2, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

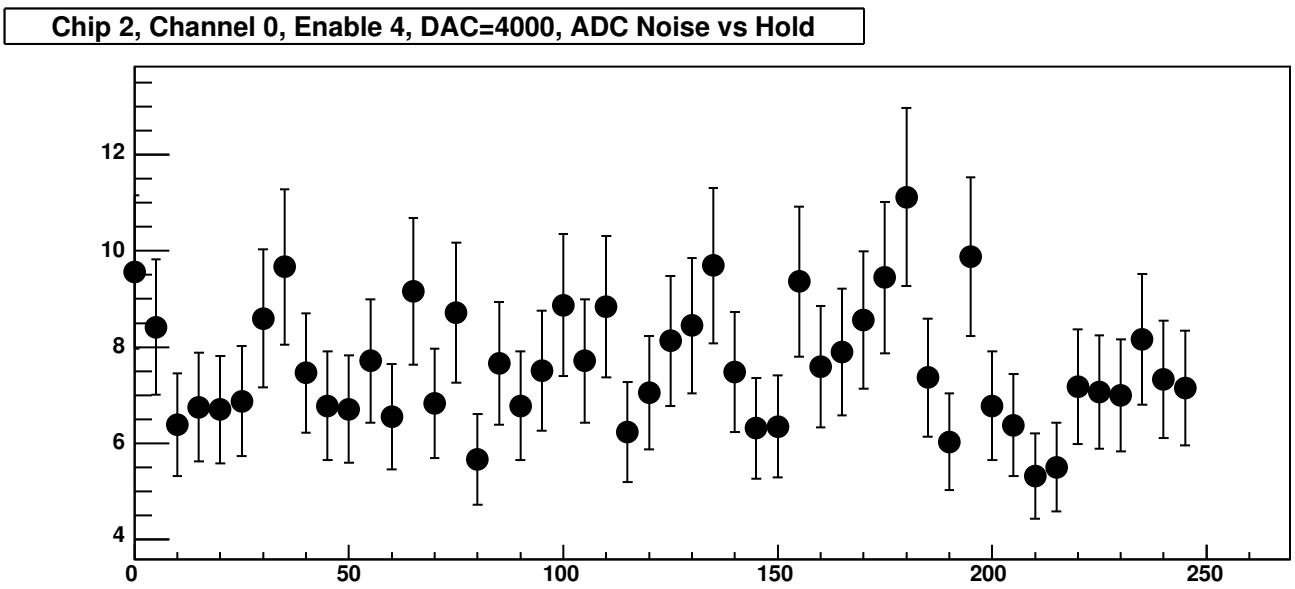

Chip 2, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
-80
-100
-120
0
50
100
150
200
250

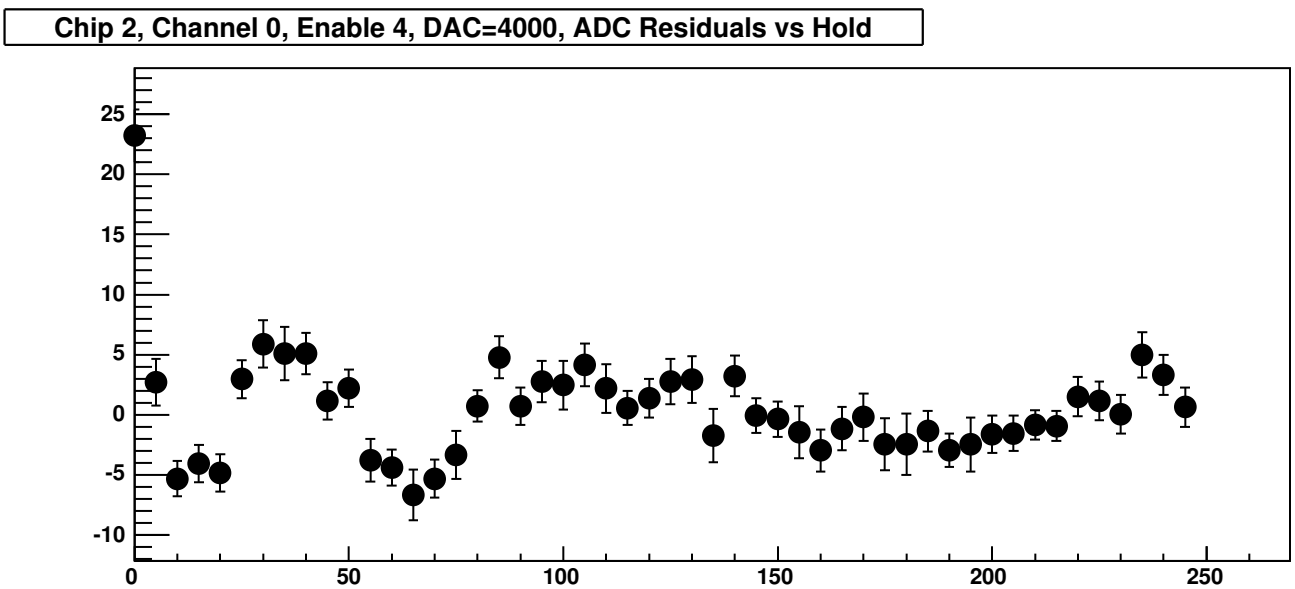

Chip 2, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 182.5 / 41
p0 302 ± 0.5285
p1 61.32 ± 0.6022
p2 -4.771e+07 ± 9.183e+05
p3 2.312e+07 ± 2.391e+05
p4 1.946 ± 0.03423
300
280
260
240
220
200
0
50
100
150
200
250


Chip 2, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

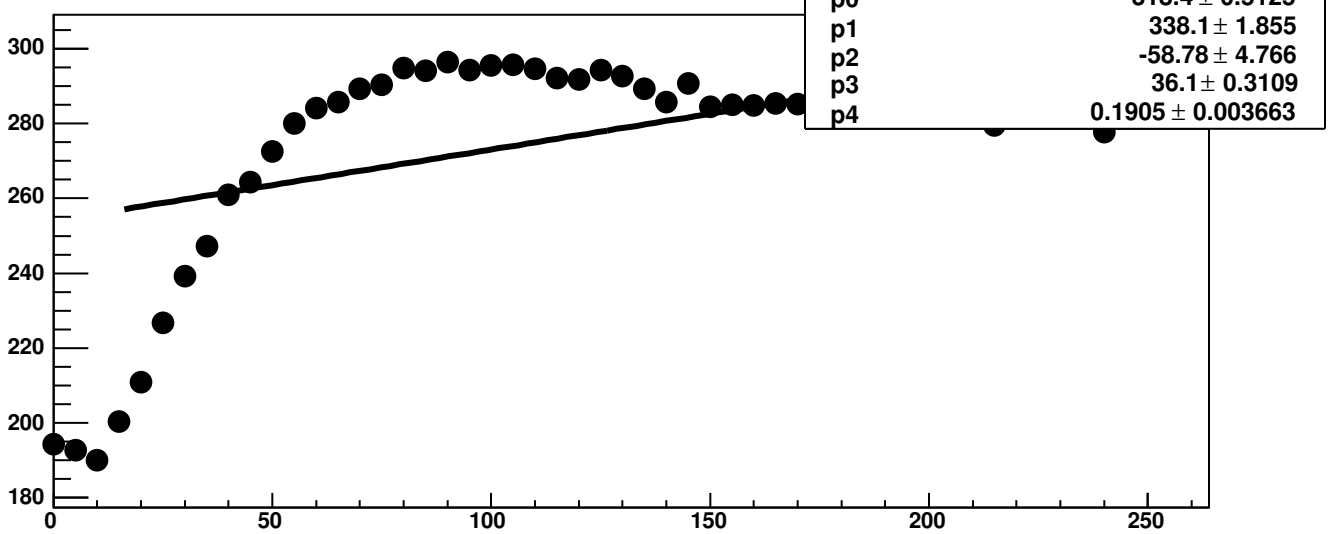

Chip 2, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
0
50
100
150
200
250


Chip 2, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 139.6 / 41
p0 306 ± 0.5403
p1 62.3 ± 0.5281
p2 -1.148e+08 ± 1.886e+06
p3 5.199e+07 ± 2.538e+05
p4 2.079 ± 0.03468
320
300
280
260
240
220
200
180
0
50
100
150
200
250

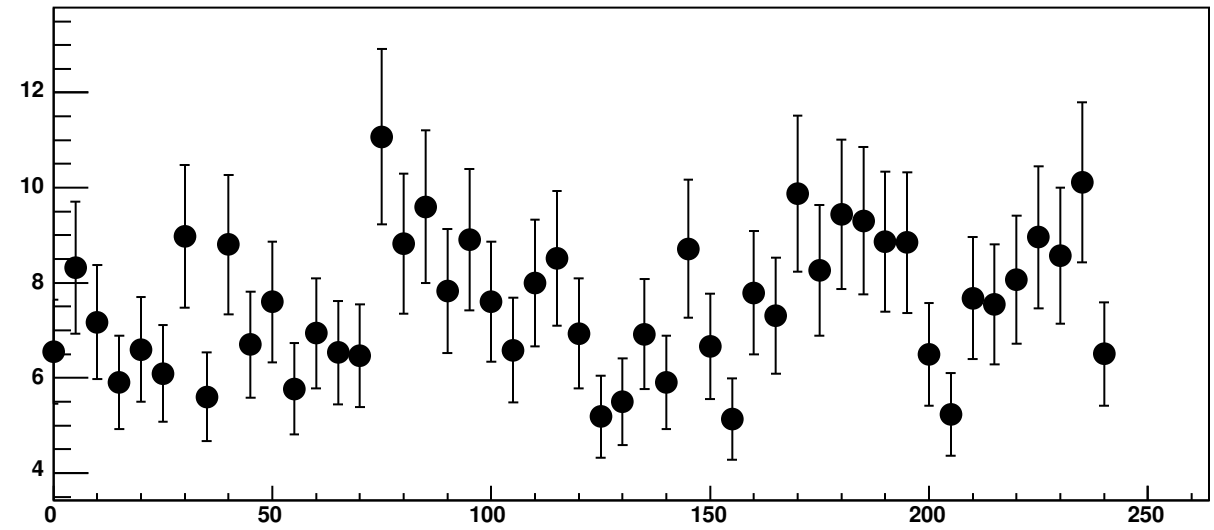

Chip 2, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 2, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
0
50
100
150
200
250

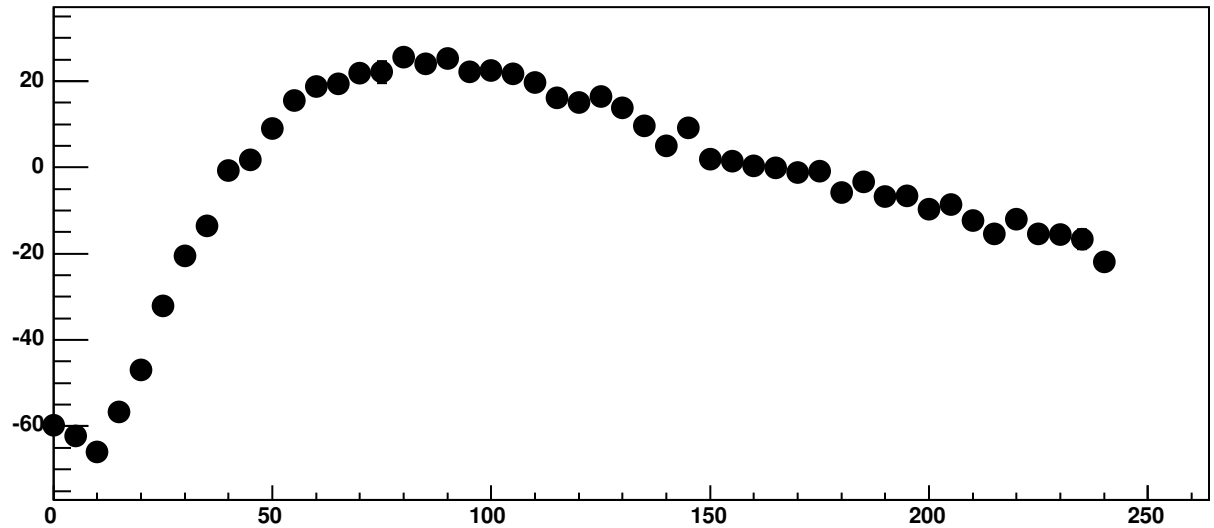
Chip 2, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold



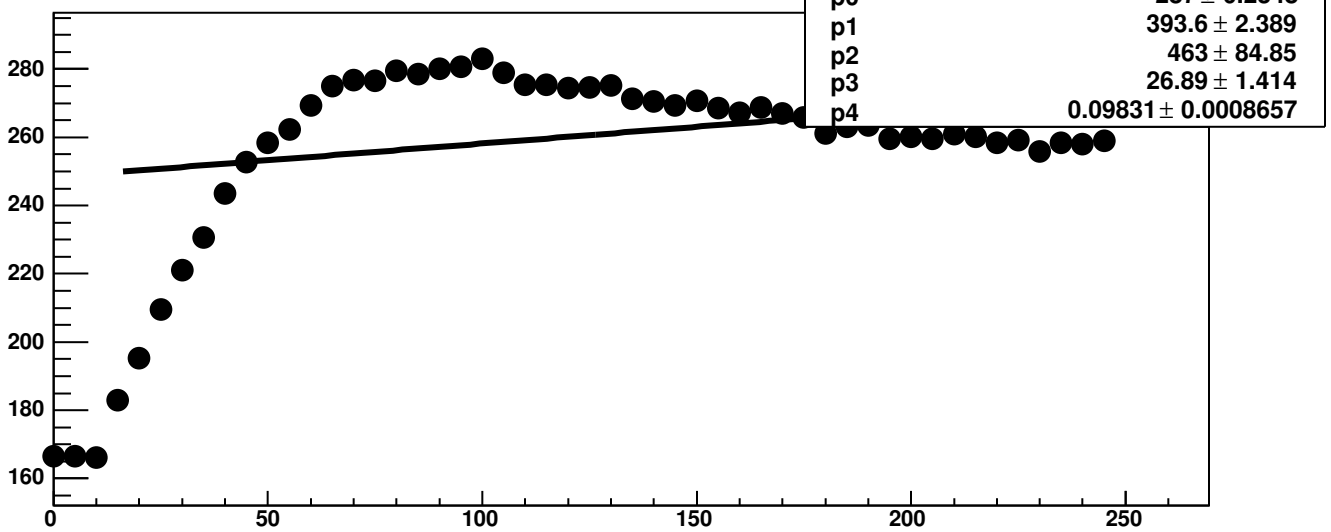
Chip 2, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



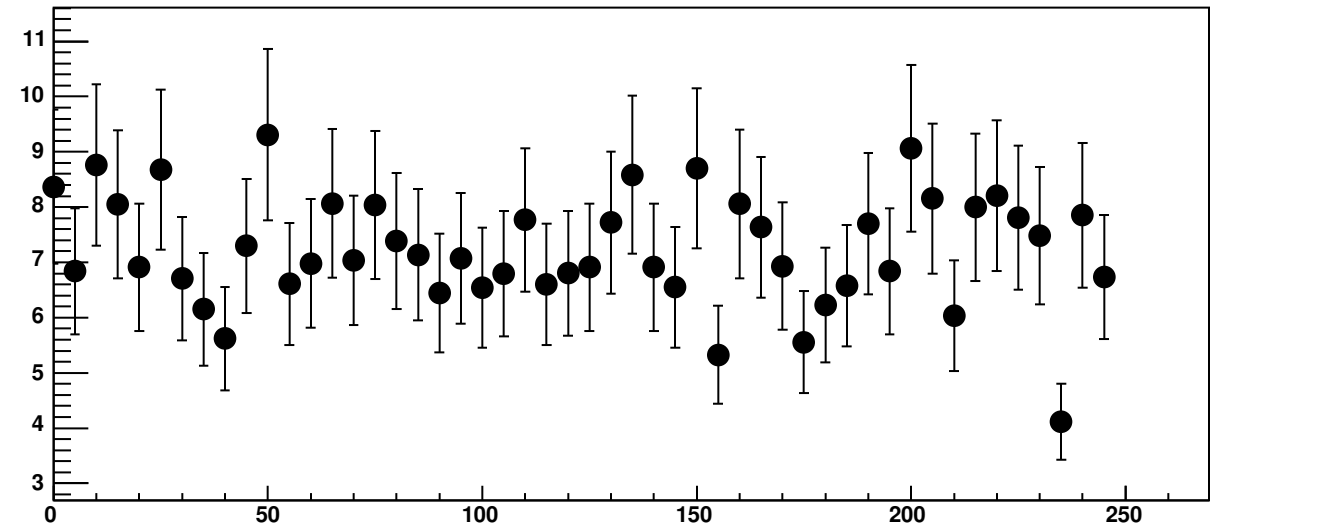
Chip 2, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold



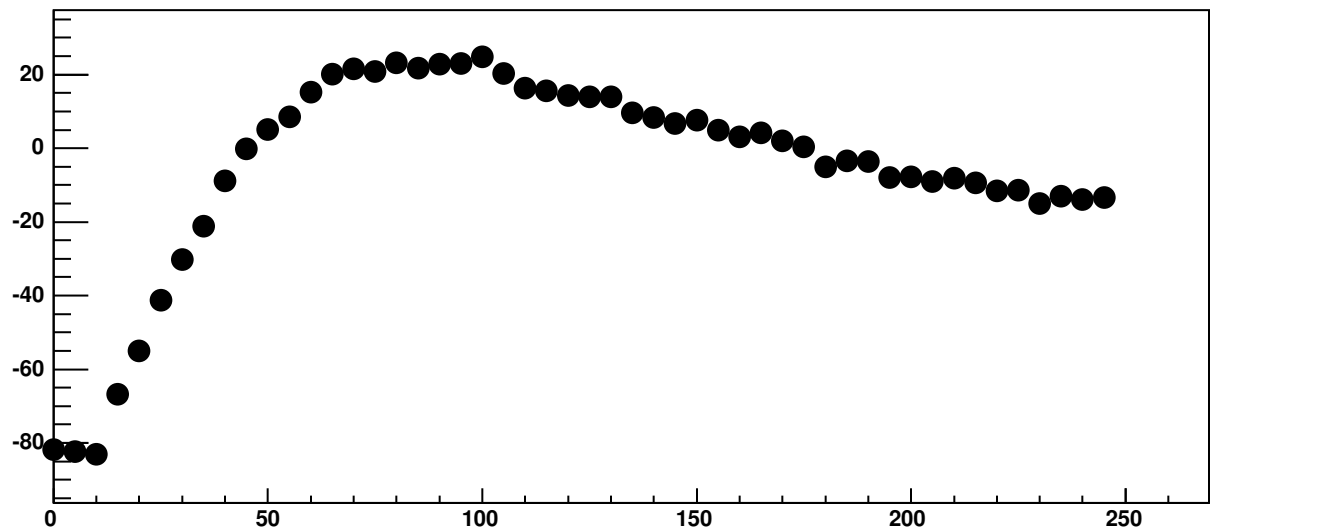
Chip 2, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold



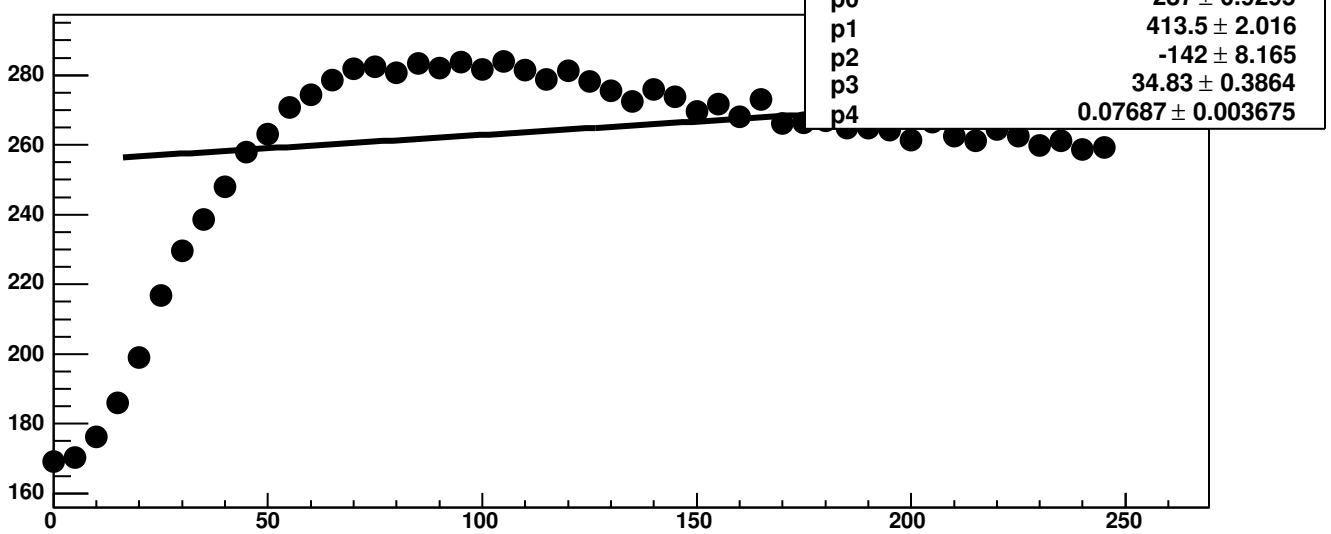
Chip 2, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold



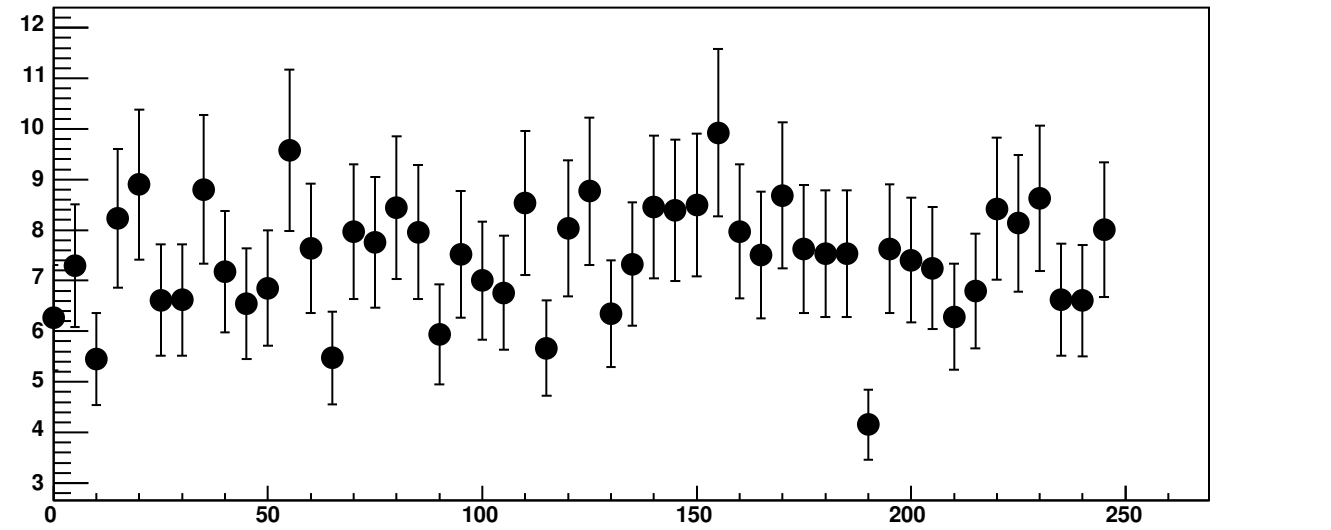
Chip 2, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold



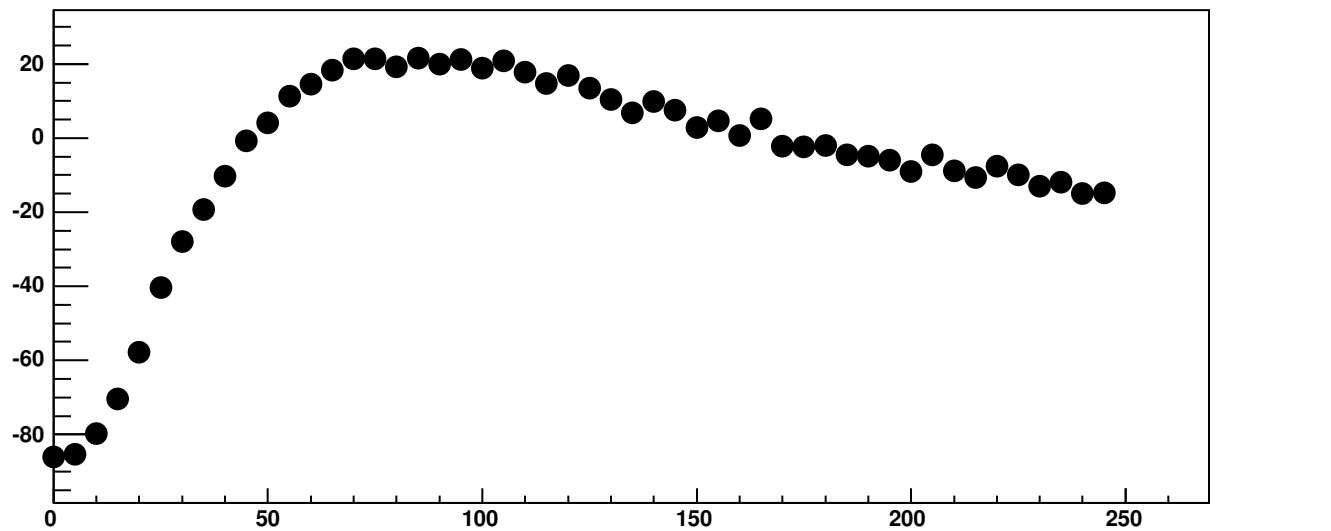
Chip 2, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold

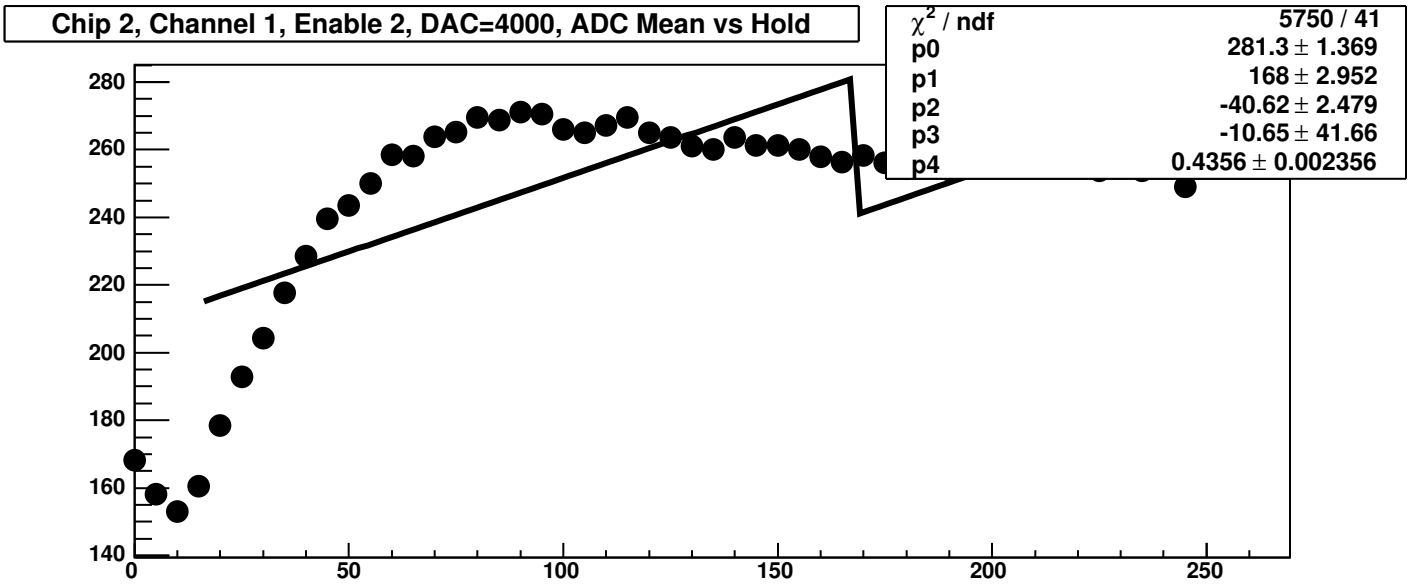


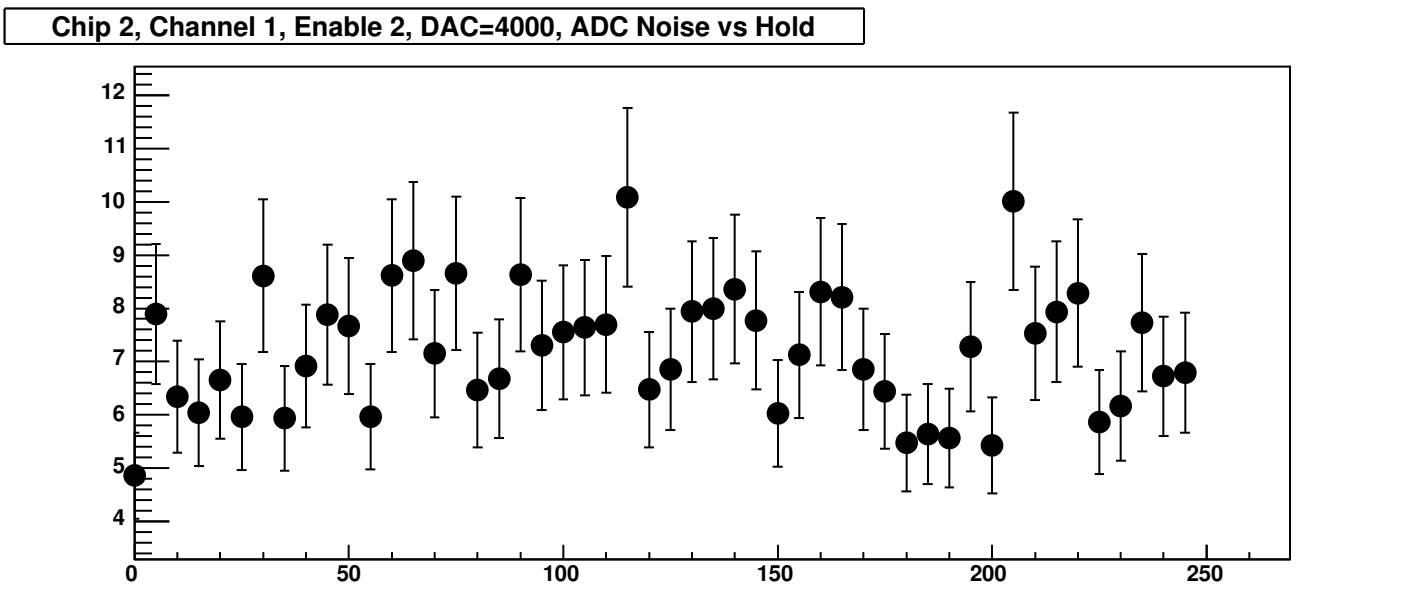
Chip 2, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold

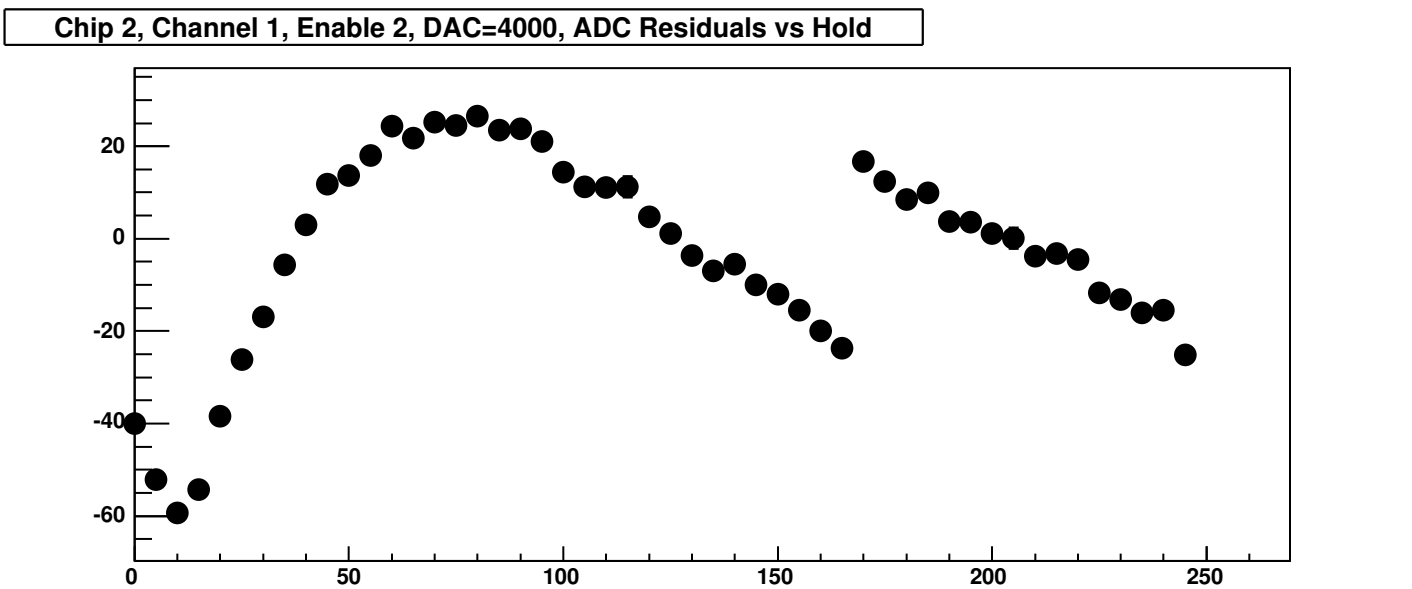


Chip 2, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold



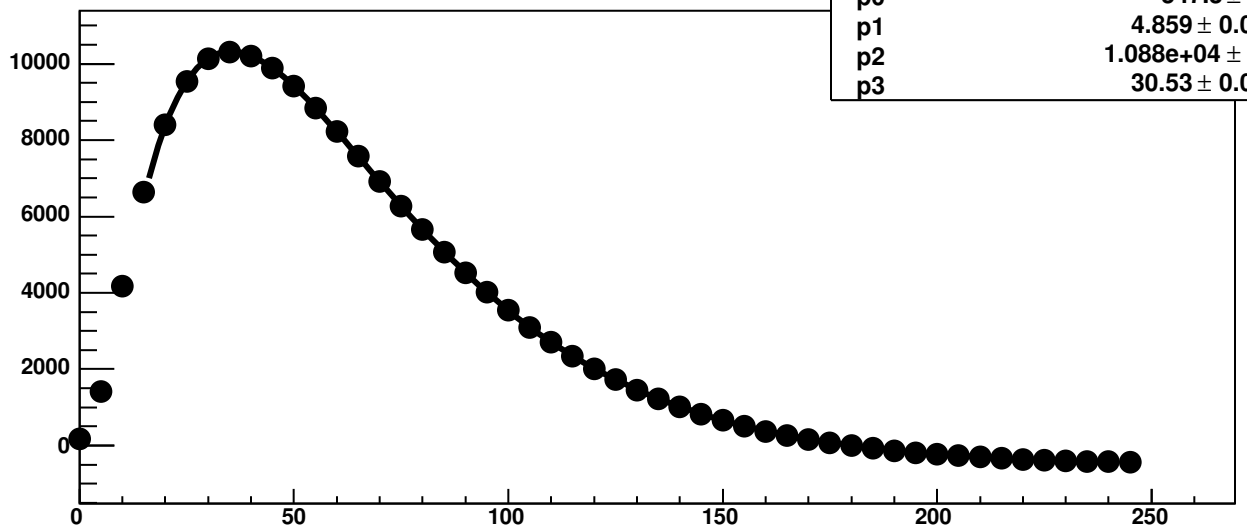

Chip 2, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 5750 / 41
p0 281.3 ± 1.369
p1 168 ± 2.952
p2 -40.62 ± 2.479
p3 -10.65 ± 41.66
p4 0.4356 ± 0.002356
280
260
240
220
200
180
160
140
0
50
100
150
200
250


Chip 2, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

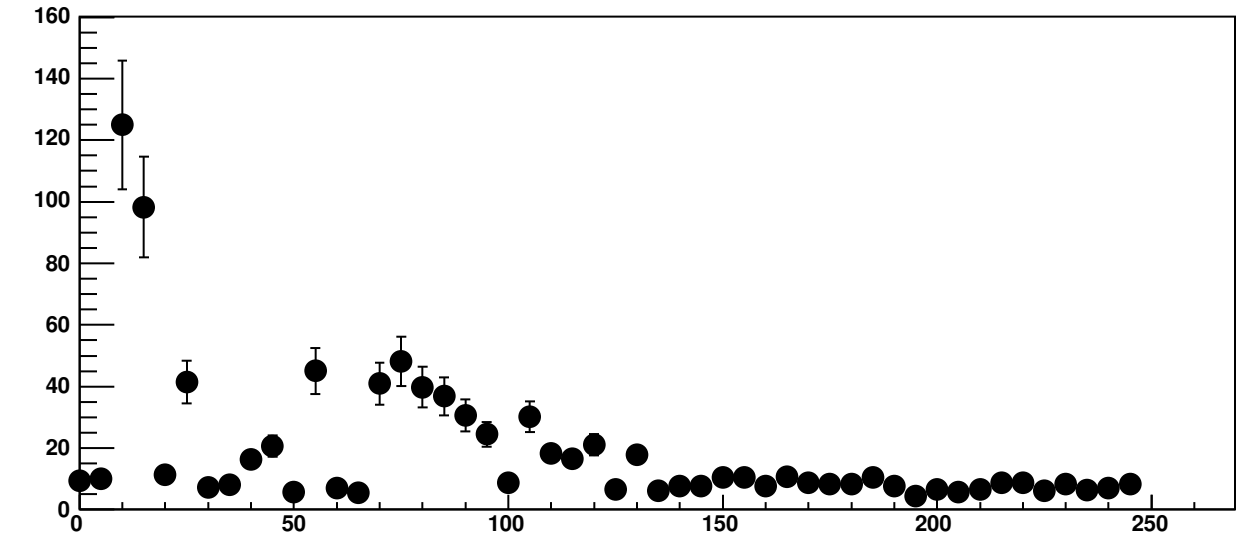

Chip 2, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
0
50
100
150
200
250

Chip 2, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold

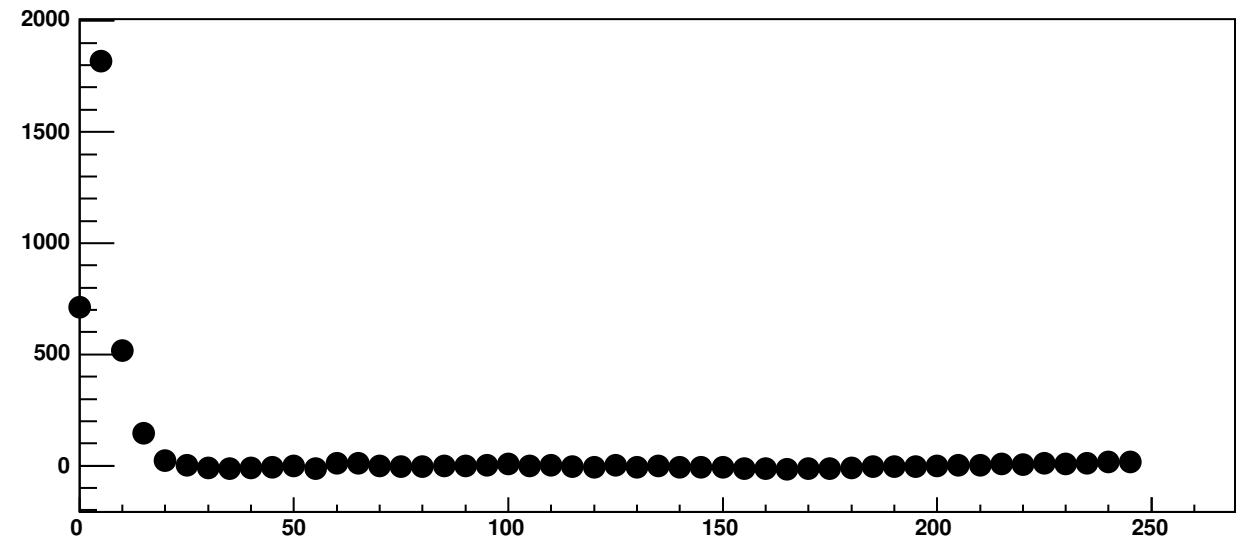
χ^2 / ndf	877.5 / 42
p0	-547.3 ± 0.5598
p1	4.859 ± 0.009514
p2	1.088e+04 ± 0.8843
p3	30.53 ± 0.006143



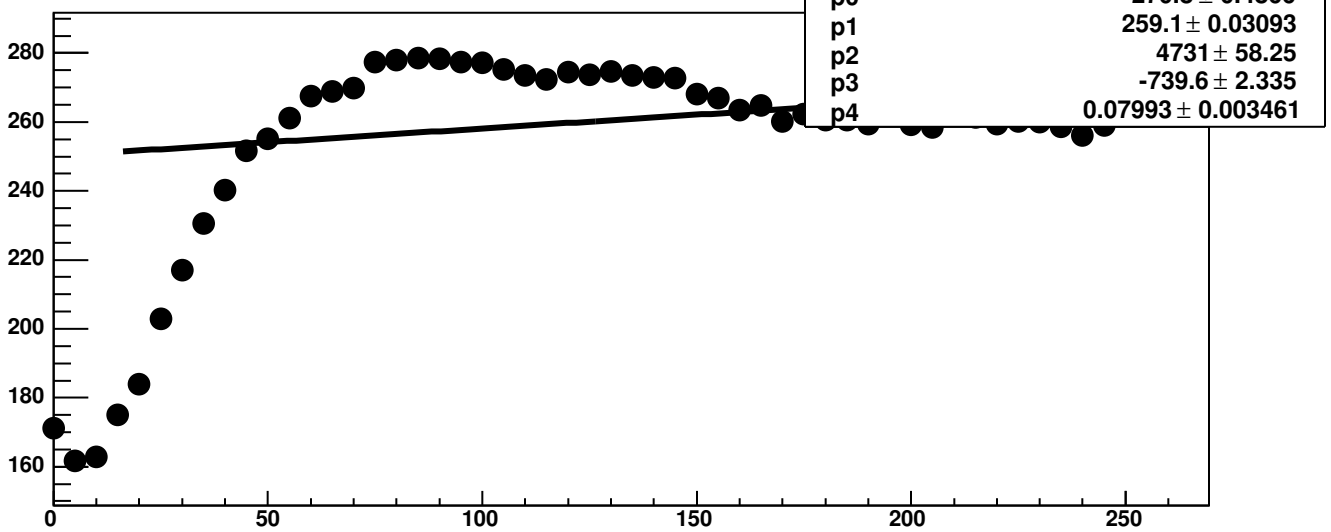
Chip 2, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold



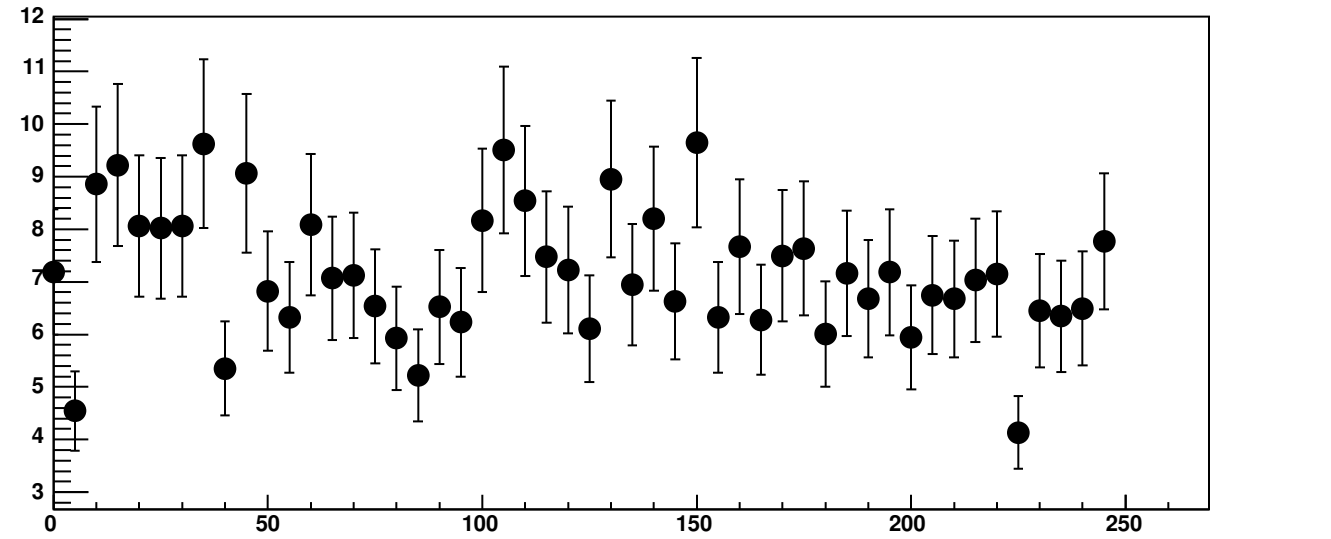
Chip 2, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold



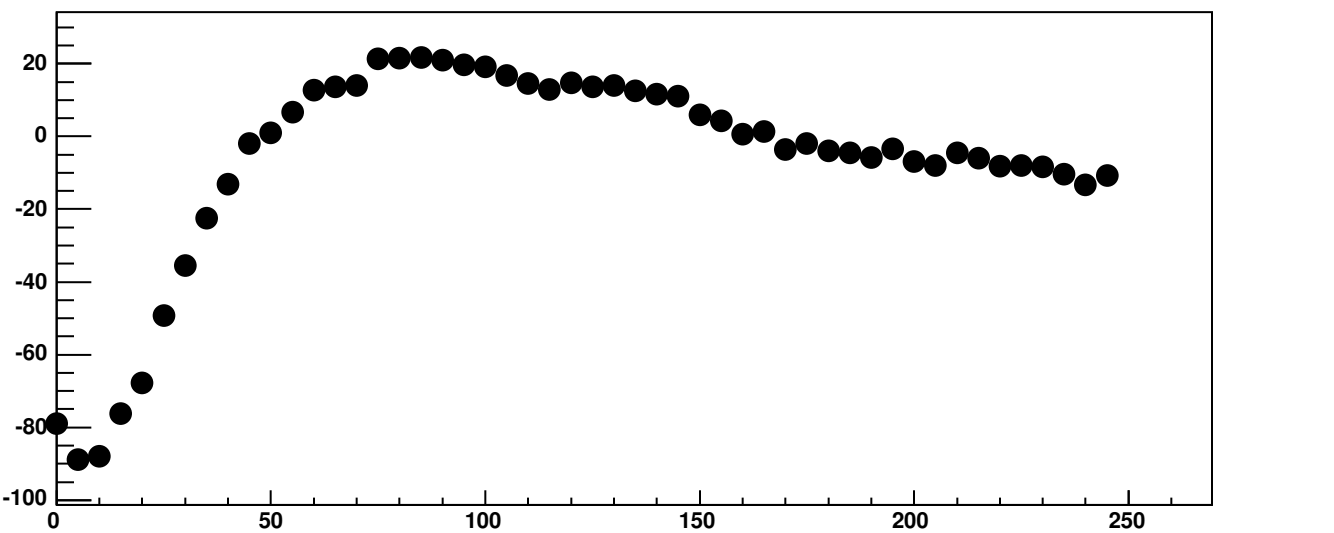
Chip 2, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold



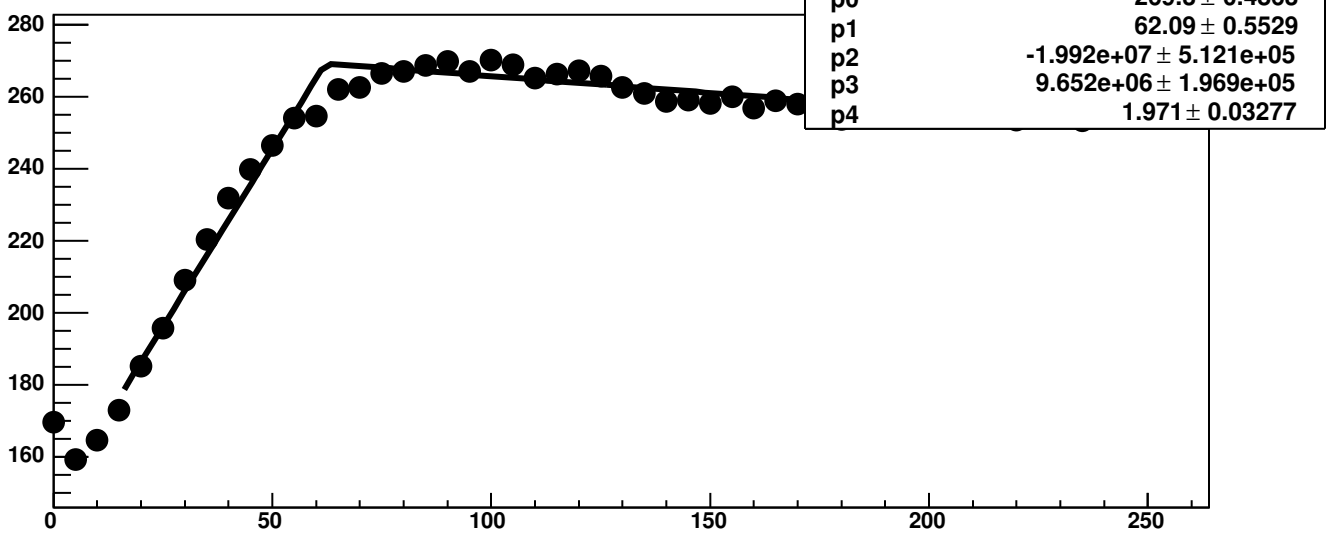
Chip 2, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



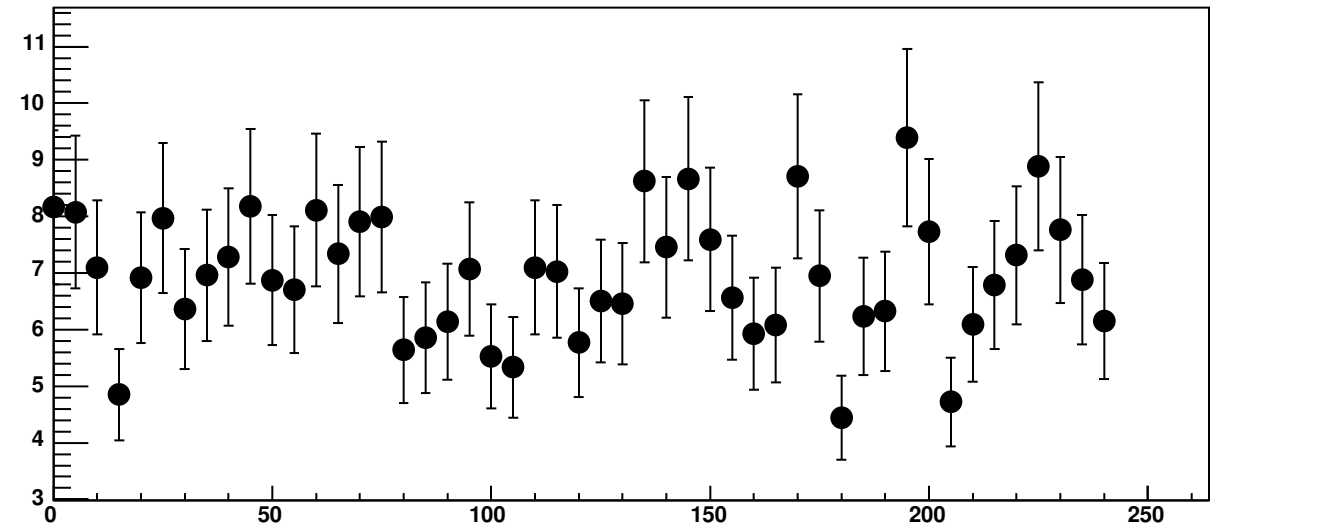
Chip 2, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold



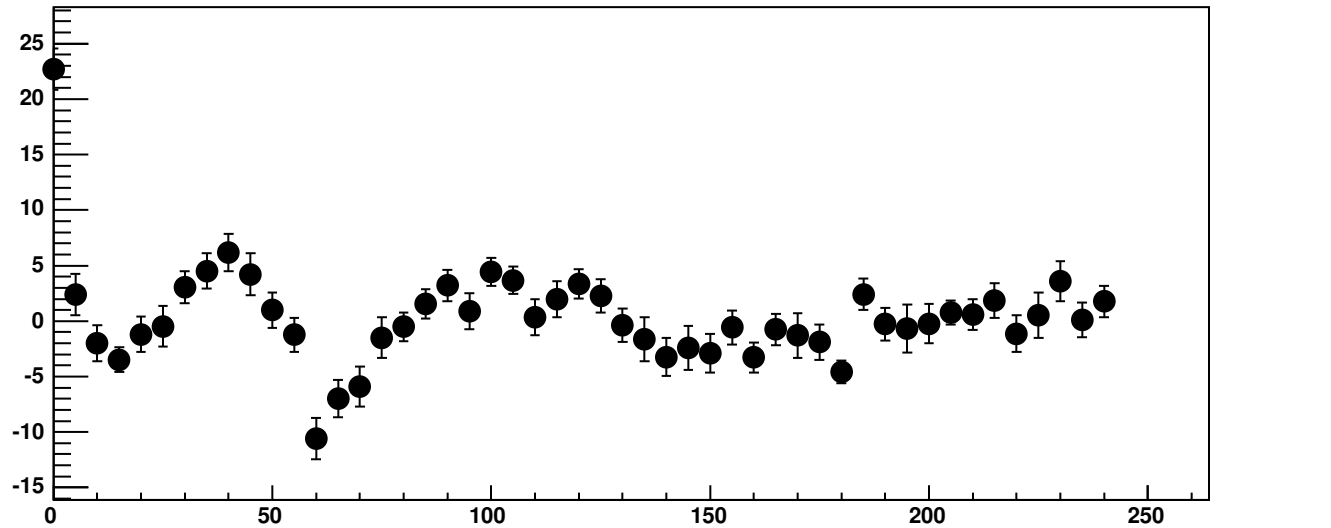
Chip 2, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold



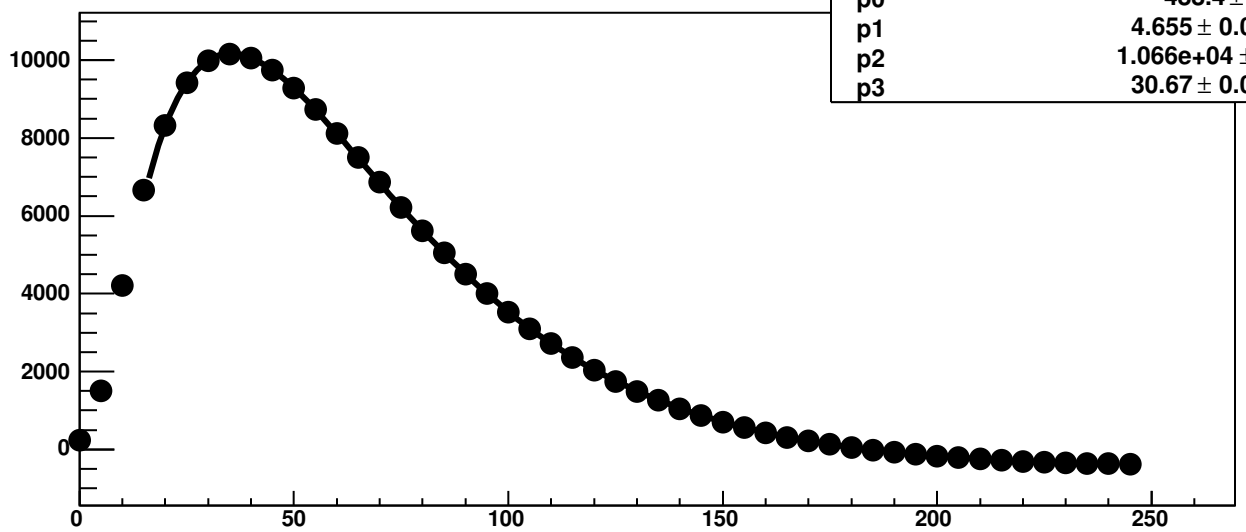
Chip 2, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



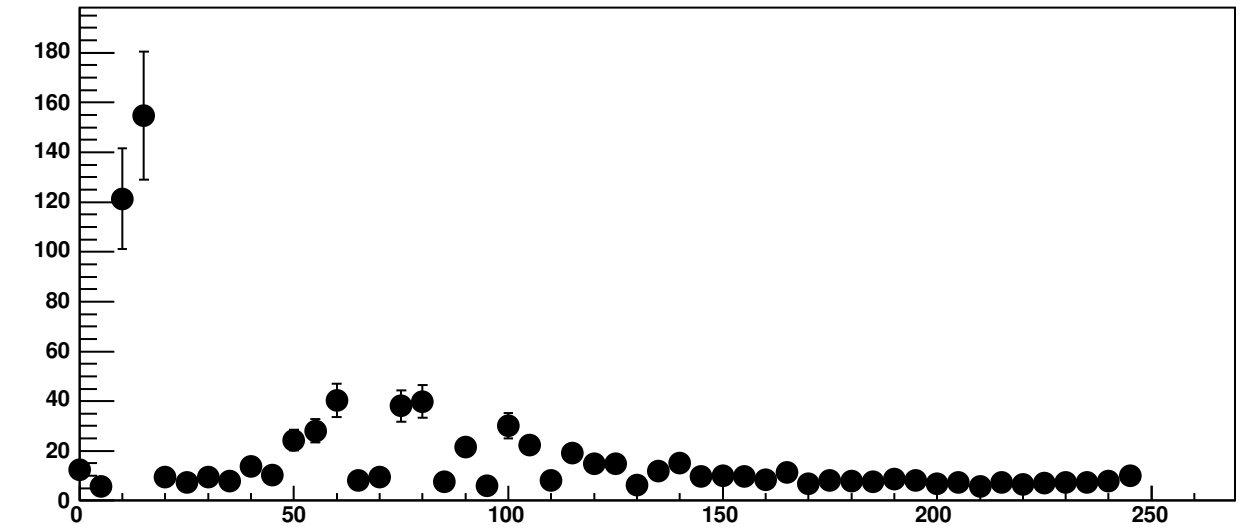
Chip 2, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold



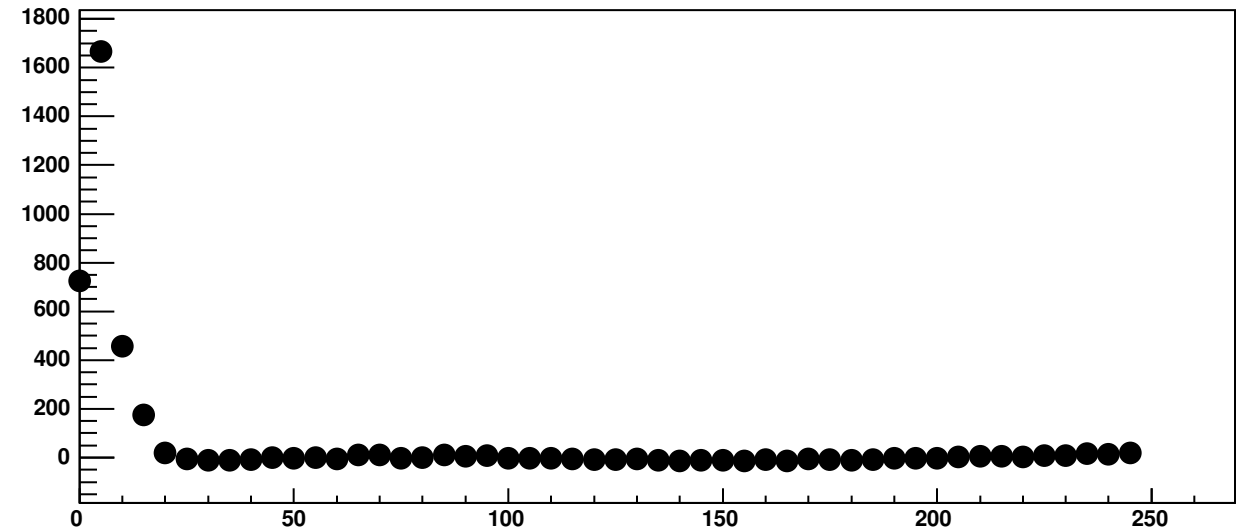
Chip 2, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold



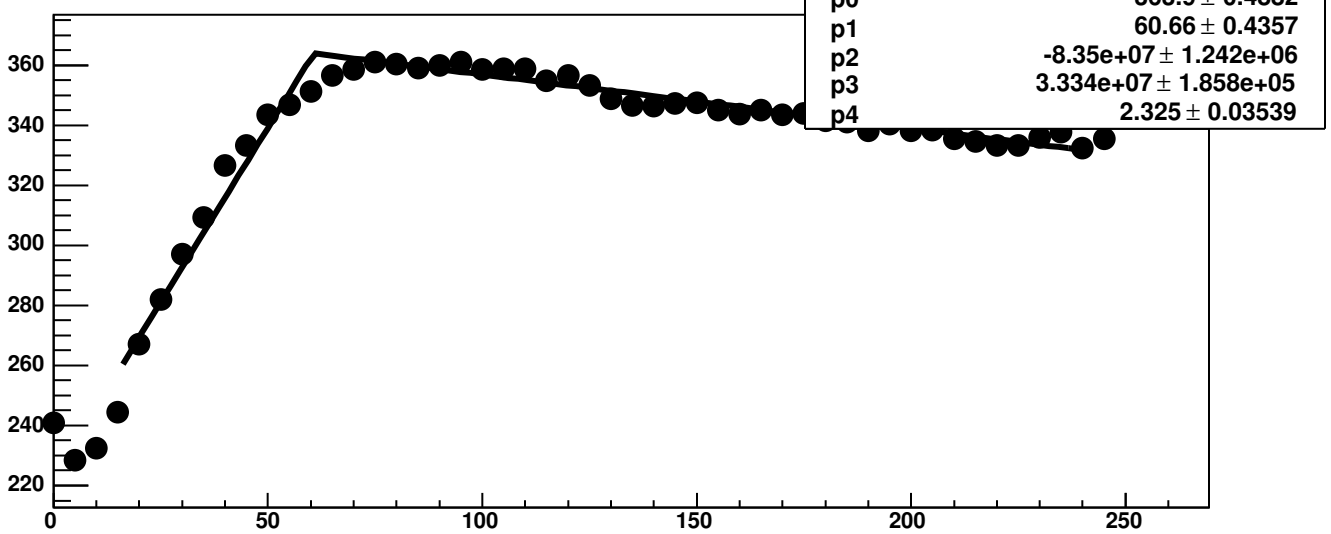
Chip 2, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold



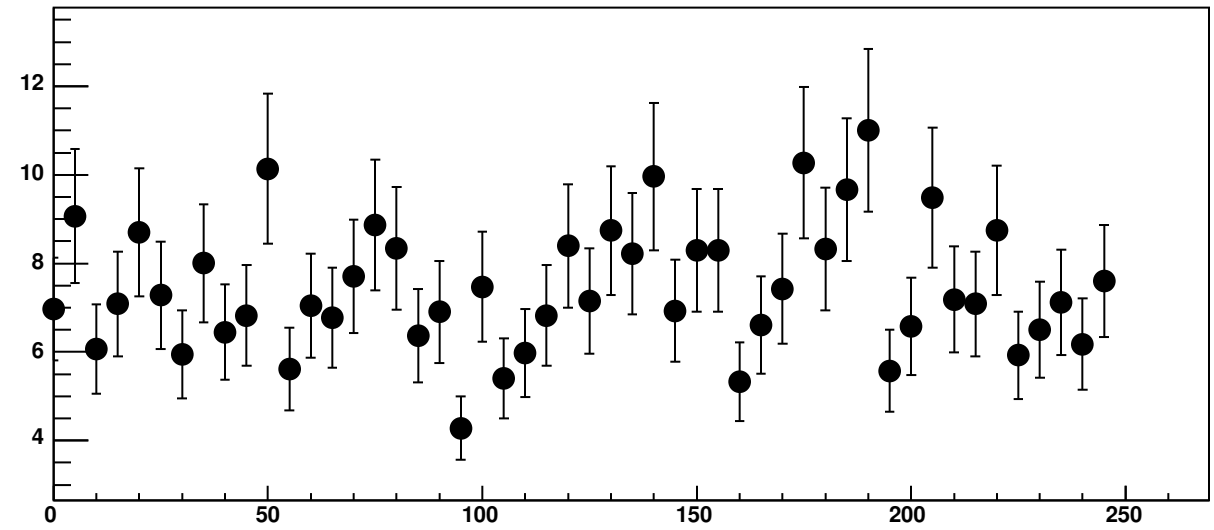
Chip 2, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold



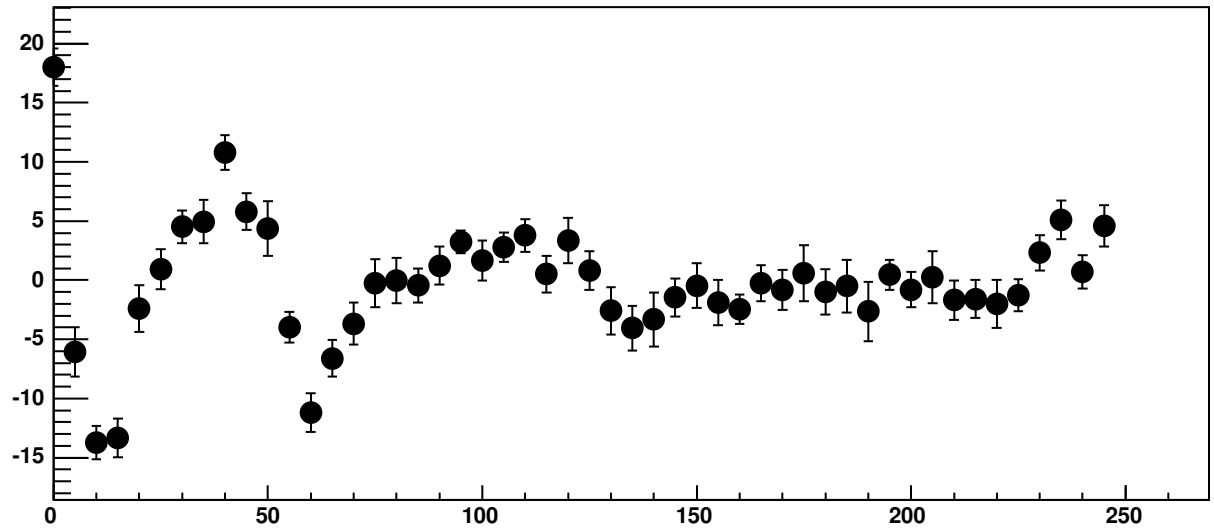
Chip 2, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold

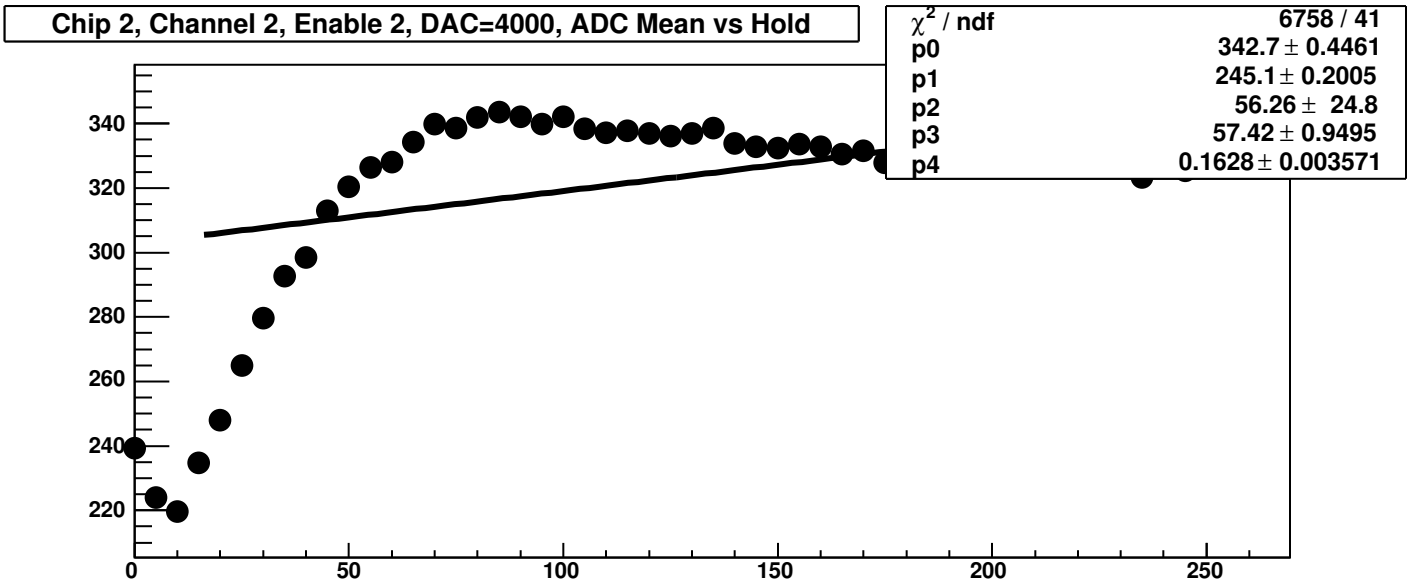


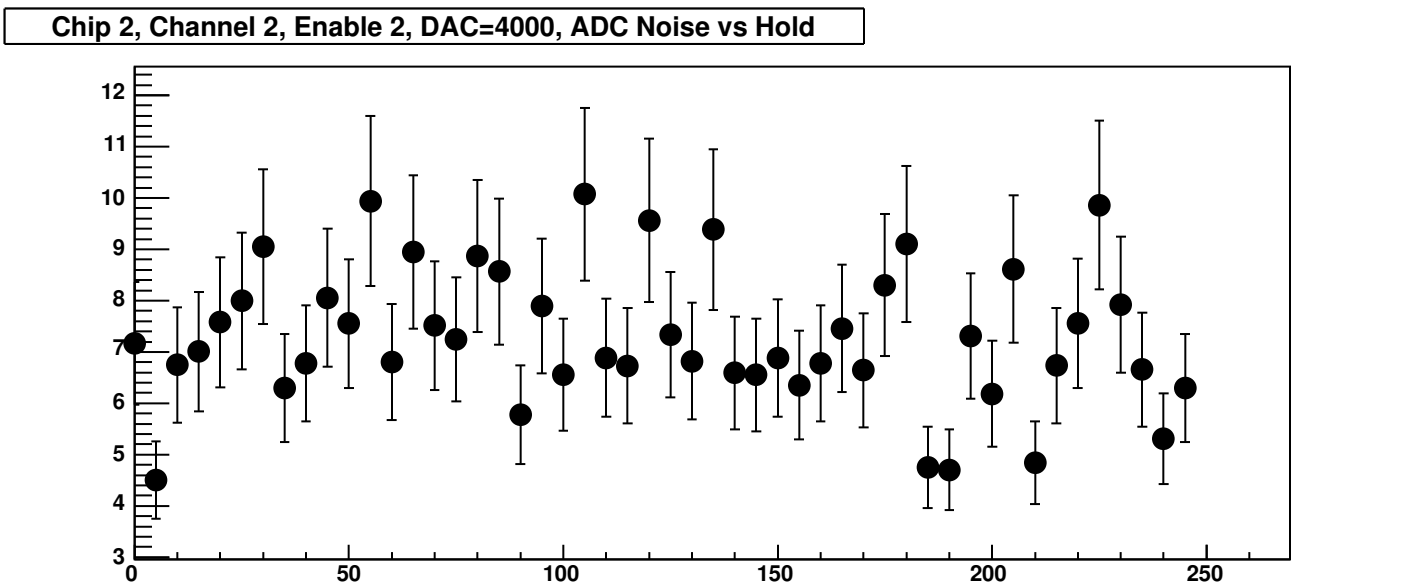
Chip 2, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold

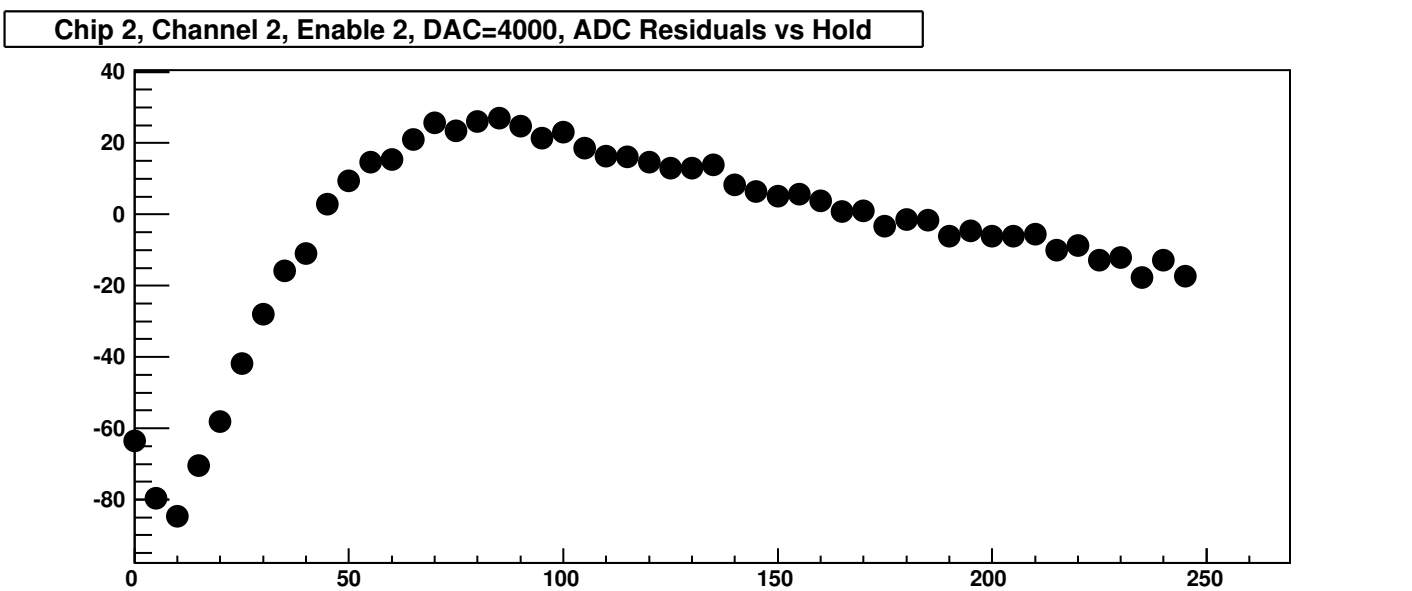


Chip 2, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold

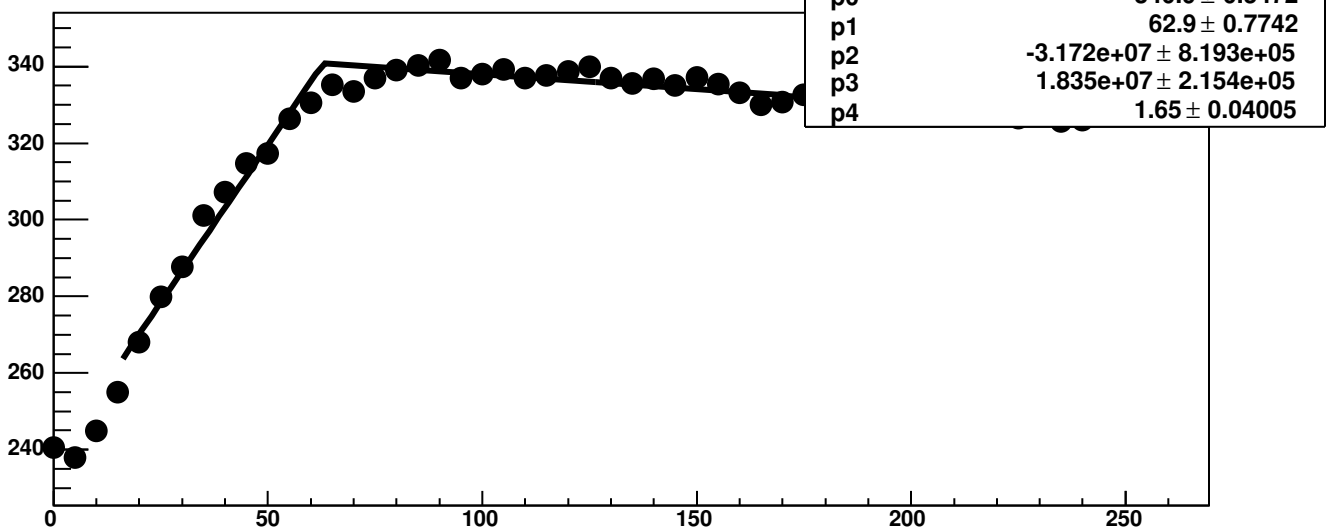



Chip 2, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 6758 / 41
p0 342.7 ± 0.4461
p1 245.1 ± 0.2005
p2 56.26 ± 24.8
p3 57.42 ± 0.9495
p4 0.1628 ± 0.003571
340
320
300
280
260
240
220
0
50
100
150
200
250

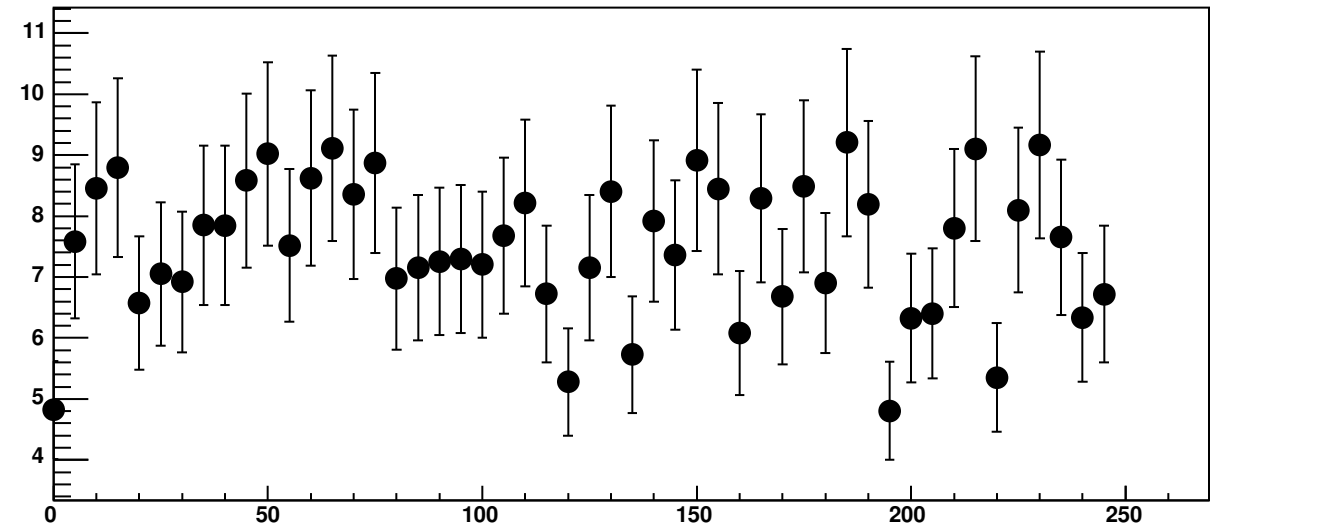

Chip 2, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 2, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
0
50
100
150
200
250

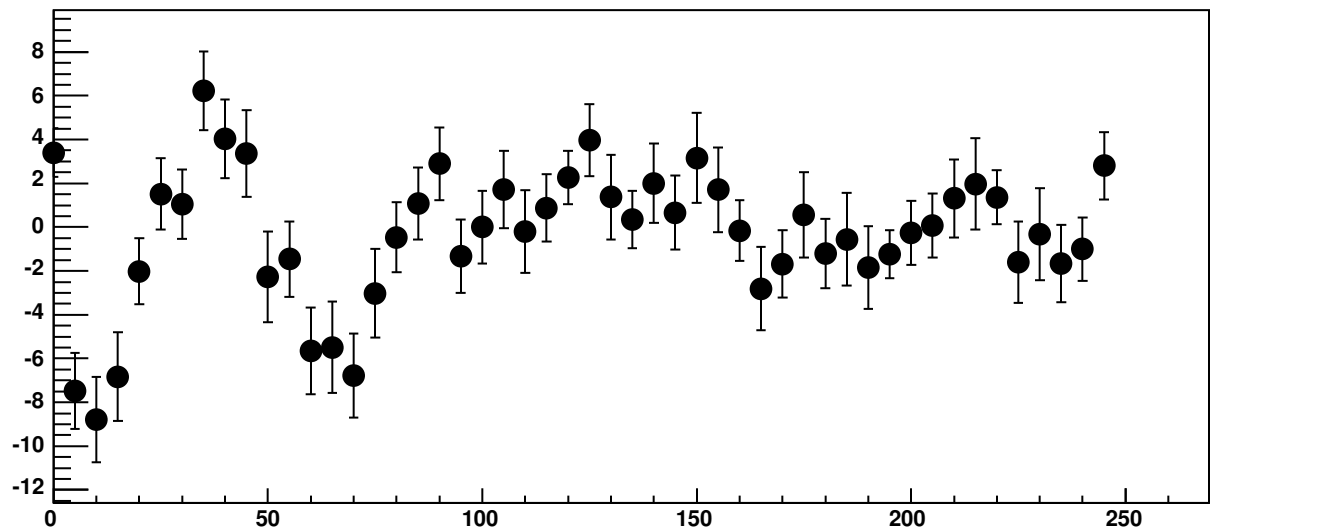
Chip 2, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold



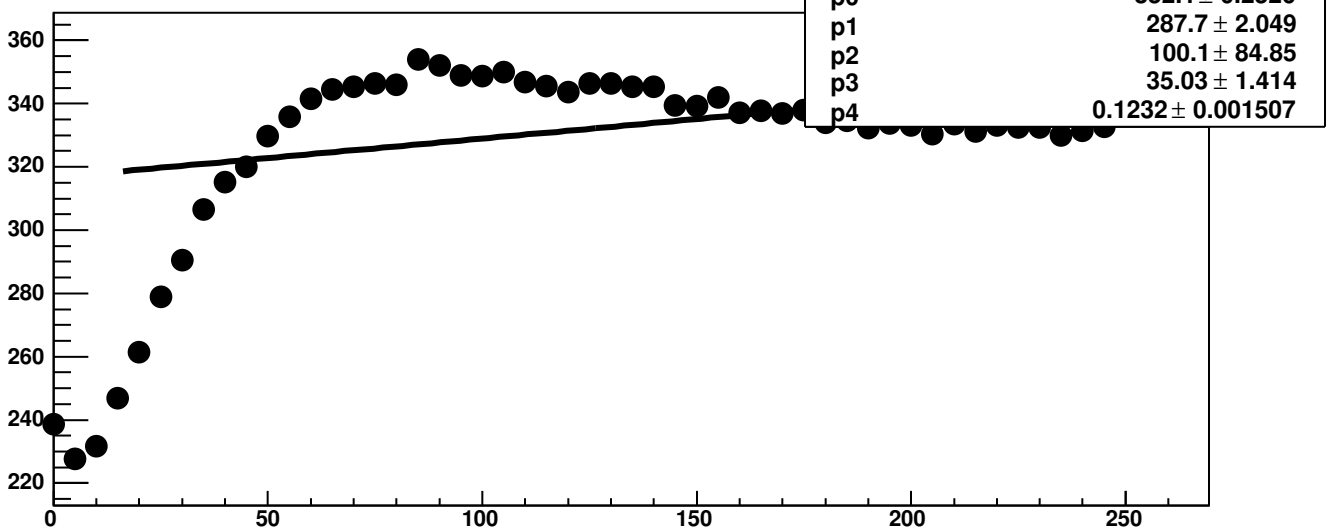
Chip 2, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold



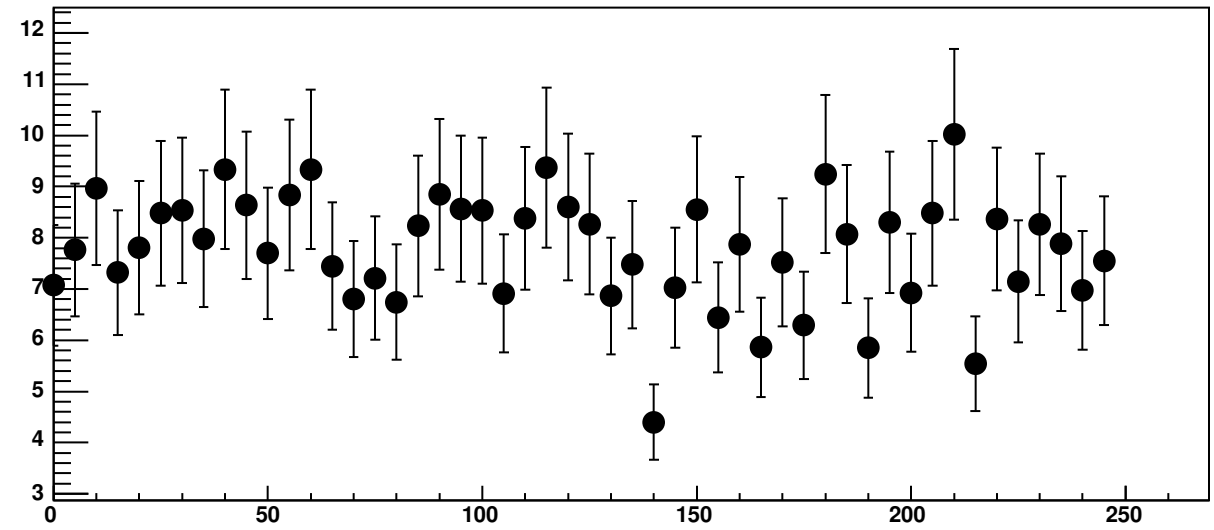
Chip 2, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold



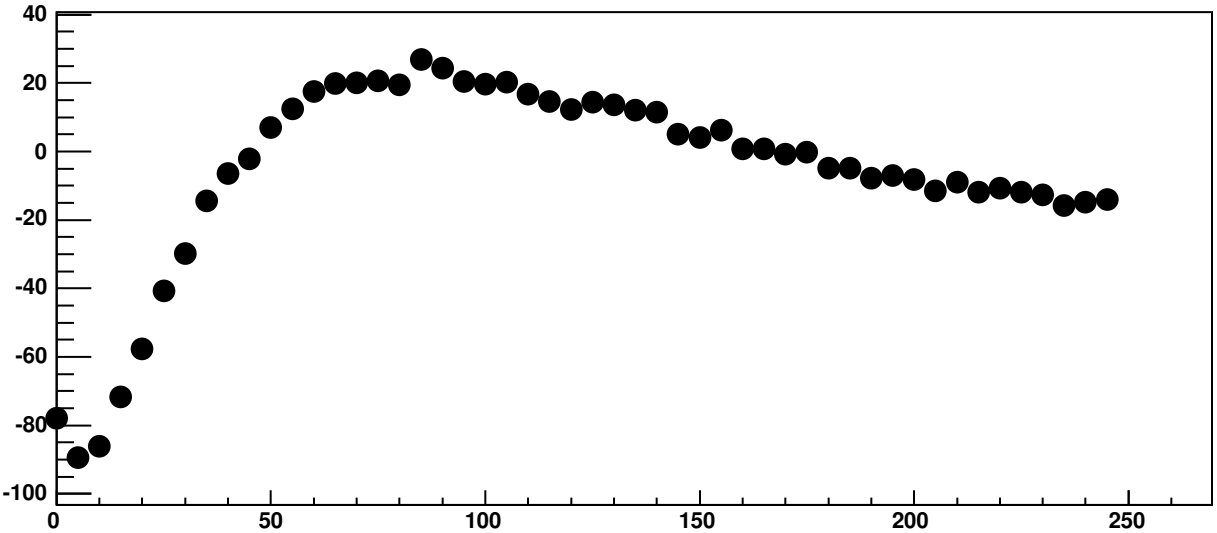
Chip 2, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold



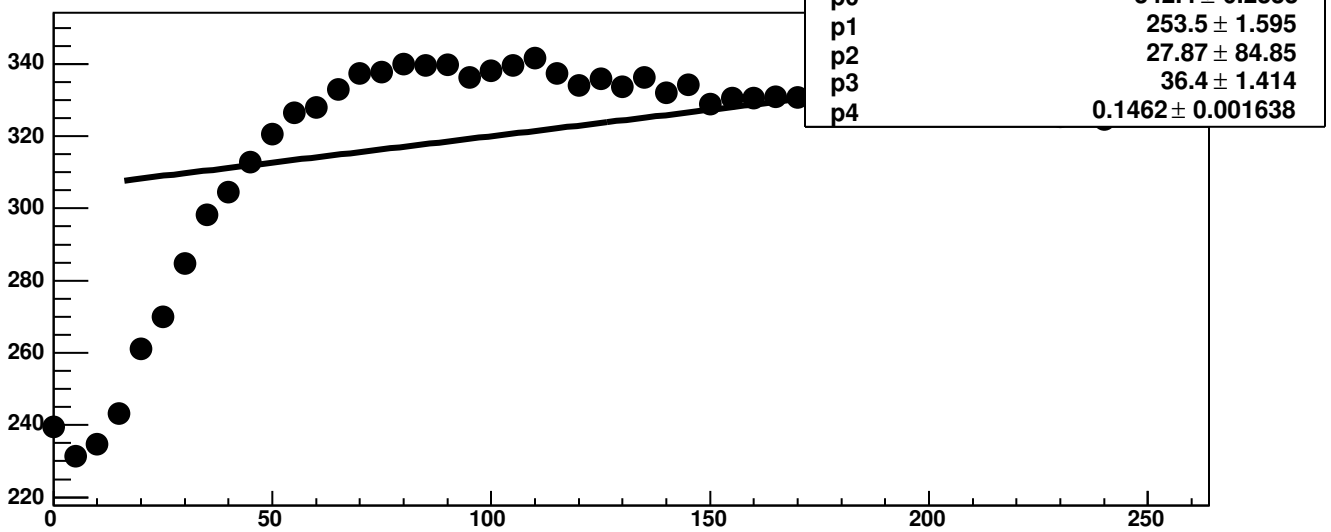
Chip 2, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



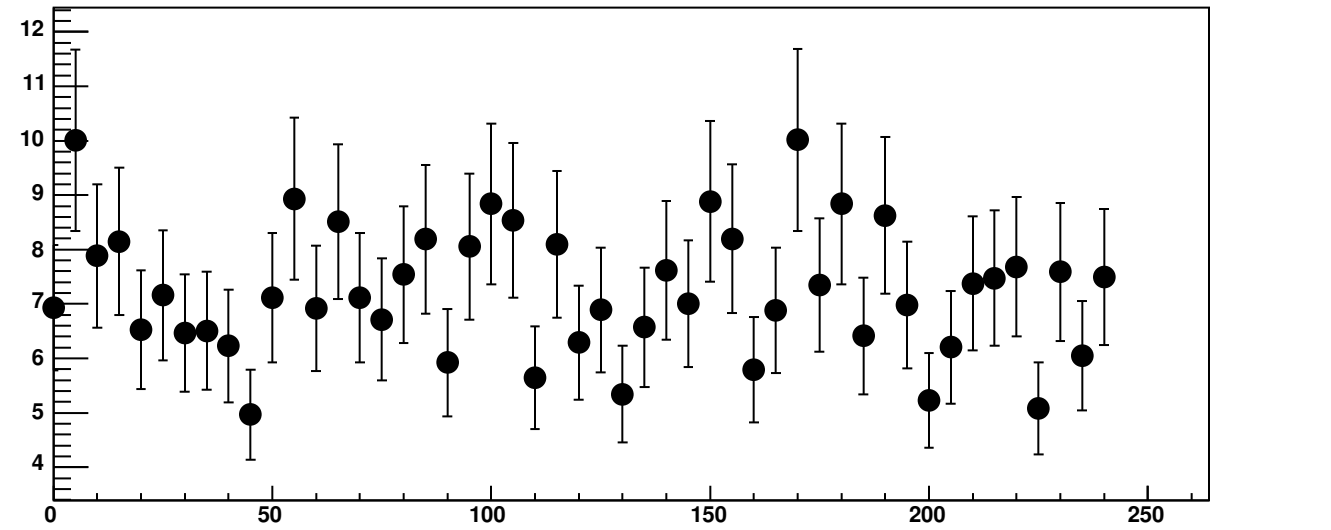
Chip 2, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold



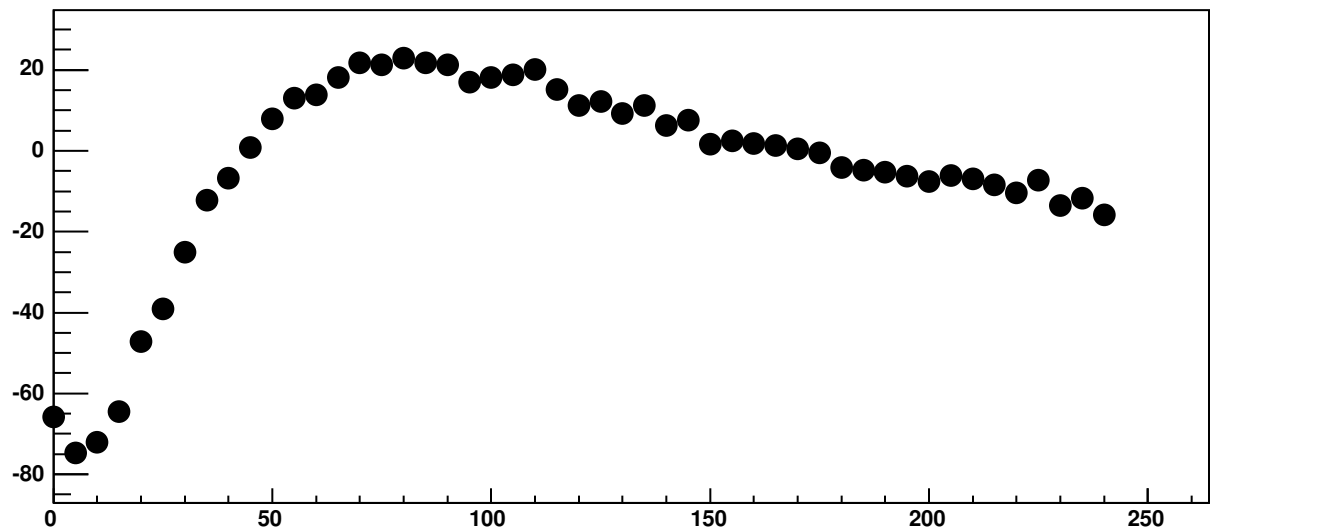
Chip 2, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

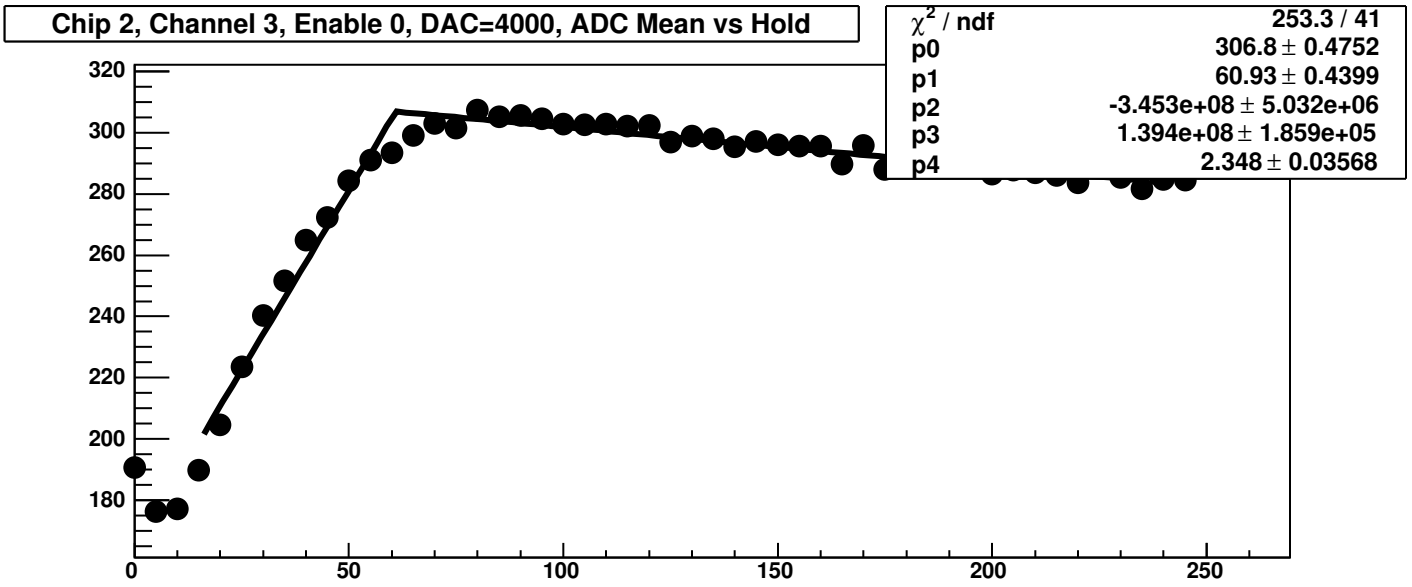


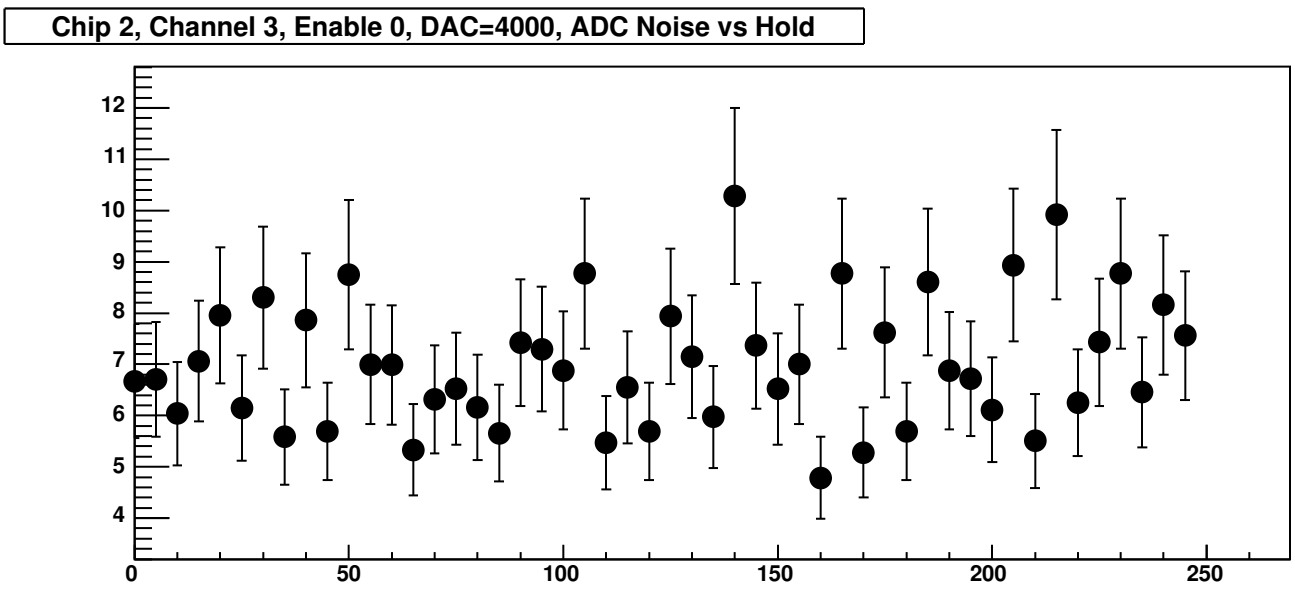
Chip 2, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold

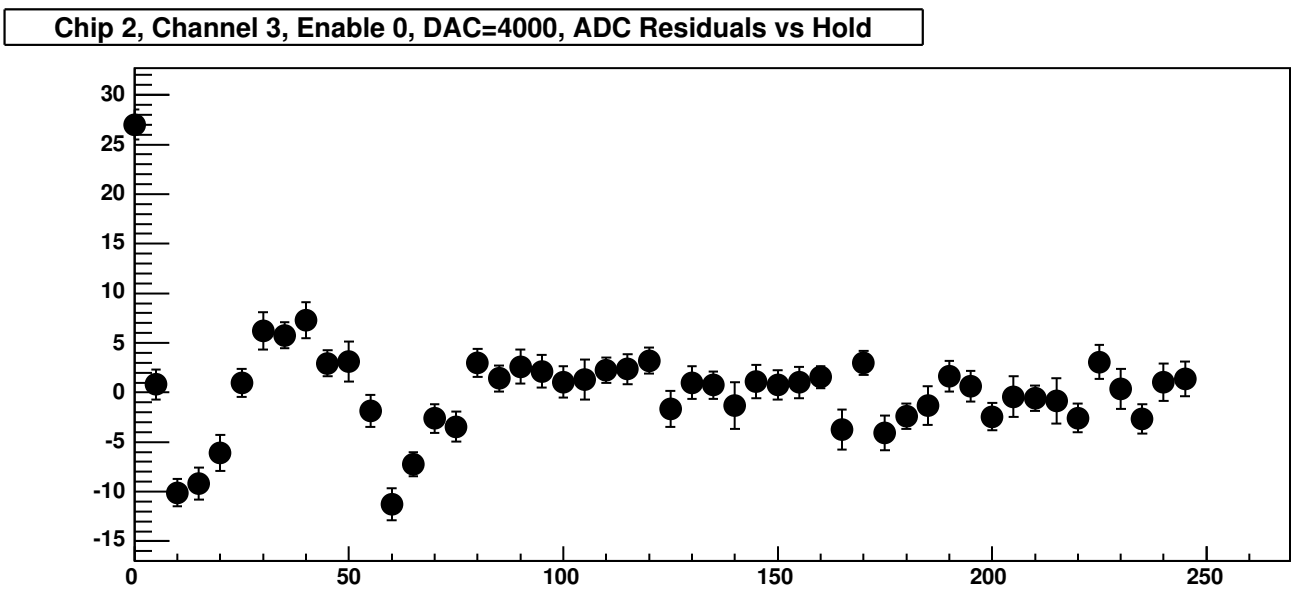


Chip 2, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold

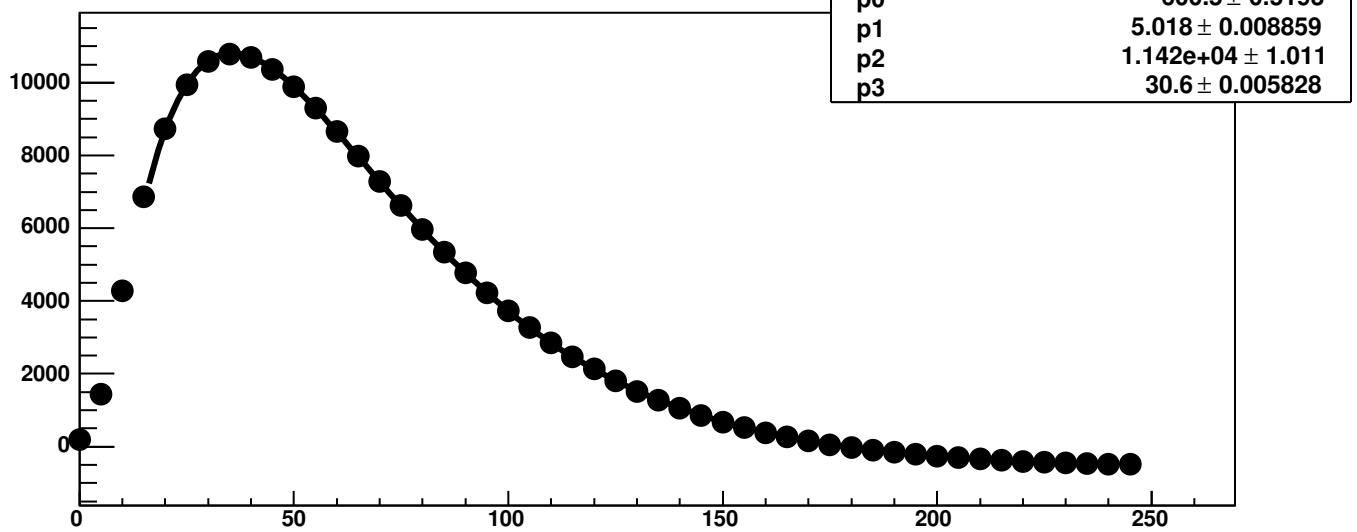



Chip 2, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 253.3 / 41
p0 306.8 ± 0.4752
p1 60.93 ± 0.4399
p2 -3.453e+08 ± 5.032e+06
p3 1.394e+08 ± 1.859e+05
p4 2.348 ± 0.03568
320
300
280
260
240
220
200
180
0
50
100
150
200
250

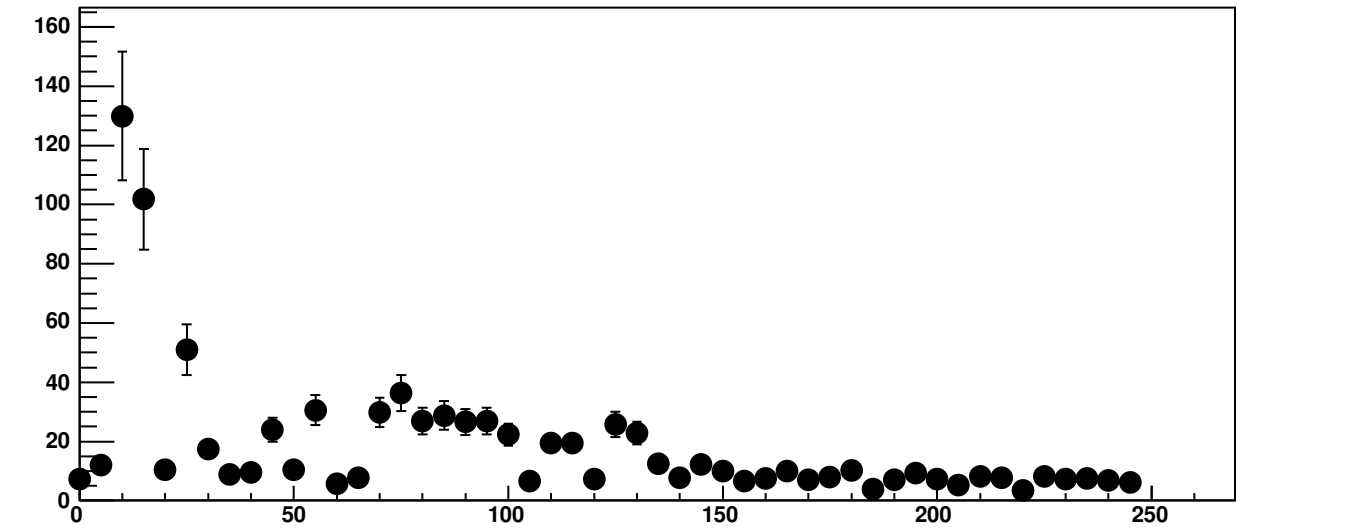

Chip 2, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 2, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

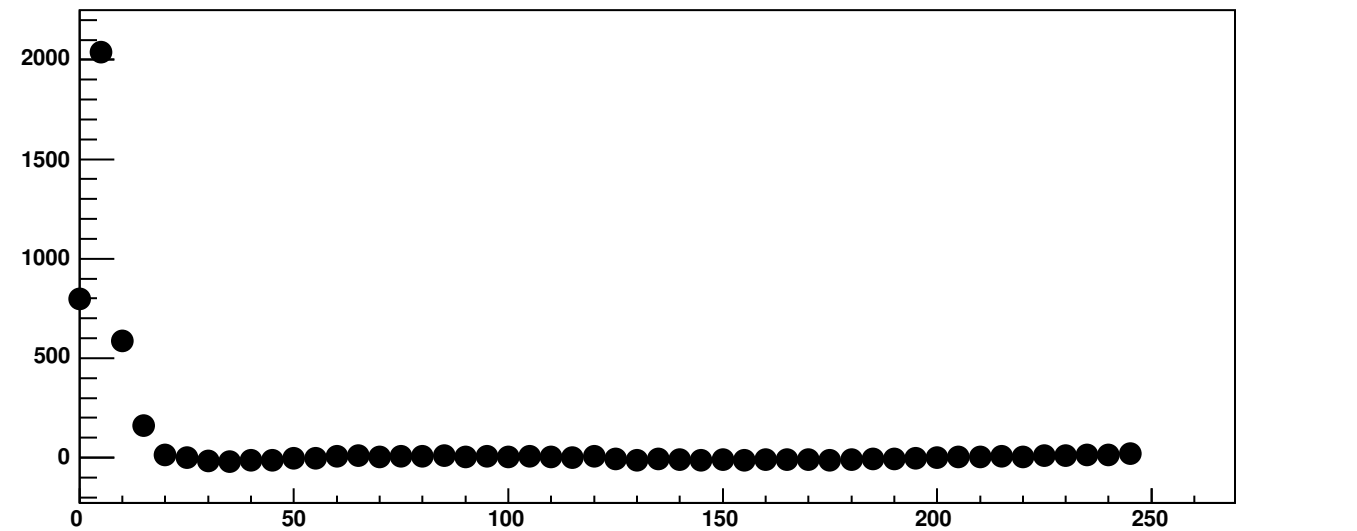
Chip 2, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold



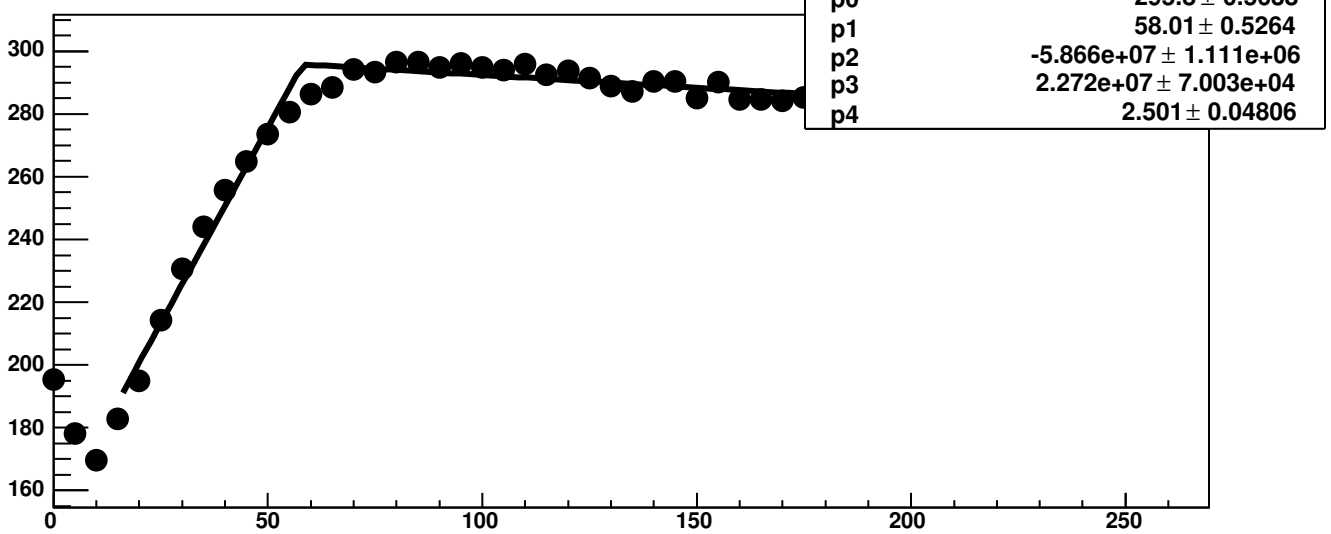
Chip 2, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold



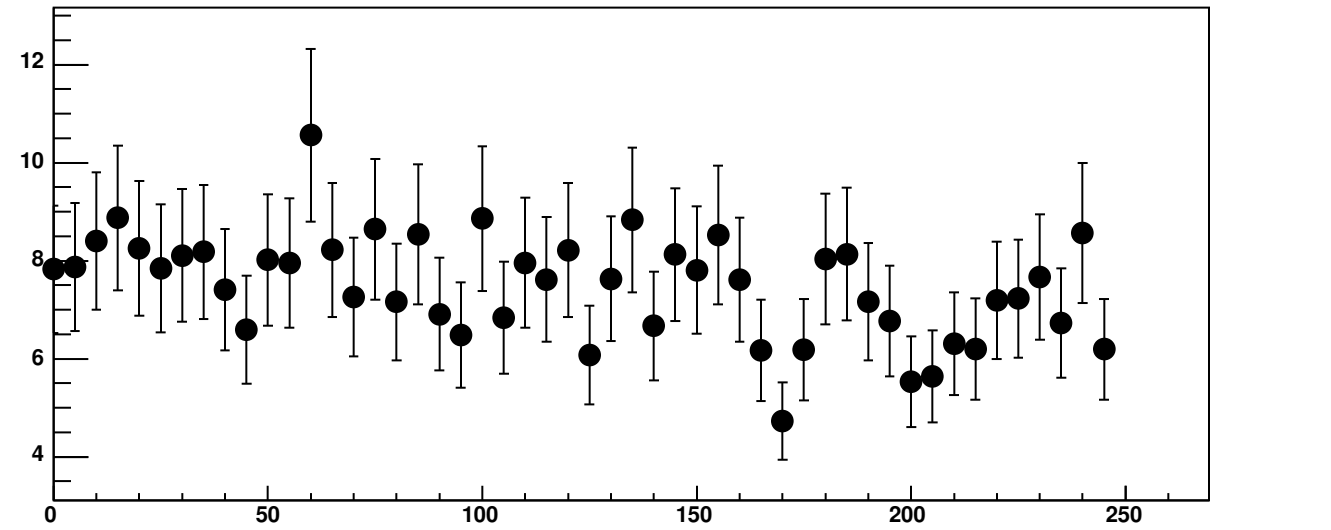
Chip 2, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold



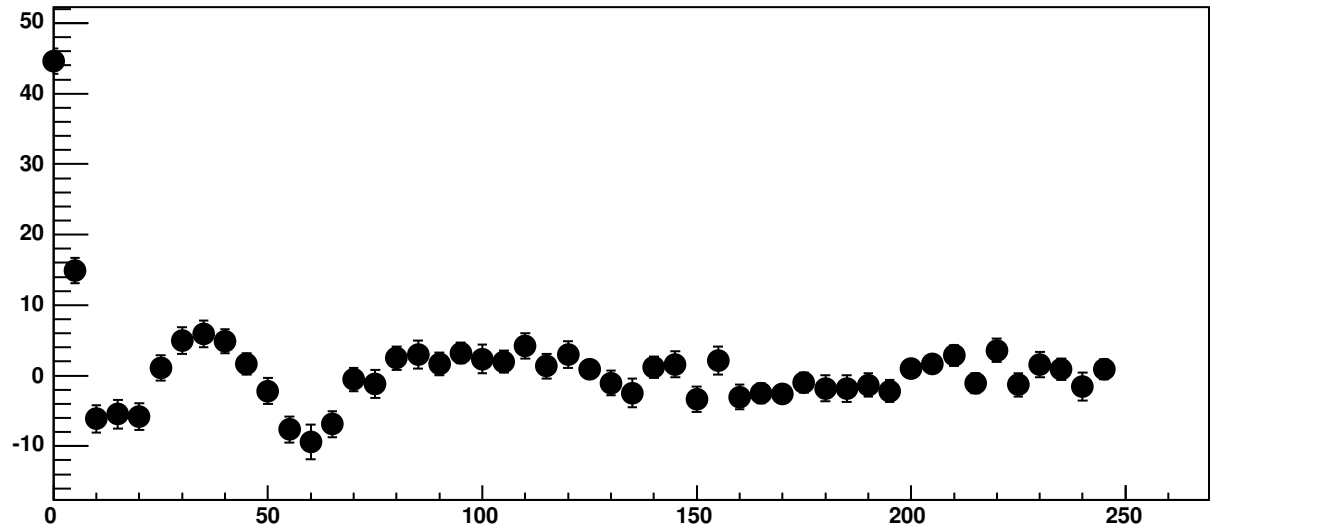
Chip 2, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

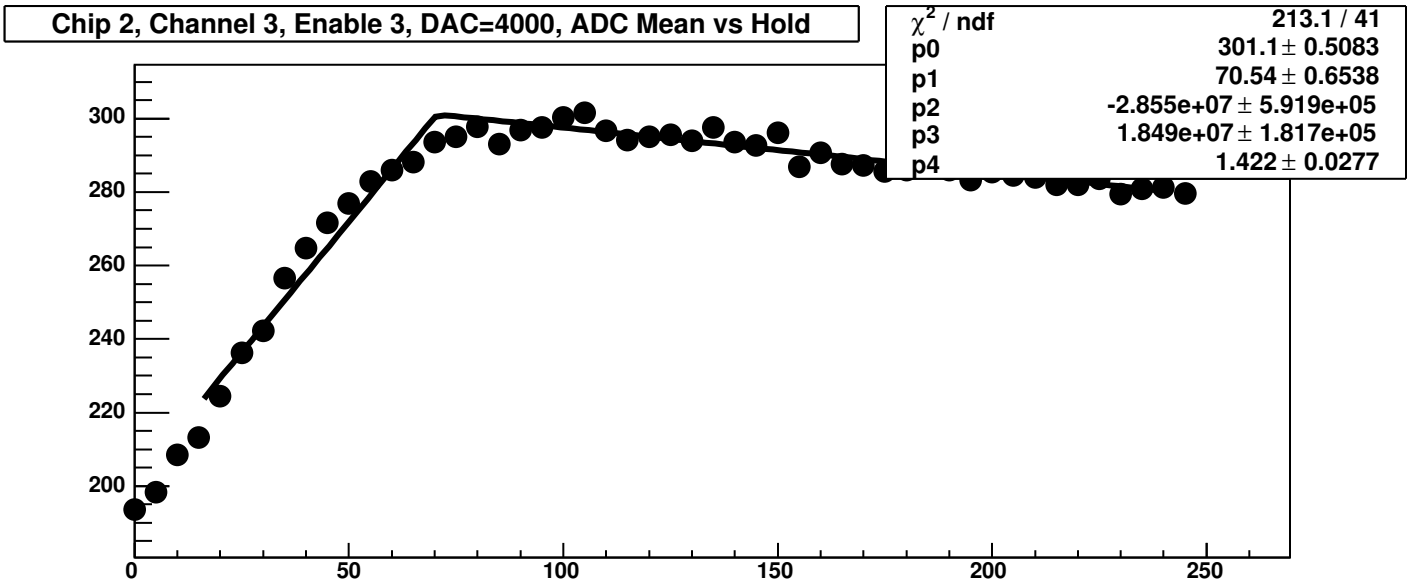


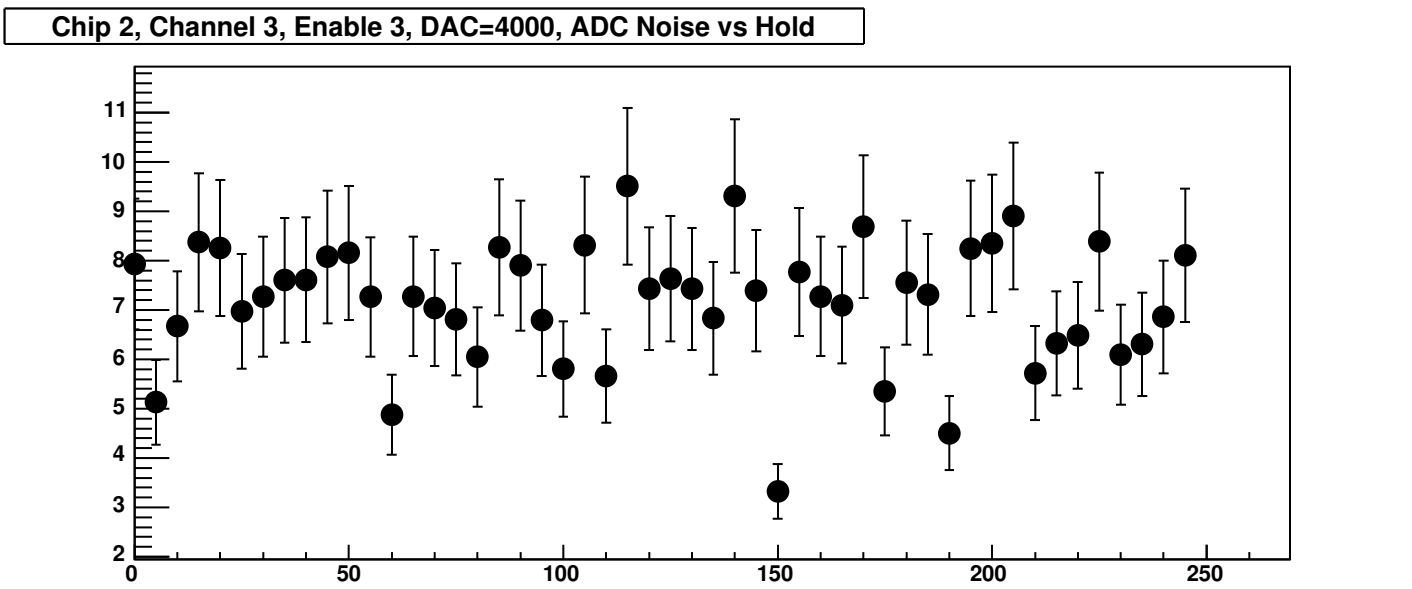
Chip 2, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold

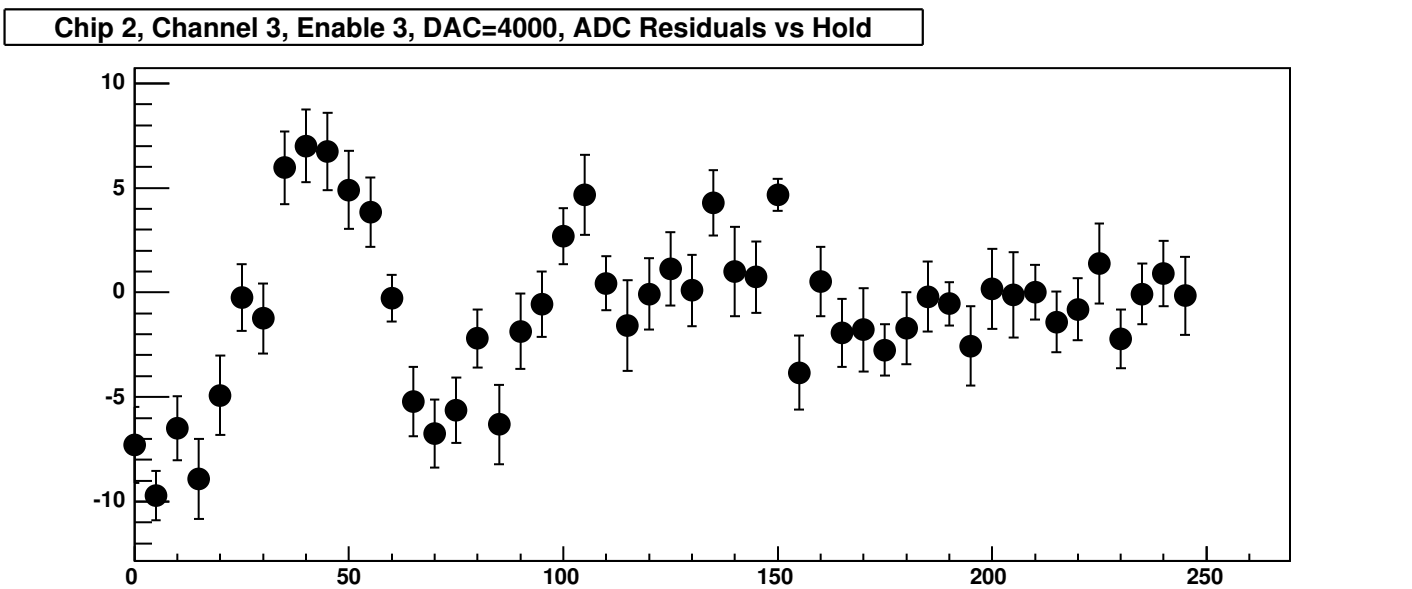


Chip 2, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

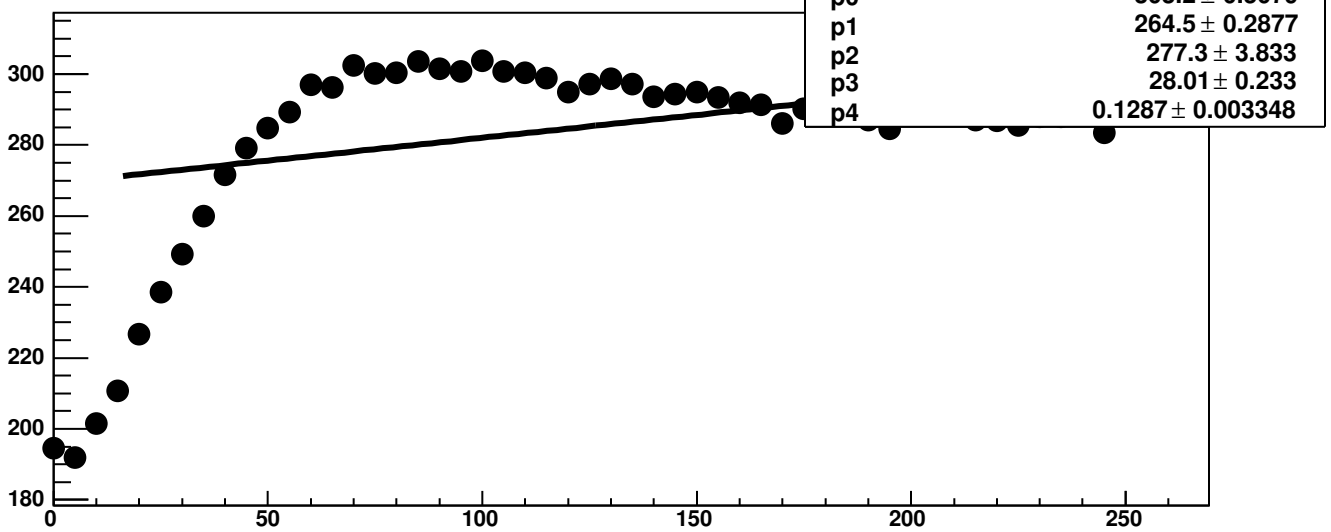



Chip 2, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 213.1 / 41
p0 301.1 ± 0.5083
p1 70.54 ± 0.6538
p2 -2.855e+07 ± 5.919e+05
p3 1.849e+07 ± 1.817e+05
p4 1.422 ± 0.0277
300
280
260
240
220
200
0
50
100
150
200
250

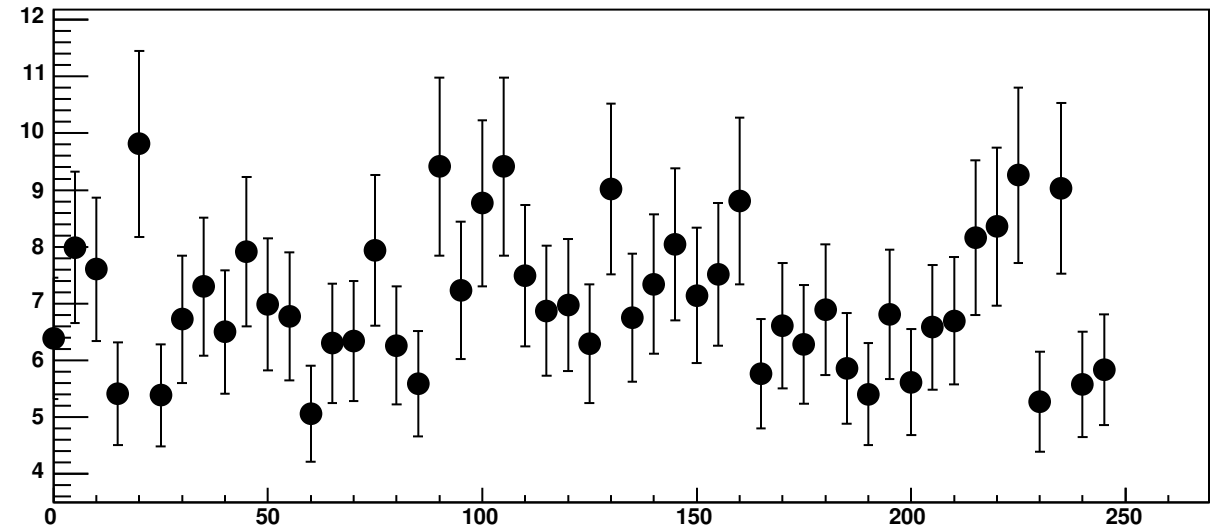

Chip 2, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
2
0
50
100
150
200
250


Chip 2, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250

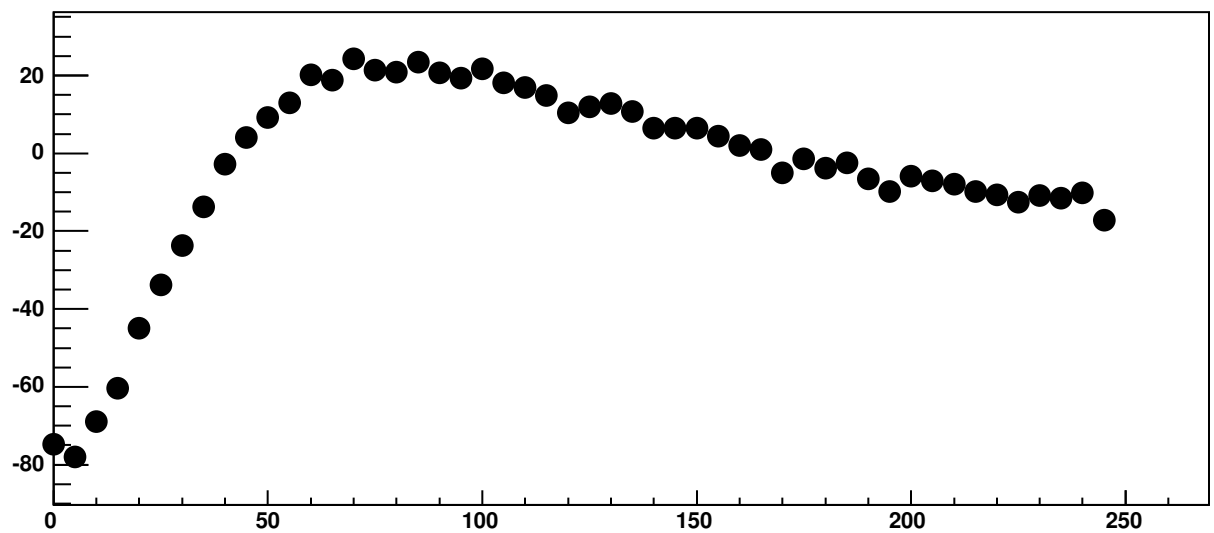
Chip 2, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold



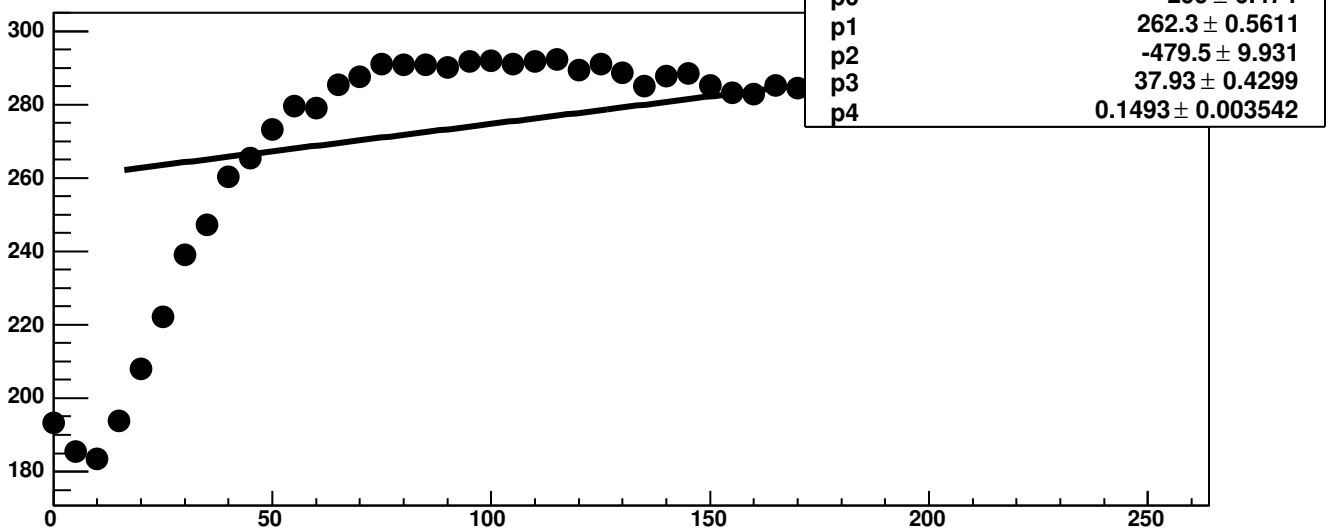
Chip 2, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold



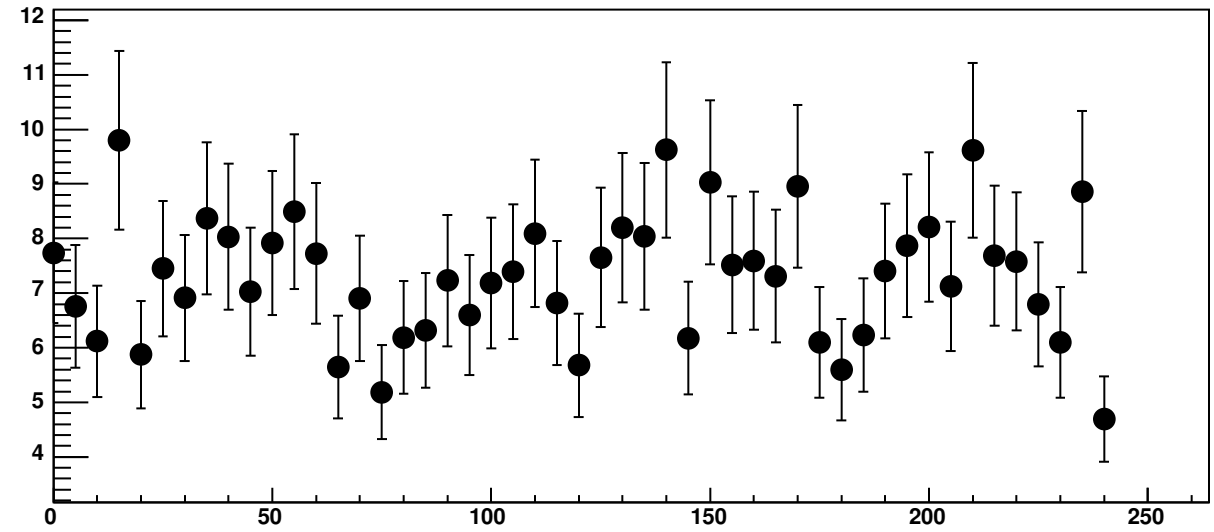
Chip 2, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold



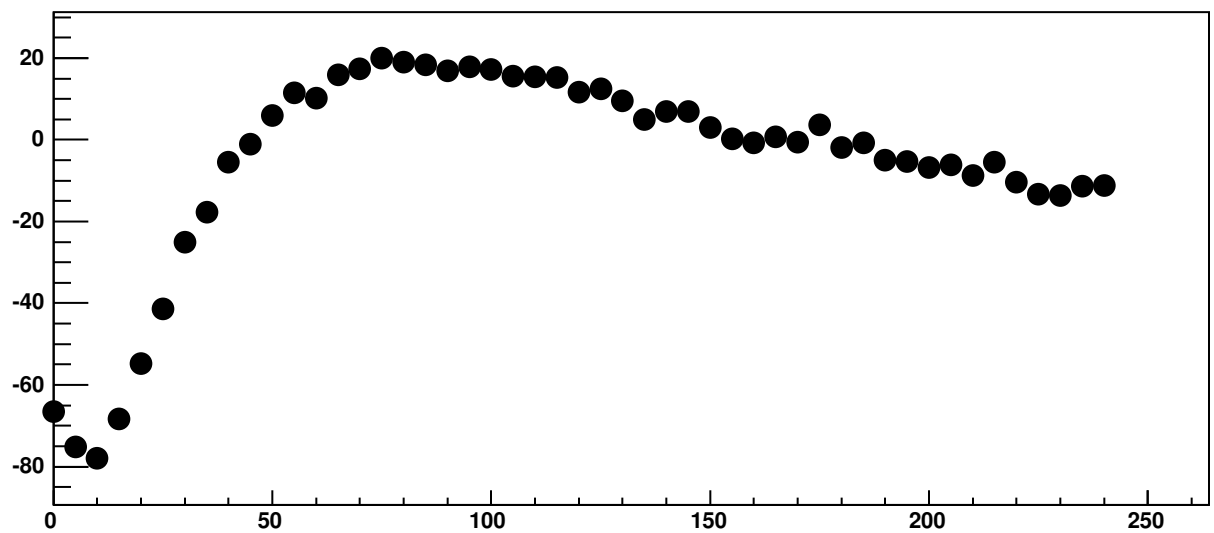
Chip 2, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold



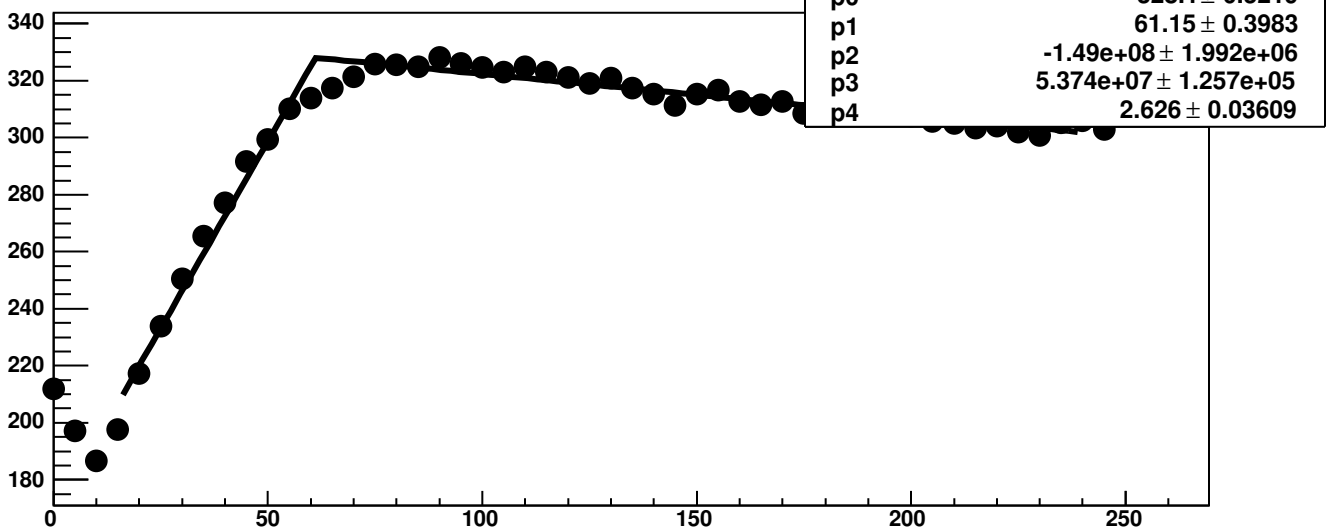
Chip 2, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



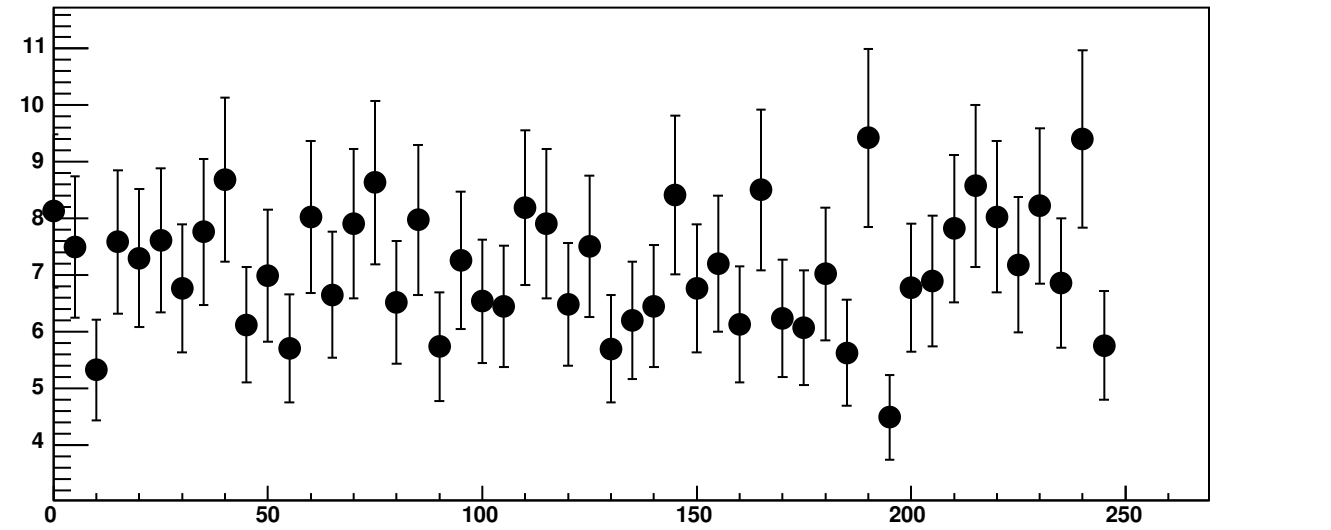
Chip 2, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold



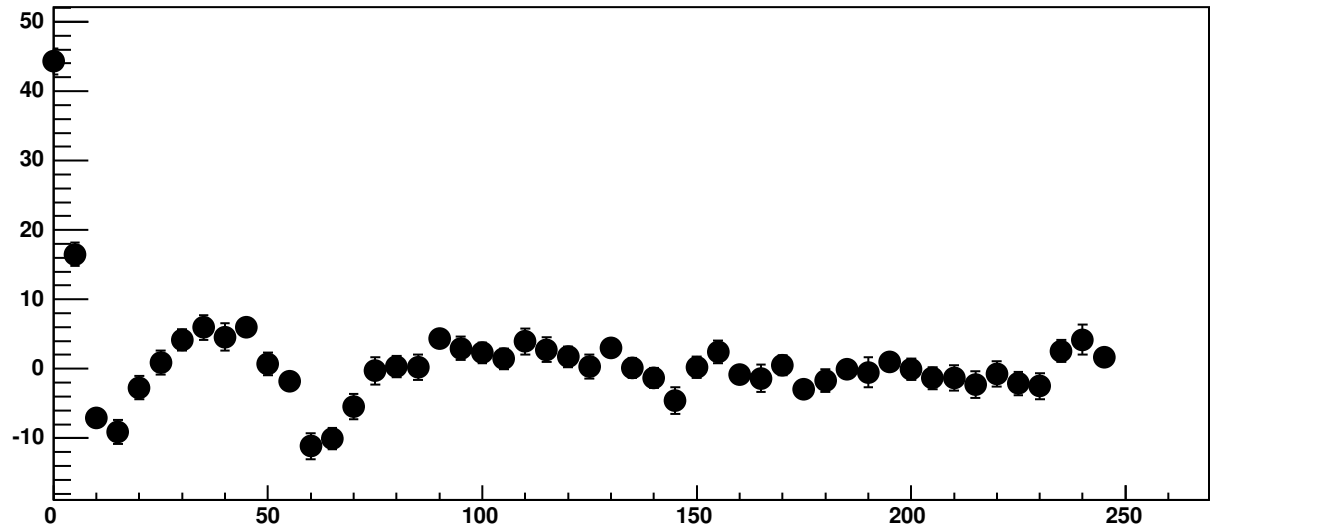
Chip 2, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

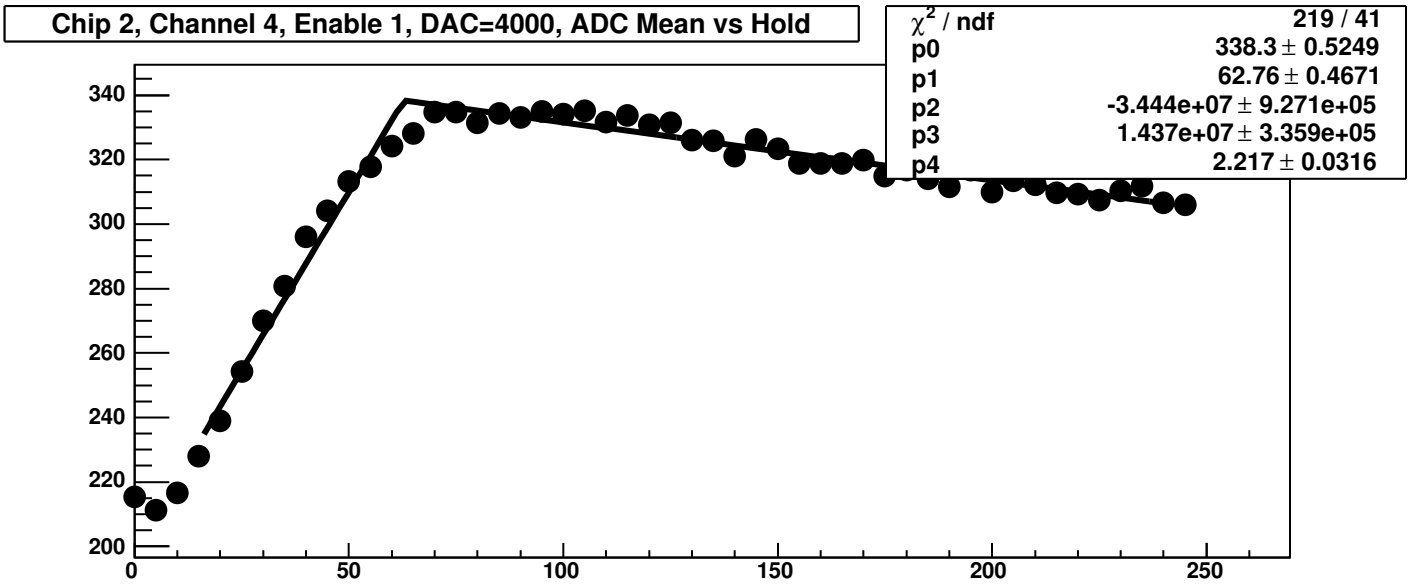


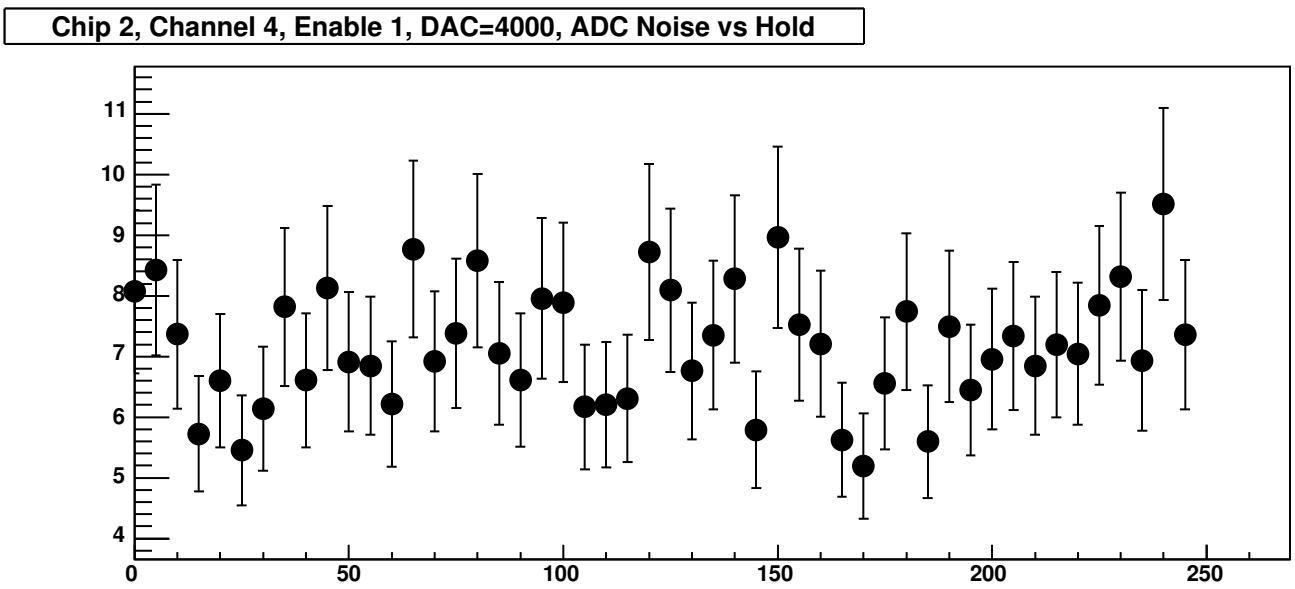
Chip 2, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold

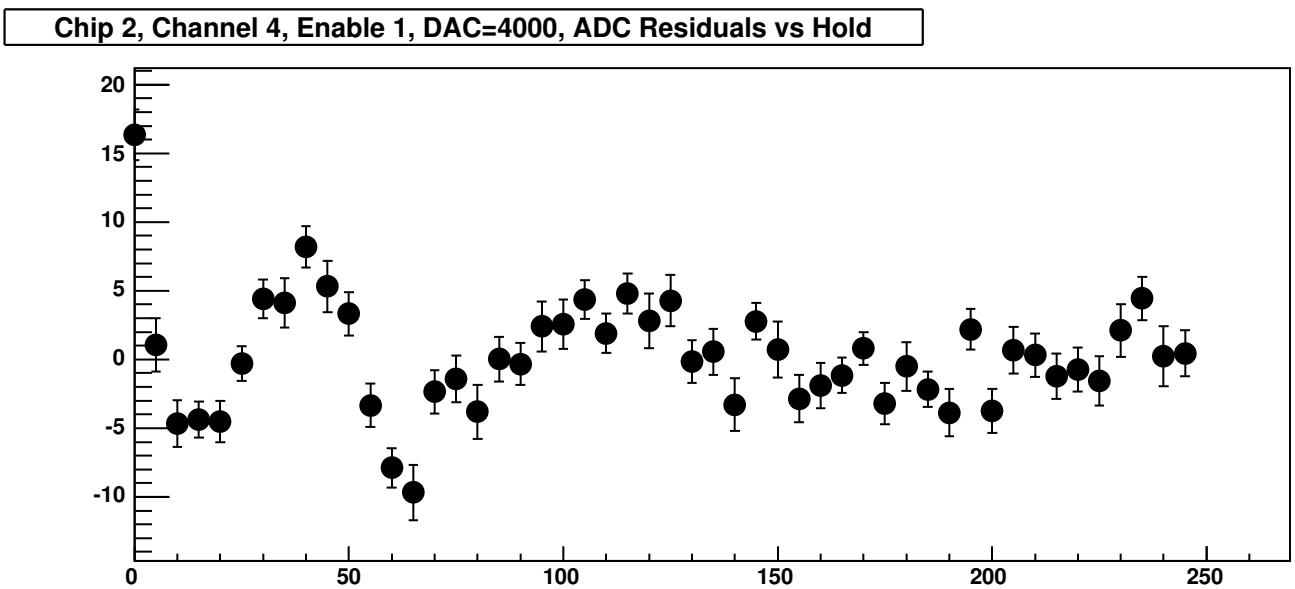


Chip 2, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

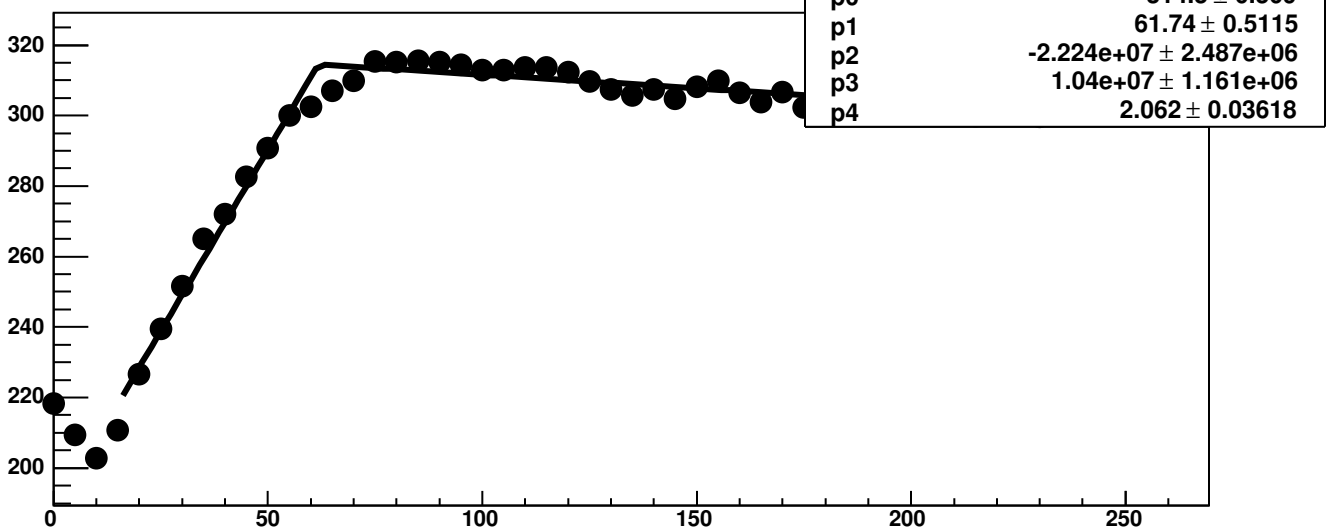



Chip 2, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 219 / 41
p0 338.3 ± 0.5249
p1 62.76 ± 0.4671
p2 -3.444e+07 ± 9.271e+05
p3 1.437e+07 ± 3.359e+05
p4 2.217 ± 0.0316
340
320
300
280
260
240
220
200
0
50
100
150
200
250

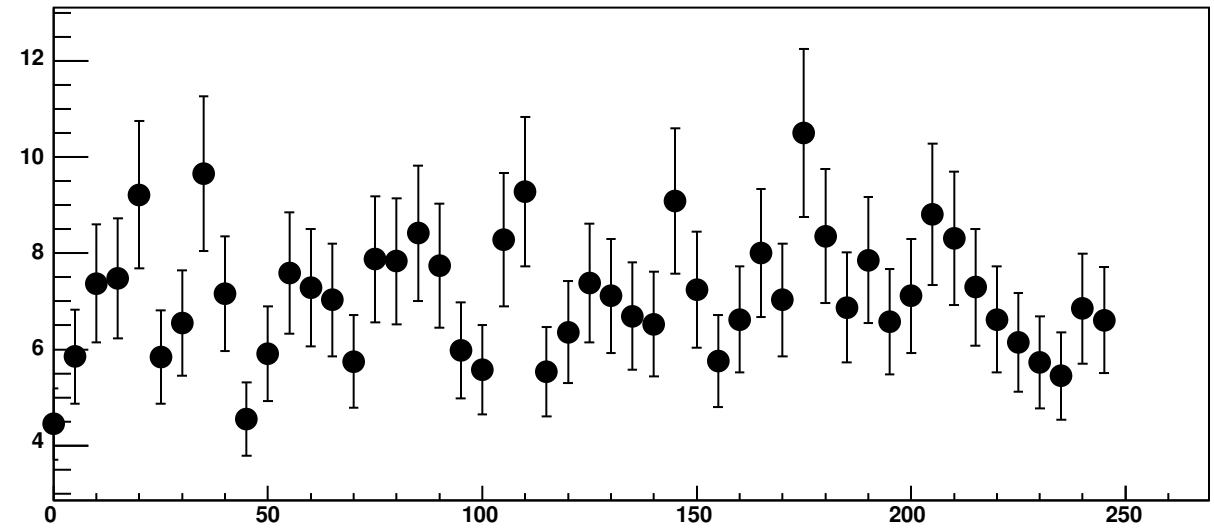

Chip 2, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 2, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
0
50
100
150
200
250

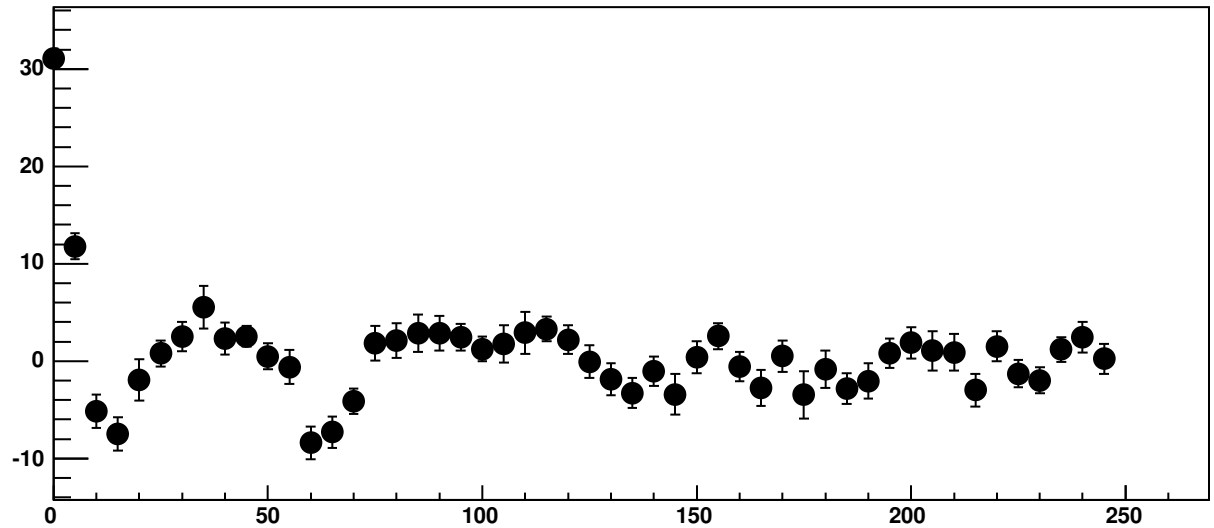
Chip 2, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold



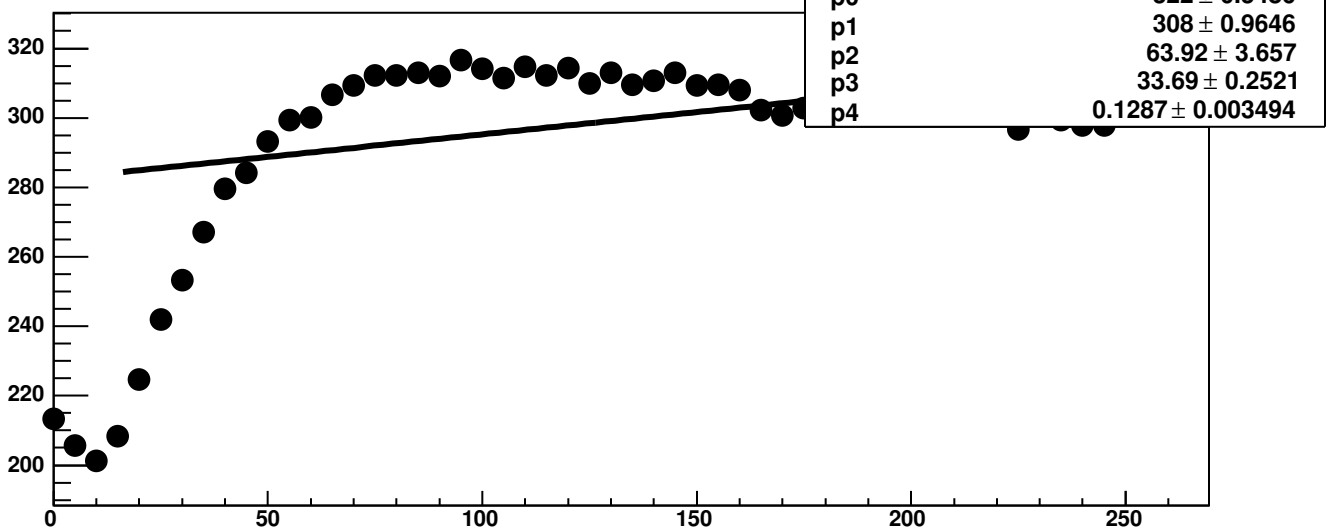
Chip 2, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



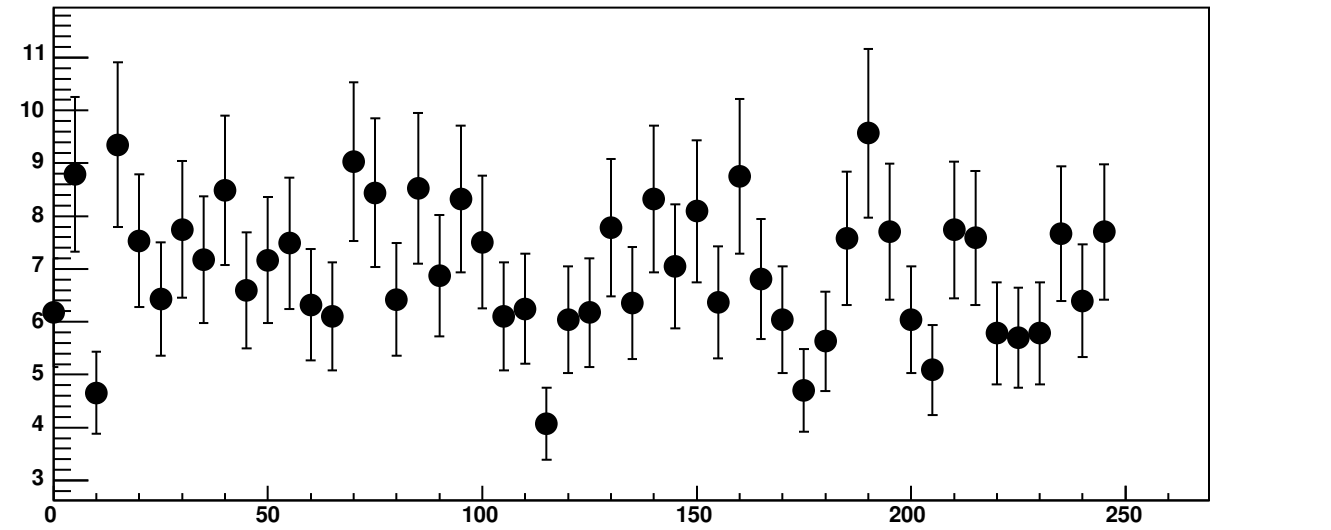
Chip 2, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold



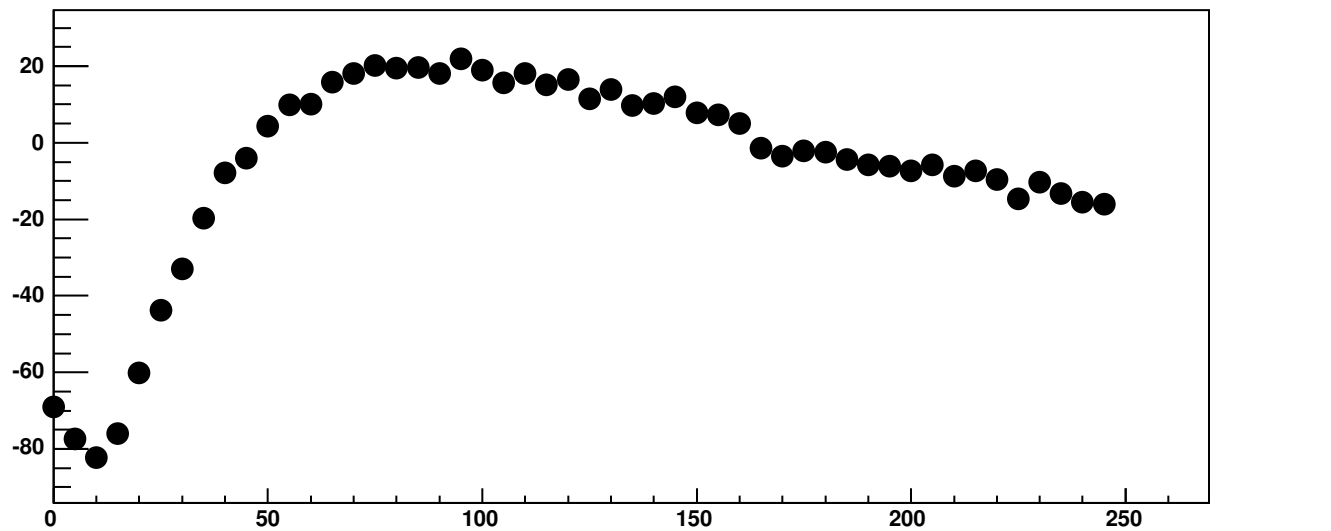
Chip 2, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold



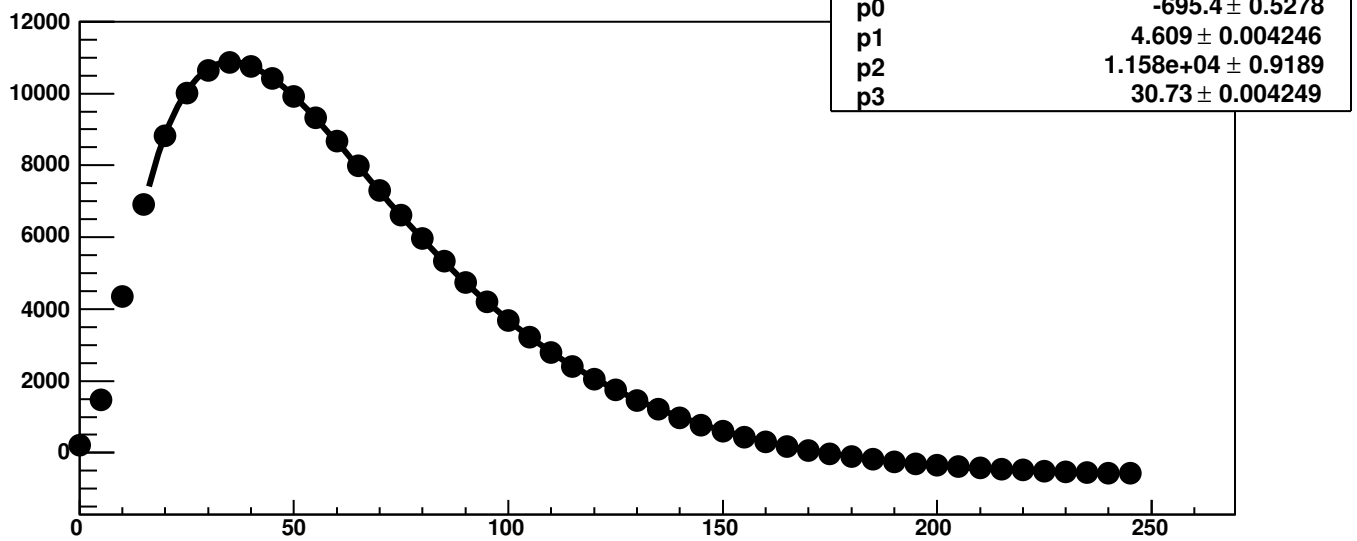
Chip 2, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



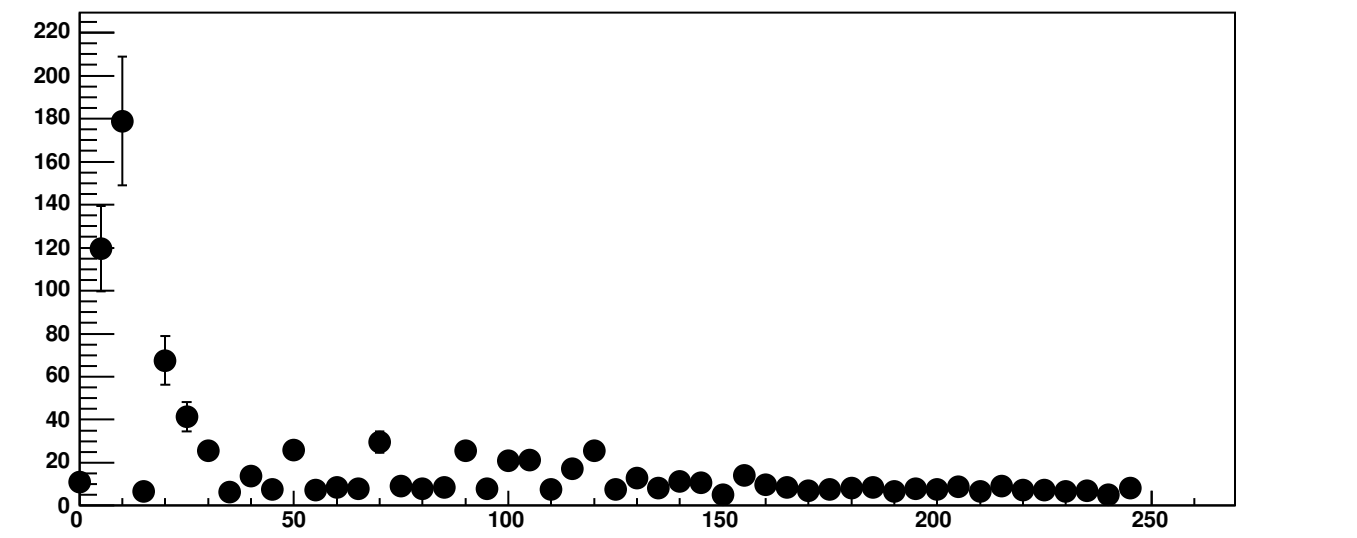
Chip 2, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold



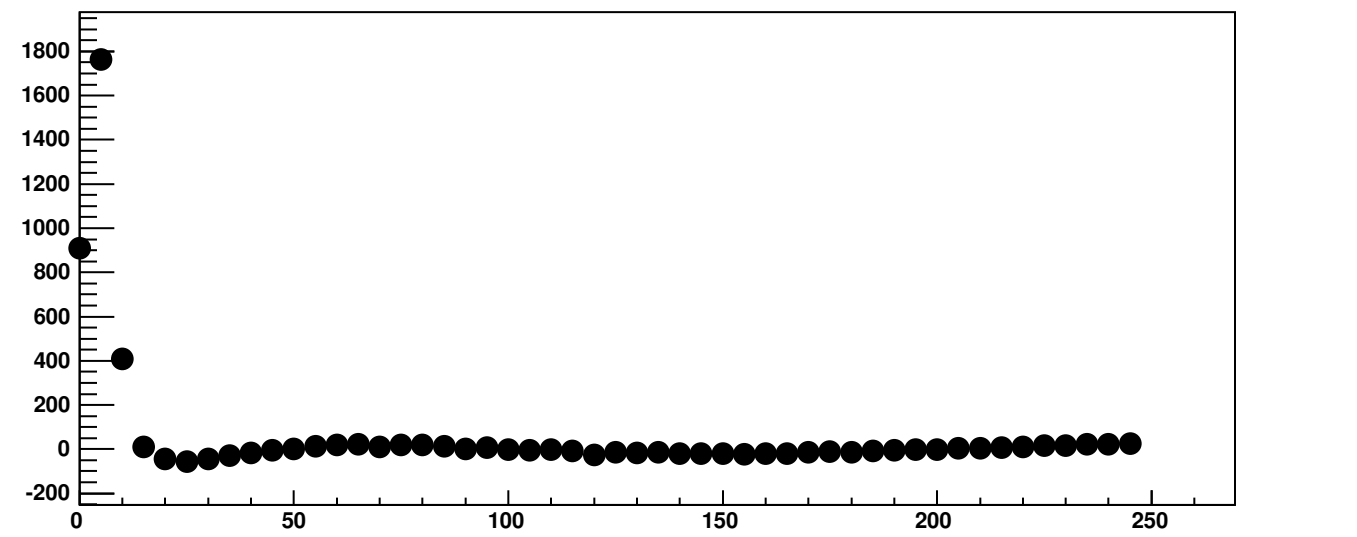
Chip 2, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold



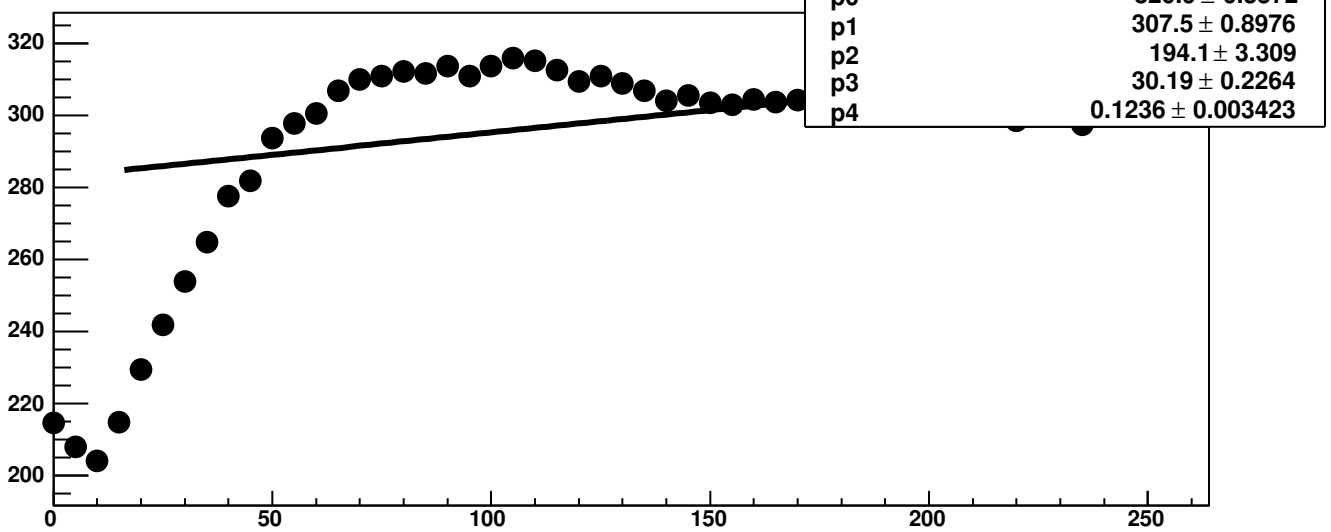
Chip 2, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold



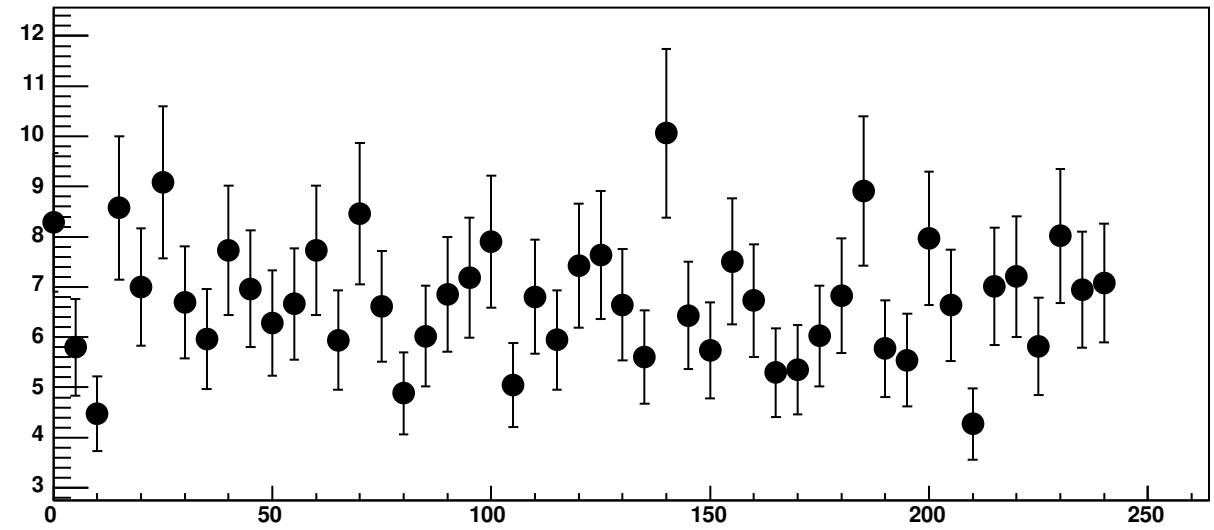
Chip 2, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold



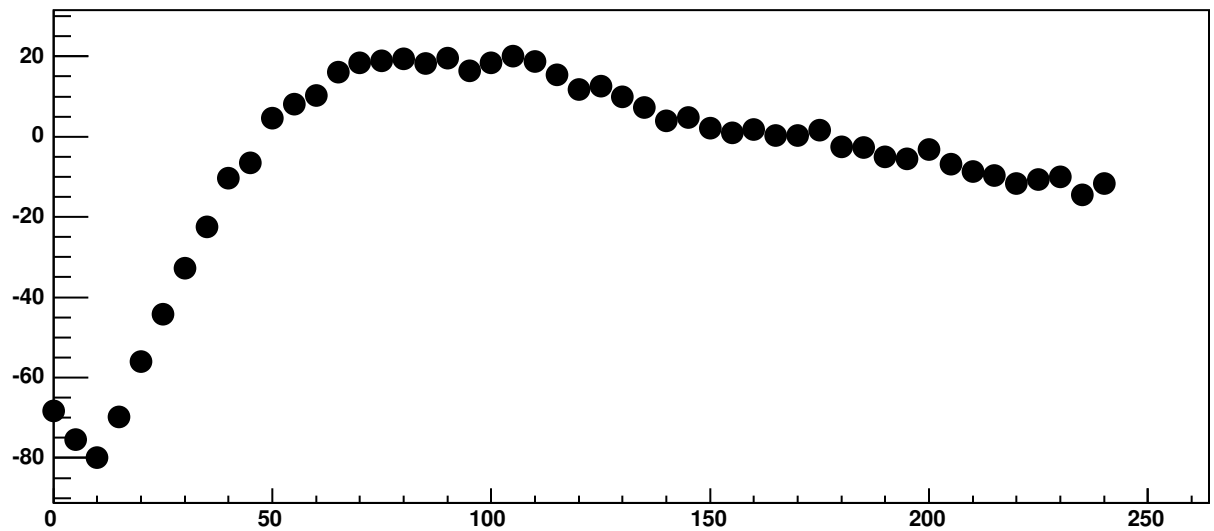
Chip 2, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold



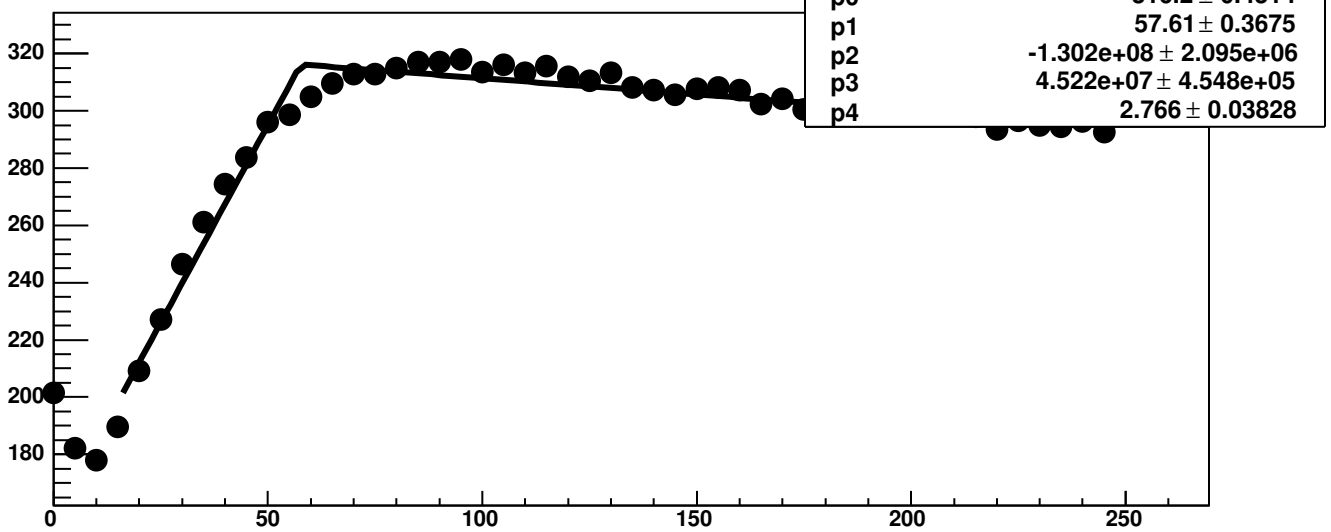
Chip 2, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold



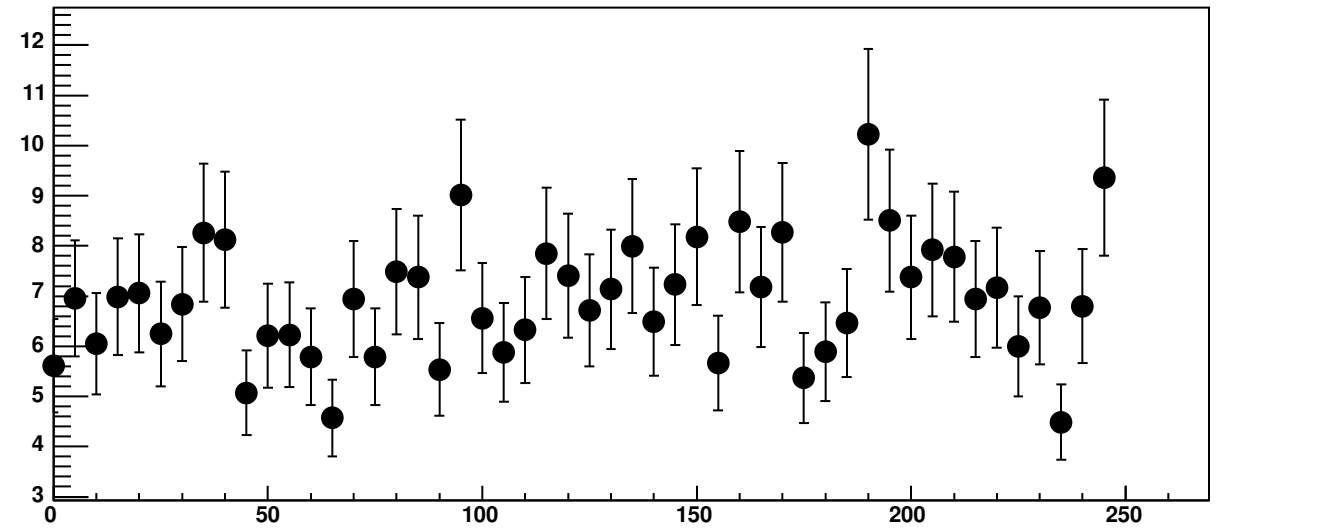
Chip 2, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold



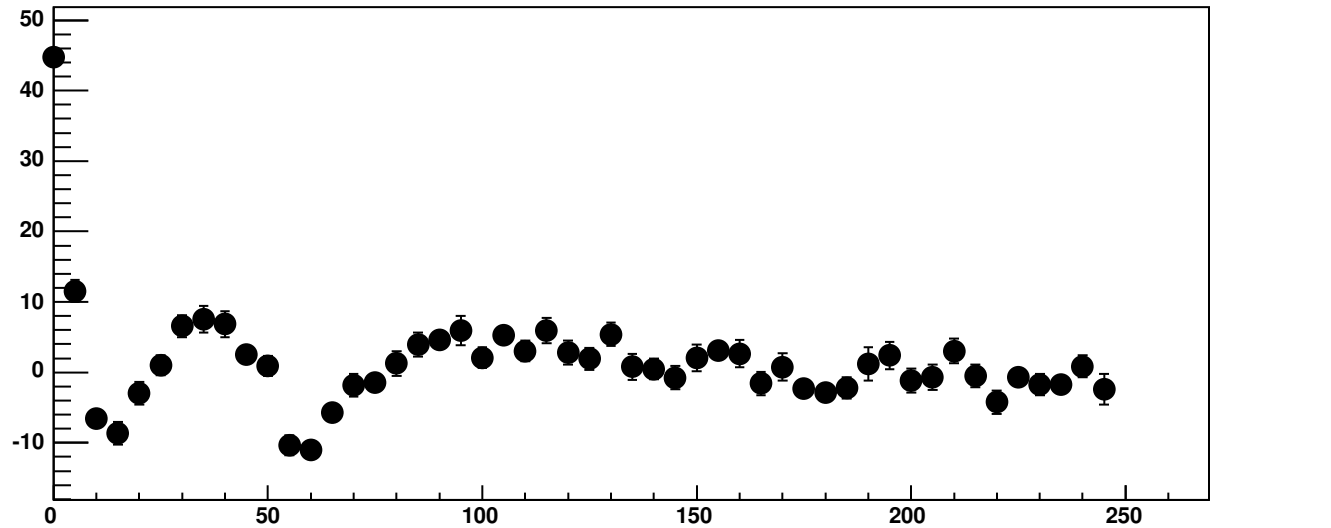
Chip 2, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold



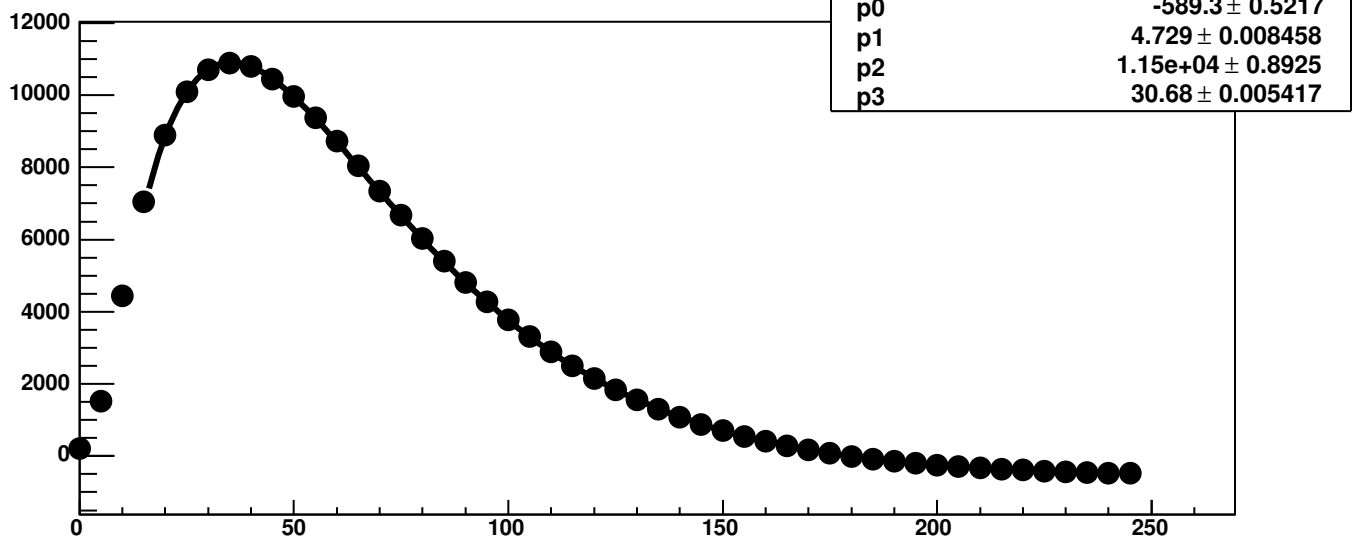
Chip 2, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



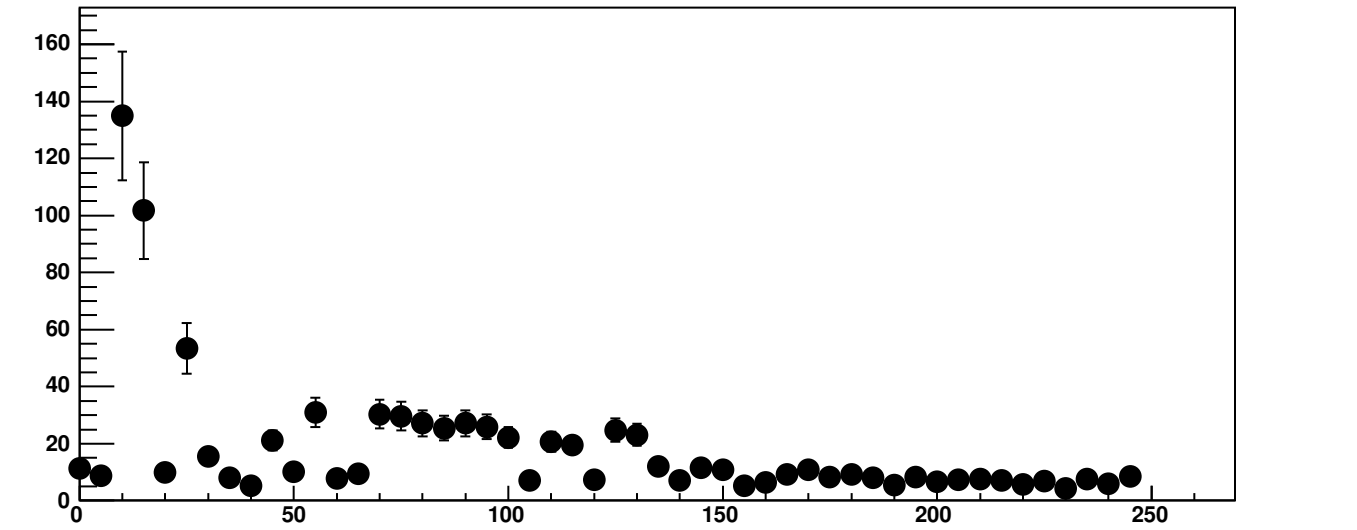
Chip 2, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold



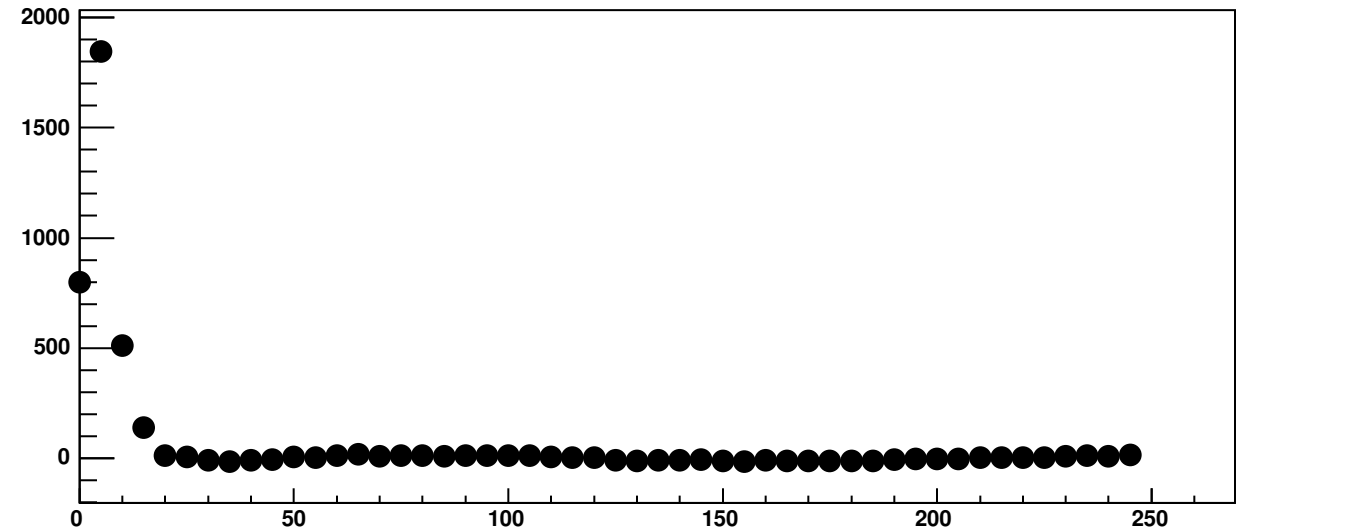
Chip 2, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold

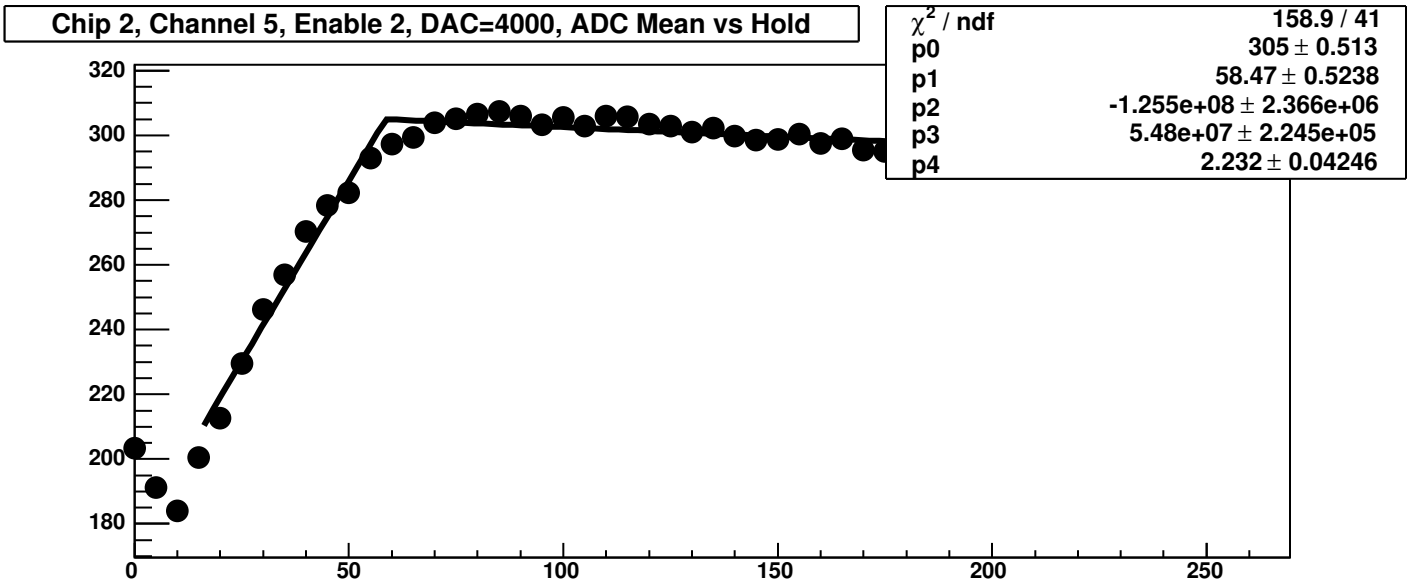


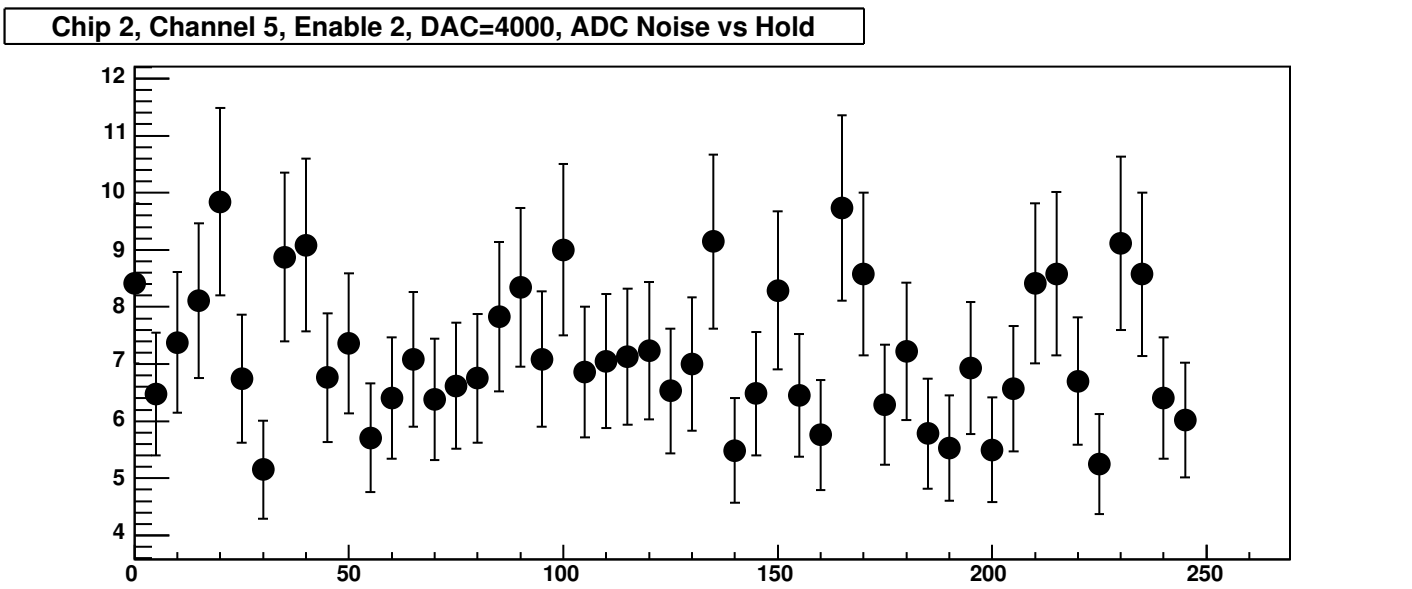
Chip 2, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold

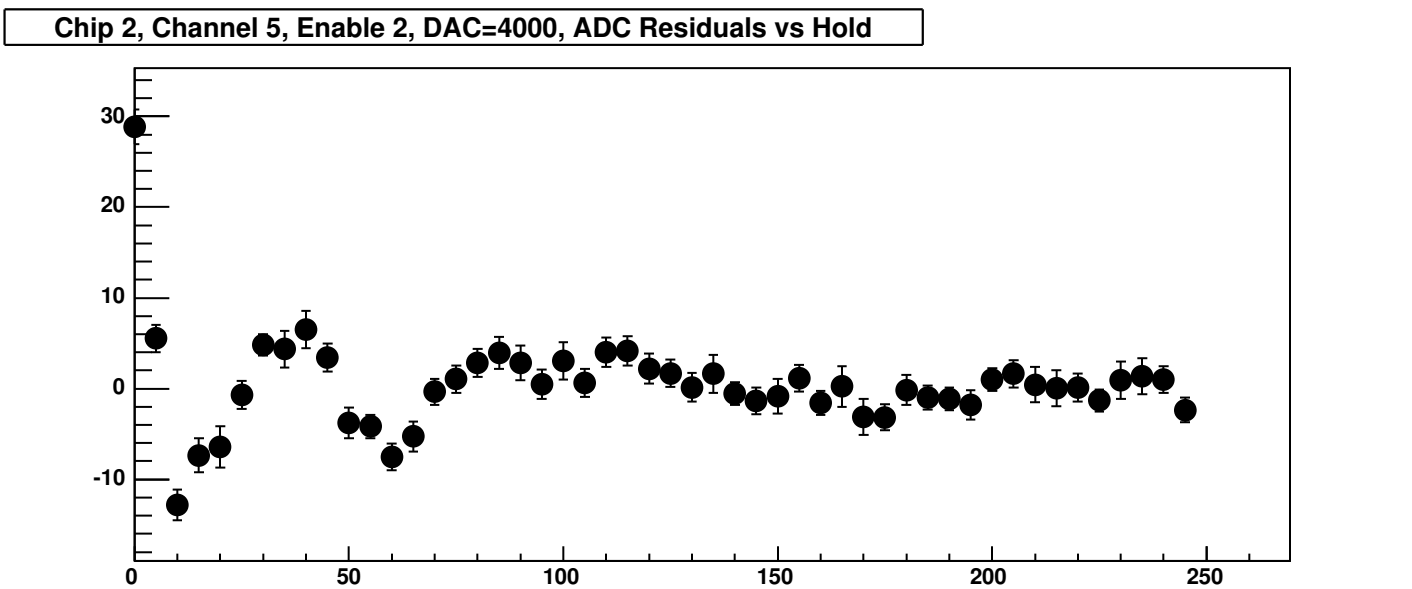


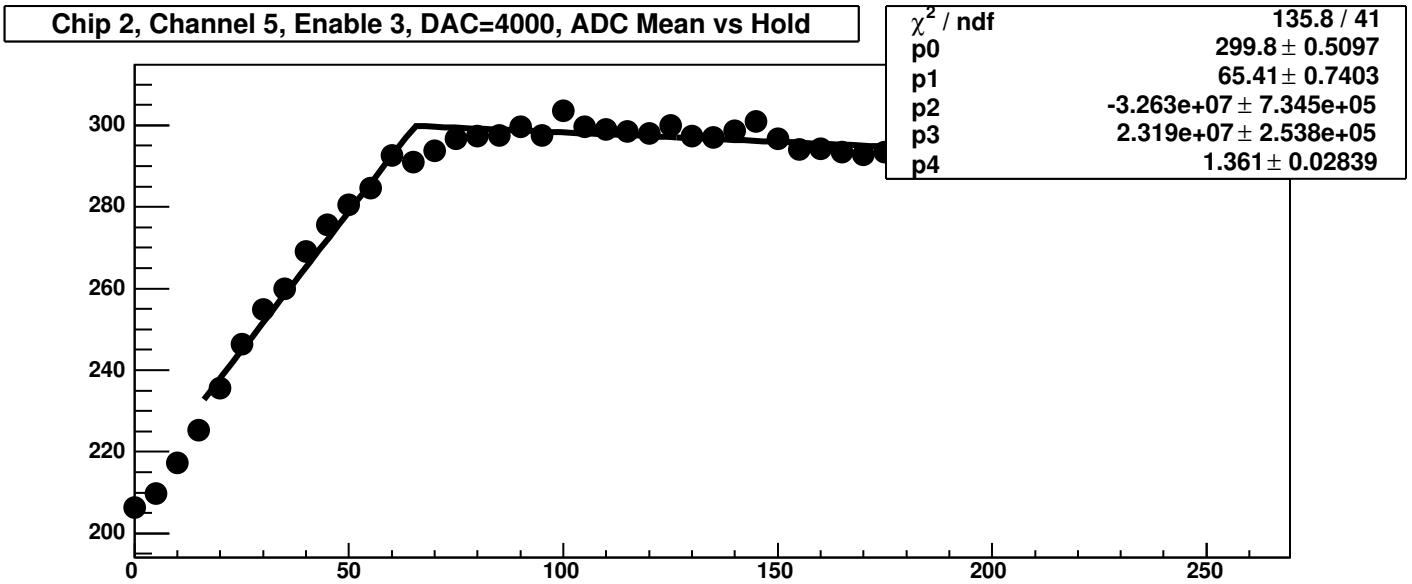
Chip 2, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold

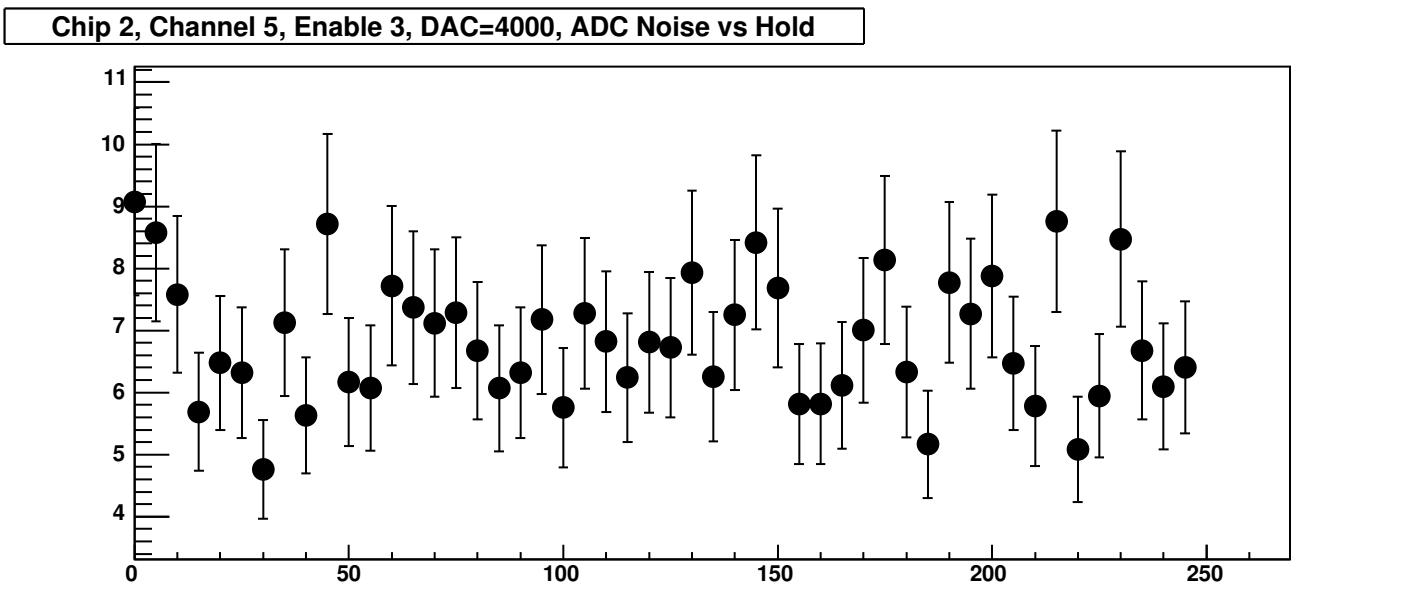


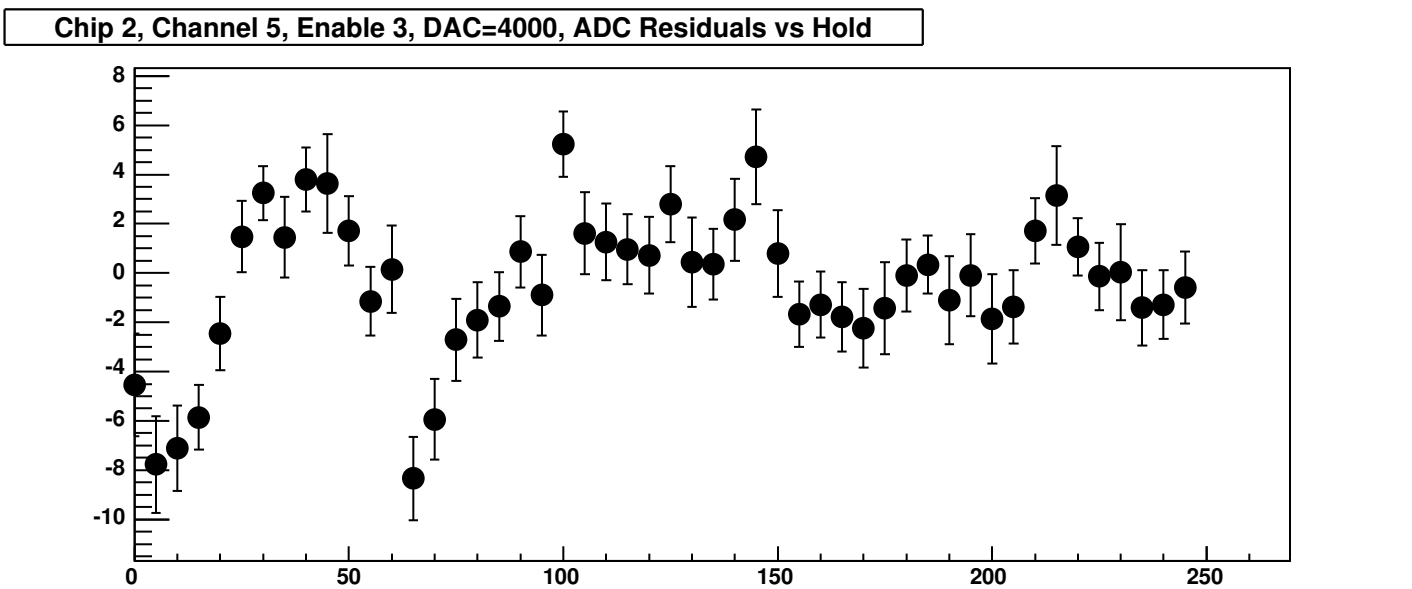

Chip 2, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 158.9 / 41
p0 305 ± 0.513
p1 58.47 ± 0.5238
p2 -1.255e+08 ± 2.366e+06
p3 5.48e+07 ± 2.245e+05
p4 2.232 ± 0.04246
320
300
280
260
240
220
200
180
0
50
100
150
200
250


Chip 2, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

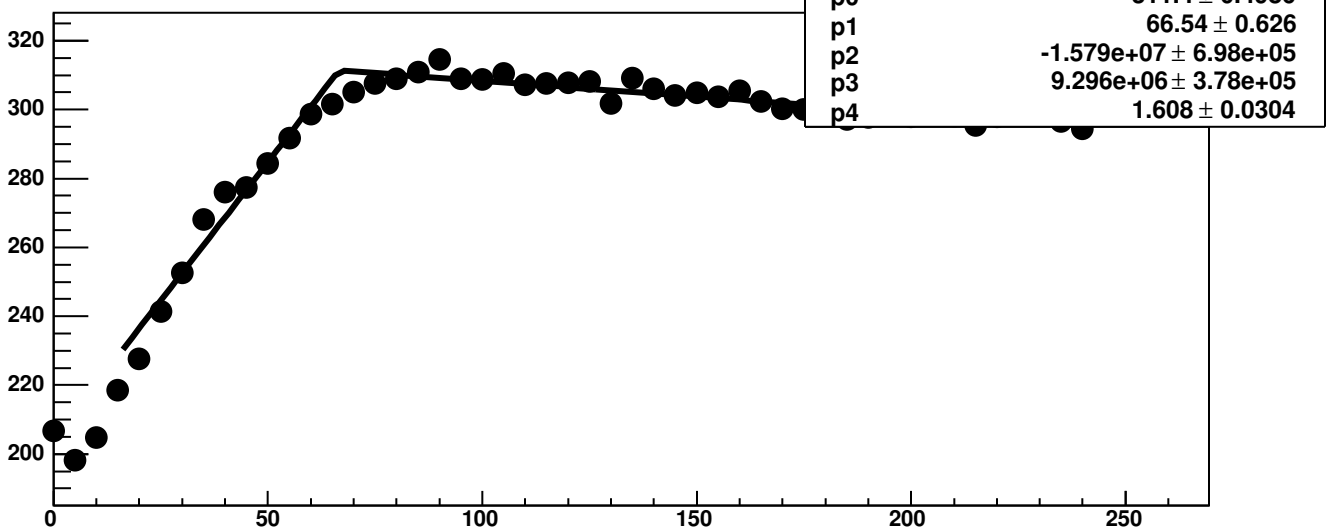

Chip 2, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
0
50
100
150
200
250


Chip 2, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 135.8 / 41
p0 299.8 ± 0.5097
p1 65.41 ± 0.7403
p2 -3.263e+07 ± 7.345e+05
p3 2.319e+07 ± 2.538e+05
p4 1.361 ± 0.02839
300
280
260
240
220
200
0
50
100
150
200
250

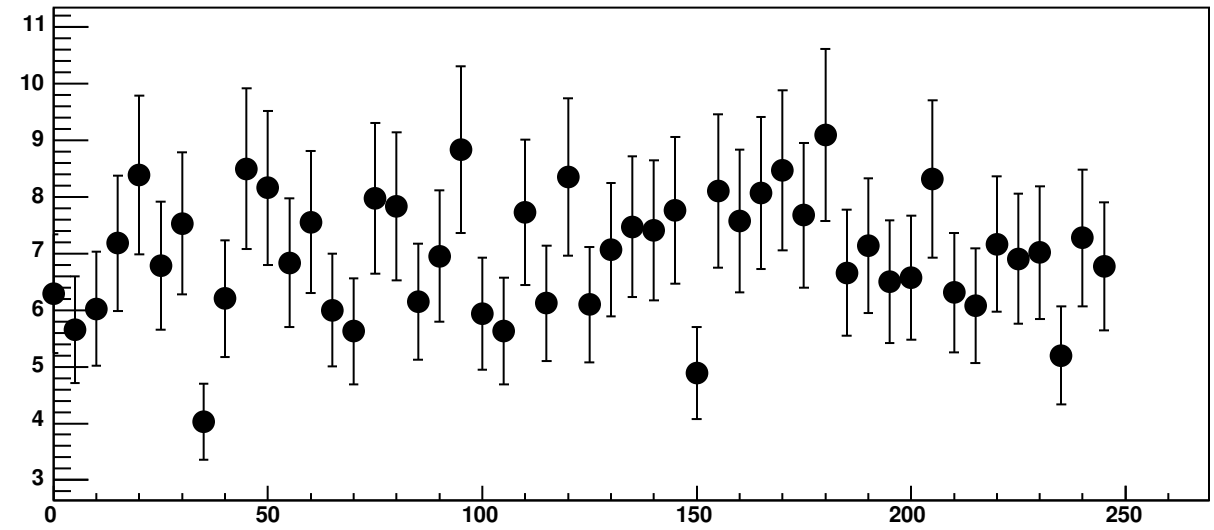

Chip 2, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 2, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold
8
6
4
2
0
-2
-4
-6
-8
-10
0
50
100
150
200
250

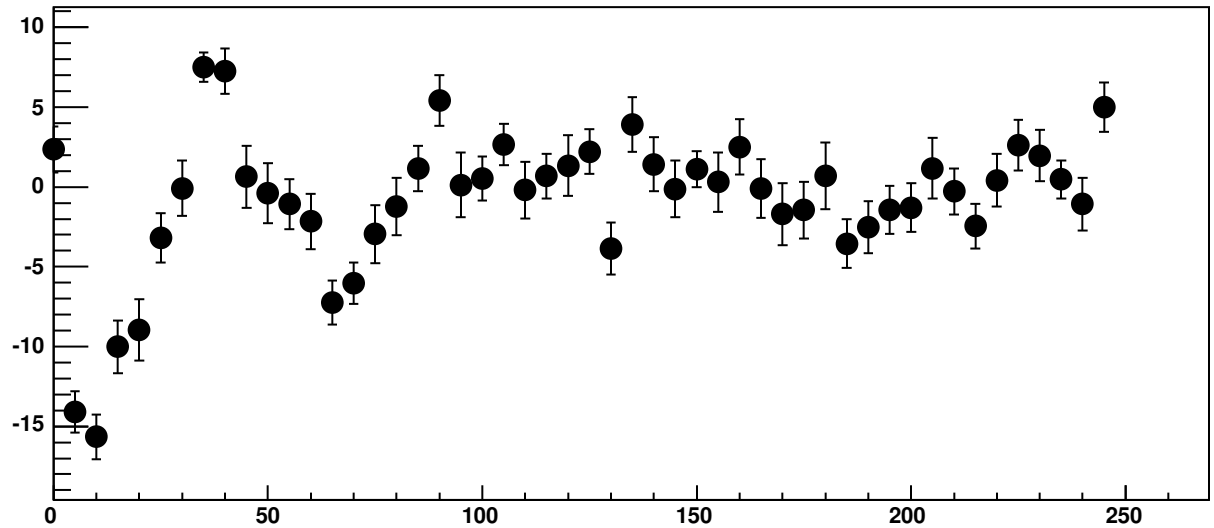
Chip 2, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold



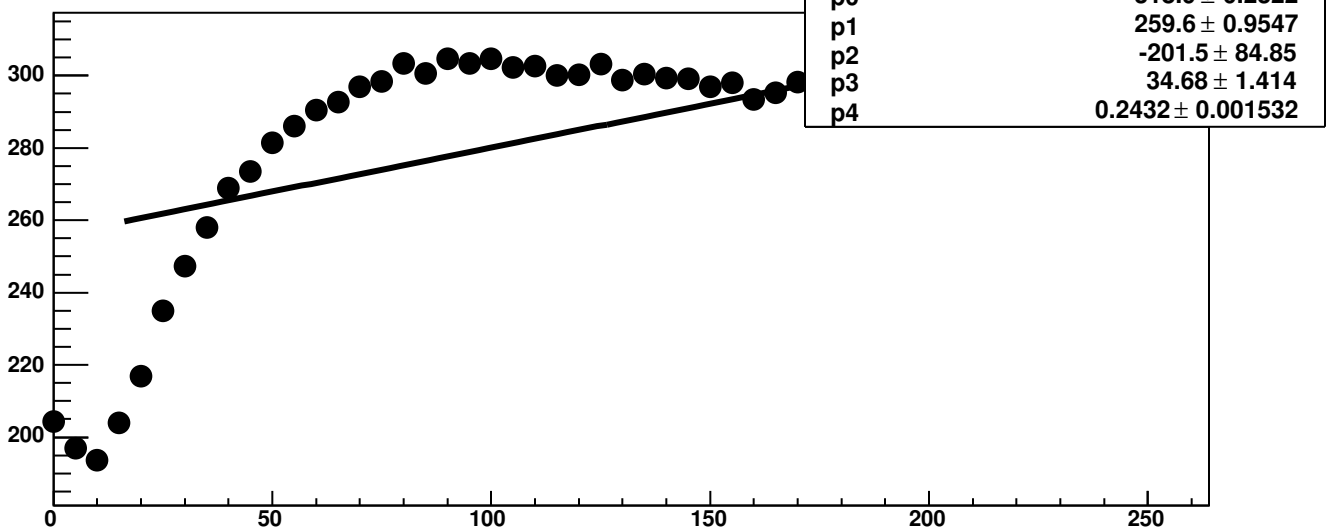
Chip 2, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold



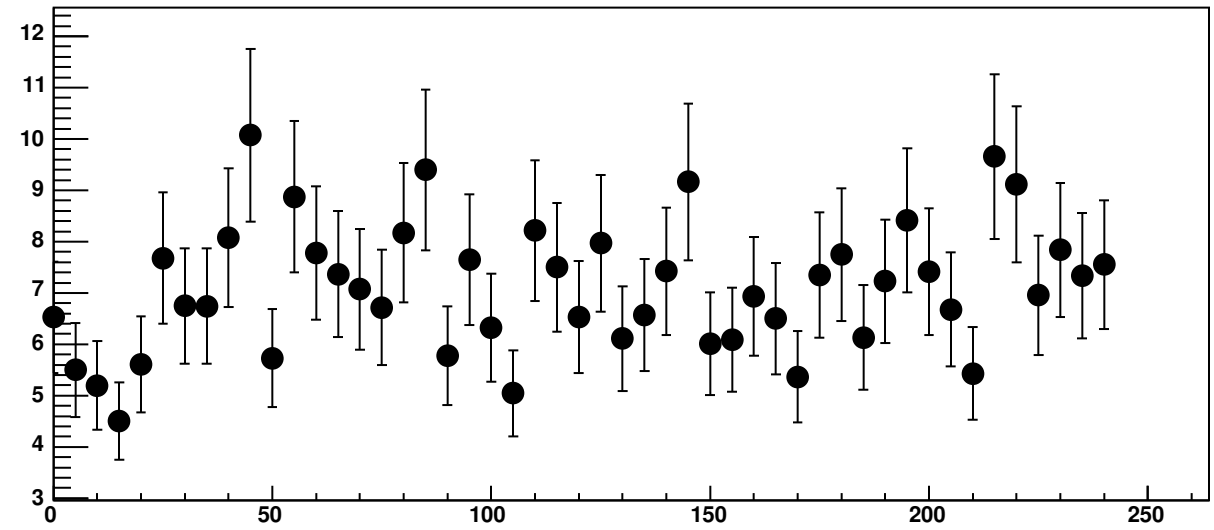
Chip 2, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold



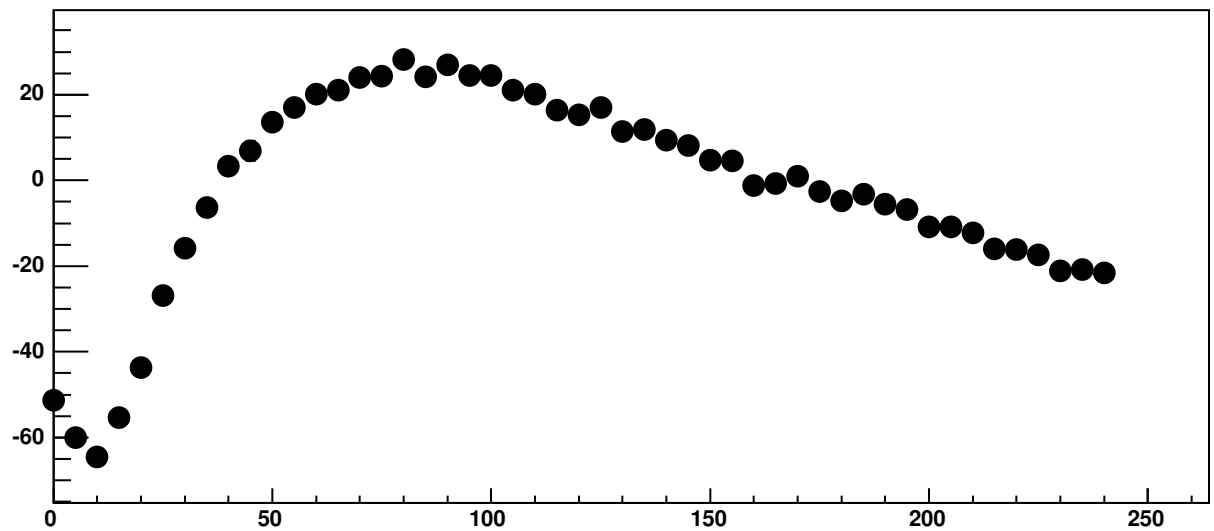
Chip 2, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold



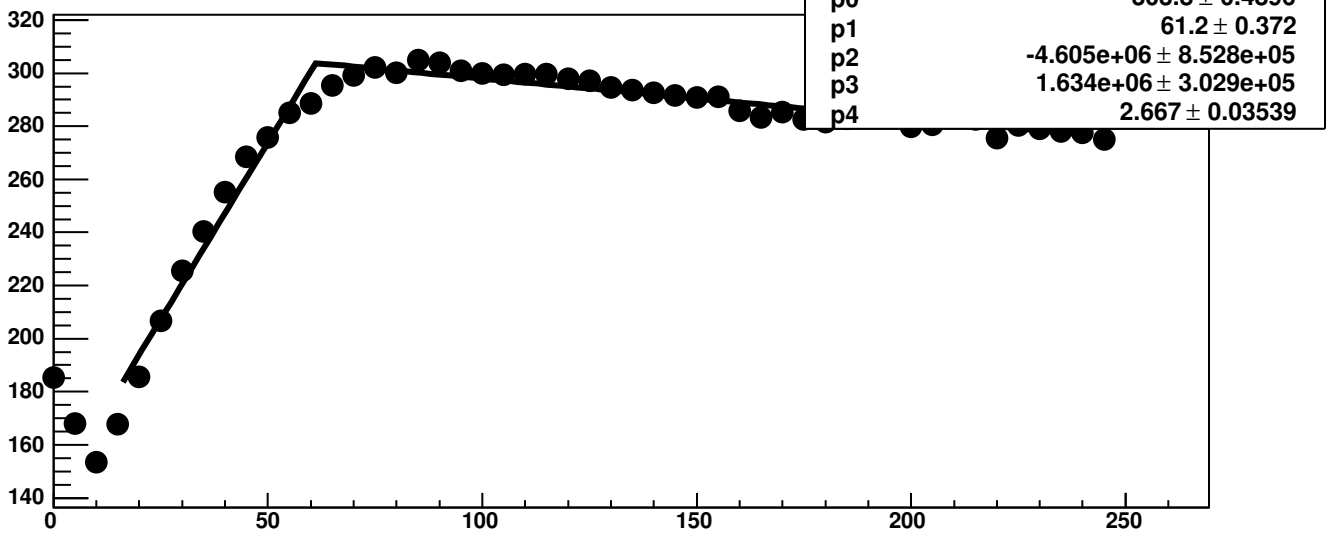
Chip 2, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



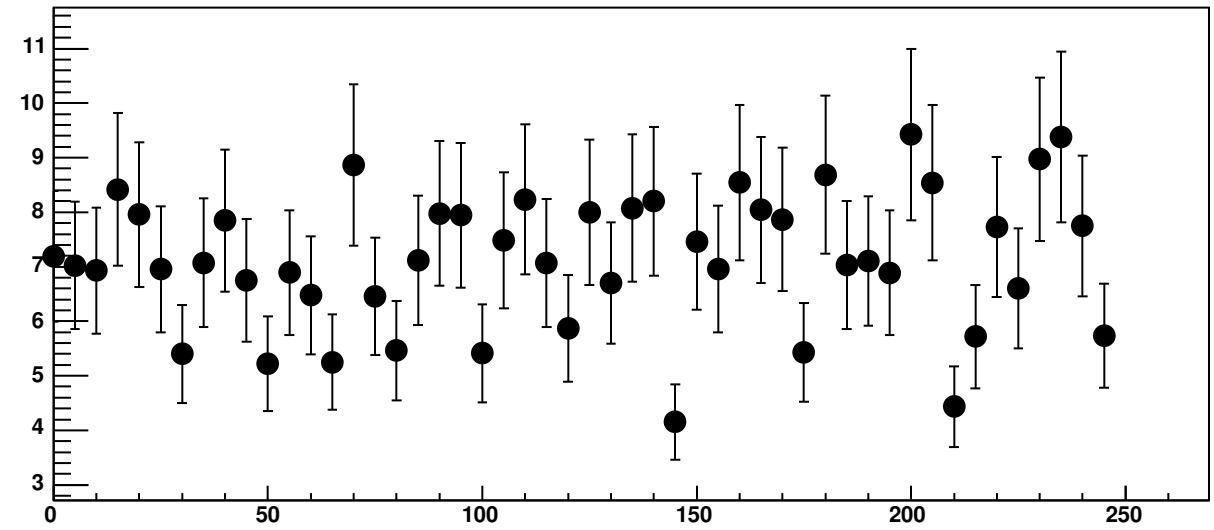
Chip 2, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold



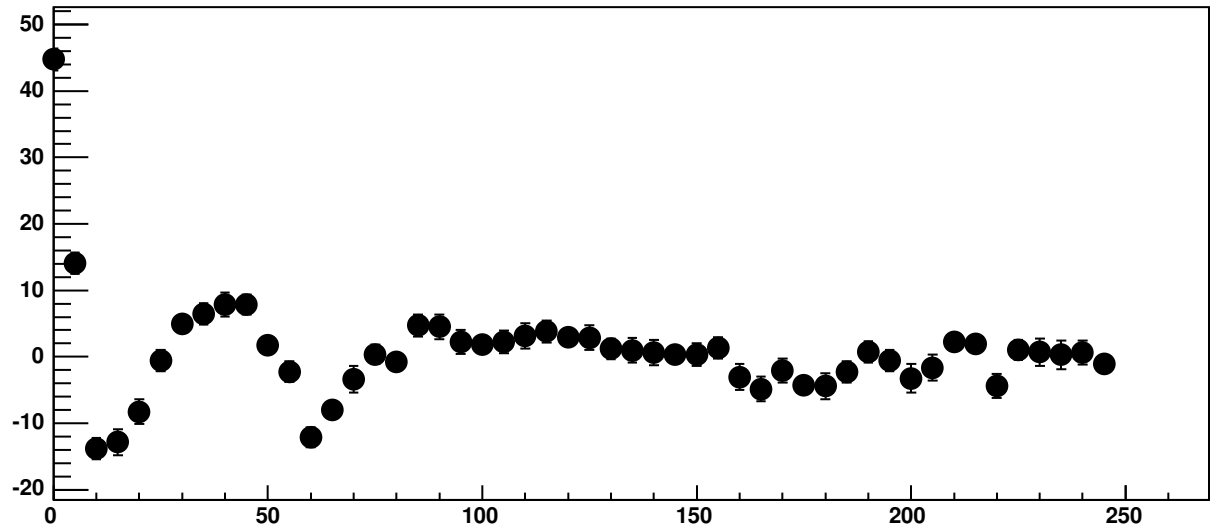
Chip 2, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold

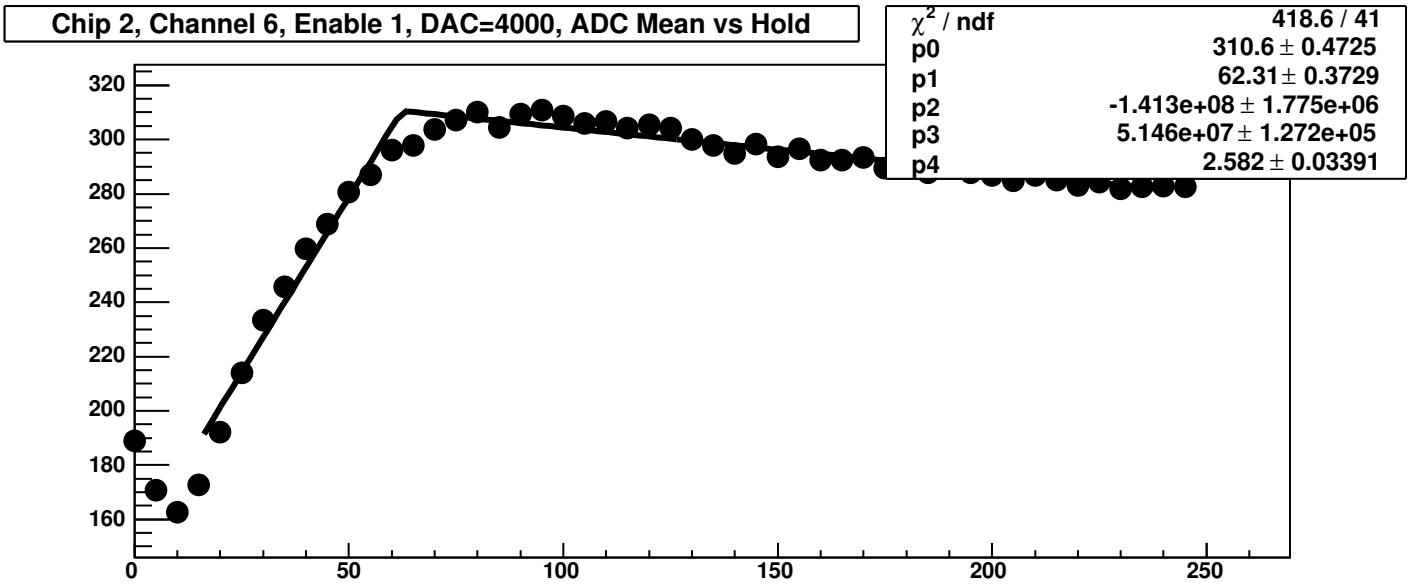


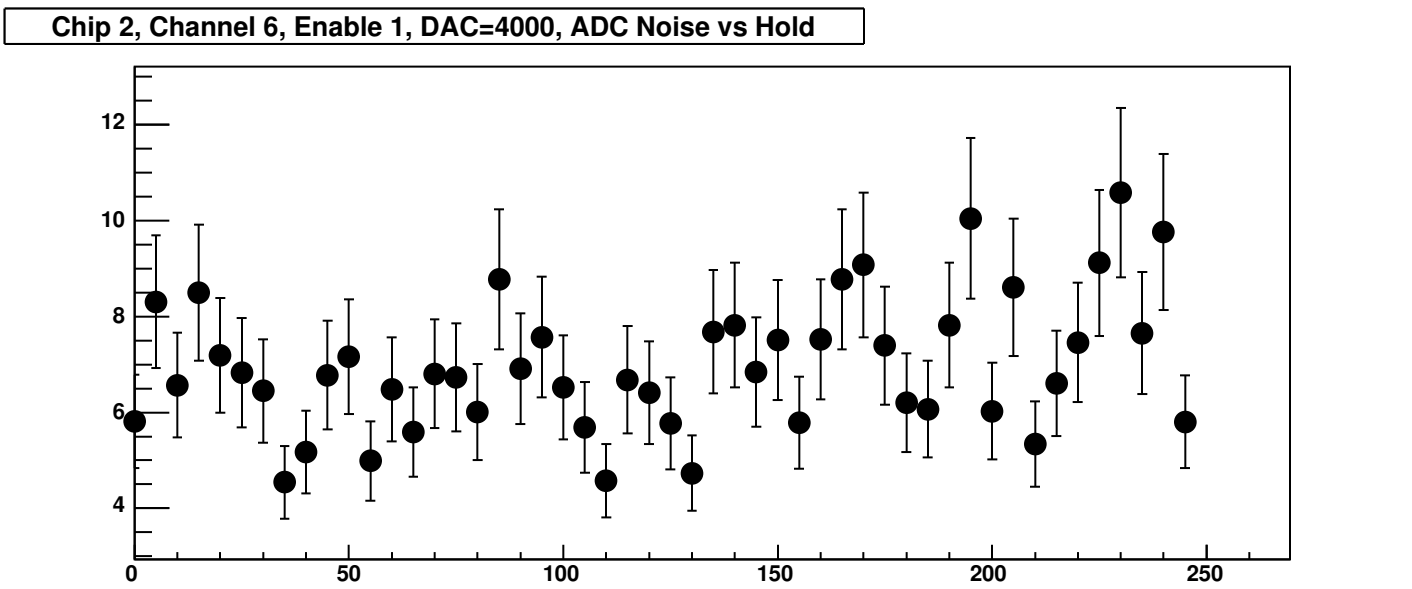
Chip 2, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold

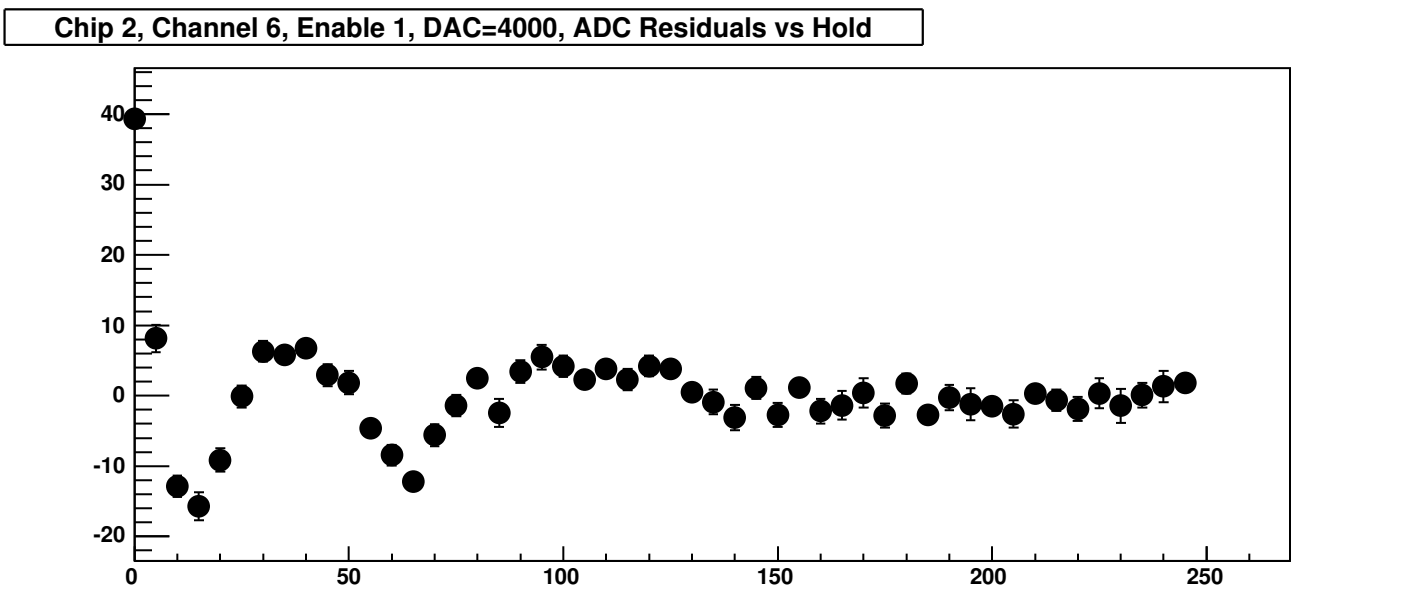


Chip 2, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold



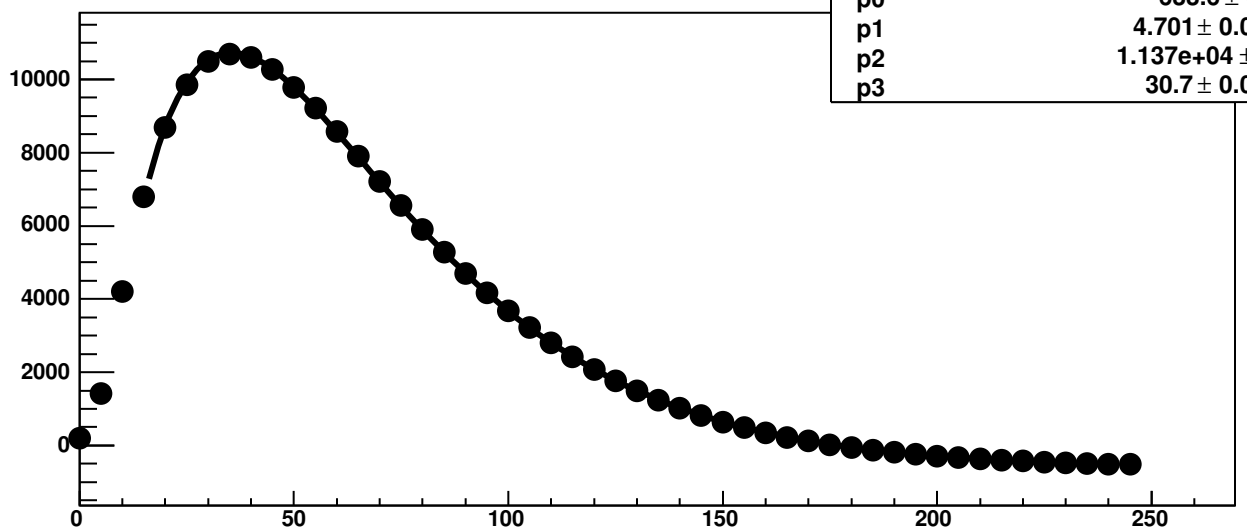

Chip 2, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 418.6 / 41
p0 310.6 ± 0.4725
p1 62.31 ± 0.3729
p2 -1.413e+08 ± 1.775e+06
p3 5.146e+07 ± 1.272e+05
p4 2.582 ± 0.03391
320
300
280
260
240
220
200
180
160
0
50
100
150
200
250


Chip 2, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

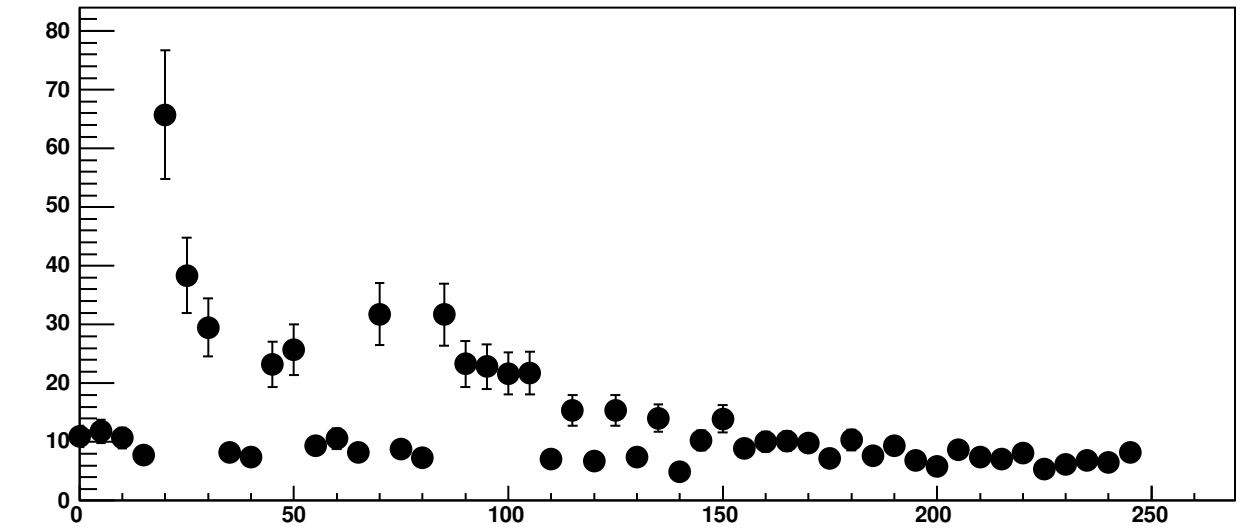

Chip 2, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
-20
0
50
100
150
200
250

Chip 2, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold

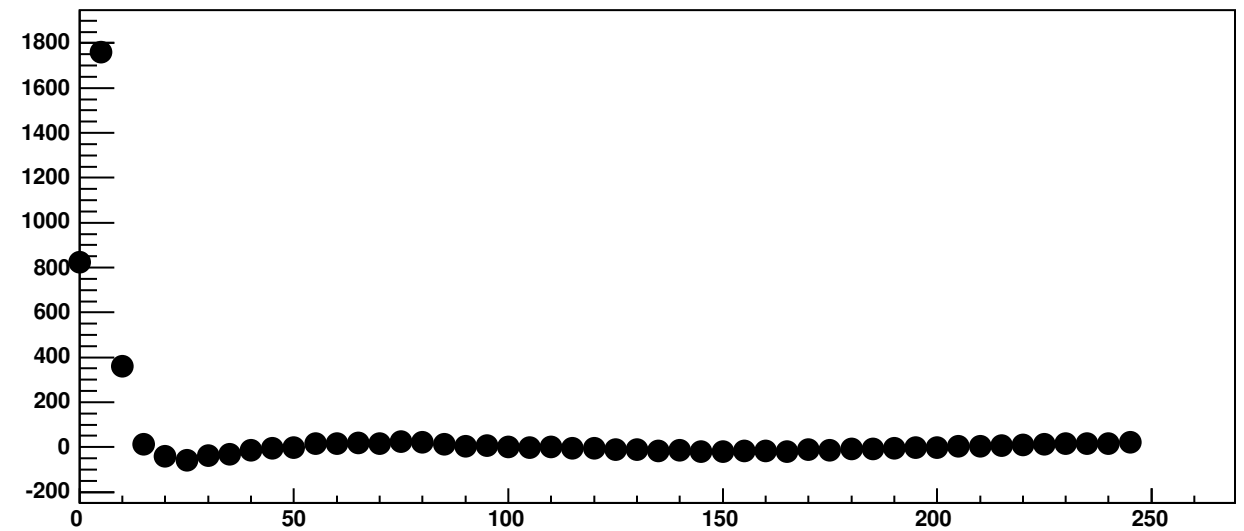
χ^2 / ndf	2096 / 42
p0	-633.6 ± 0.5296
p1	4.701 ± 0.004828
p2	1.137e+04 ± 1.025
p3	30.7 ± 0.004516



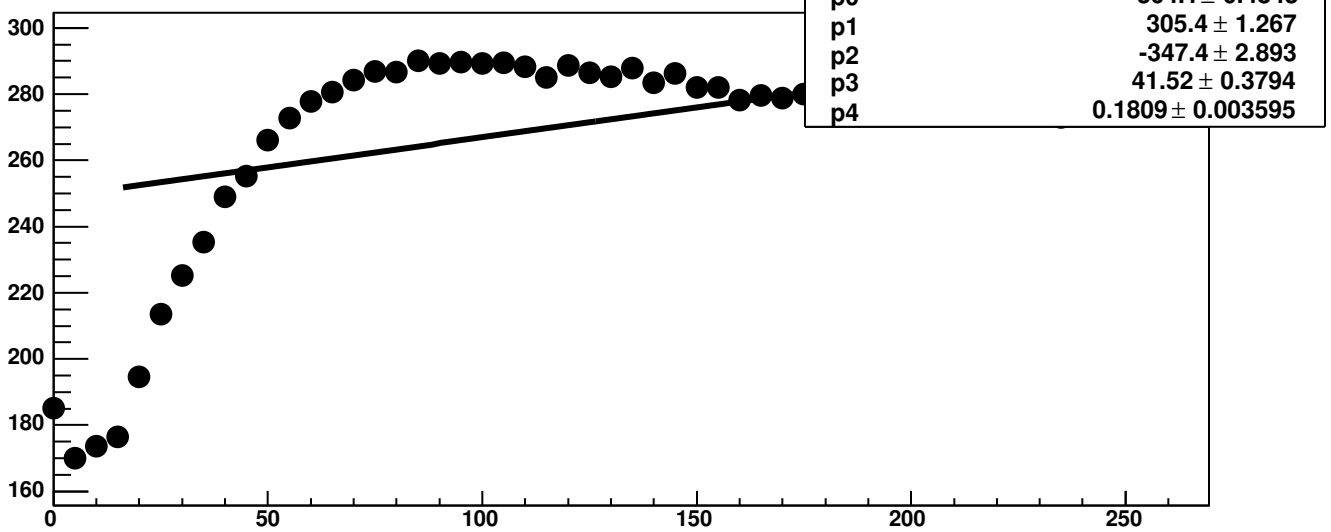
Chip 2, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold



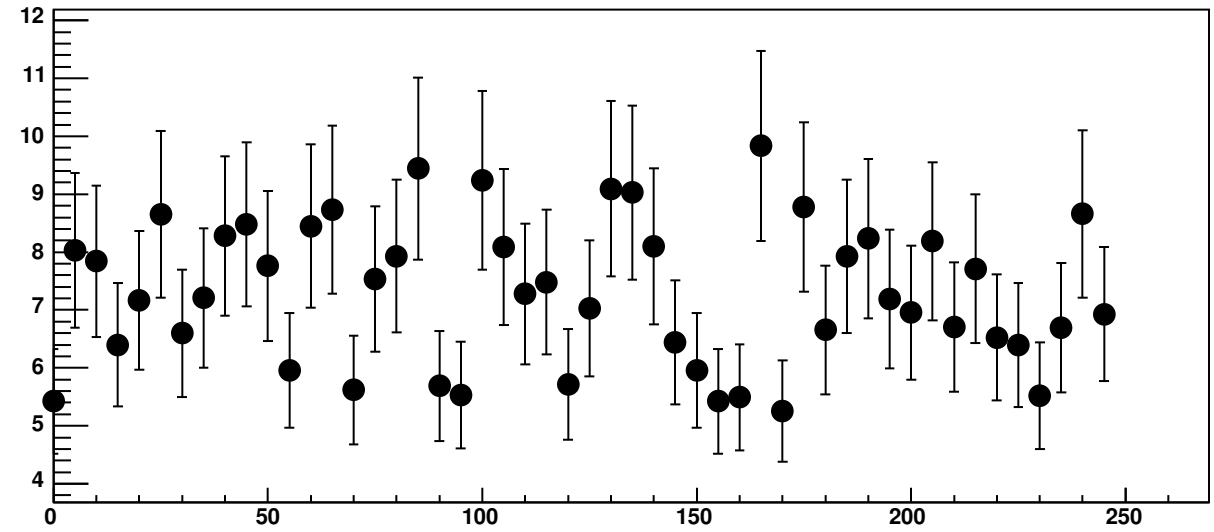
Chip 2, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold



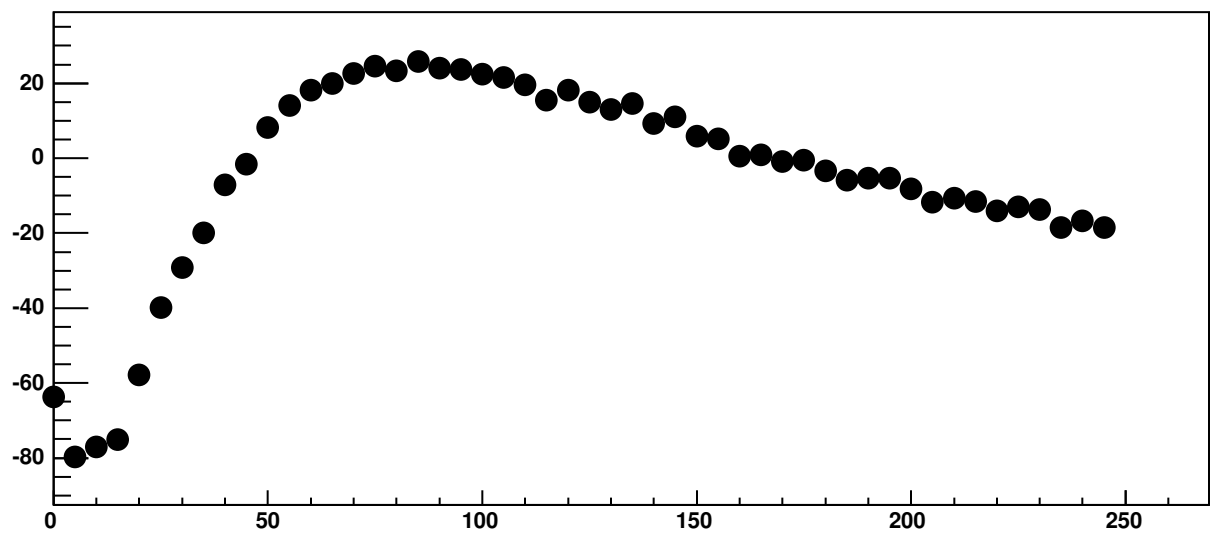
Chip 2, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold



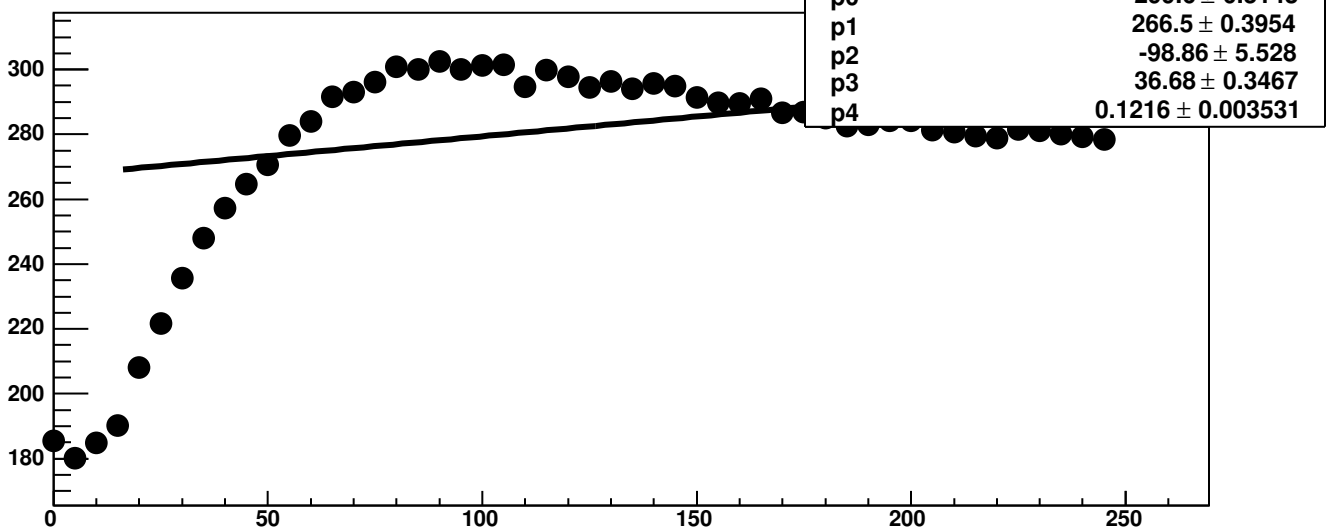
Chip 2, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold



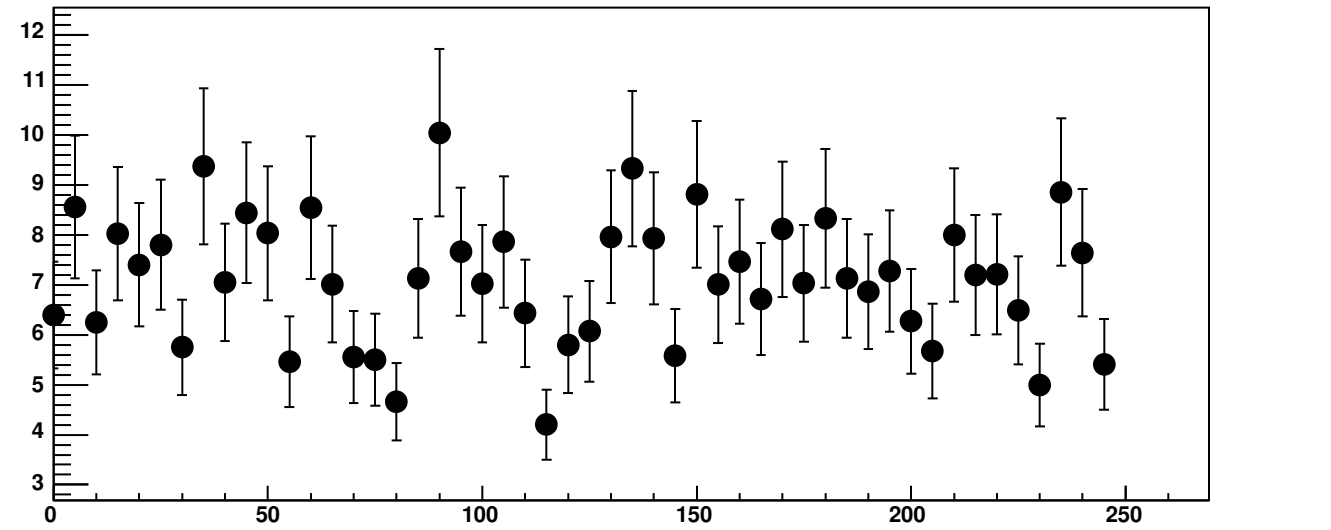
Chip 2, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold



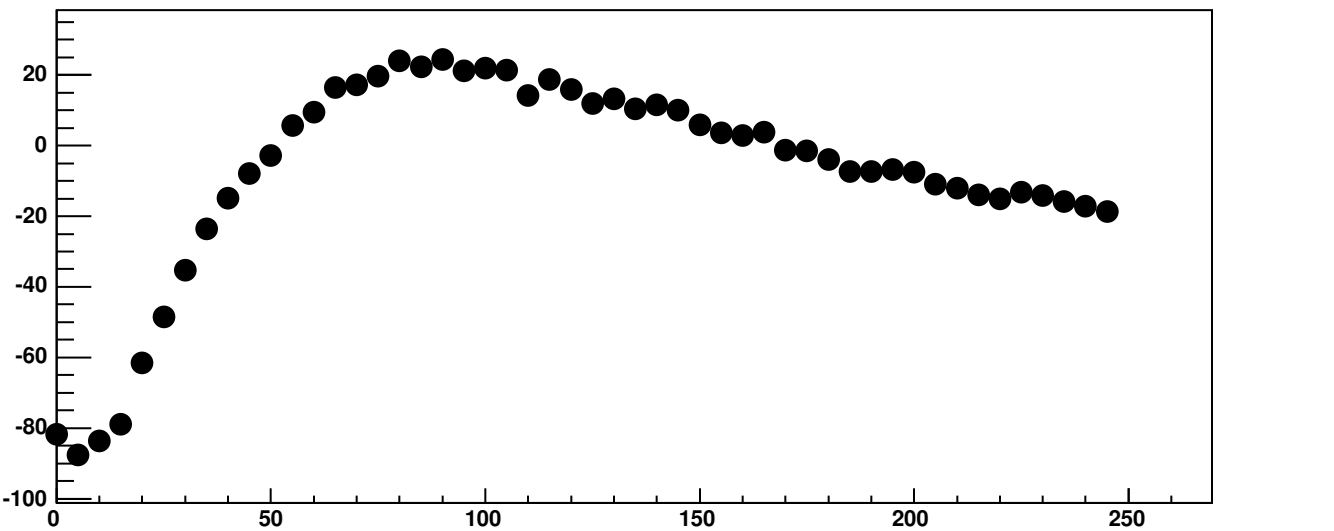
Chip 2, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold



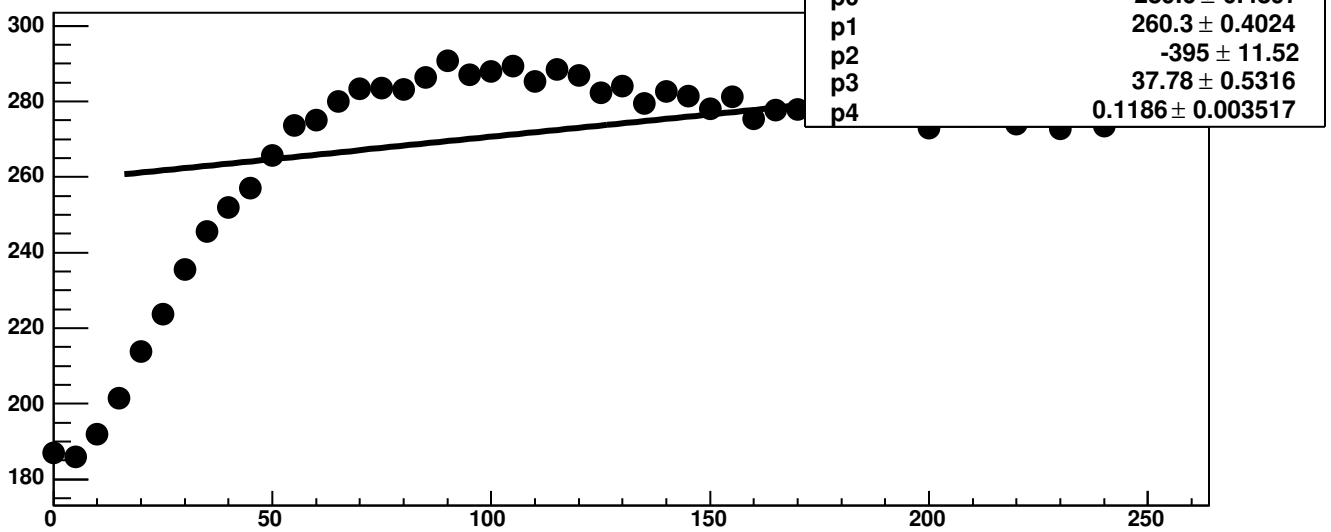
Chip 2, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold



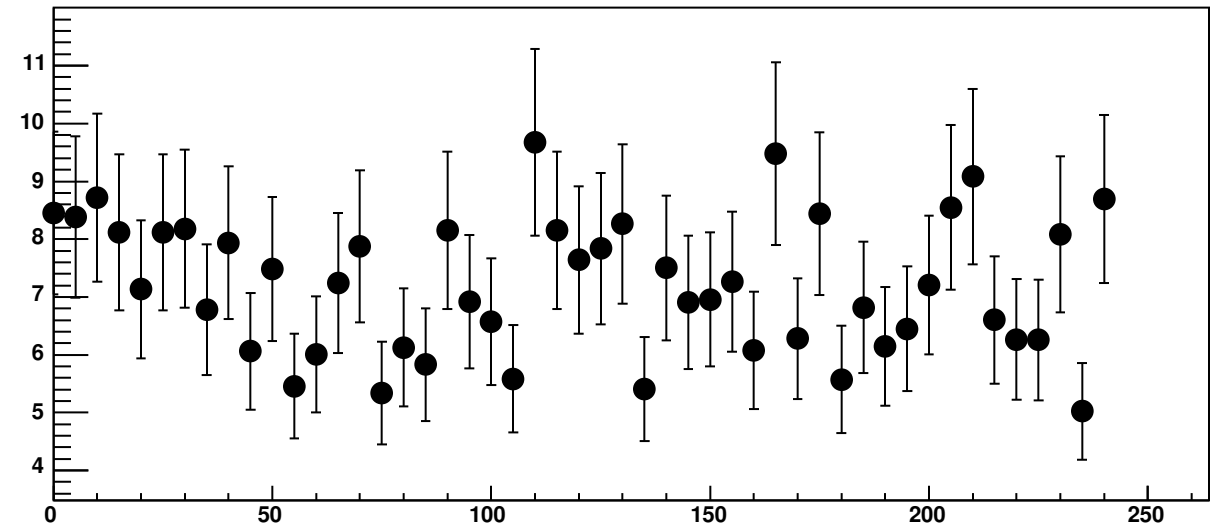
Chip 2, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold



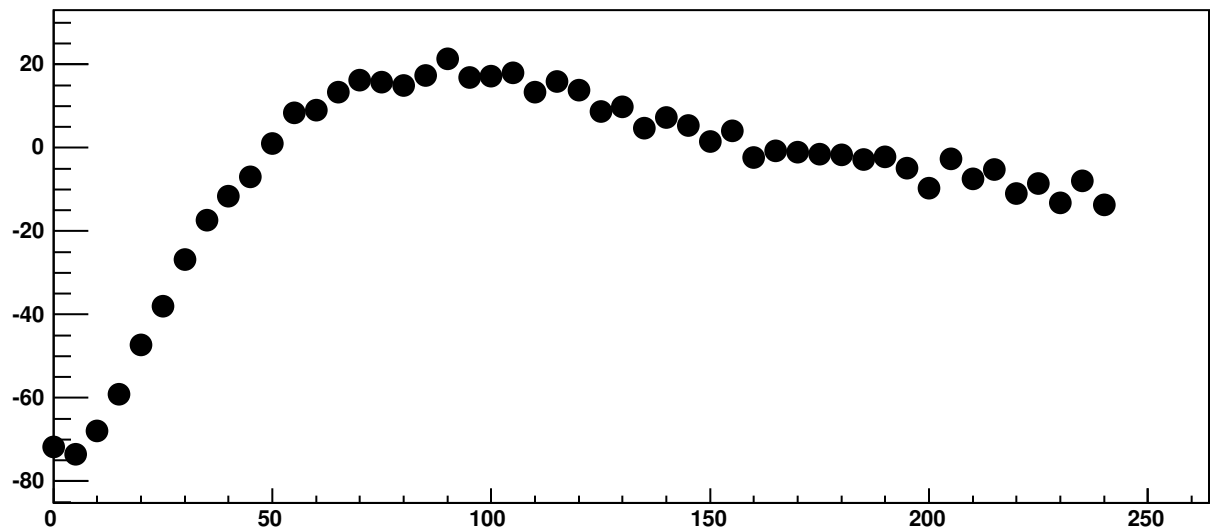
Chip 2, Channel 6, Enable 5, DAC=4000, ADC Mean vs Hold



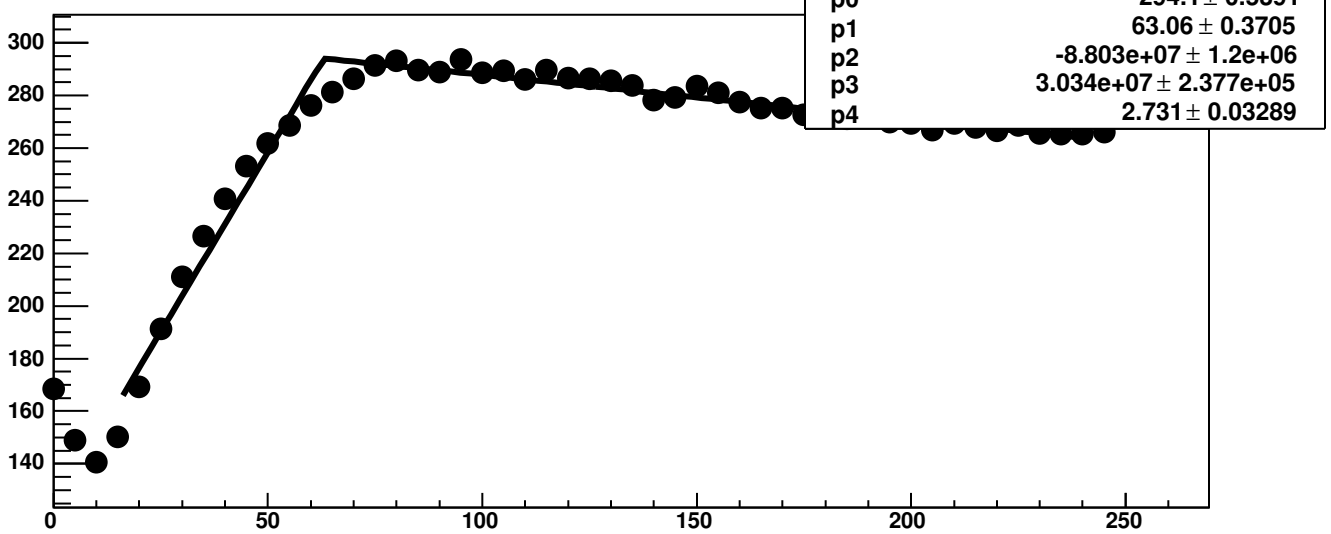
Chip 2, Channel 6, Enable 5, DAC=4000, ADC Noise vs Hold



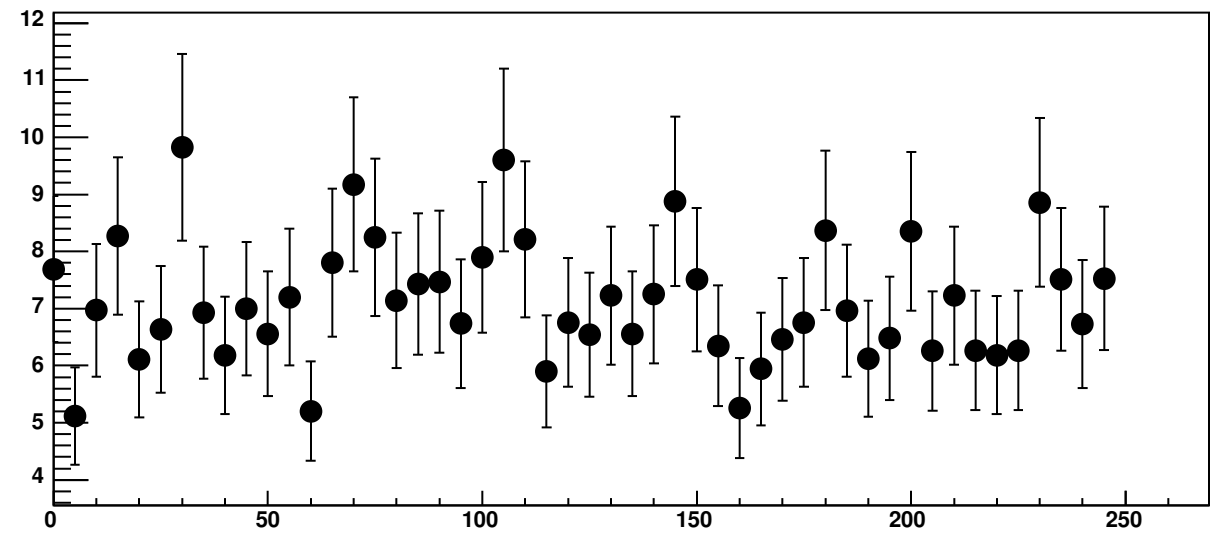
Chip 2, Channel 6, Enable 5, DAC=4000, ADC Residuals vs Hold



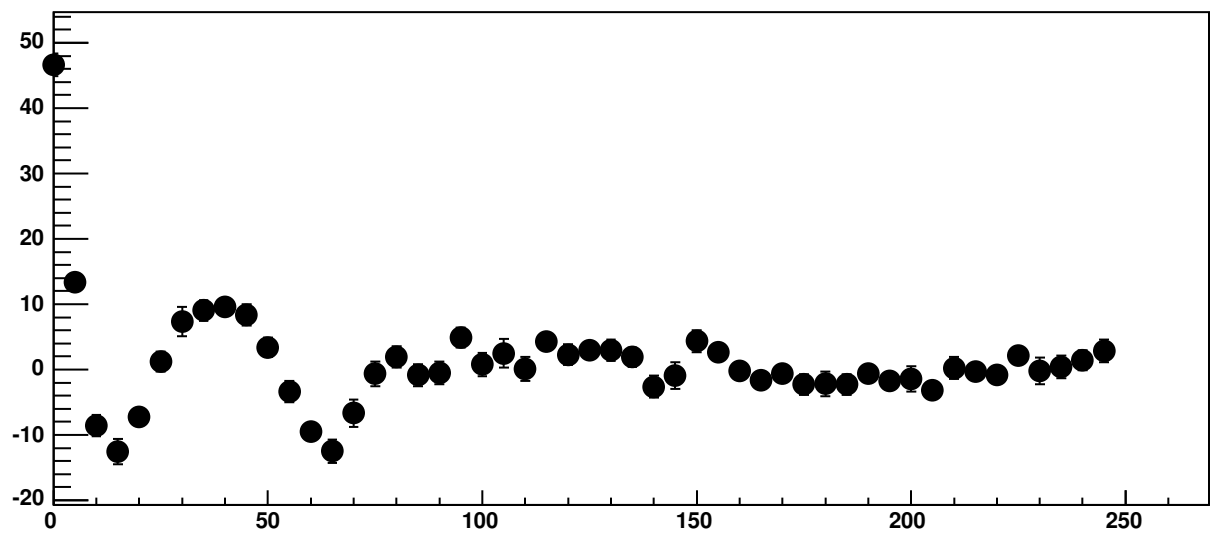
Chip 2, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold



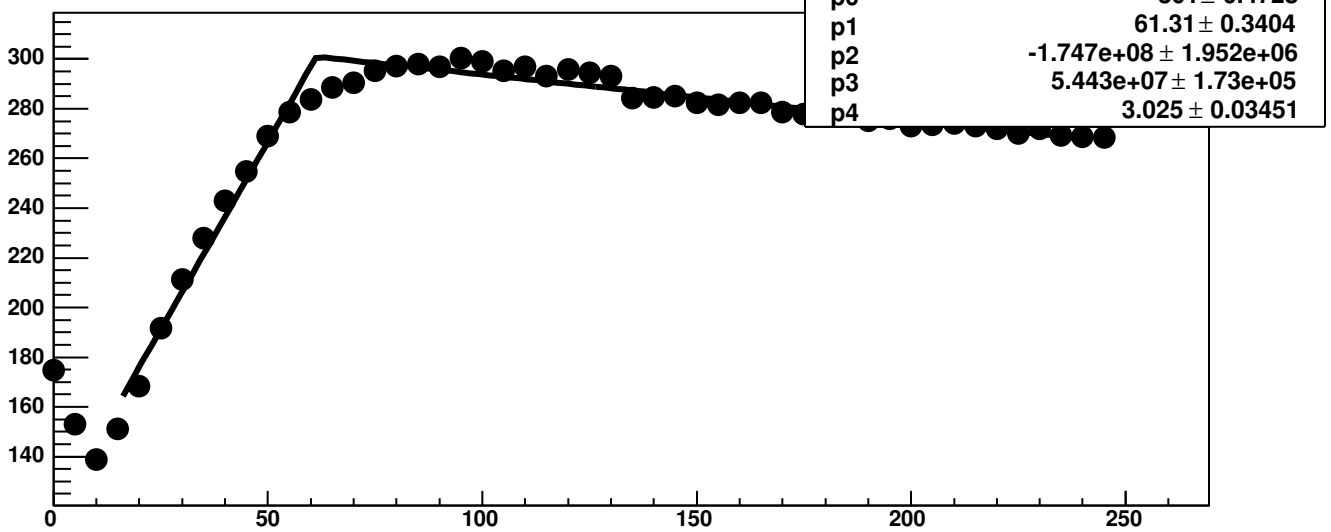
Chip 2, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



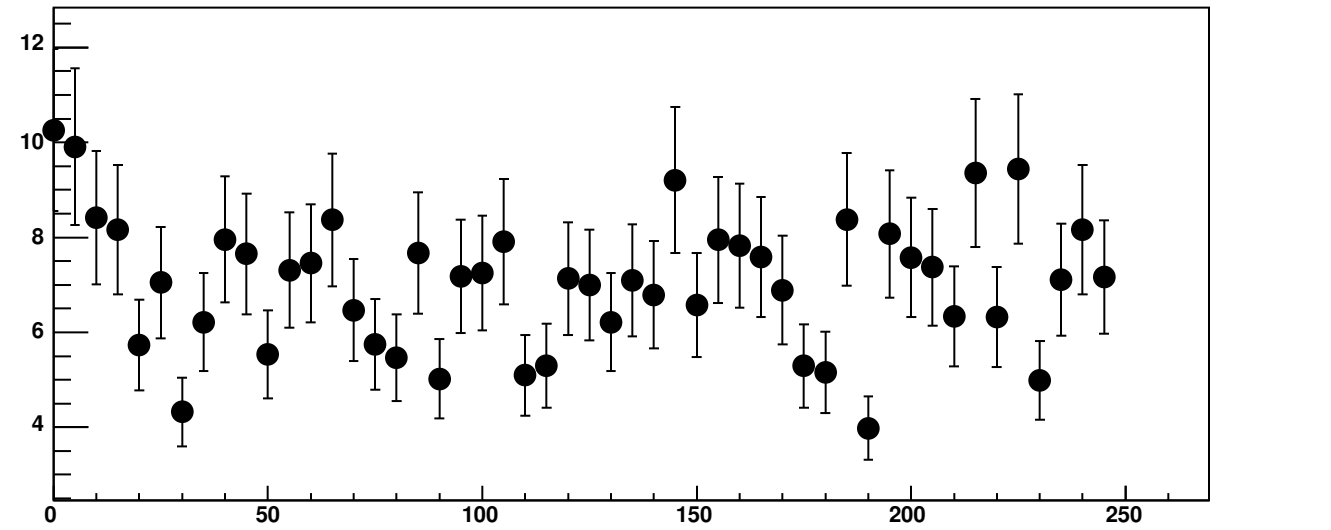
Chip 2, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold



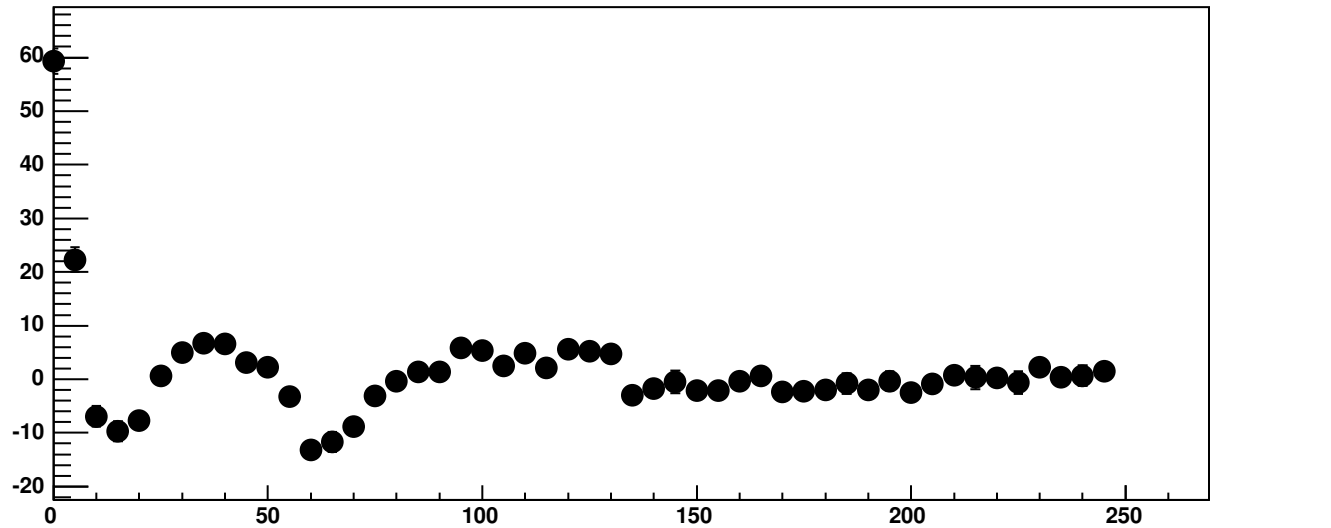
Chip 2, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold



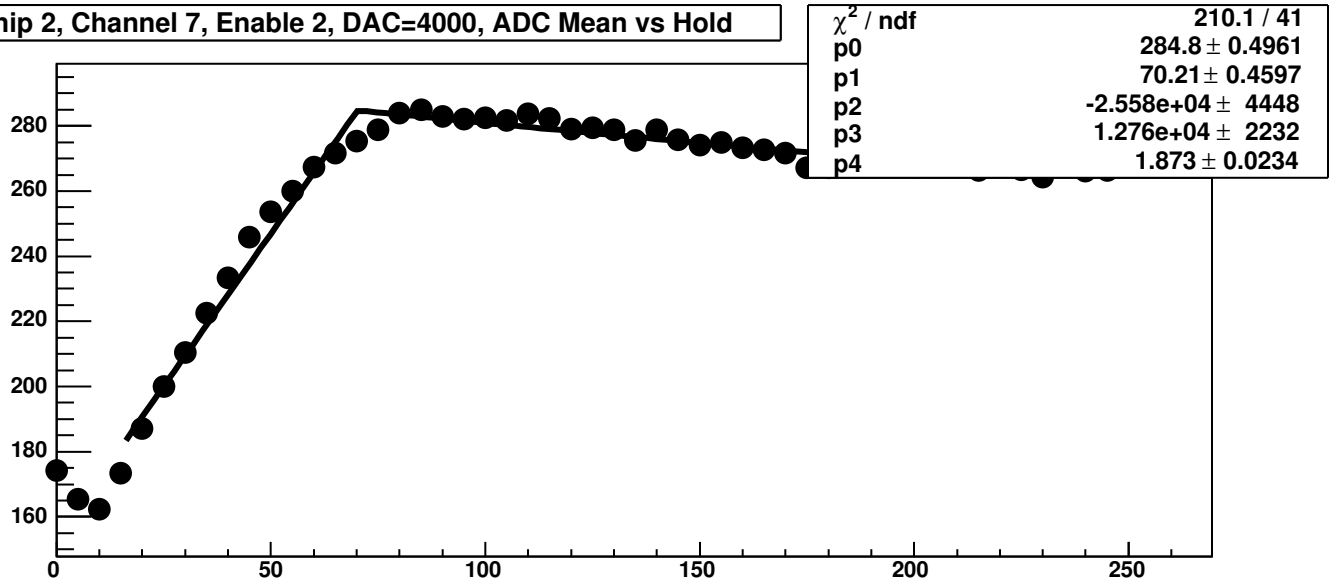
Chip 2, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



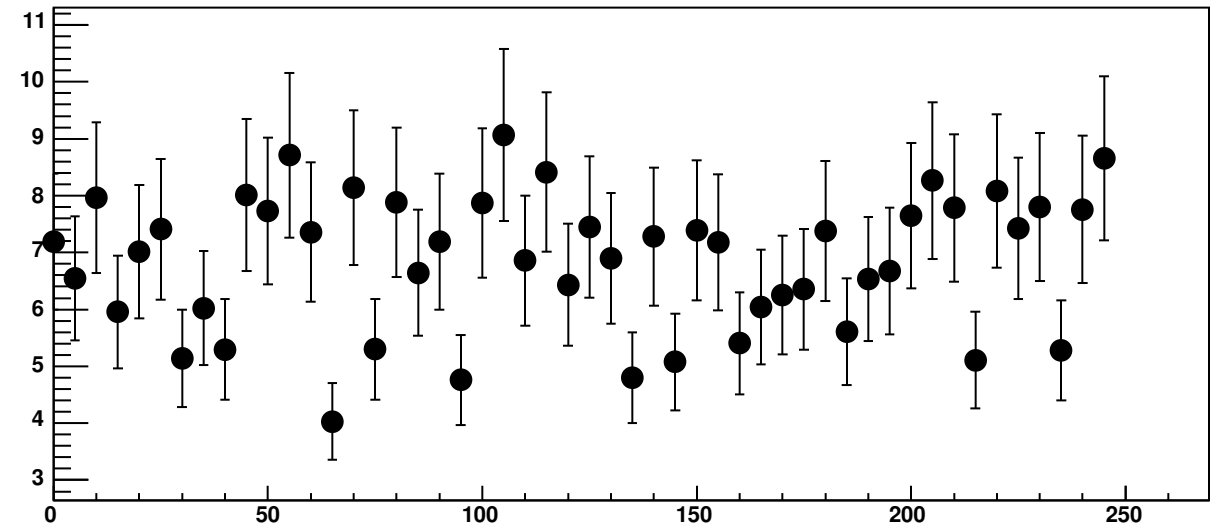
Chip 2, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold



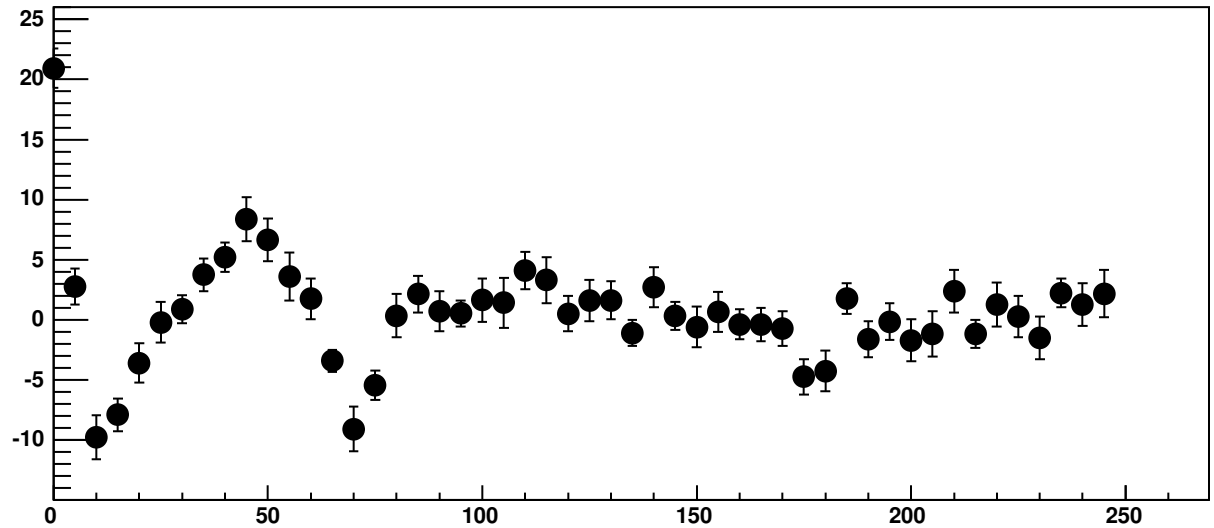
Chip 2, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold



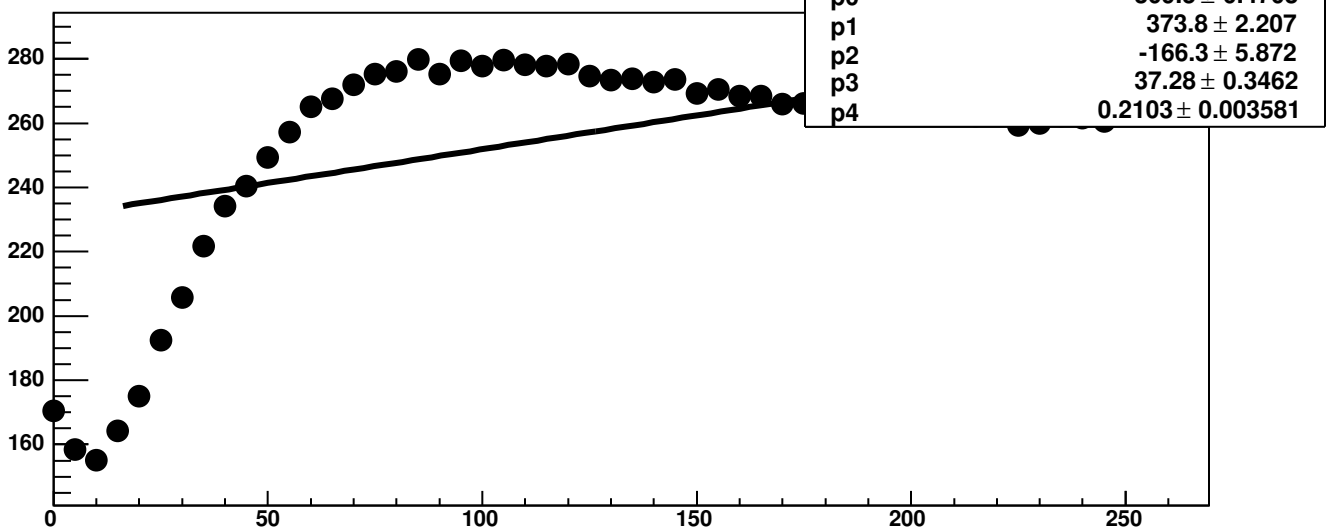
Chip 2, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold



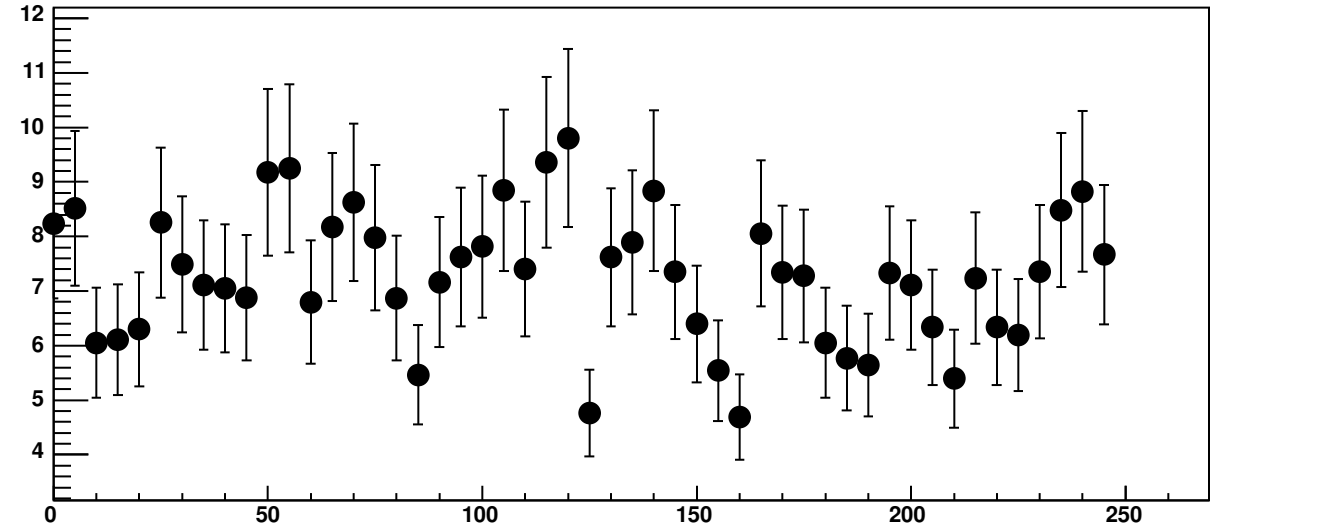
Chip 2, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold



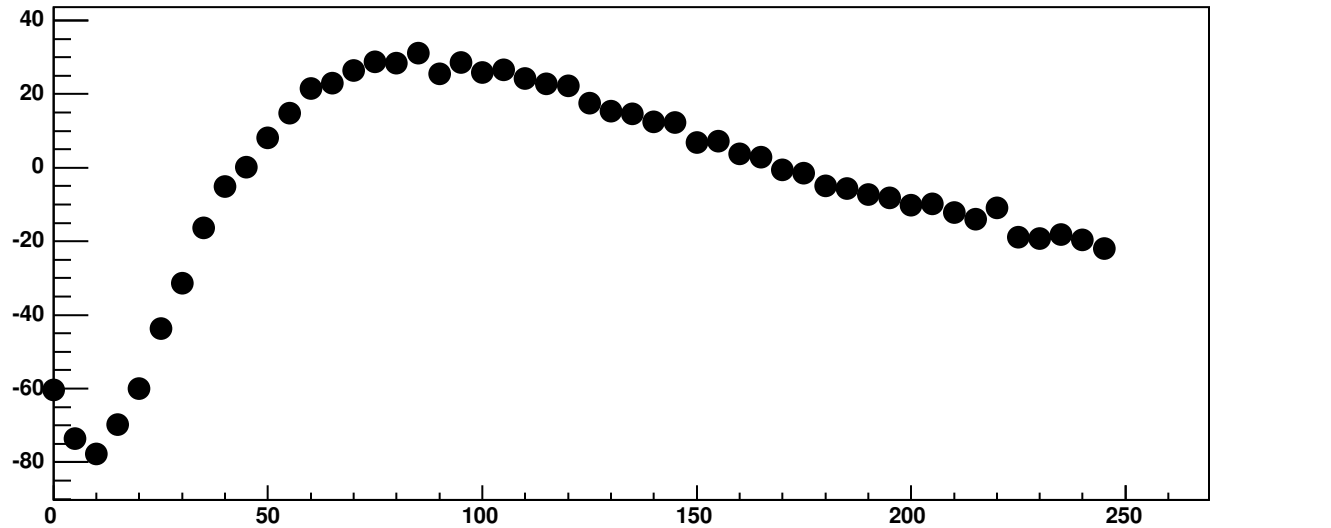
Chip 2, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

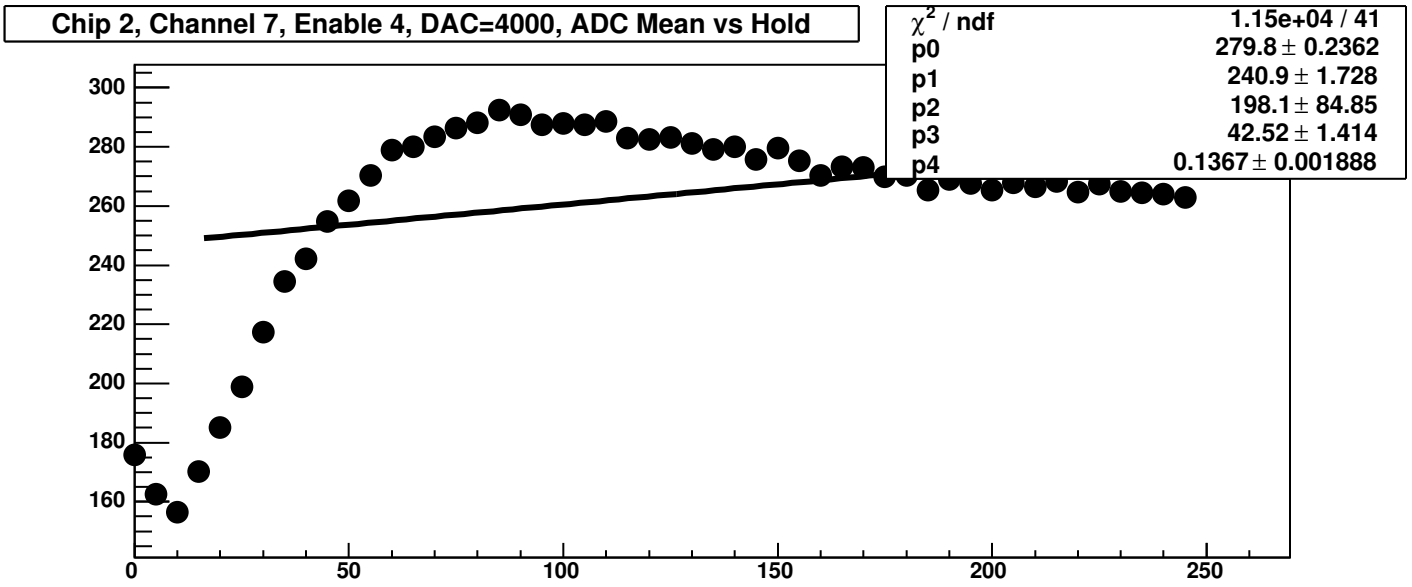


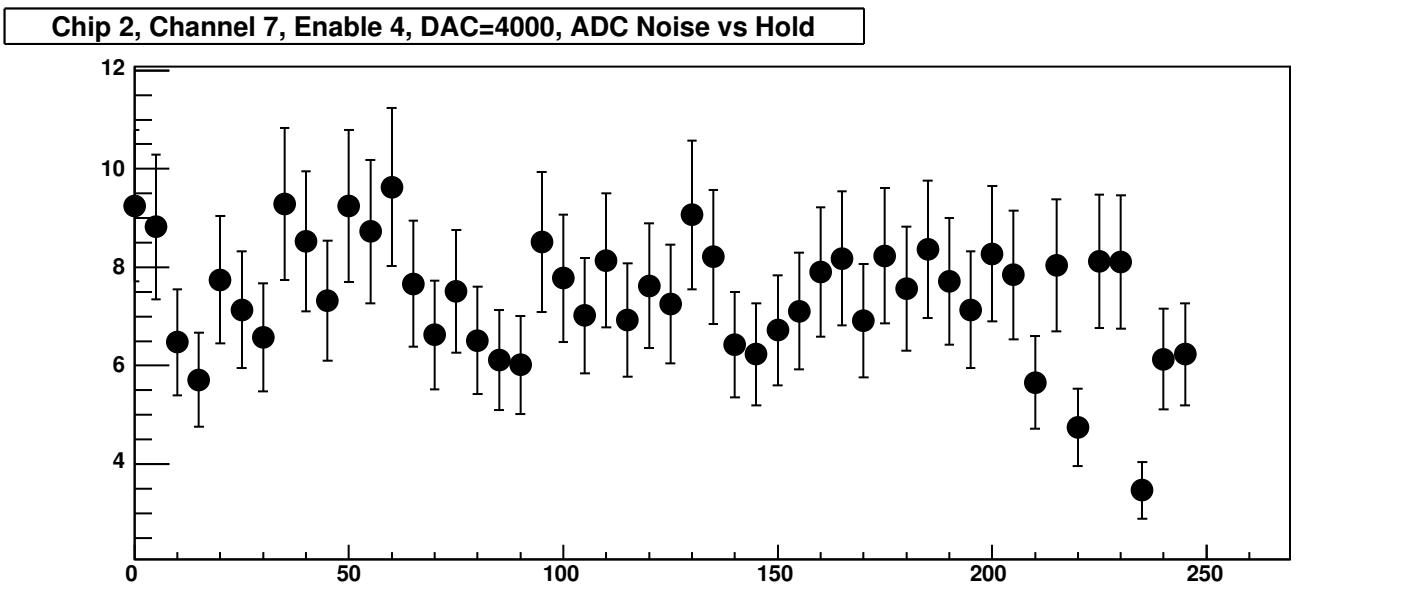
Chip 2, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold

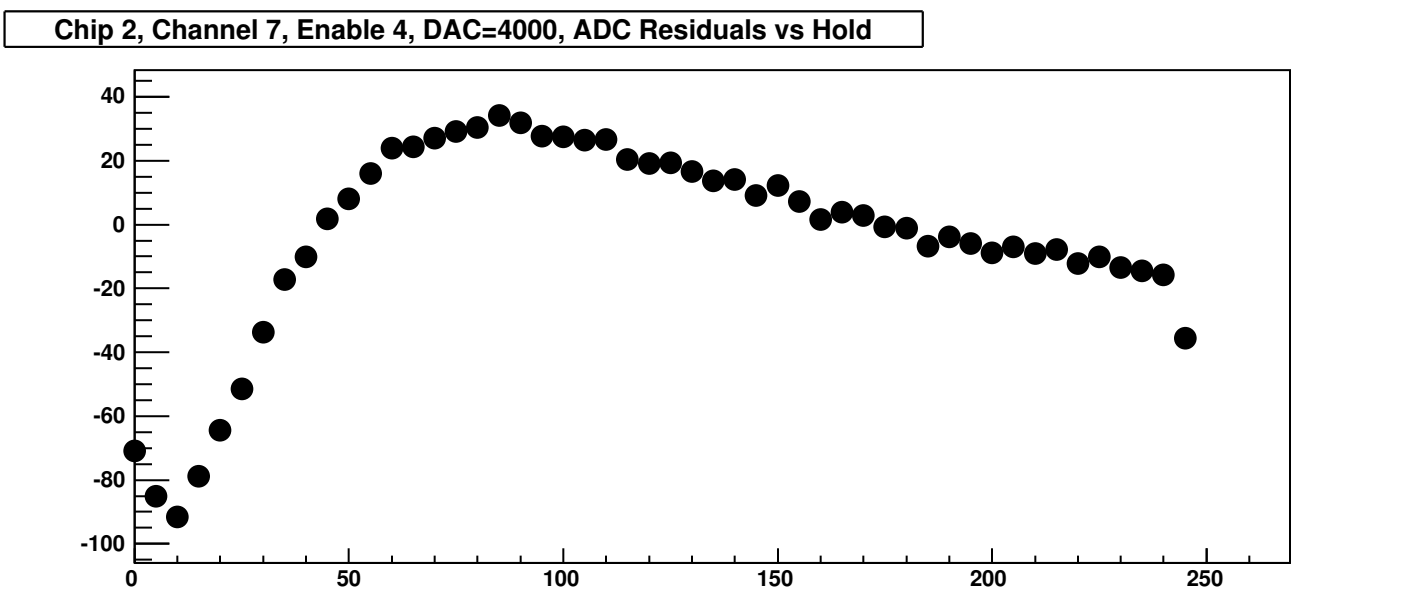


Chip 2, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

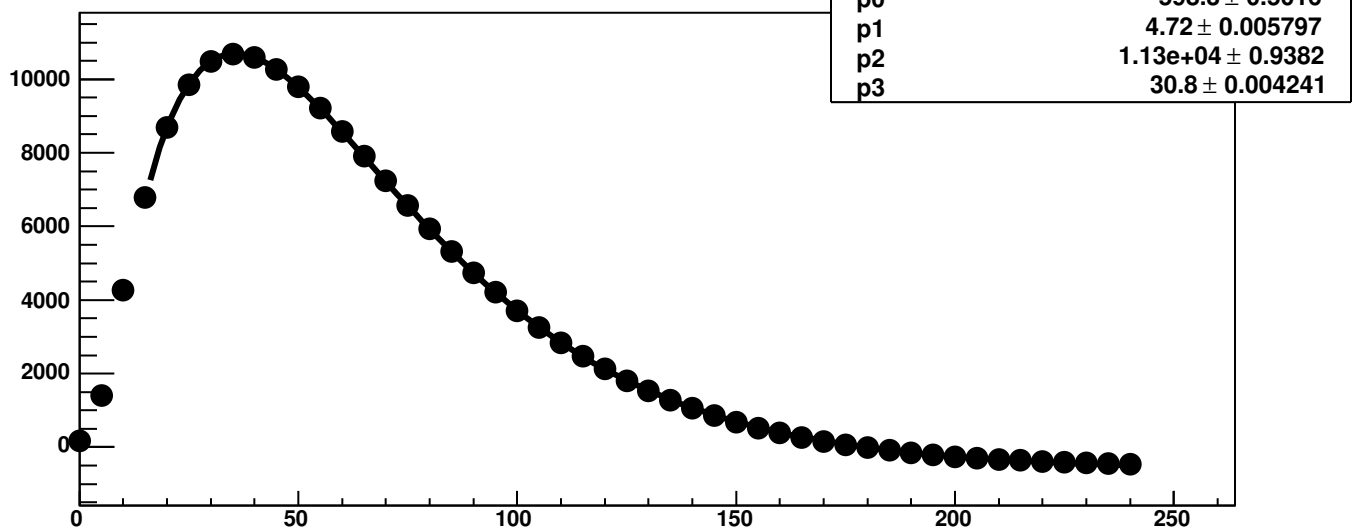



Chip 2, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.15e+04 / 41
p0 279.8 ± 0.2362
p1 240.9 ± 1.728
p2 198.1 ± 84.85
p3 42.52 ± 1.414
p4 0.1367 ± 0.001888
300
280
260
240
220
200
180
160
0
50
100
150
200
250

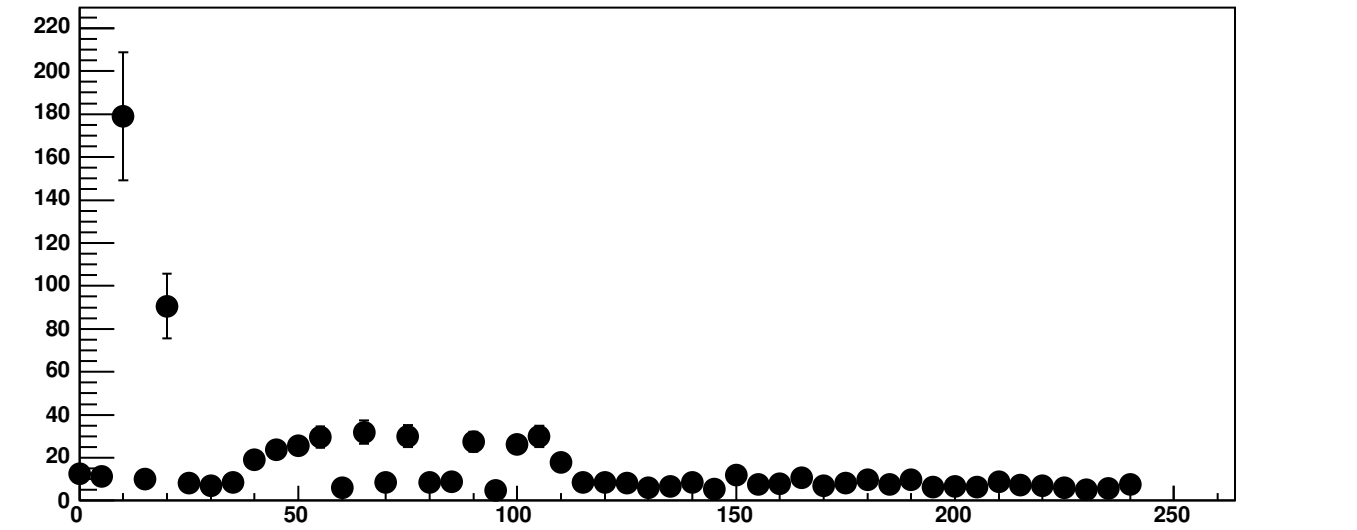

Chip 2, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 2, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
-100
0
50
100
150
200
250

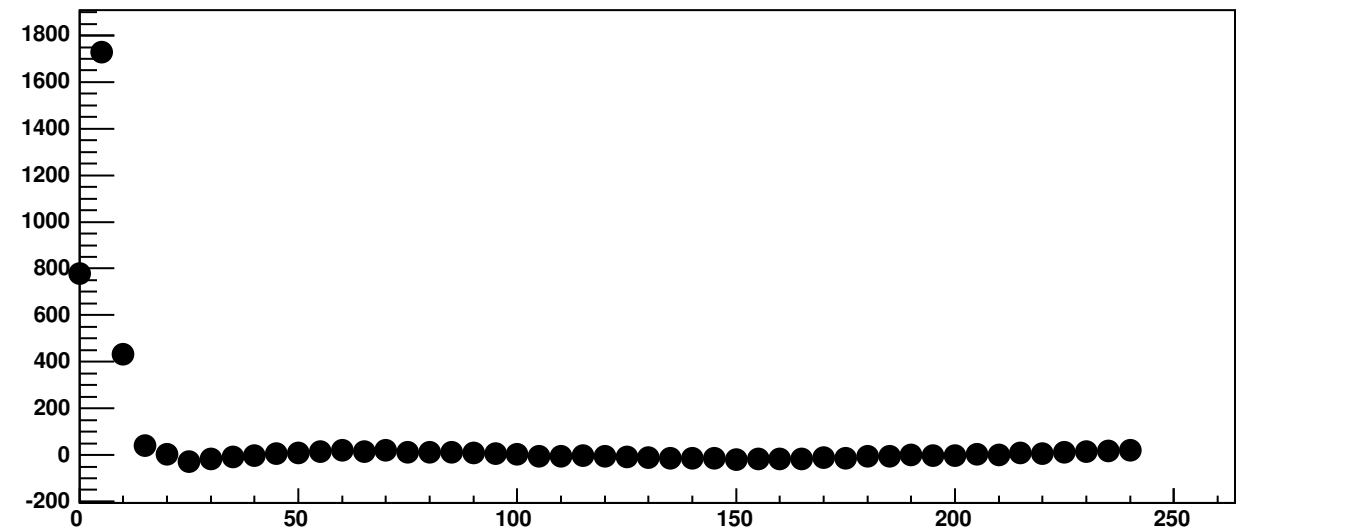
Chip 2, Channel 7, Enable 5!, DAC=4000, ADC Mean vs Hold

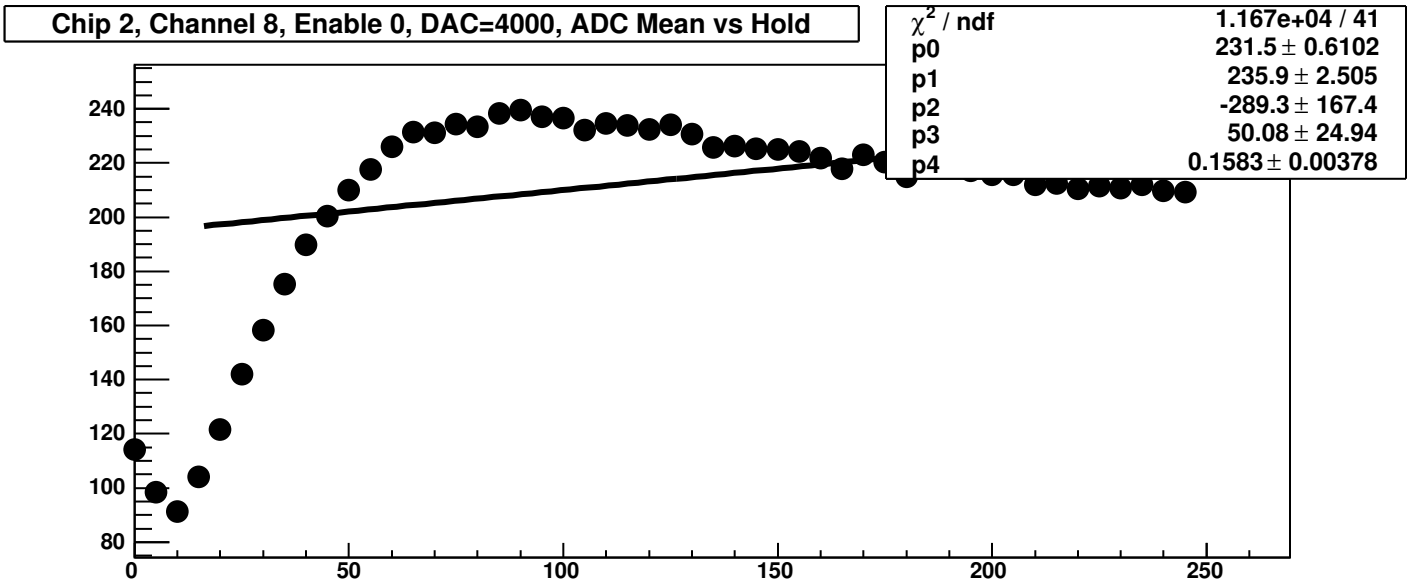


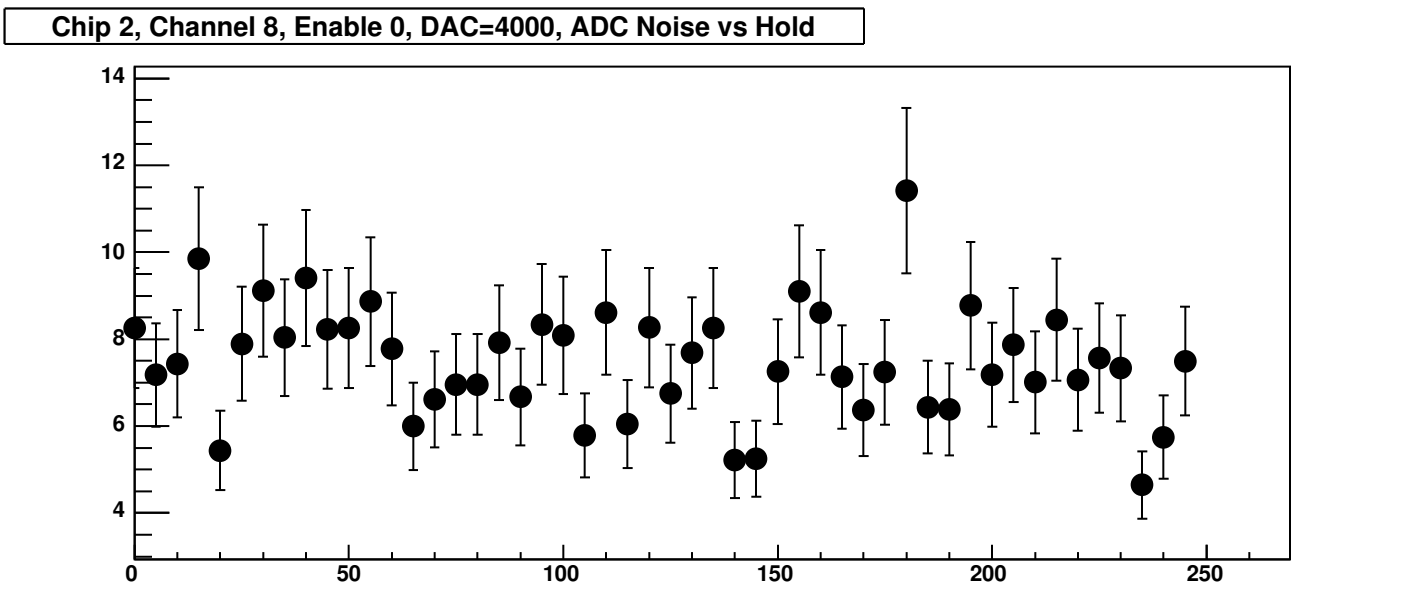
Chip 2, Channel 7, Enable 5!, DAC=4000, ADC Noise vs Hold

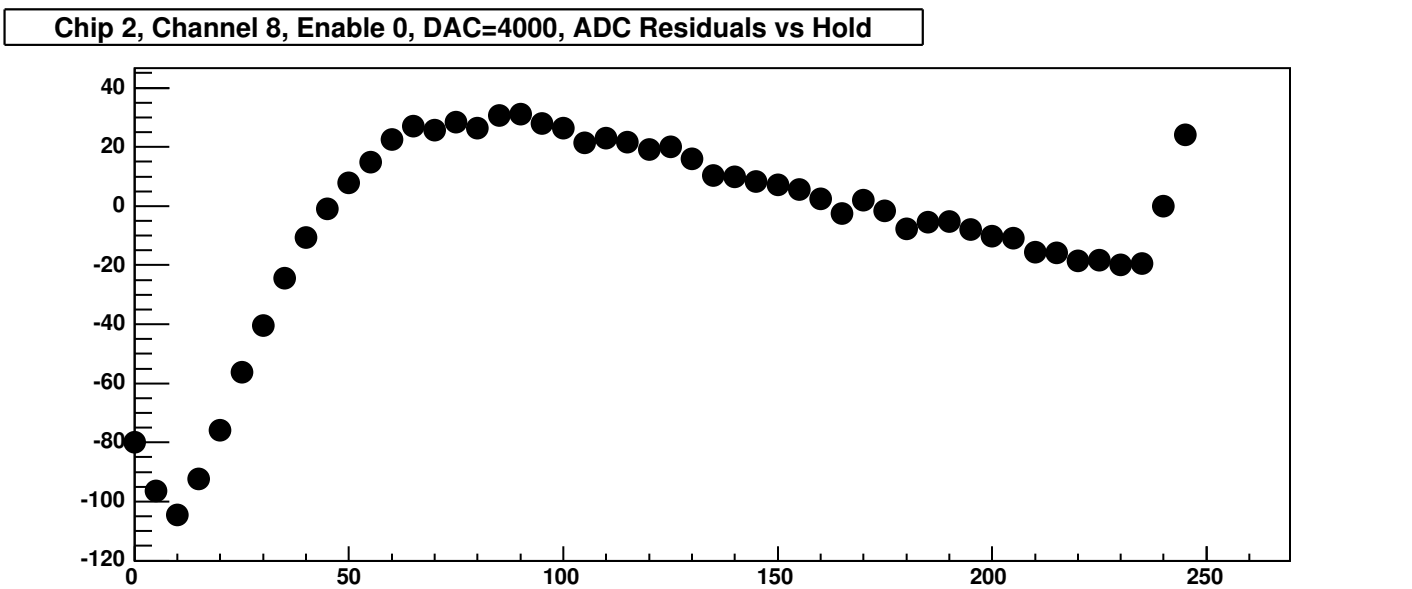


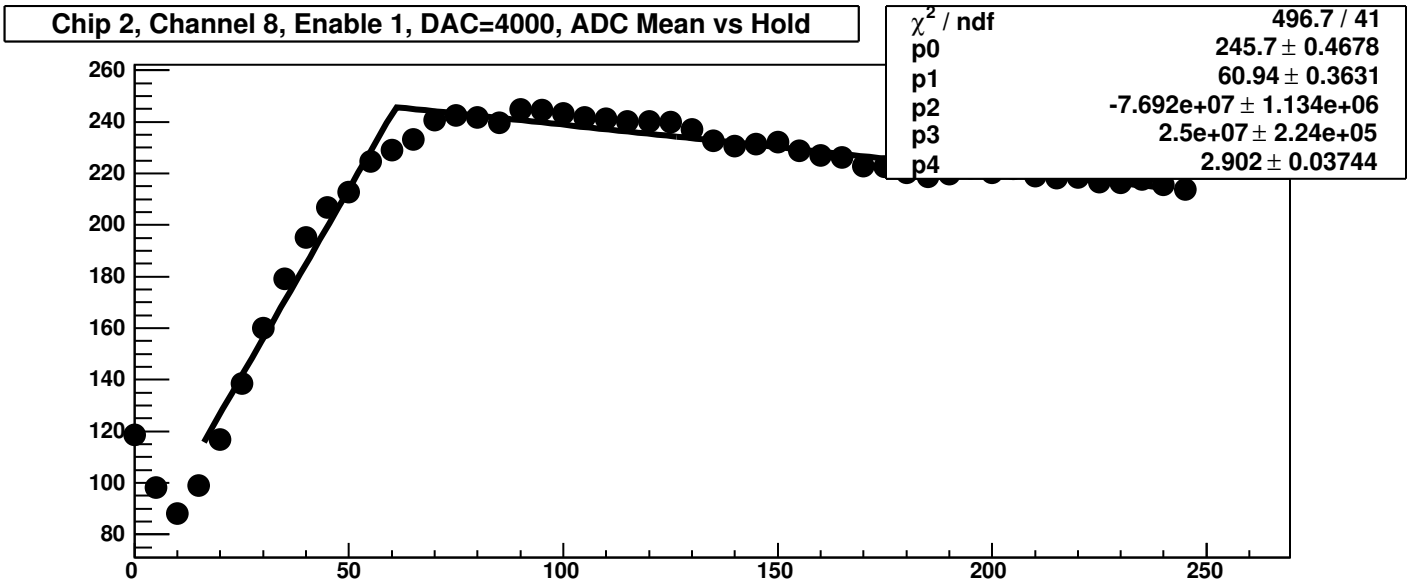
Chip 2, Channel 7, Enable 5!, DAC=4000, ADC Residuals vs Hold

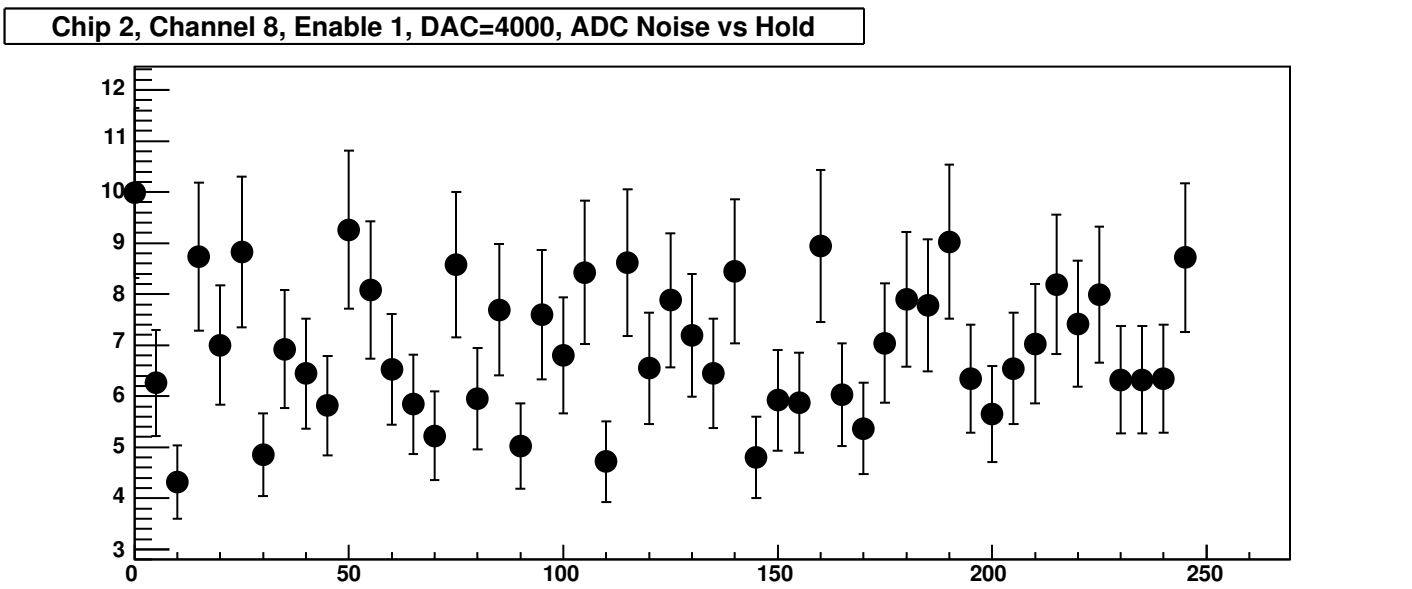


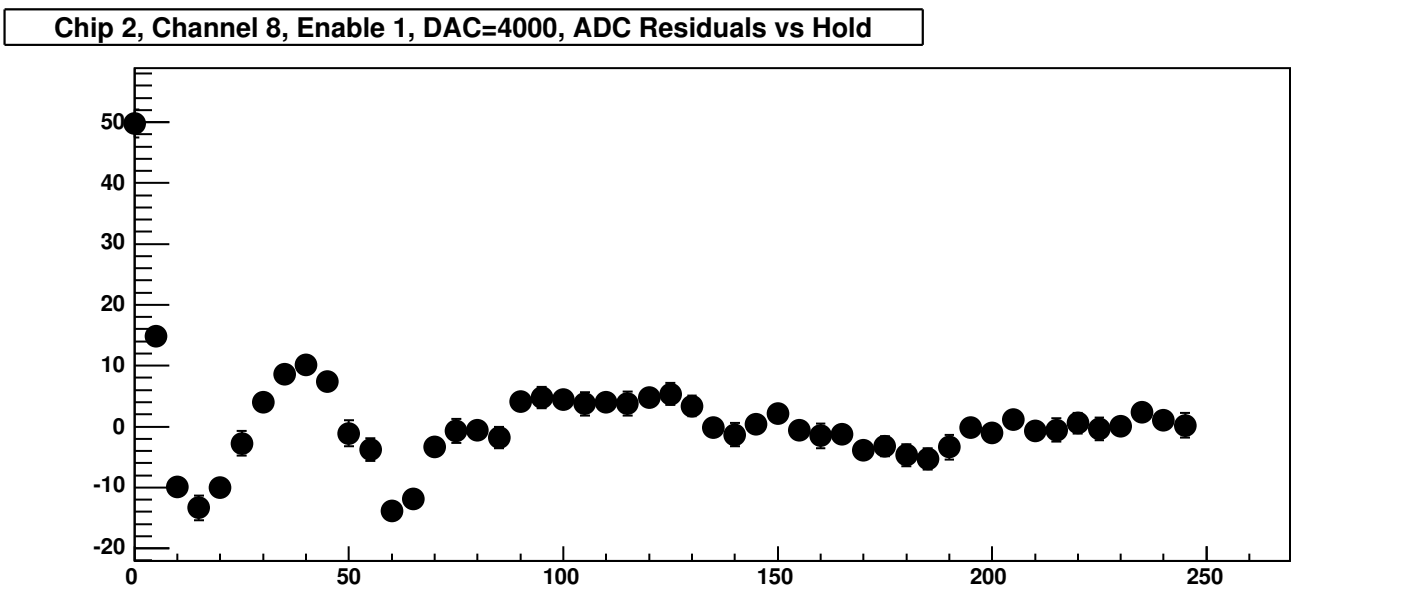

Chip 2, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.167e+04 / 41
p0 231.5 ± 0.6102
p1 235.9 ± 2.505
p2 -289.3 ± 167.4
p3 50.08 ± 24.94
p4 0.1583 ± 0.00378
240
220
200
180
160
140
120
100
80
0
50
100
150
200
250


Chip 2, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

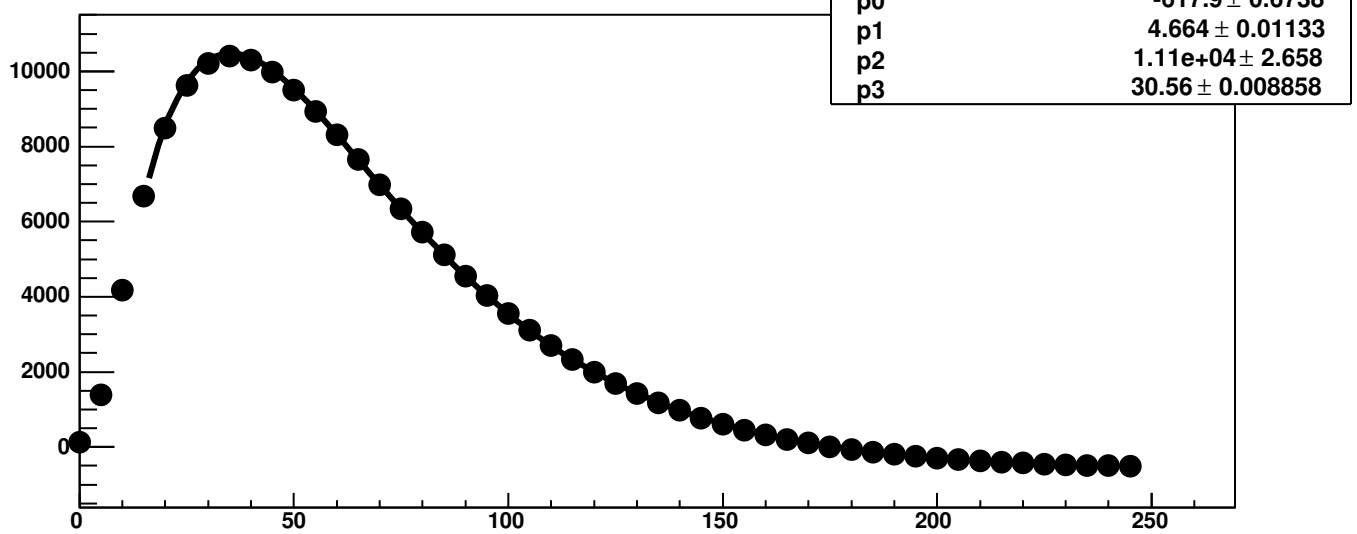

Chip 2, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
-100
-120
0
50
100
150
200
250


Chip 2, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 496.7 / 41
p0 245.7 ± 0.4678
p1 60.94 ± 0.3631
p2 -7.692e+07 ± 1.134e+06
p3 2.5e+07 ± 2.24e+05
p4 2.902 ± 0.03744
260
240
220
200
180
160
140
120
100
80
0
50
100
150
200
250

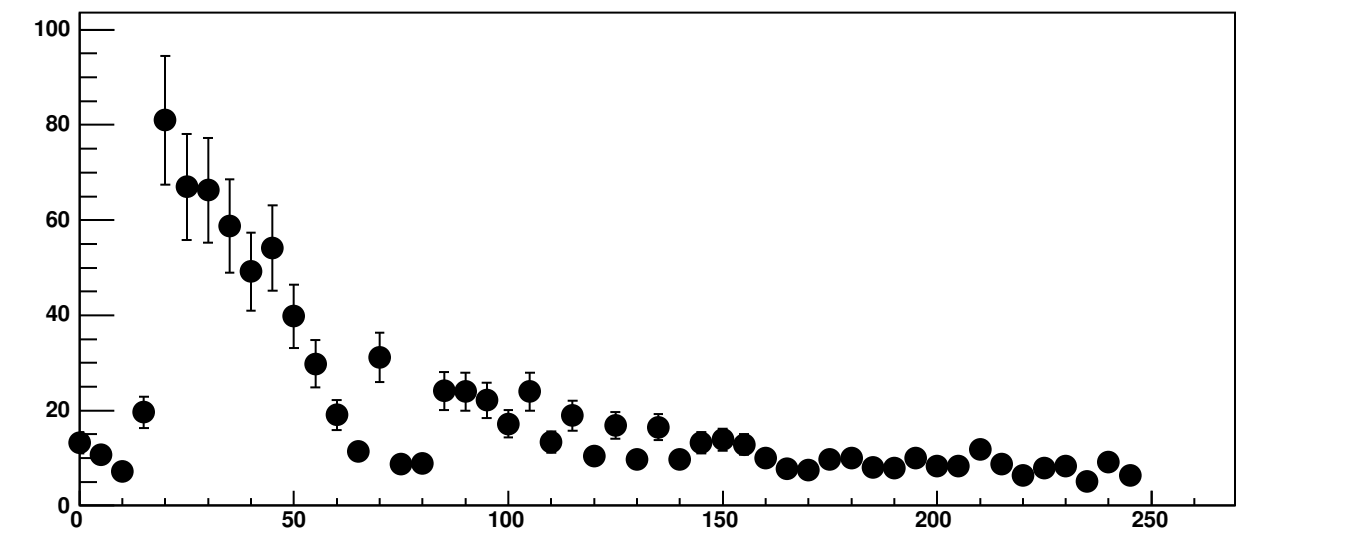

Chip 2, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 2, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
-20
0
50
100
150
200
250

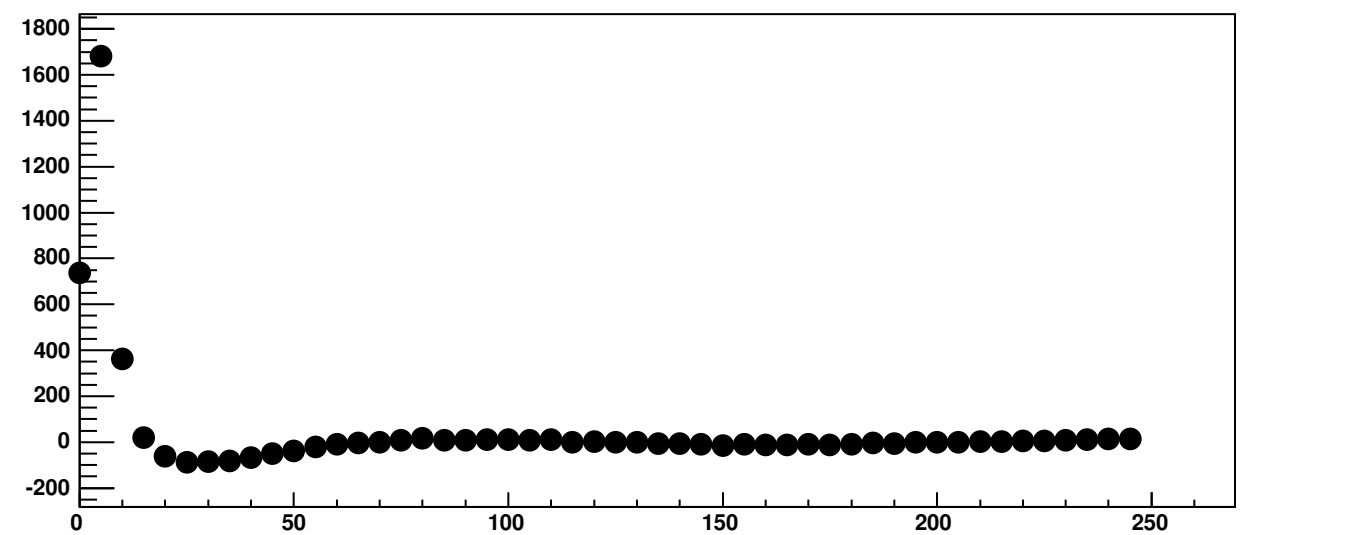
Chip 2, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold

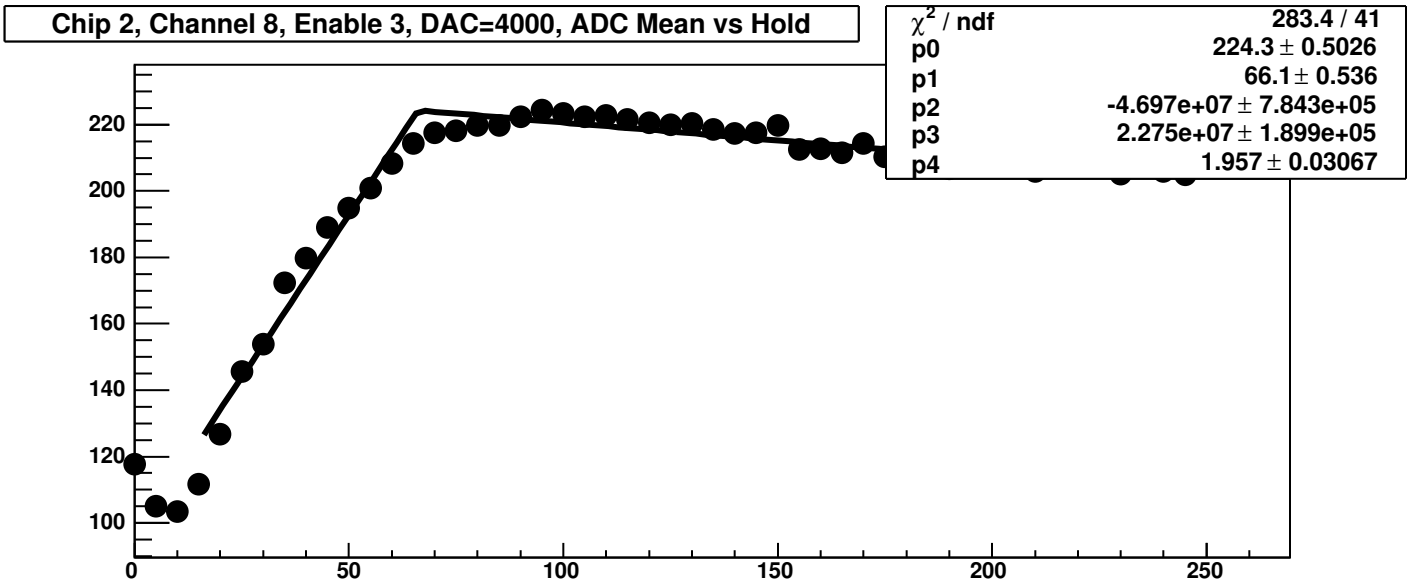


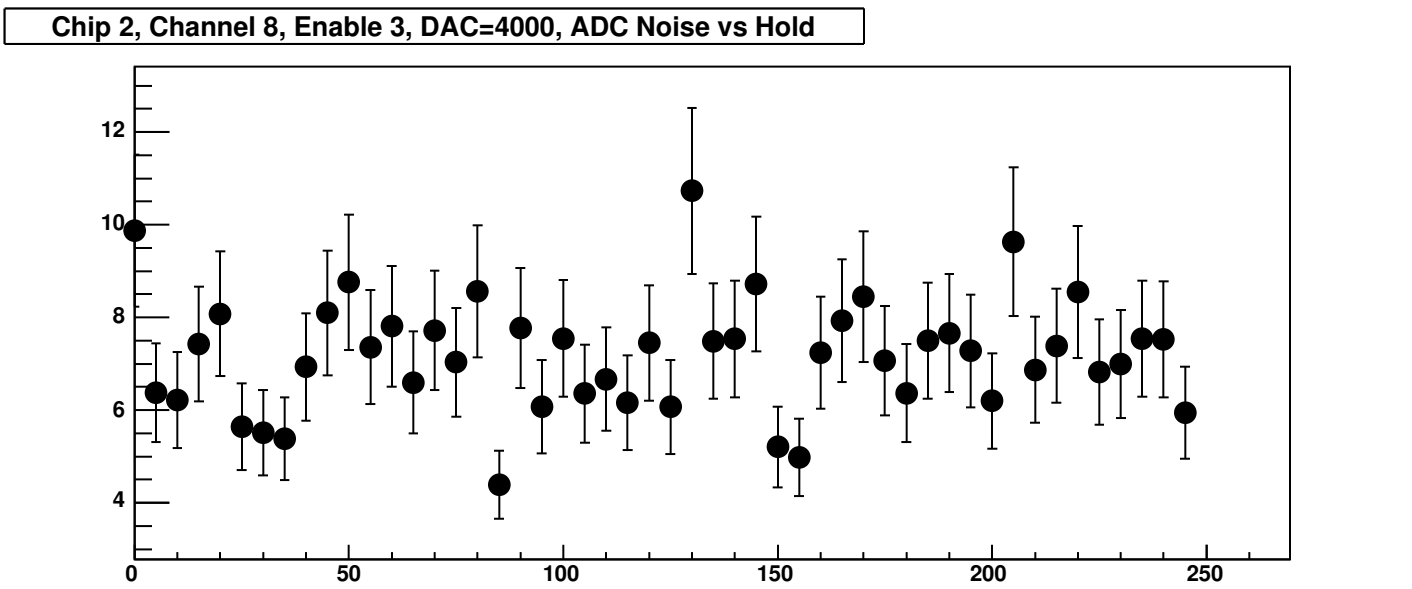
Chip 2, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold

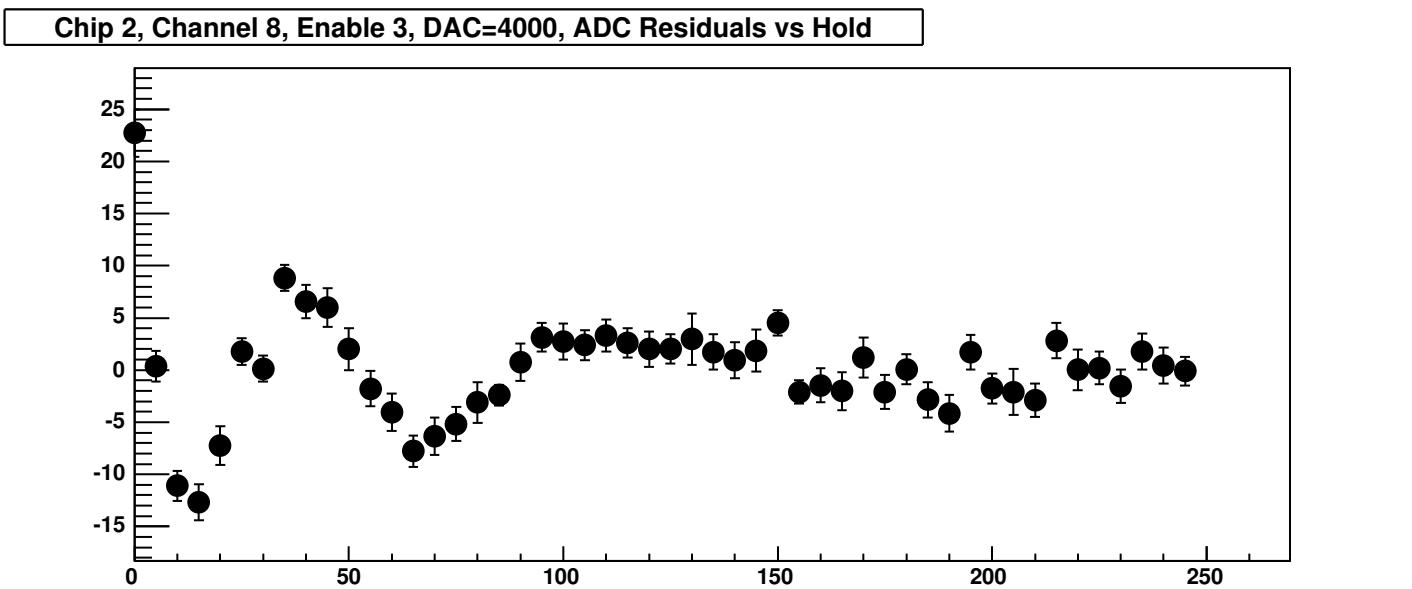


Chip 2, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold

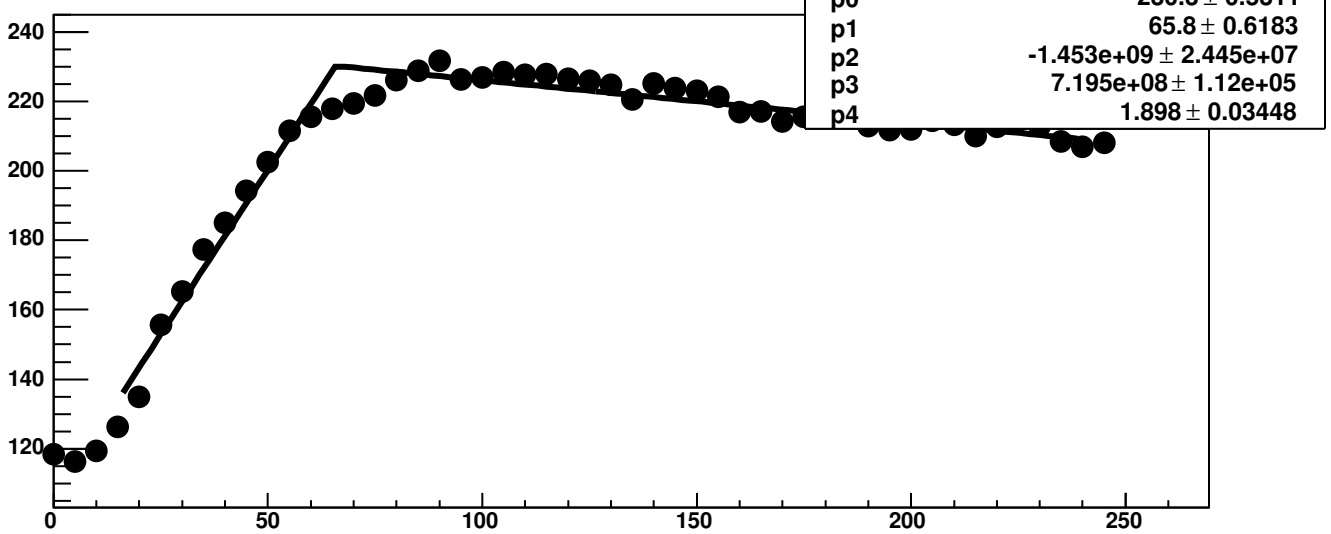



Chip 2, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 283.4 / 41
p0 224.3 ± 0.5026
p1 66.1 ± 0.536
p2 -4.697e+07 ± 7.843e+05
p3 2.275e+07 ± 1.899e+05
p4 1.957 ± 0.03067
220
200
180
160
140
120
100
0
50
100
150
200
250

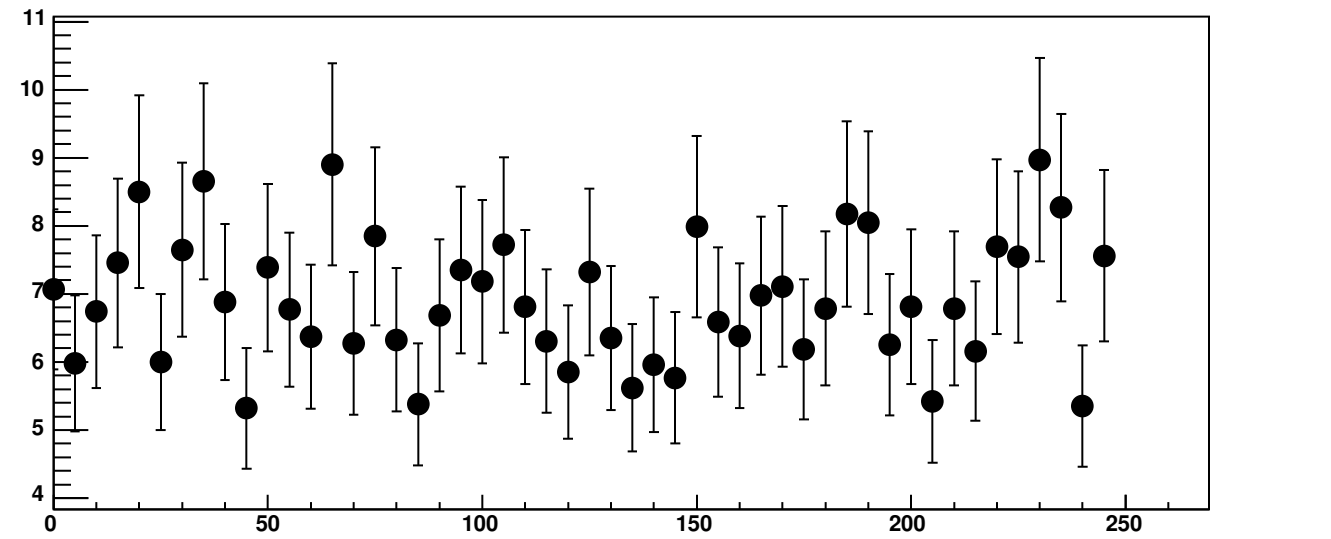

Chip 2, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 2, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

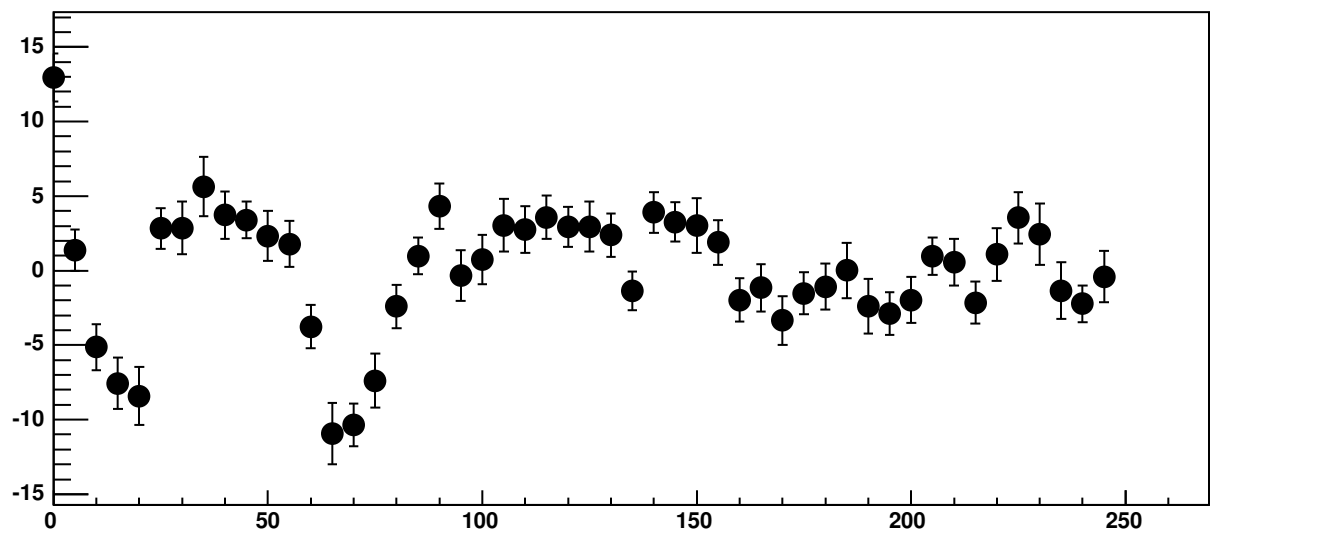
Chip 2, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold

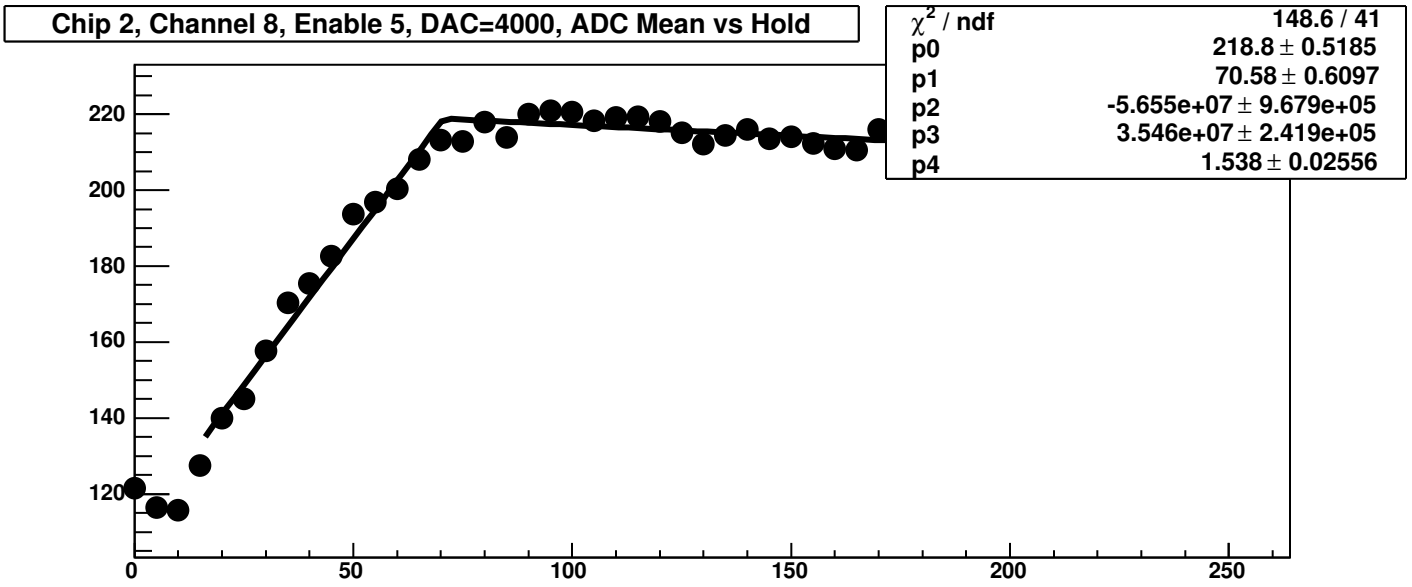


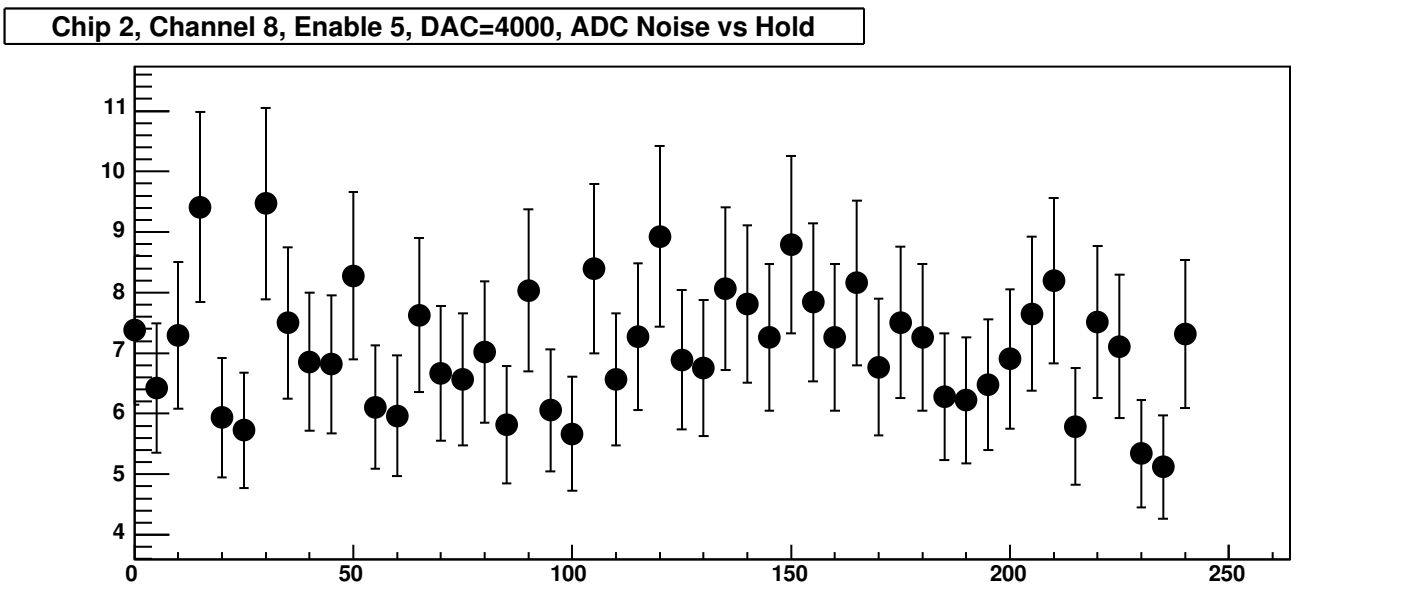
Chip 2, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold

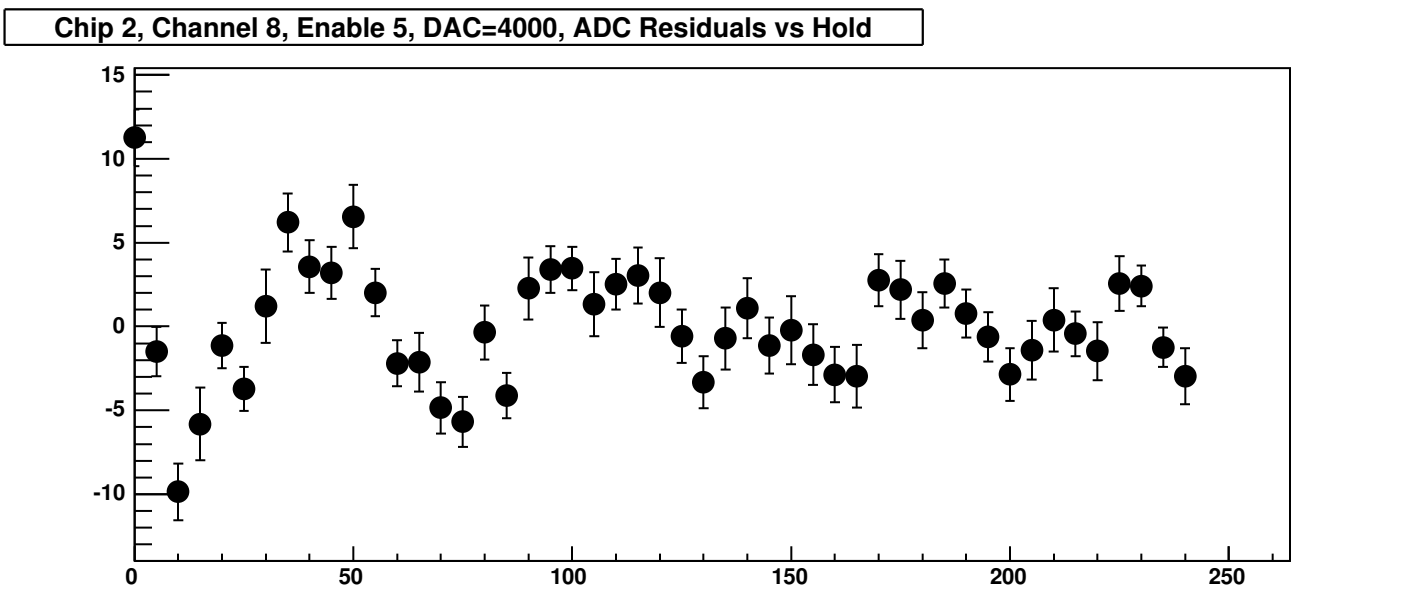


Chip 2, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

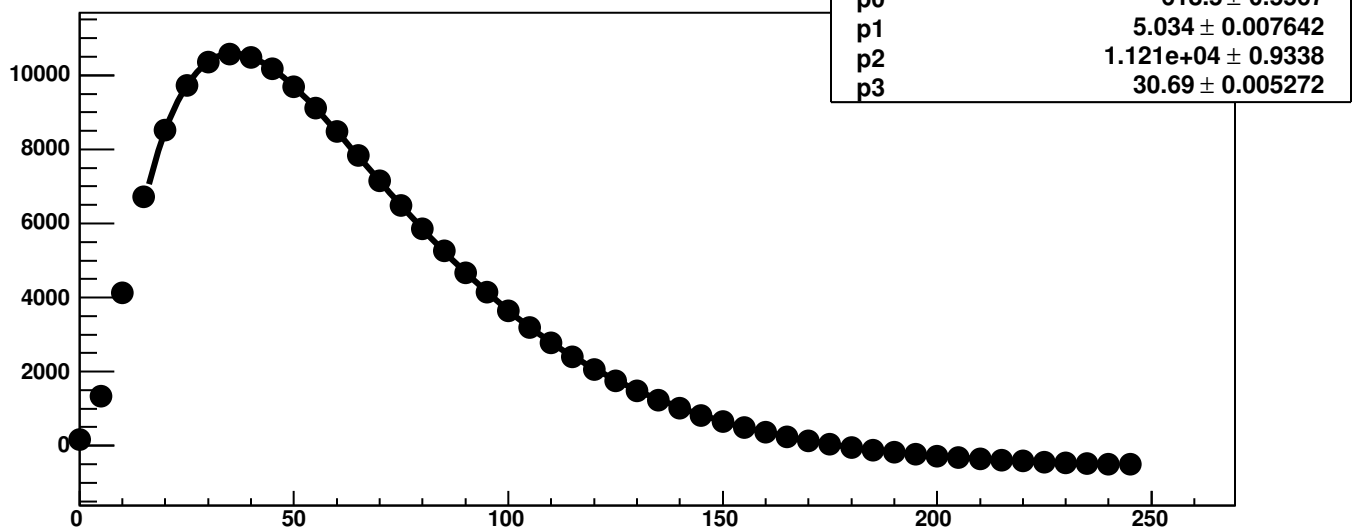



Chip 2, Channel 8, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 148.6 / 41
p0 218.8 ± 0.5185
p1 70.58 ± 0.6097
p2 -5.655e+07 ± 9.679e+05
p3 3.546e+07 ± 2.419e+05
p4 1.538 ± 0.02556
220
200
180
160
140
120
0
50
100
150
200
250

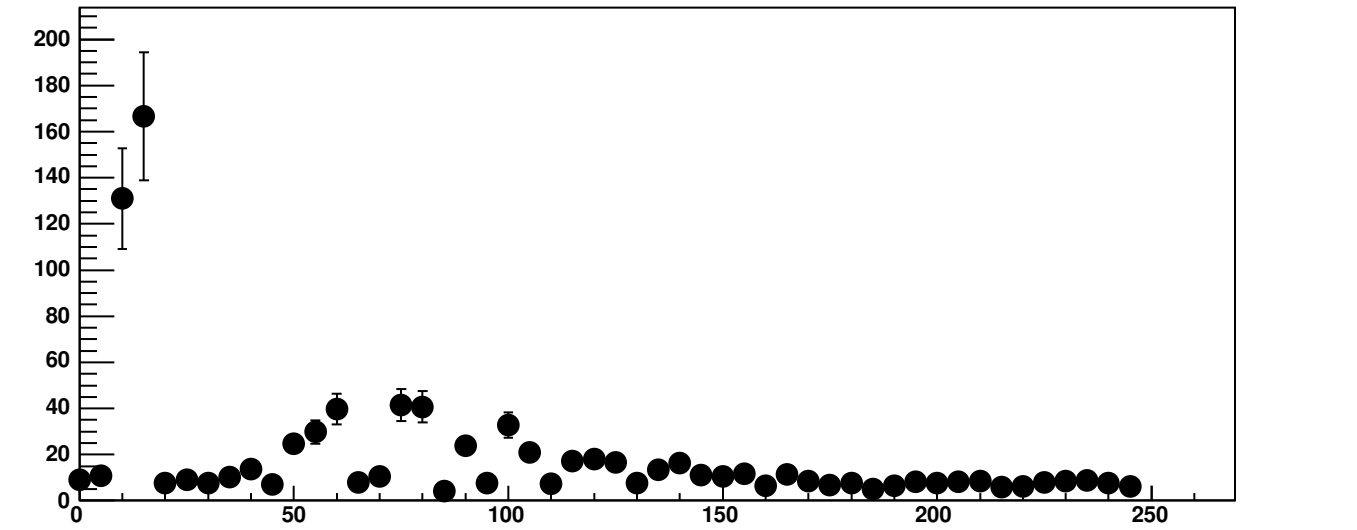

Chip 2, Channel 8, Enable 5, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 2, Channel 8, Enable 5, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
0
50
100
150
200
250

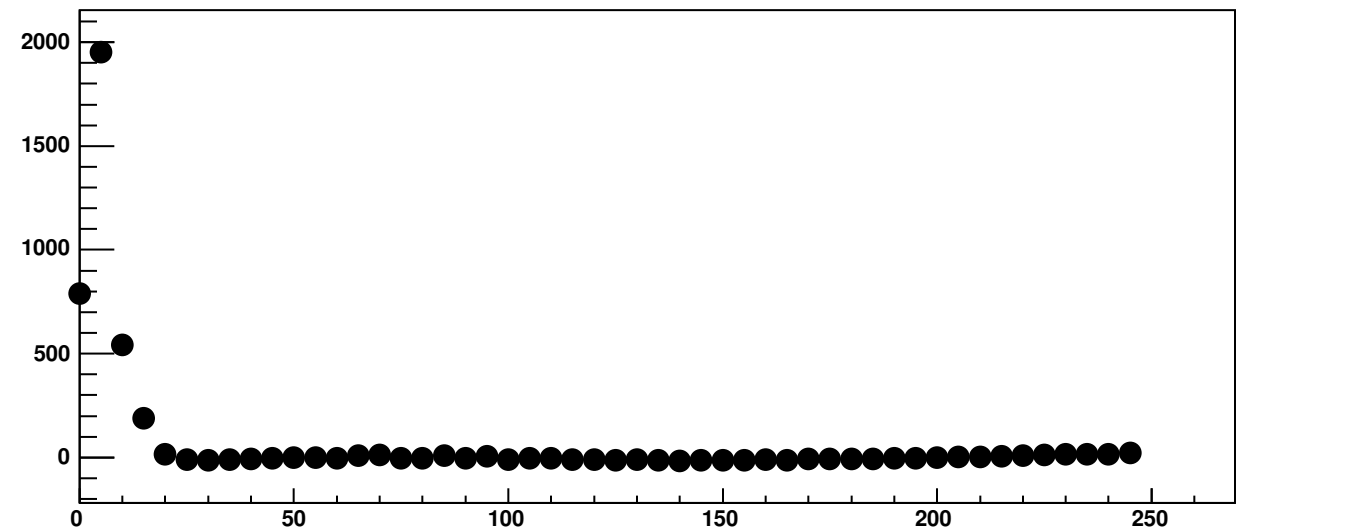
Chip 2, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold

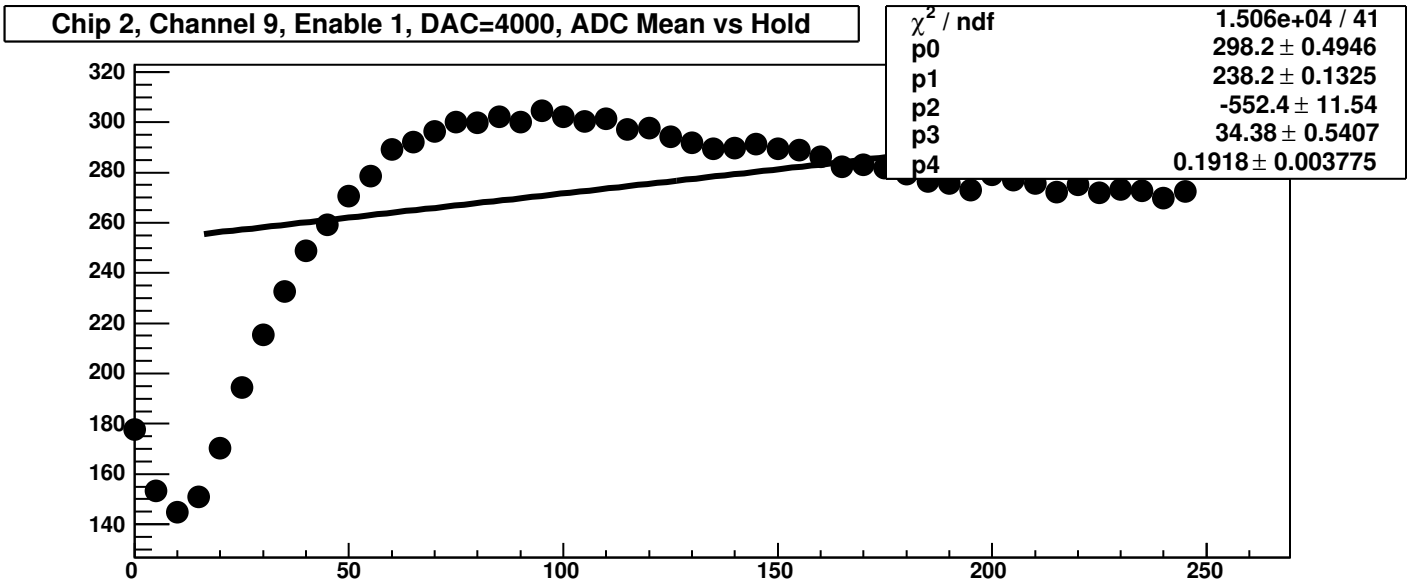


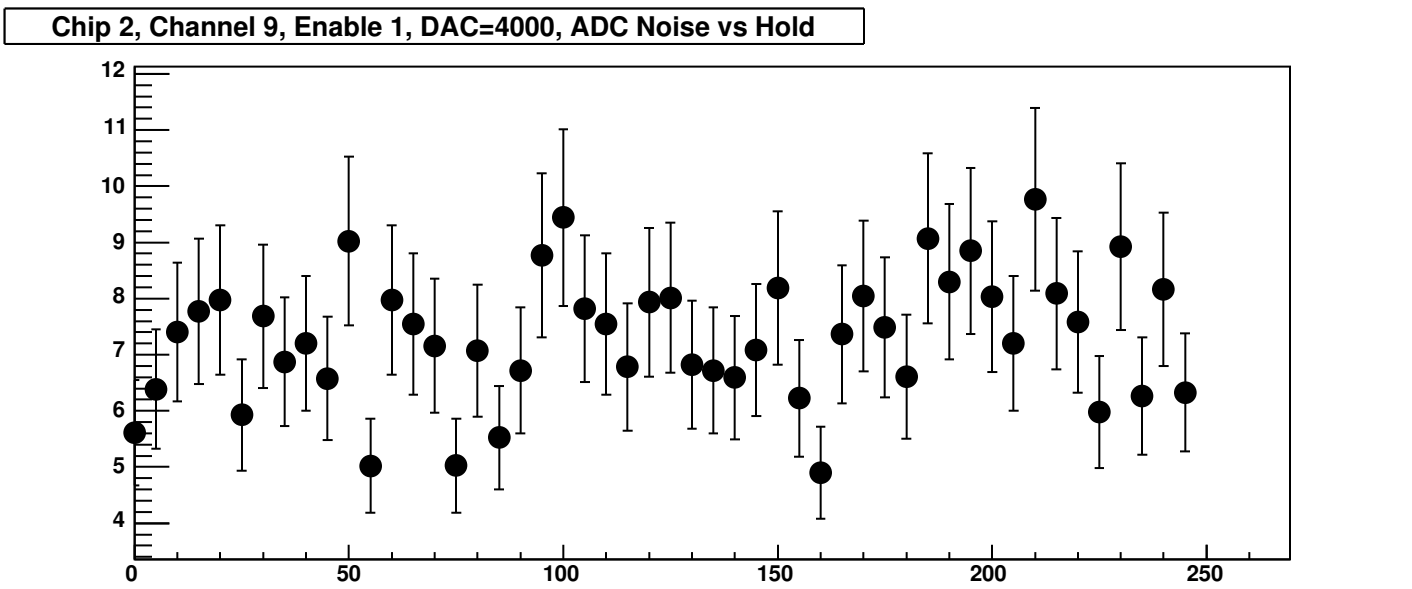
Chip 2, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold

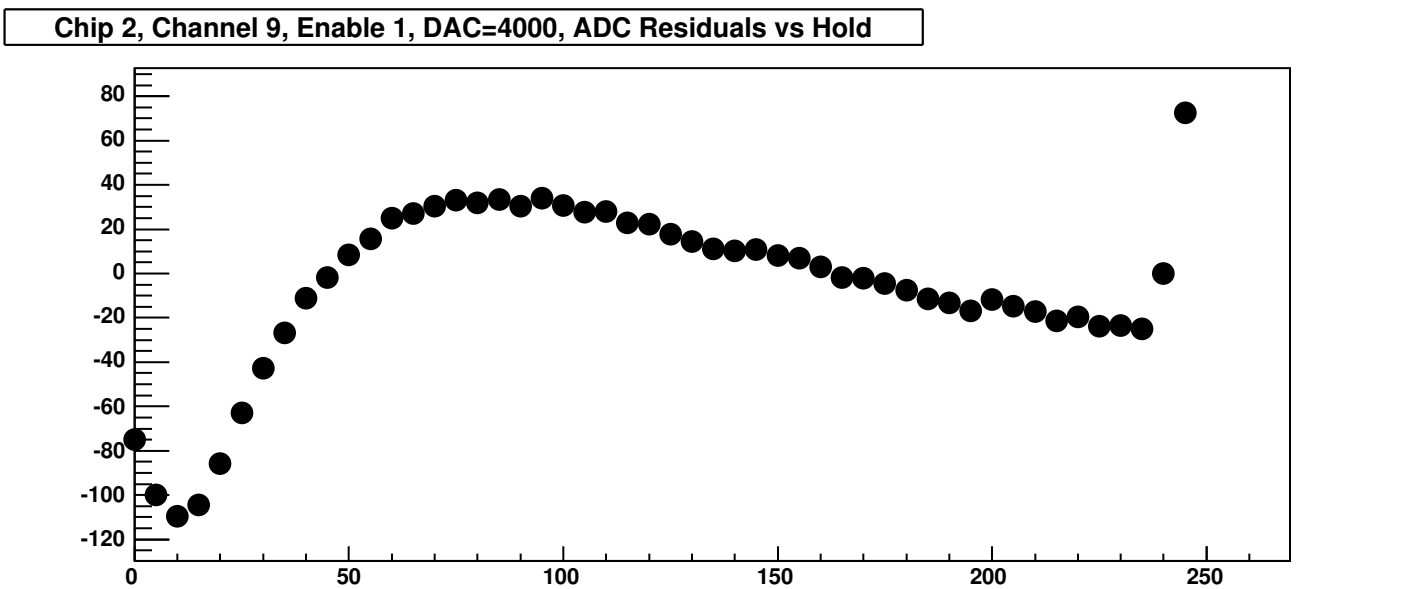


Chip 2, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold

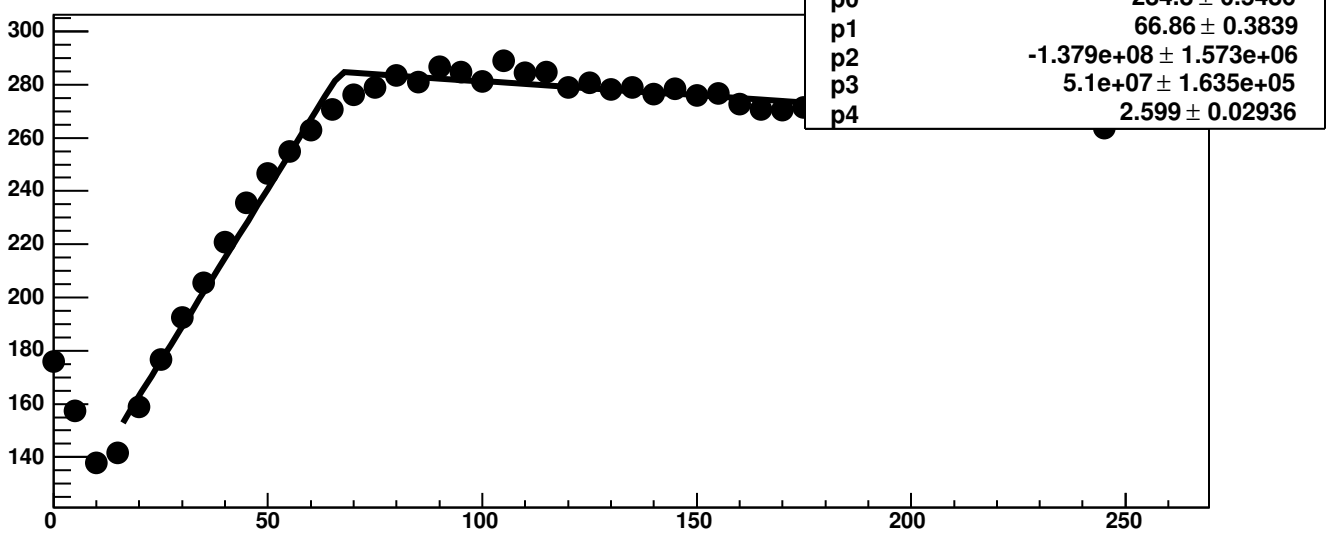



Chip 2, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.506e+04 / 41
p0 298.2 ± 0.4946
p1 238.2 ± 0.1325
p2 -552.4 ± 11.54
p3 34.38 ± 0.5407
p4 0.1918 ± 0.003775
320
300
280
260
240
220
200
180
160
140
0
50
100
150
200
250

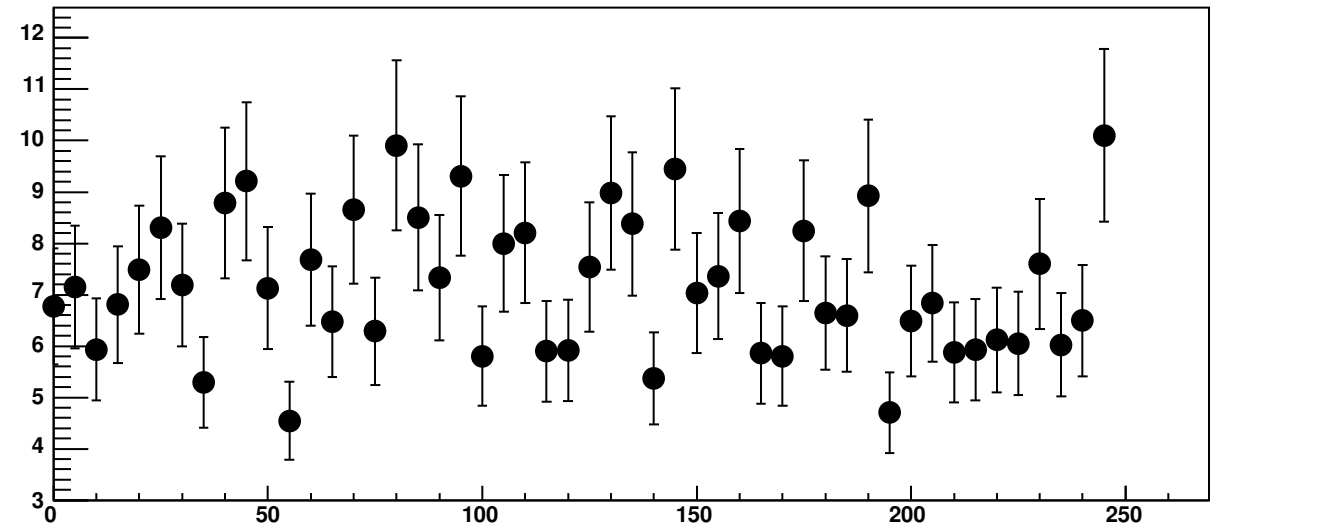

Chip 2, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 2, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold
80
60
40
20
0
-20
-40
-60
-80
-100
-120
0
50
100
150
200
250

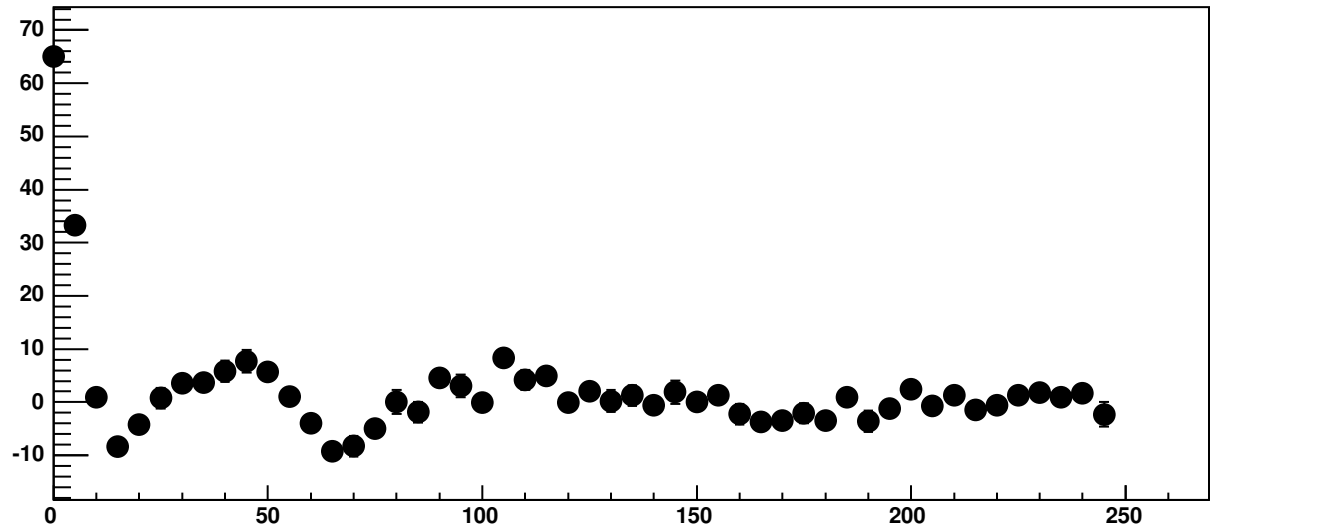
Chip 2, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold



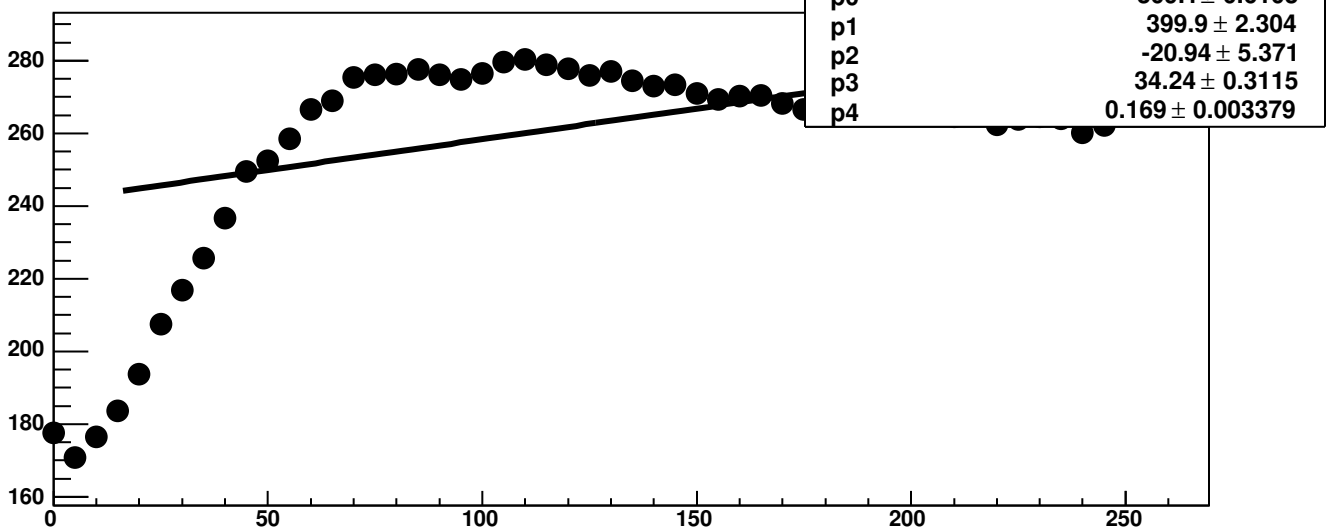
Chip 2, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



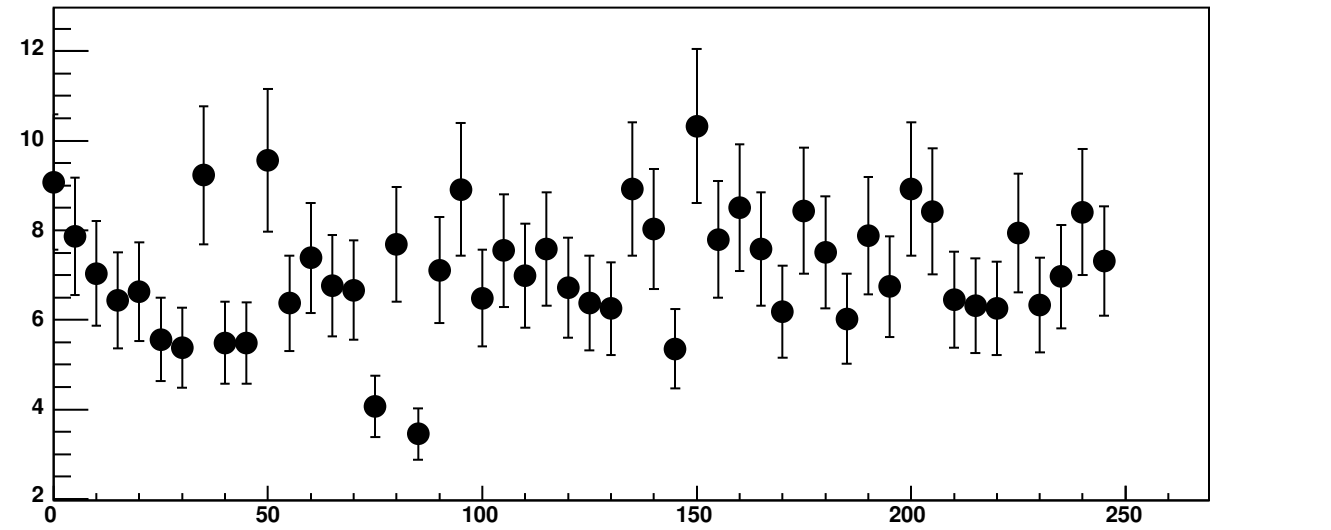
Chip 2, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold



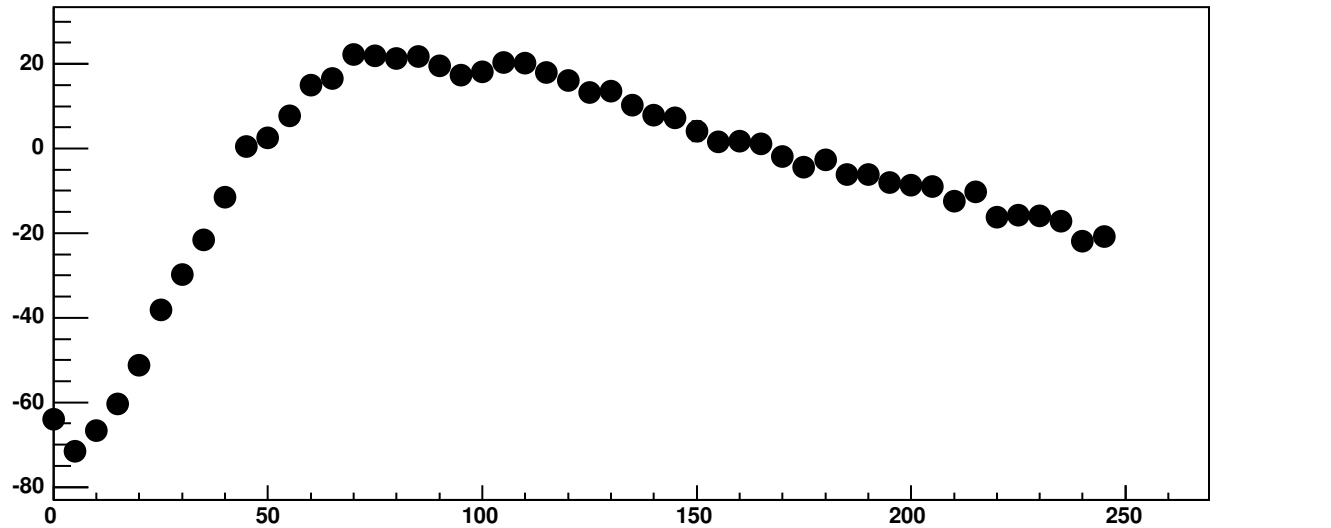
Chip 2, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold

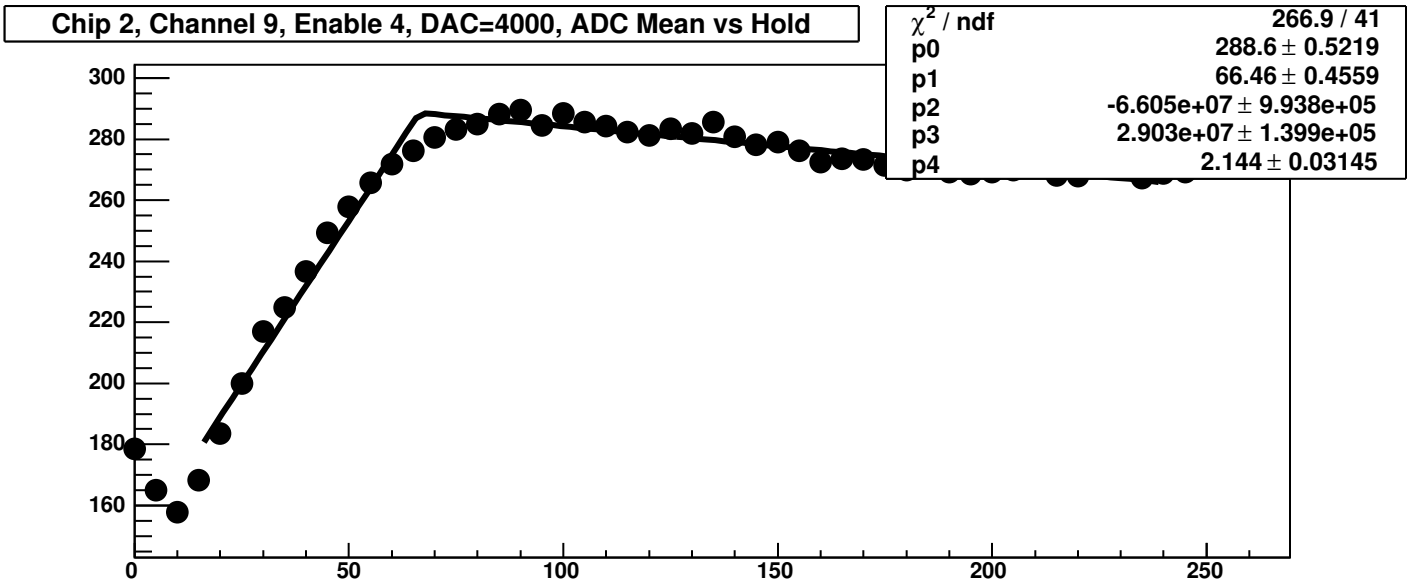


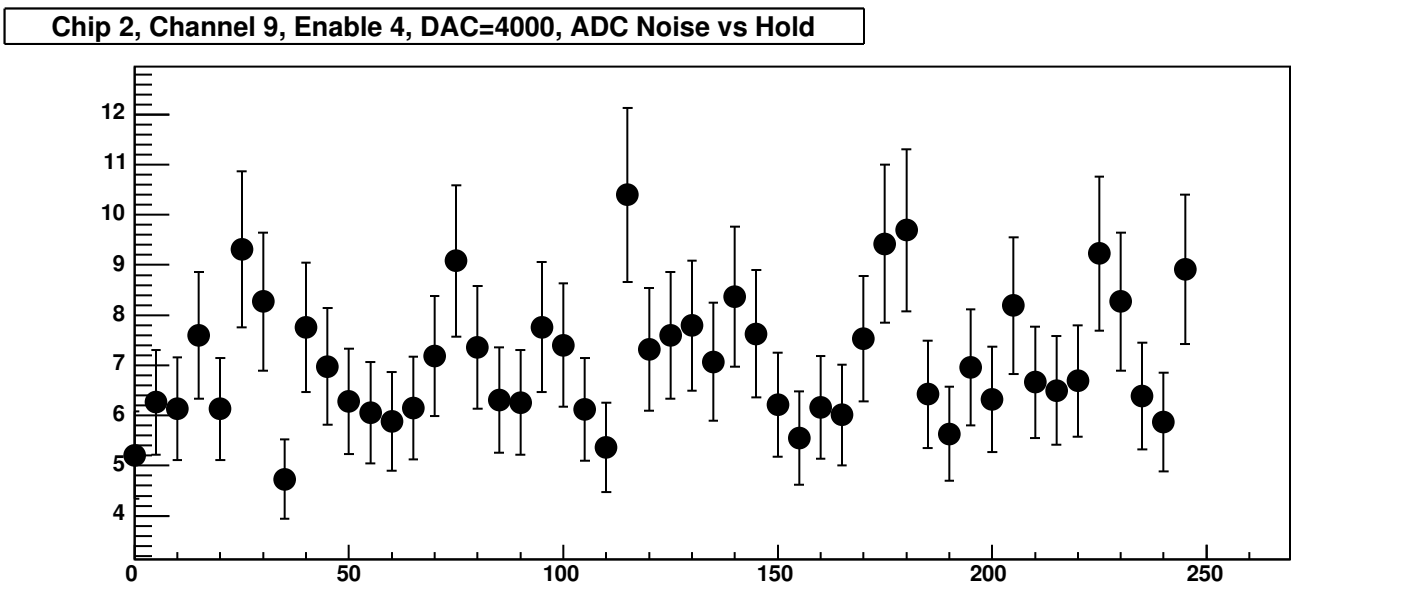
Chip 2, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold

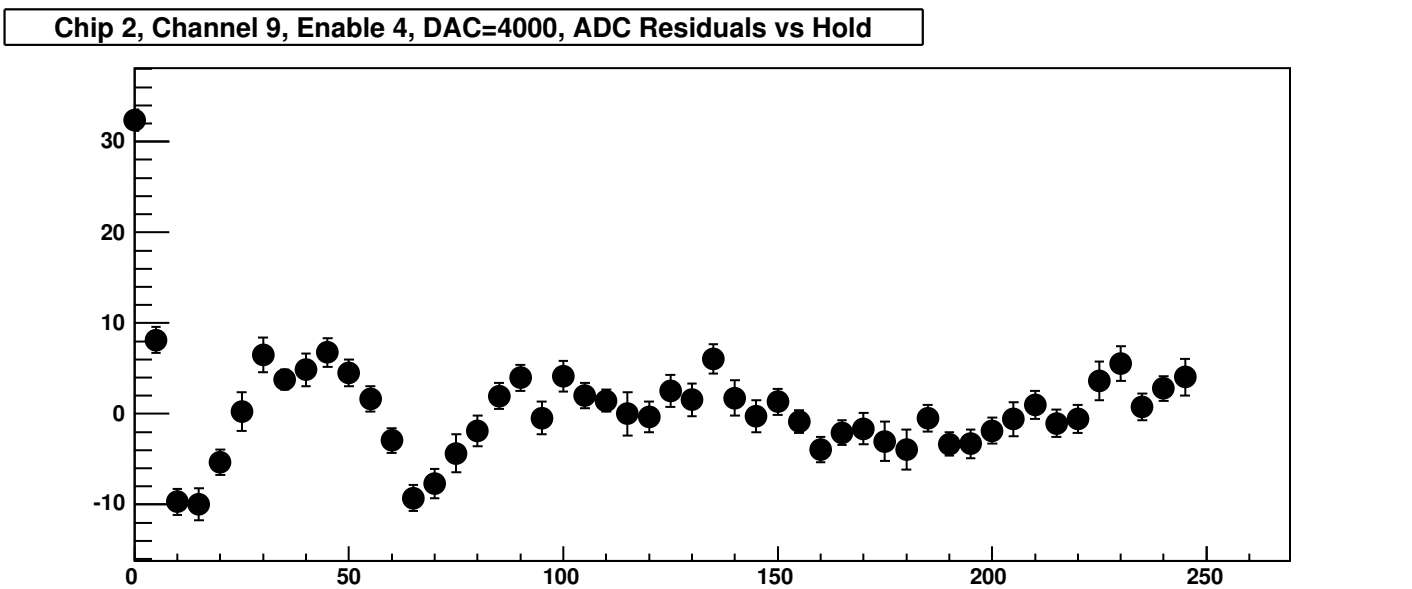


Chip 2, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold

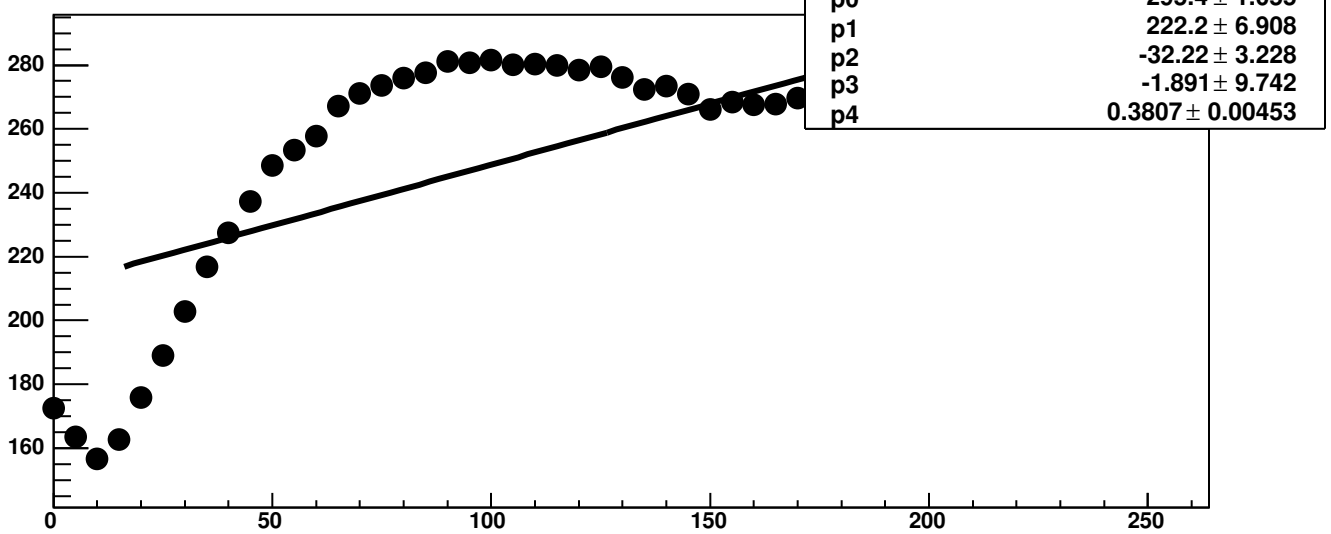



Chip 2, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 266.9 / 41
p0 288.6 ± 0.5219
p1 66.46 ± 0.4559
p2 -6.605e+07 ± 9.938e+05
p3 2.903e+07 ± 1.399e+05
p4 2.144 ± 0.03145
300
280
260
240
220
200
180
160
0
50
100
150
200
250

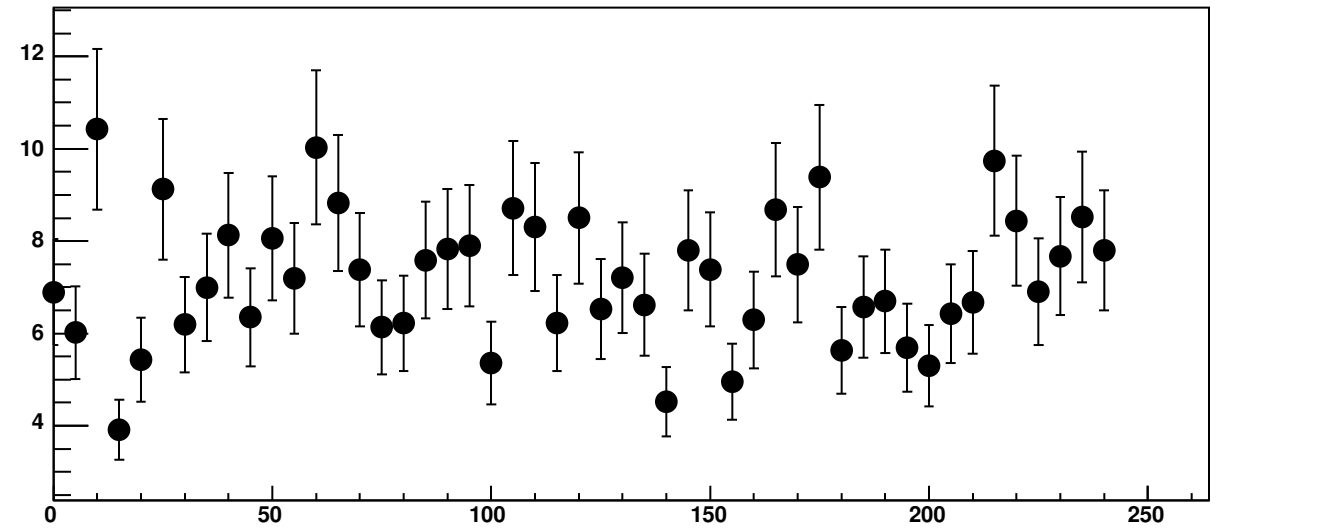

Chip 2, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 2, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
0
50
100
150
200
250

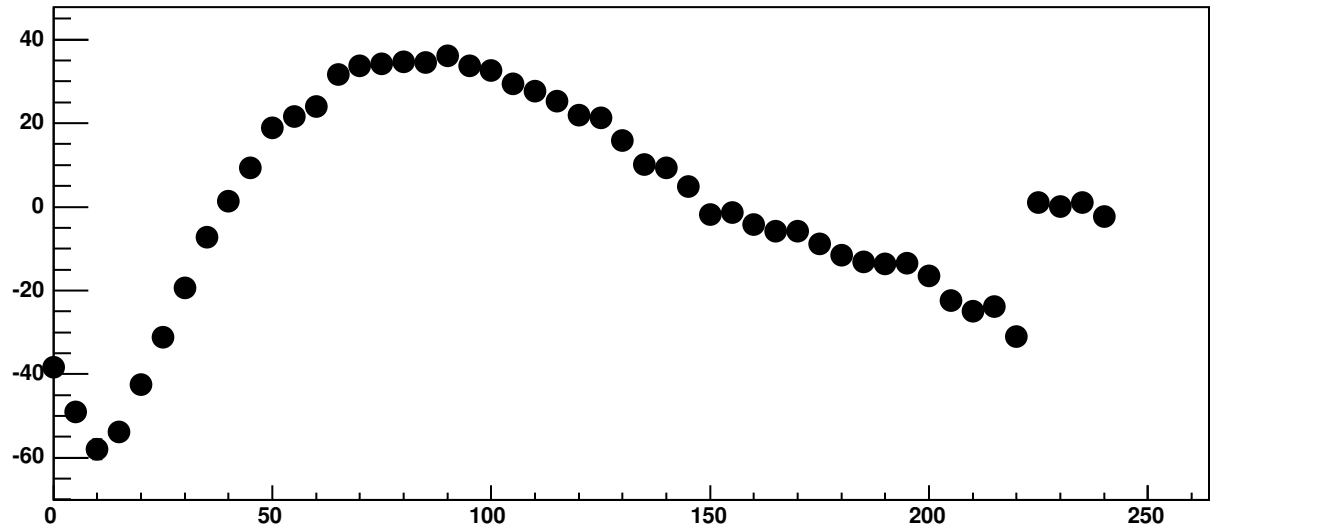
Chip 2, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold



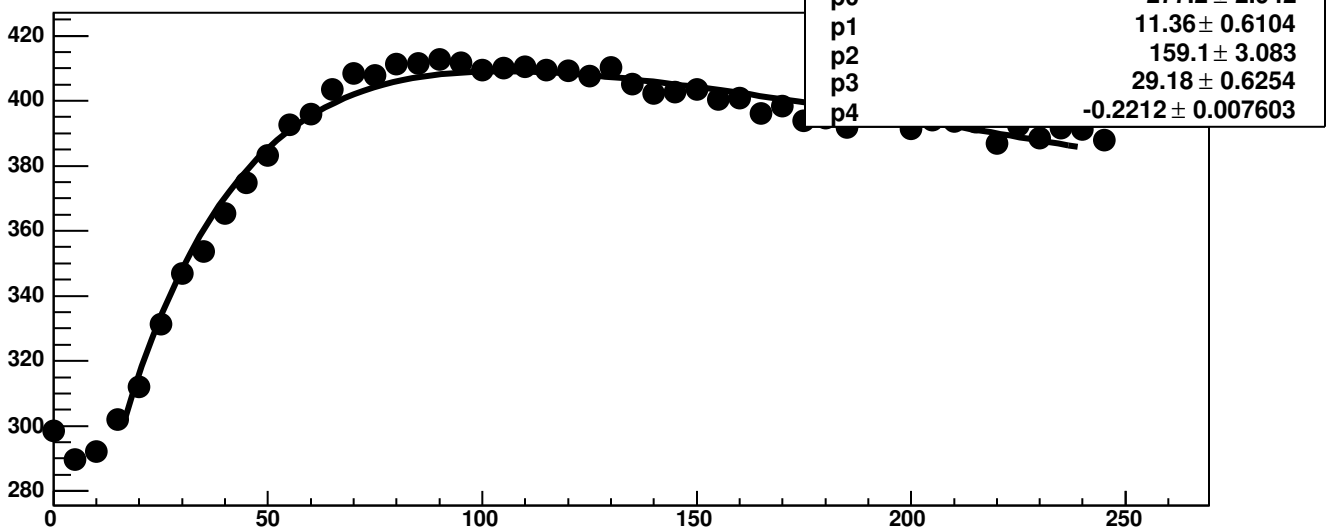
Chip 2, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



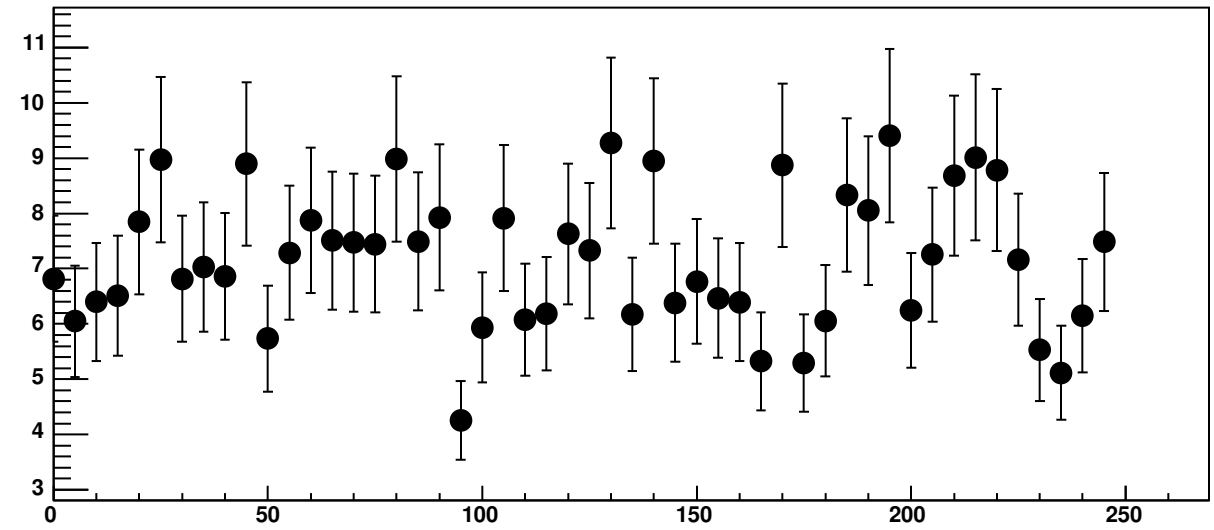
Chip 2, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold



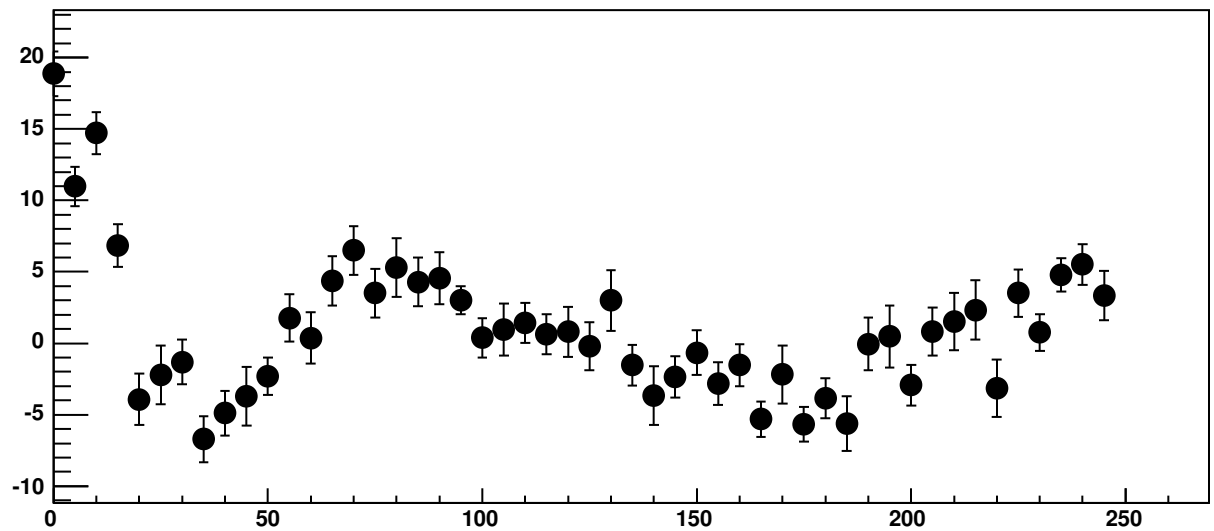
Chip 2, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold

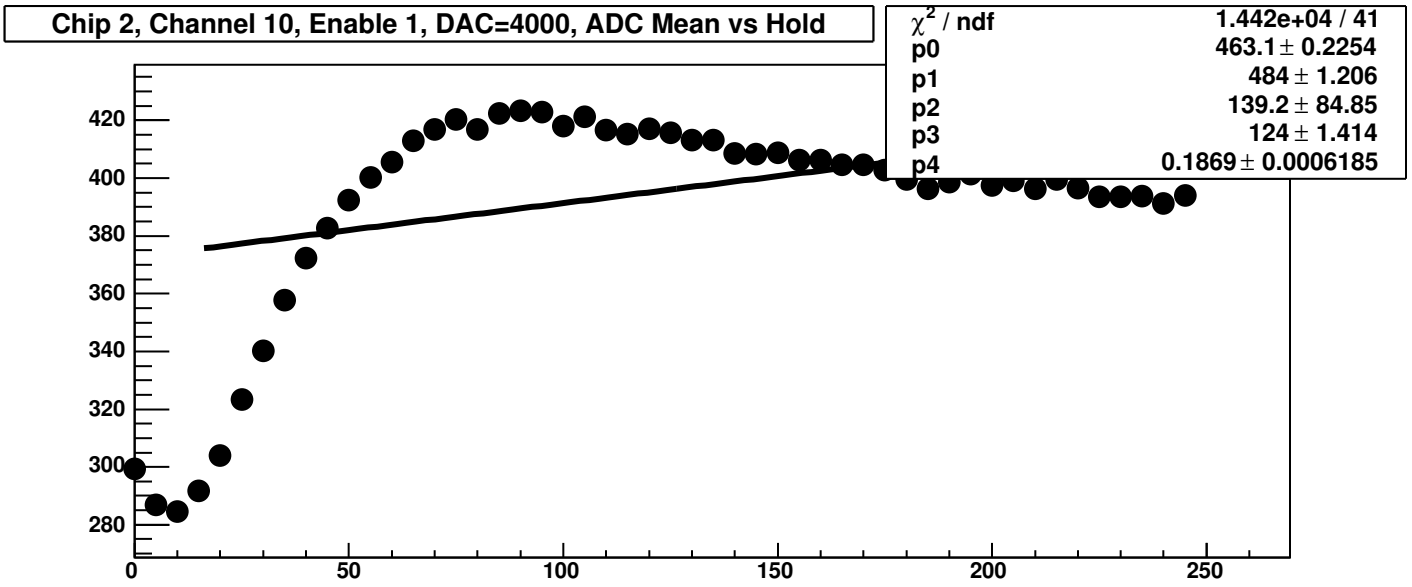


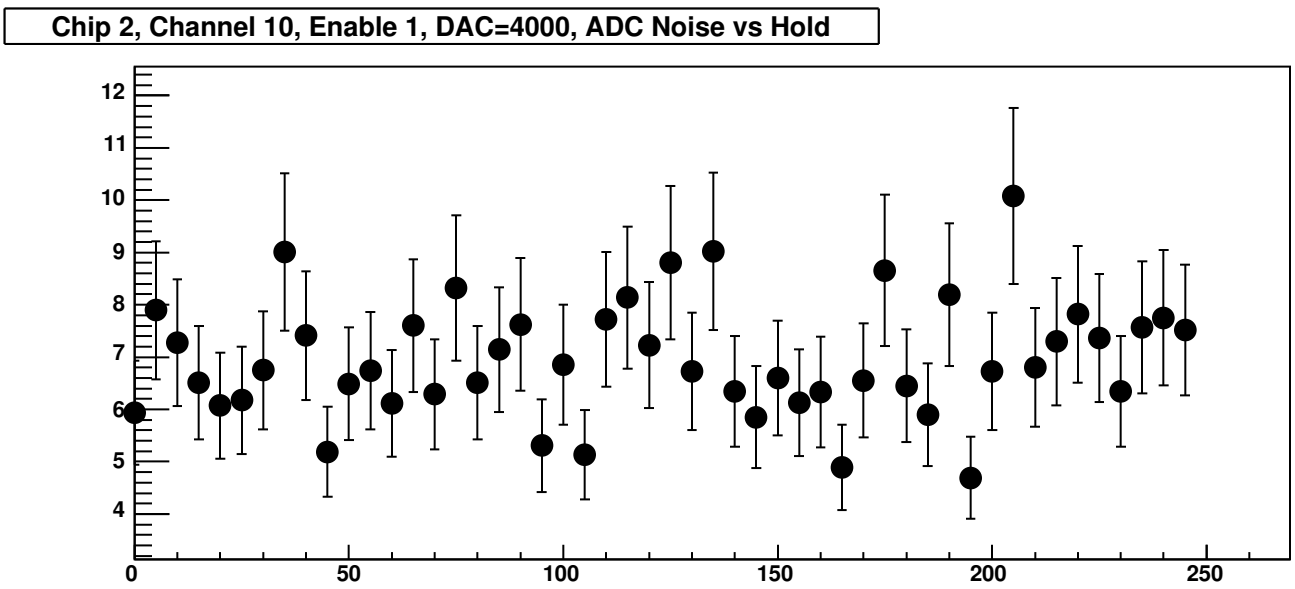
Chip 2, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold

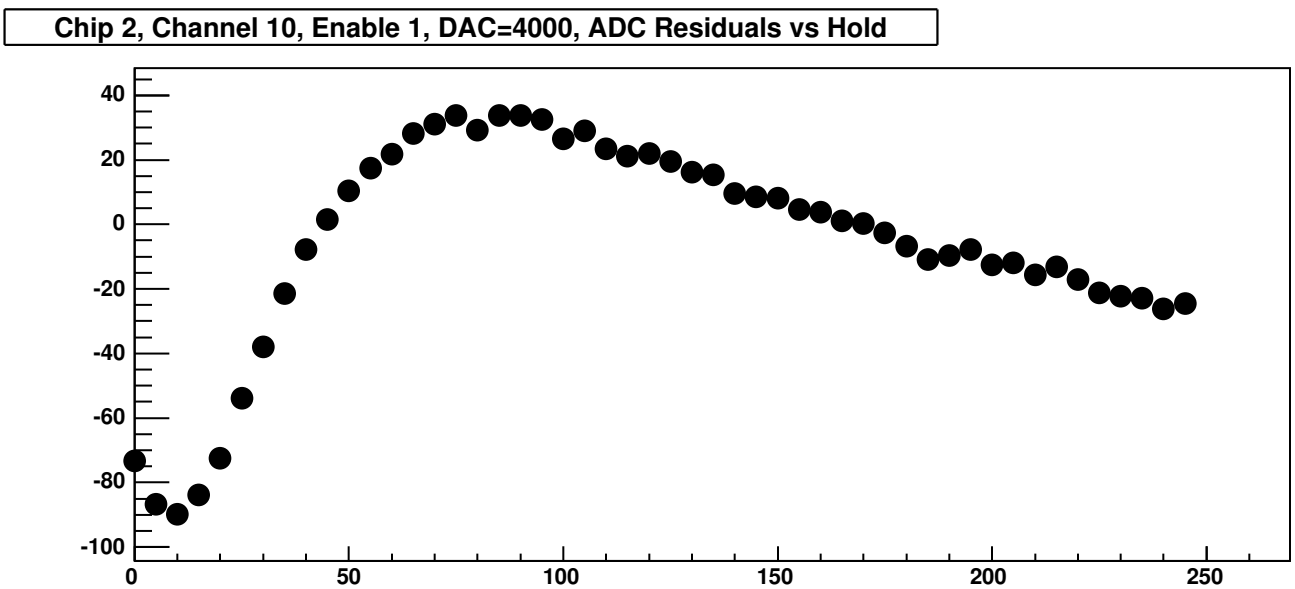


Chip 2, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold

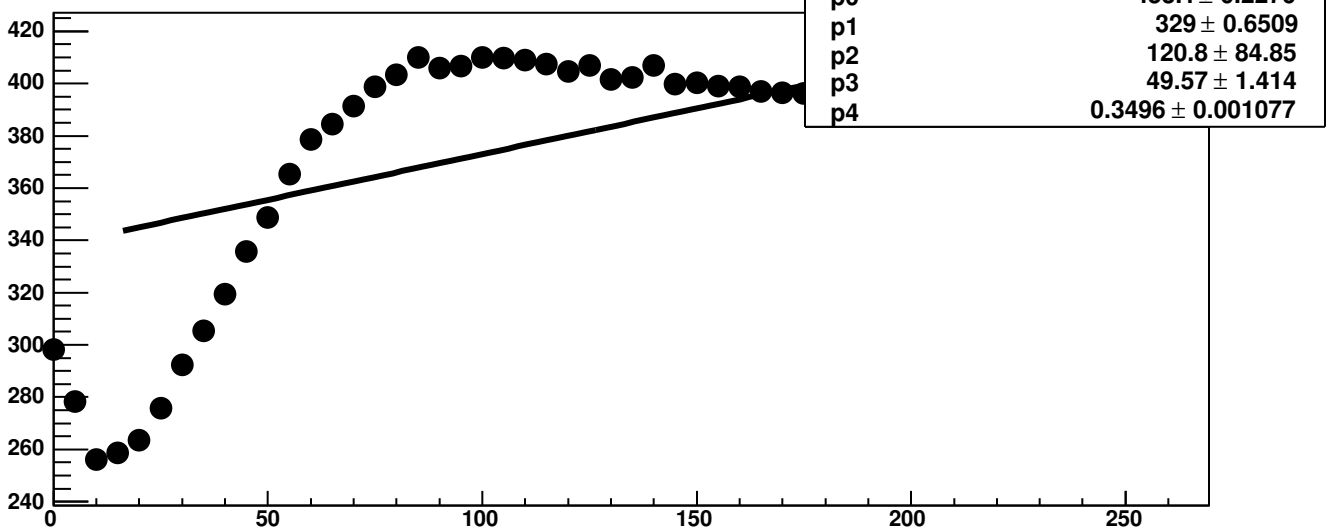



Chip 2, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.442e+04 / 41
p0 463.1 ± 0.2254
p1 484 ± 1.206
p2 139.2 ± 84.85
p3 124 ± 1.414
p4 0.1869 ± 0.0006185
420
400
380
360
340
320
300
280
0
50
100
150
200
250

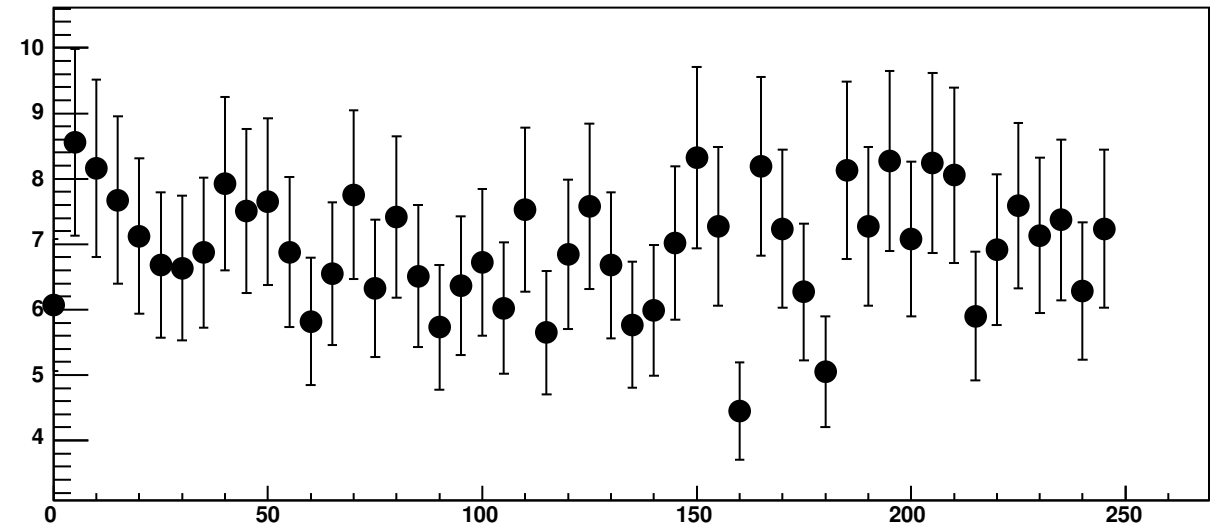

Chip 2, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 2, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
-100
0
50
100
150
200
250

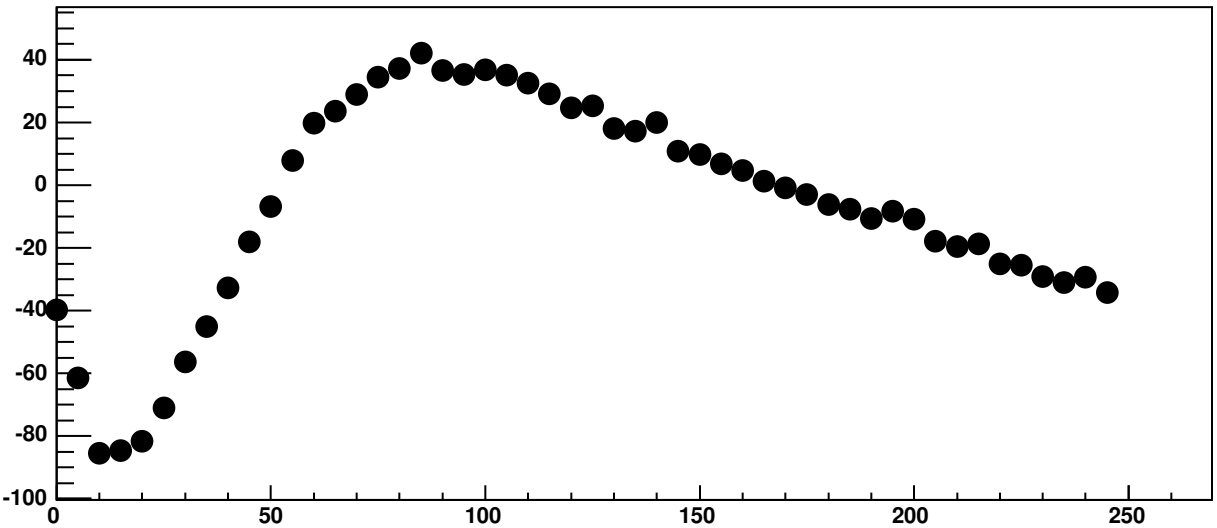
Chip 2, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold



Chip 2, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold

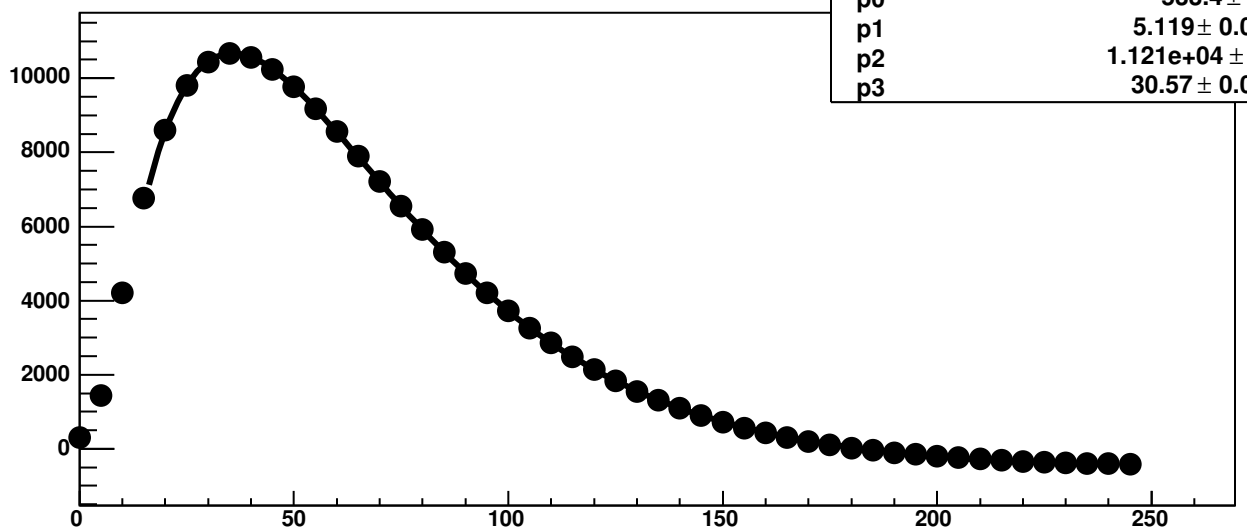


Chip 2, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

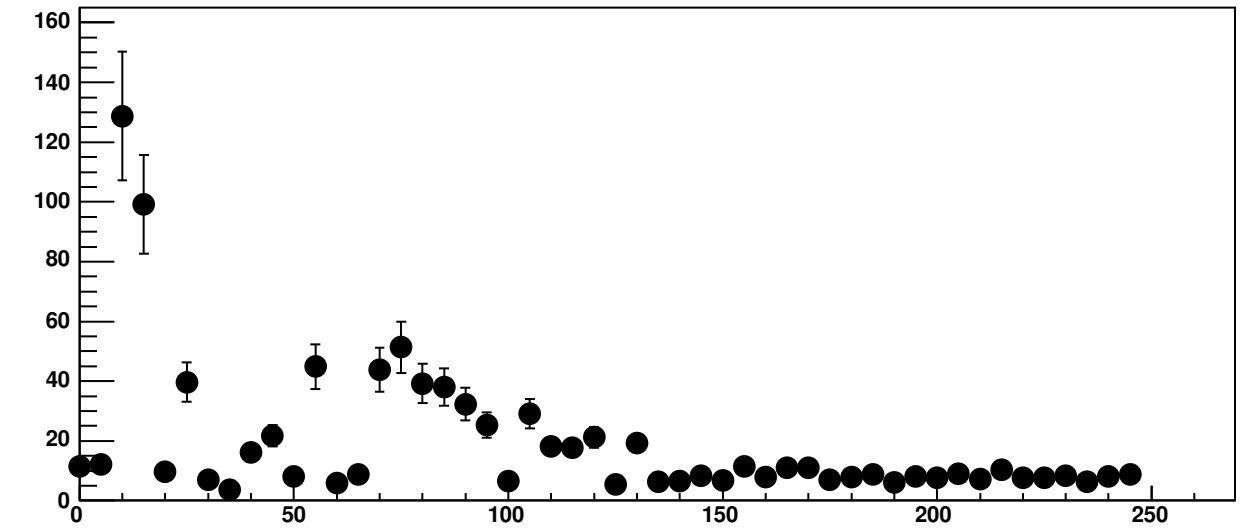


Chip 2, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold

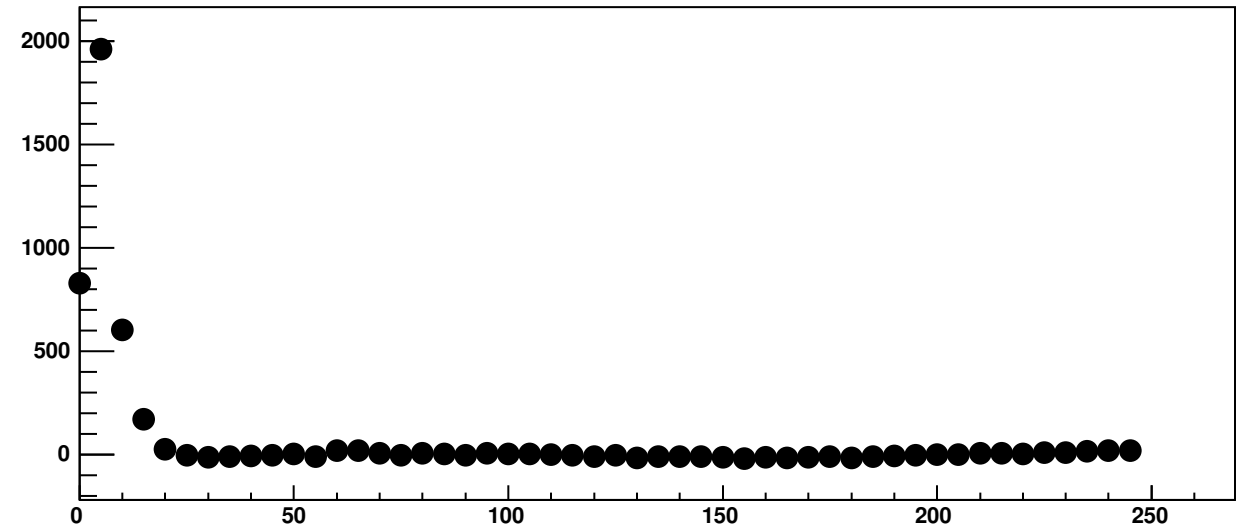
χ^2 / ndf	1505 / 42
p0	-533.4 ± 0.5973
p1	5.119 ± 0.008199
p2	1.121e+04 ± 0.7891
p3	30.57 ± 0.005308



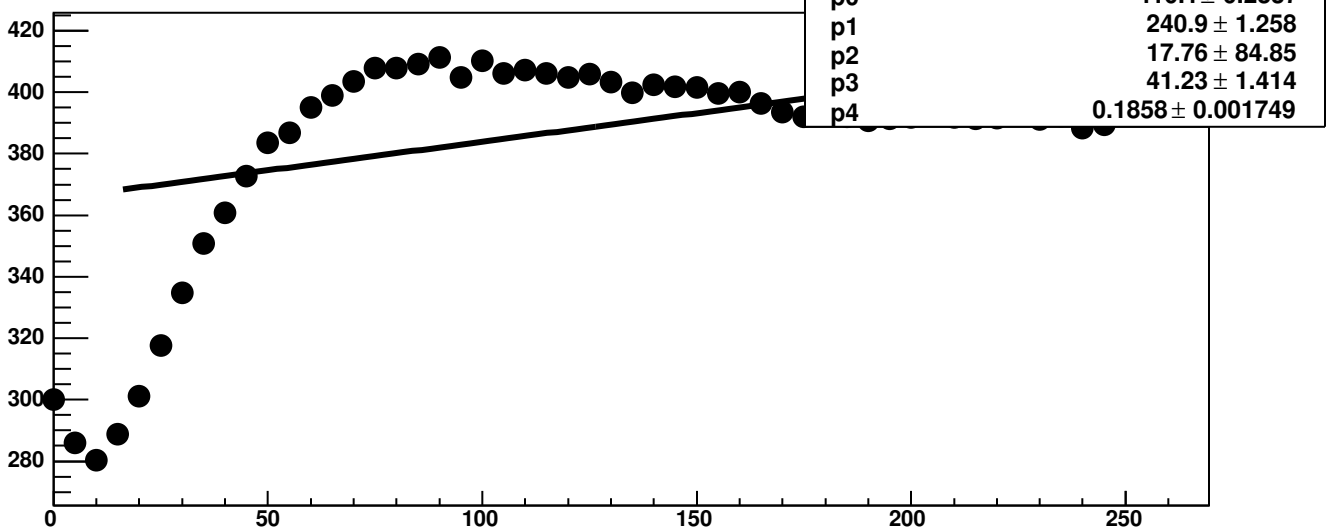
Chip 2, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold



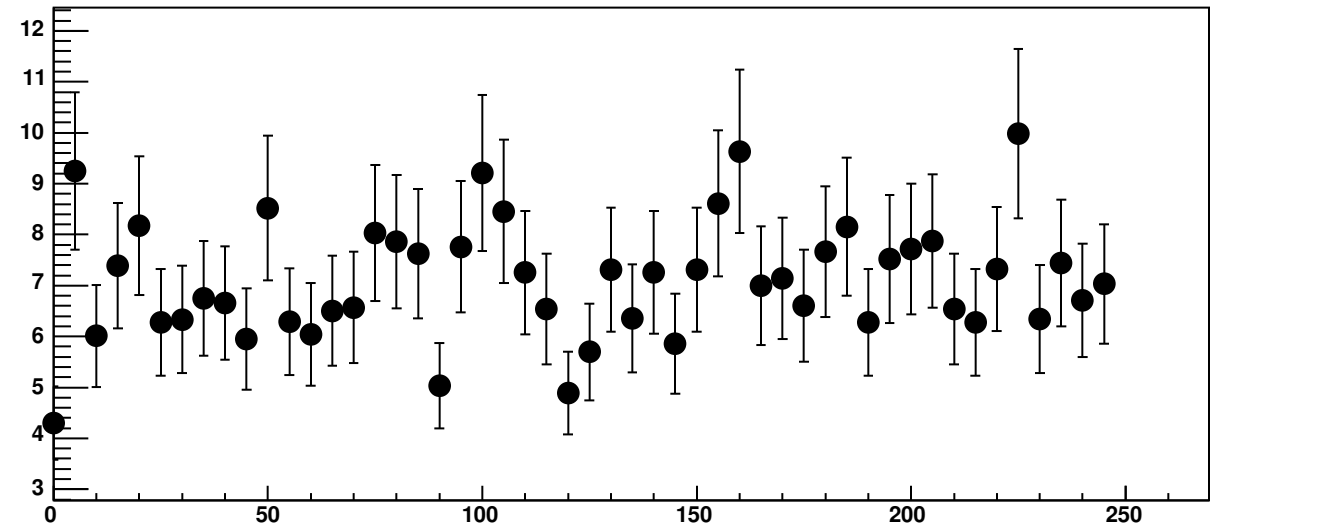
Chip 2, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold



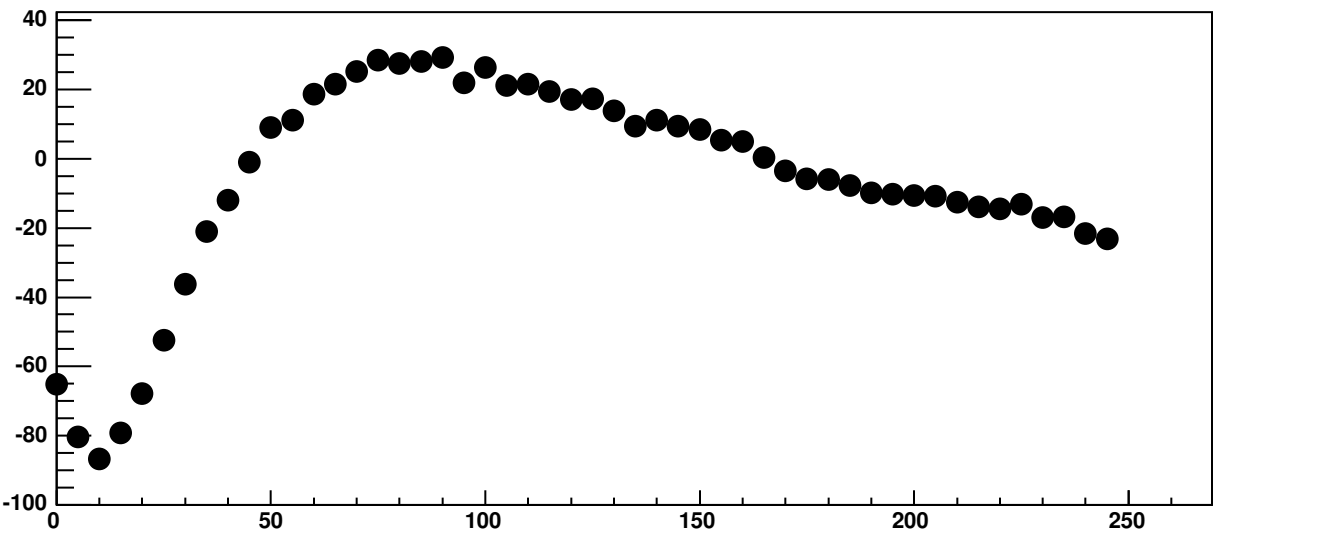
Chip 2, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold



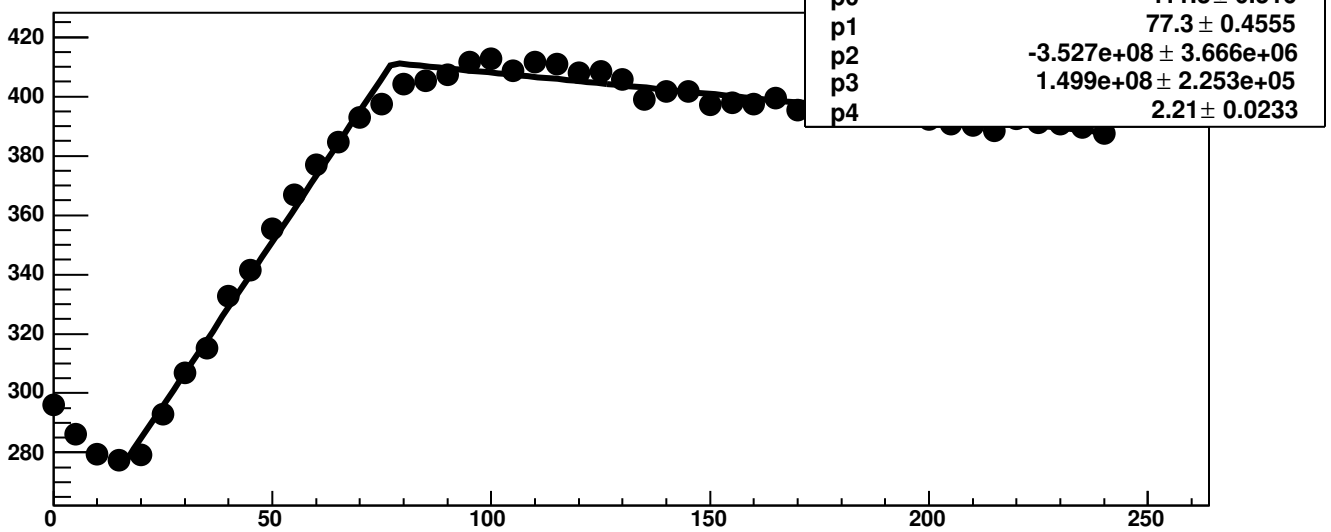
Chip 2, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold



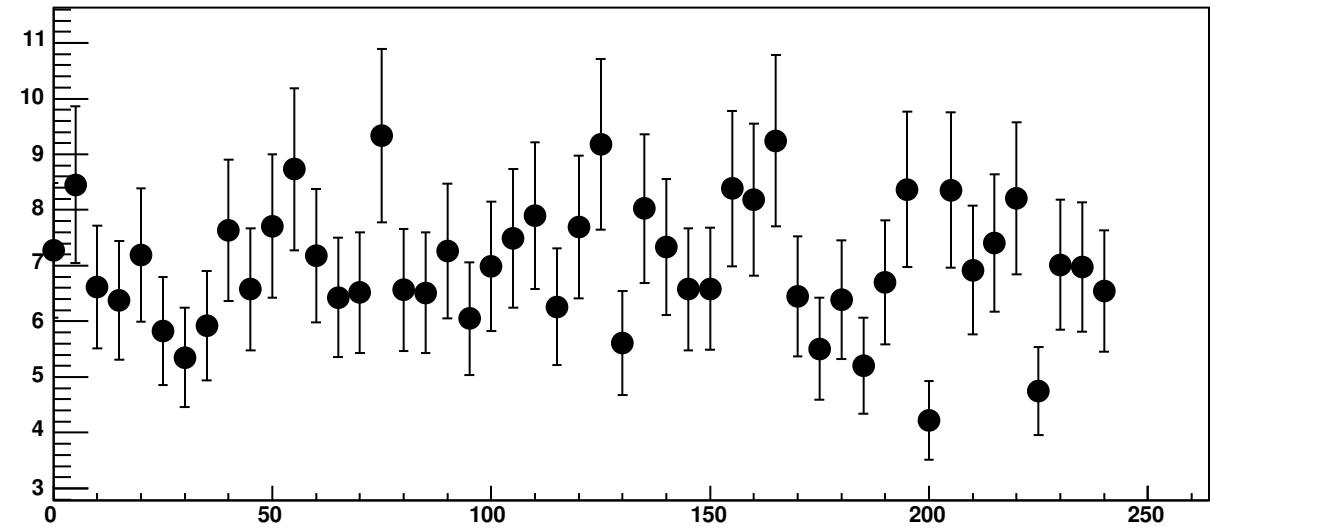
Chip 2, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold



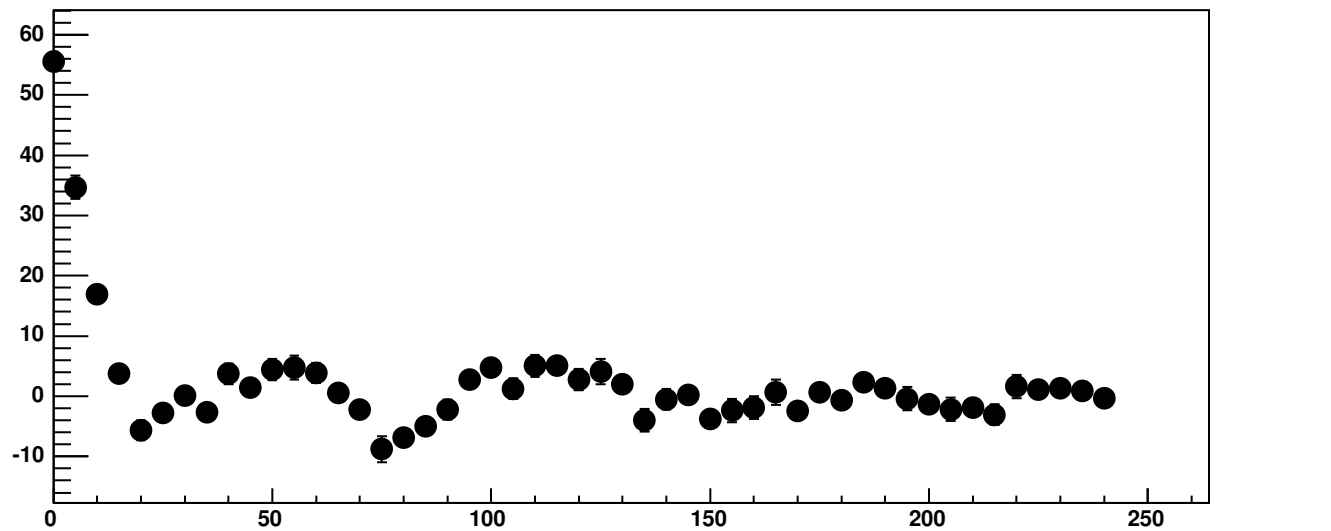
Chip 2, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold



Chip 2, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold

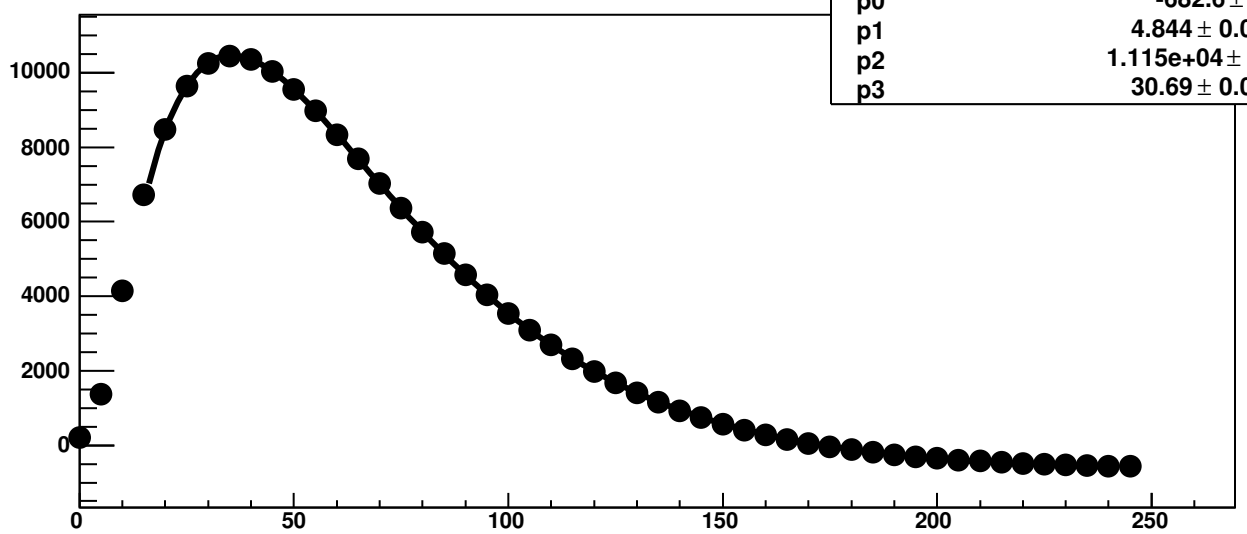


Chip 2, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold

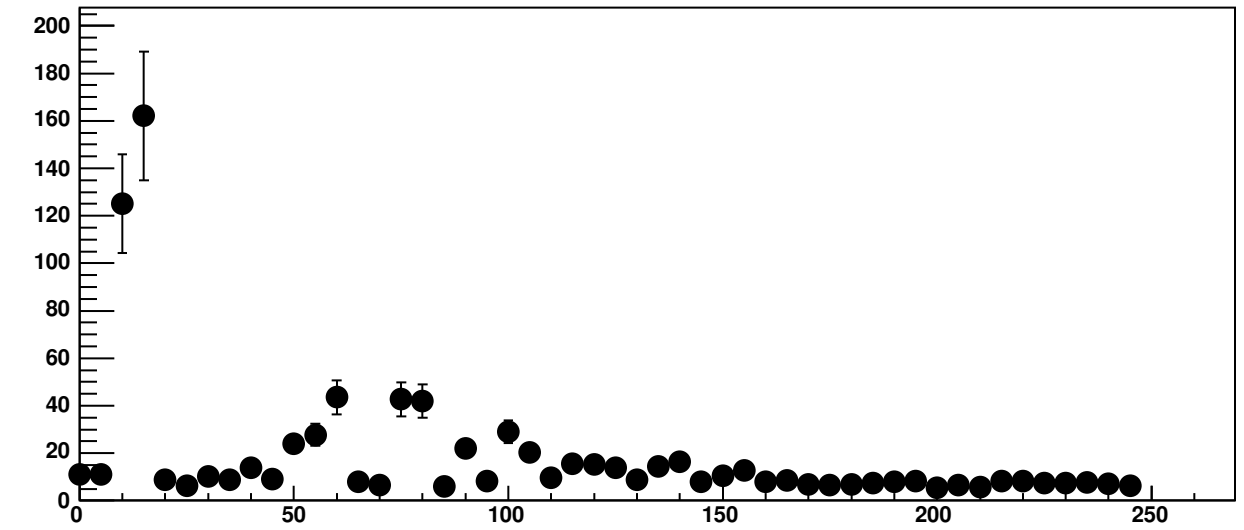


Chip 2, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold

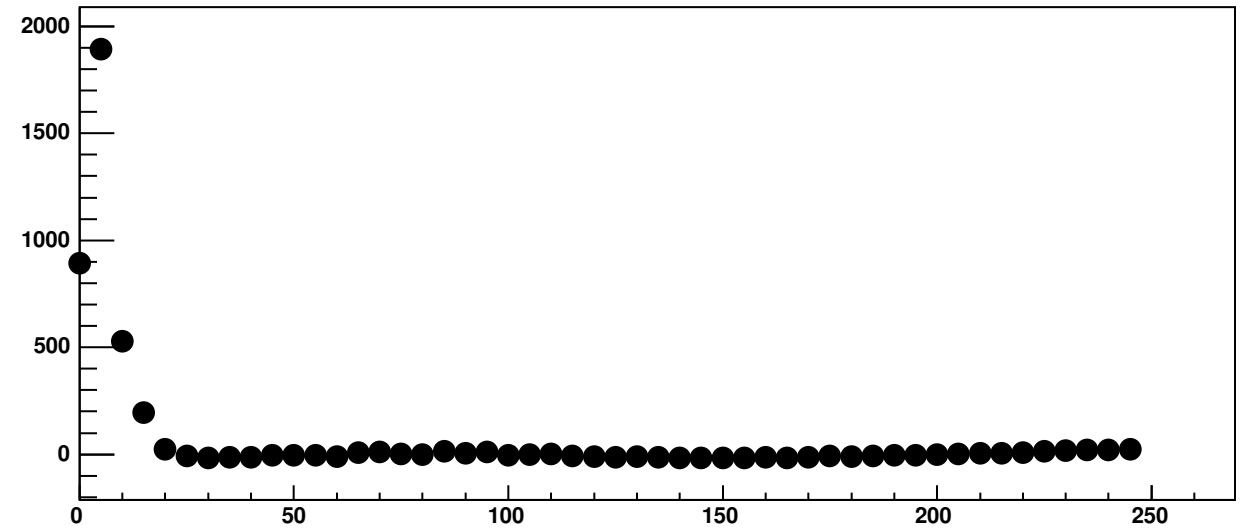
χ^2 / ndf	1540 / 42
p0	-682.6 ± 0.5561
p1	4.844 ± 0.008408
p2	1.115e+04 ± 0.9417
p3	30.69 ± 0.005823



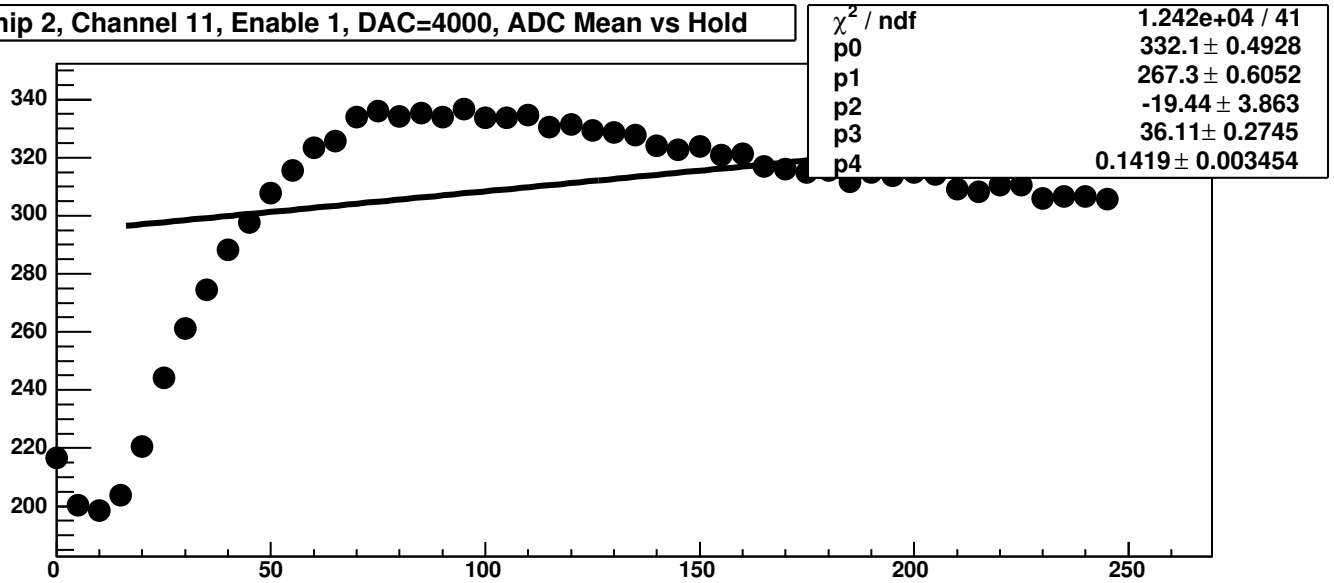
Chip 2, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold



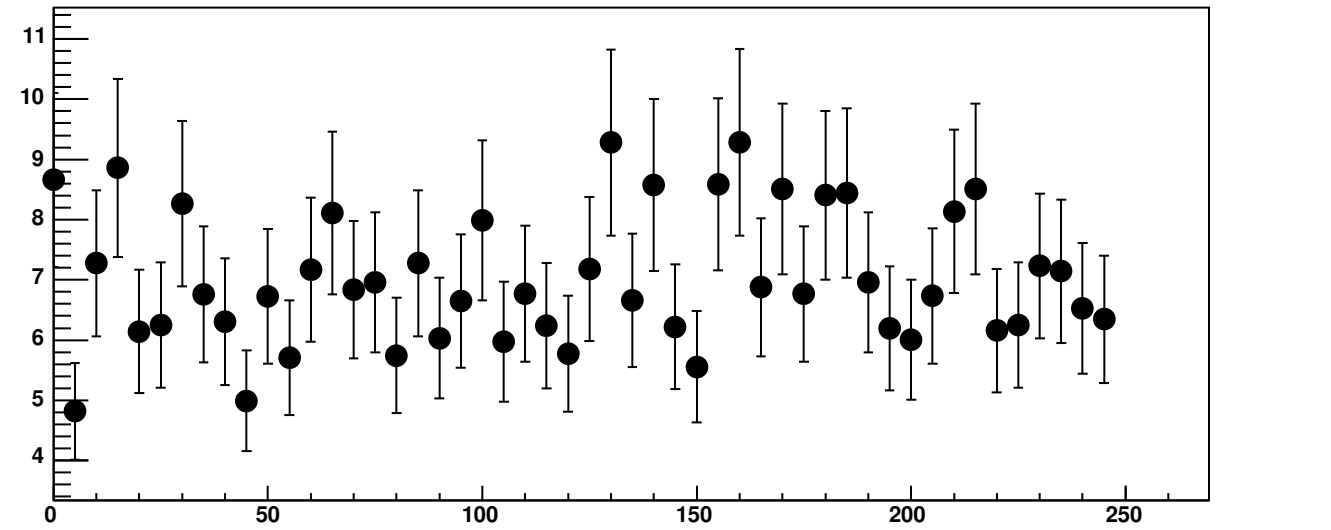
Chip 2, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold



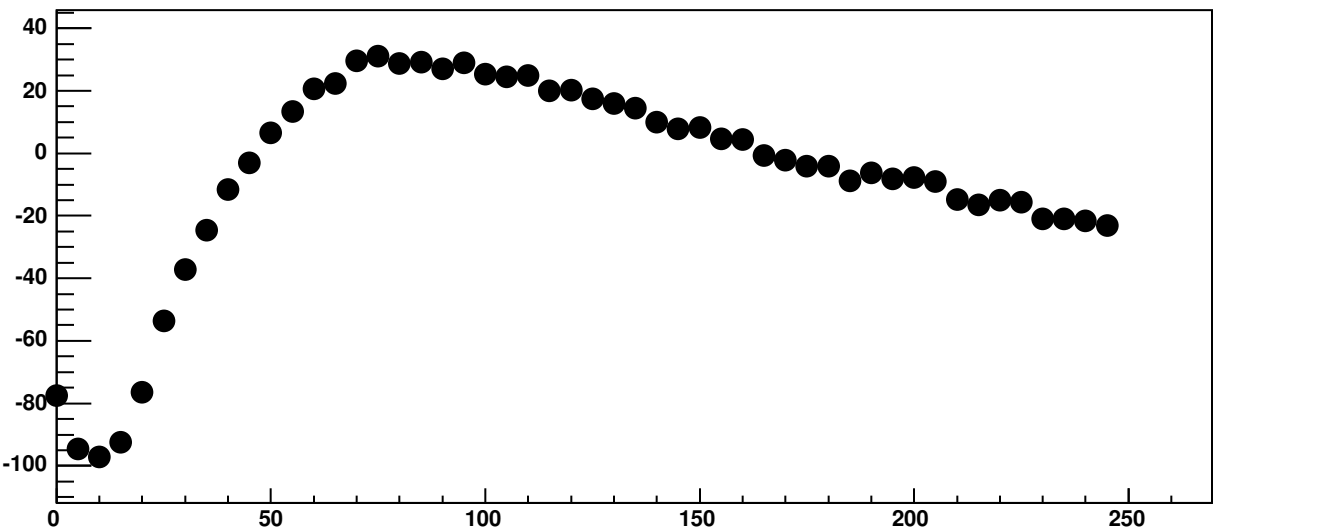
Chip 2, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold



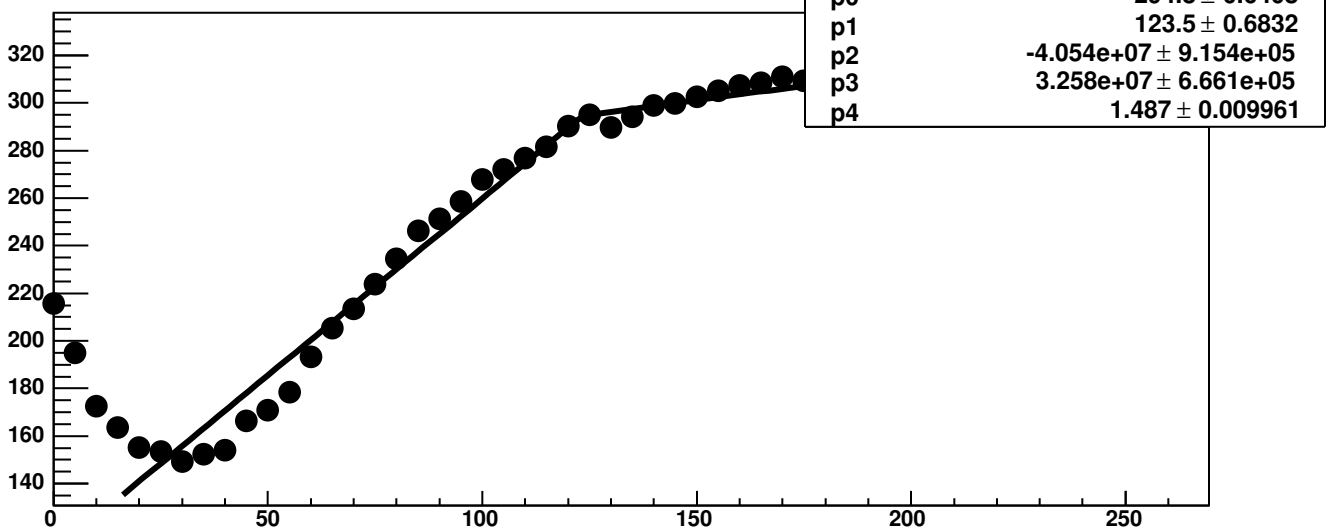
Chip 2, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



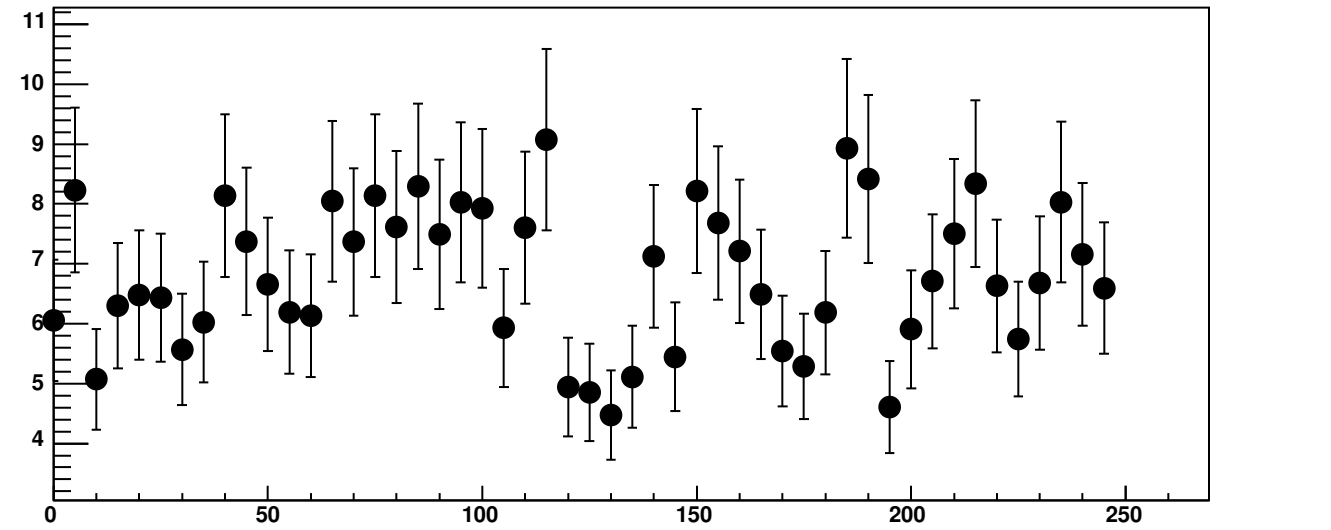
Chip 2, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold



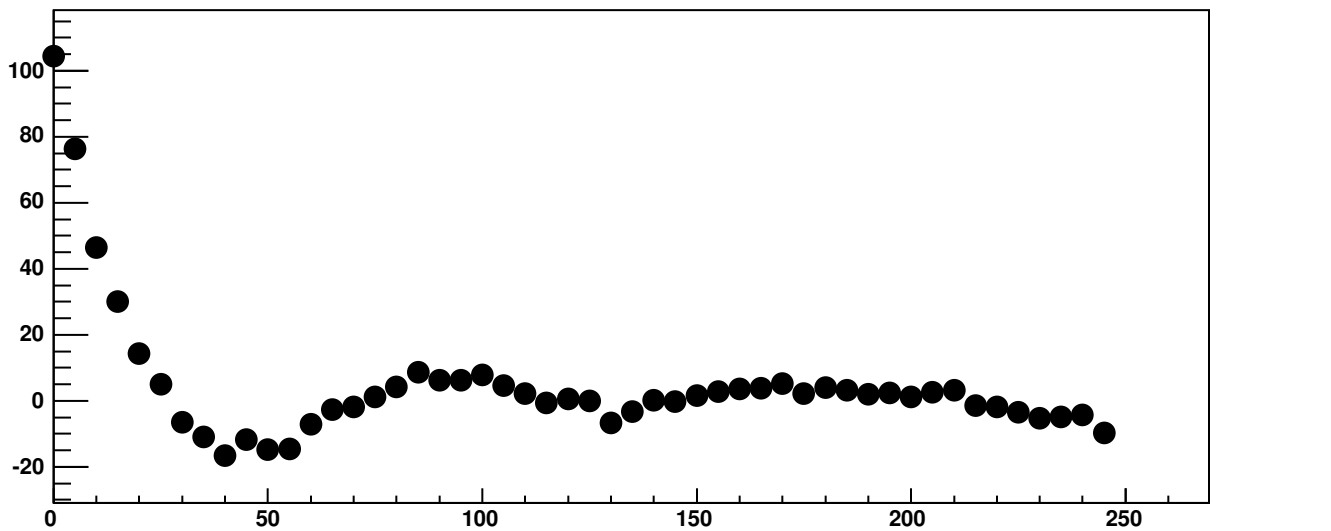
Chip 2, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold



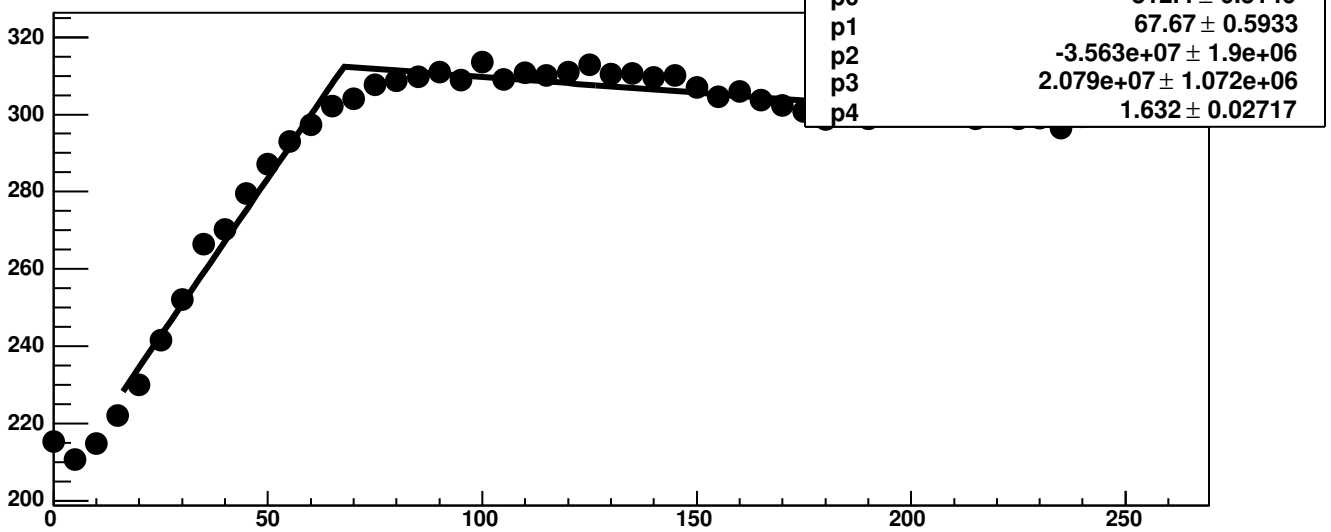
Chip 2, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold



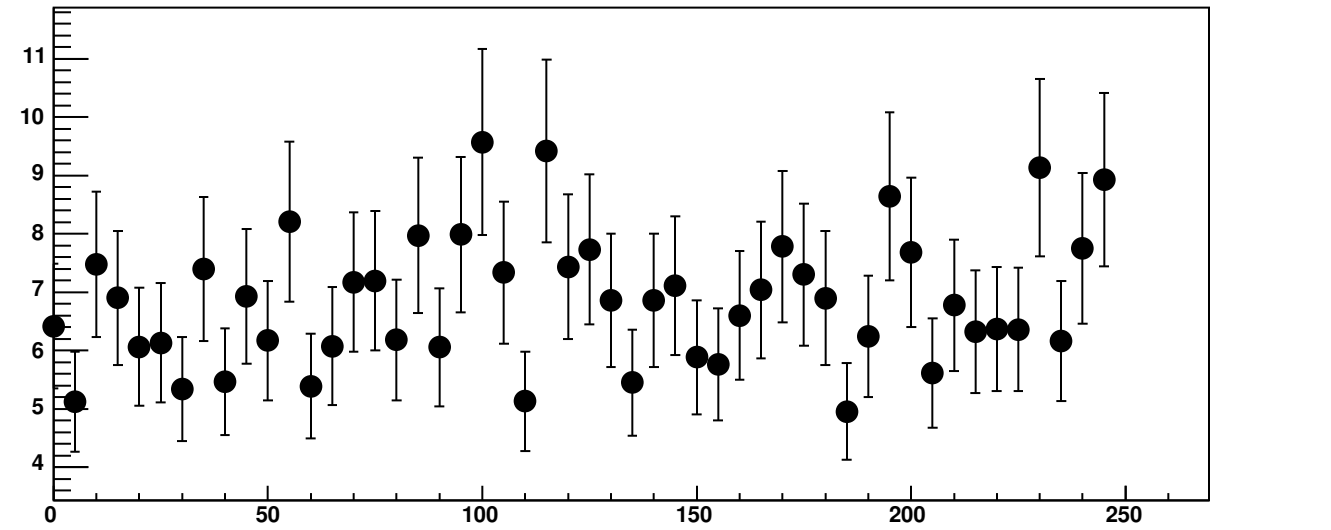
Chip 2, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold



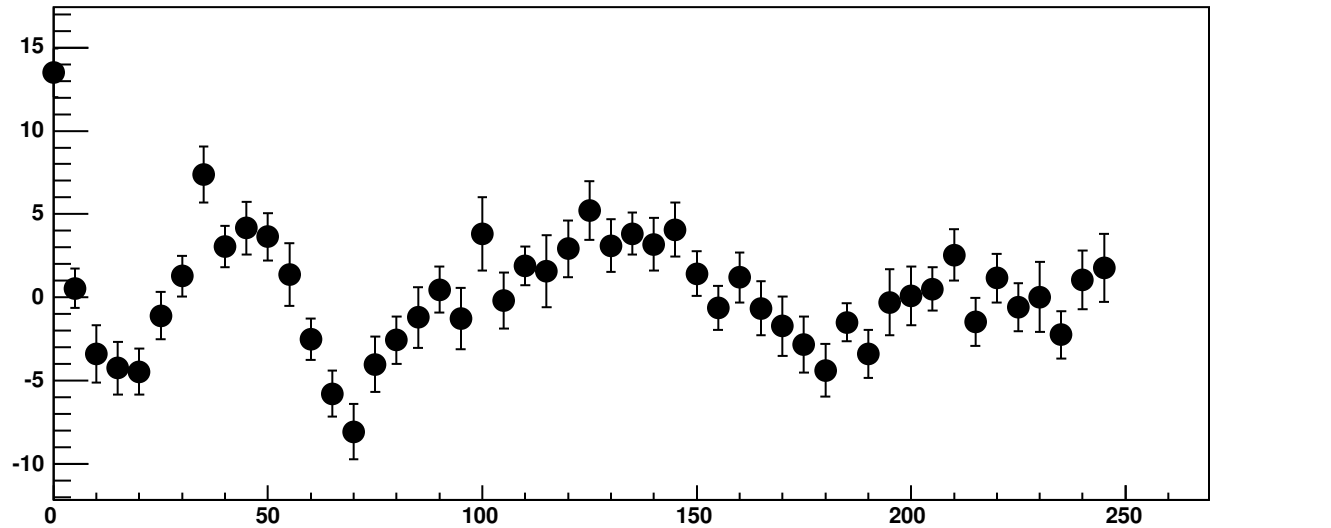
Chip 2, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold



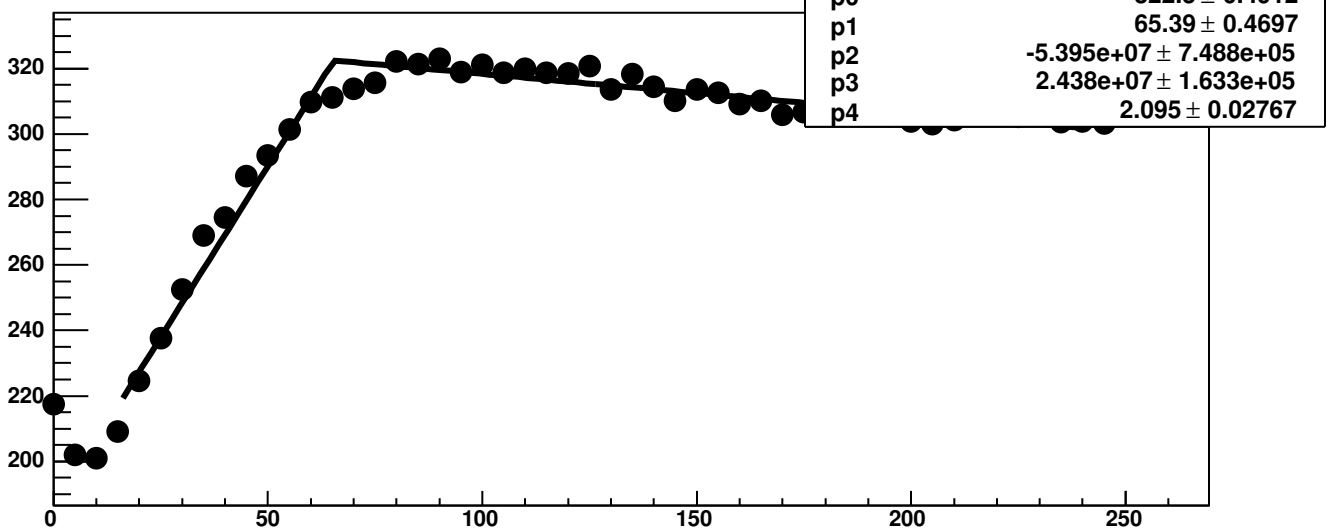
Chip 2, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold



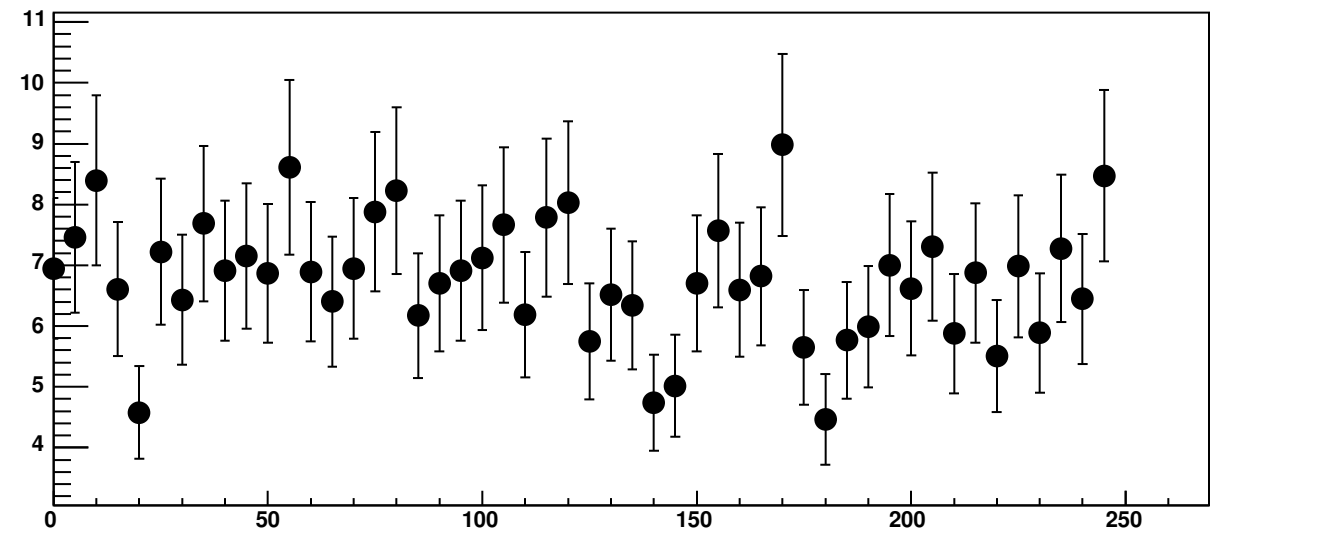
Chip 2, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold



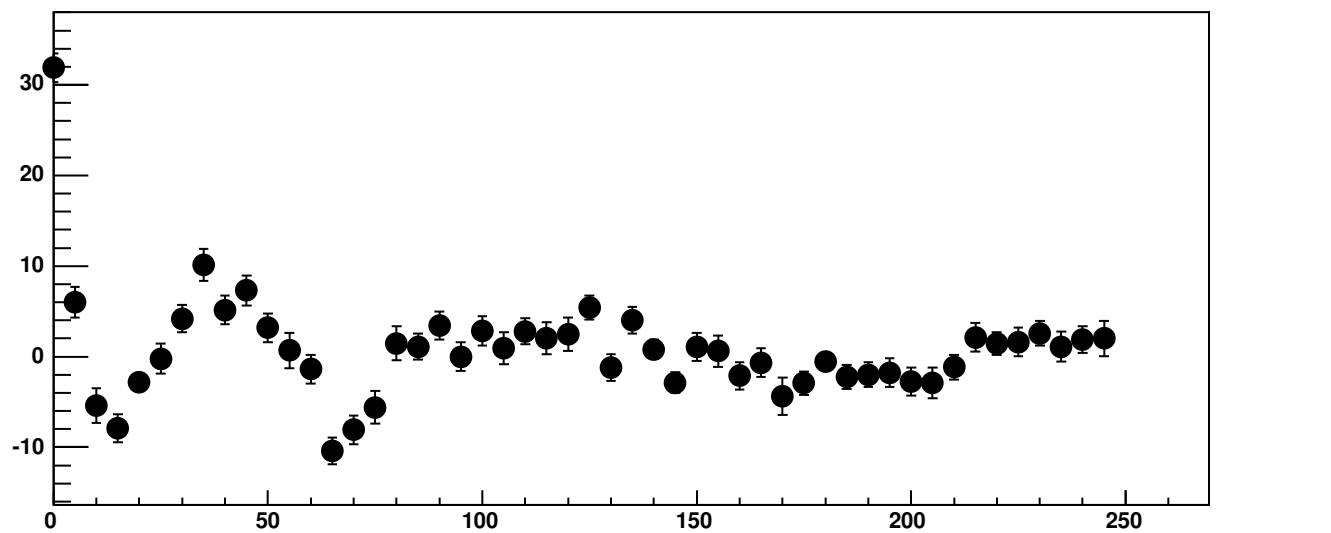
Chip 2, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold



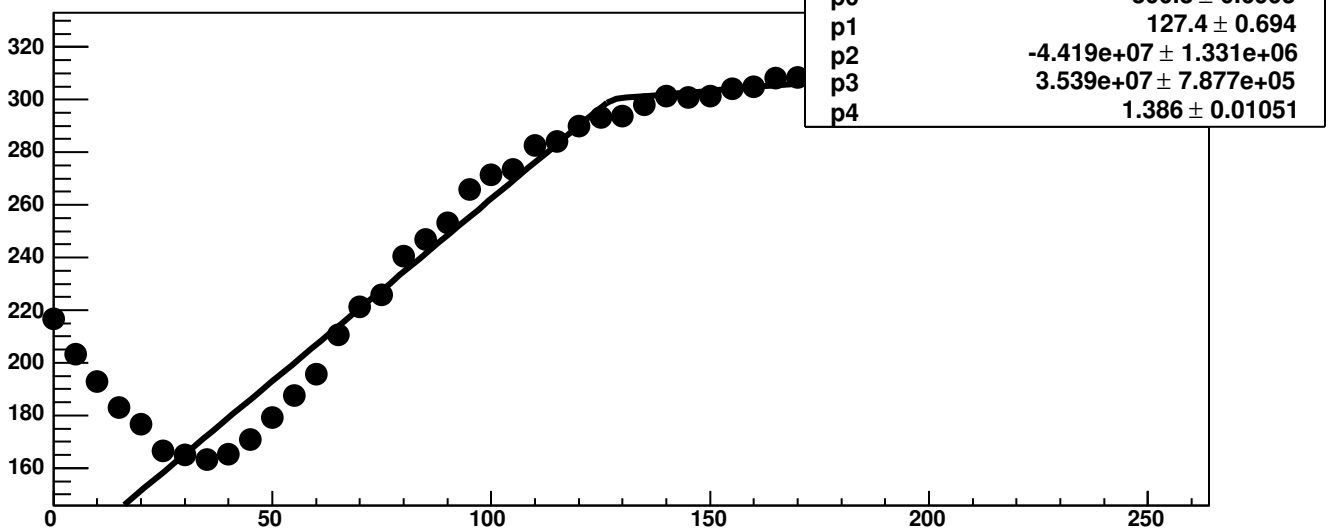
Chip 2, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



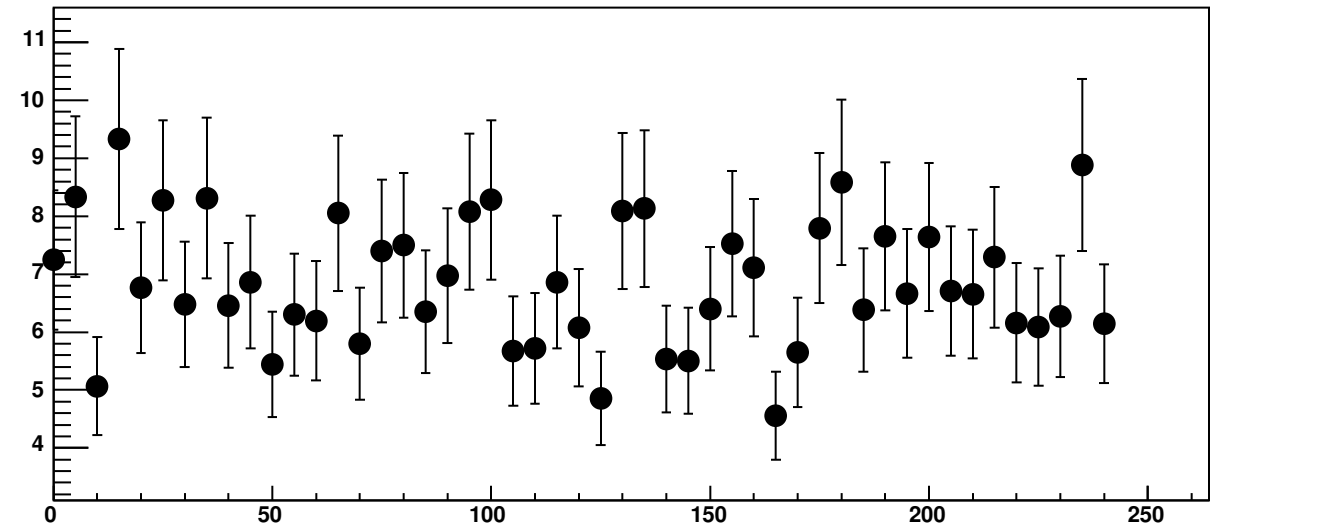
Chip 2, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold



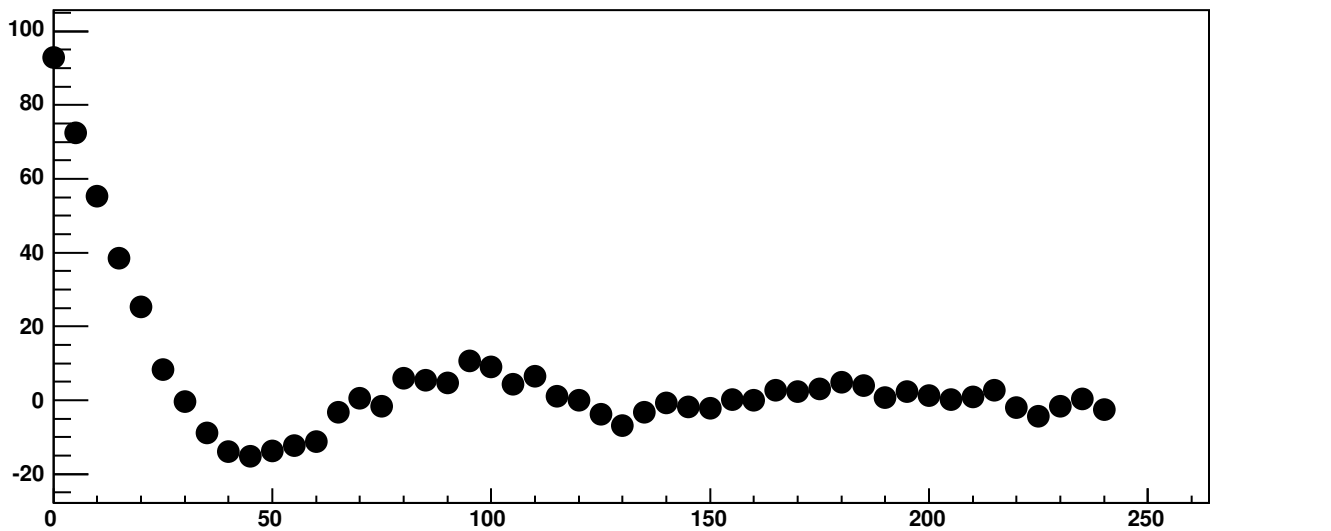
Chip 2, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold



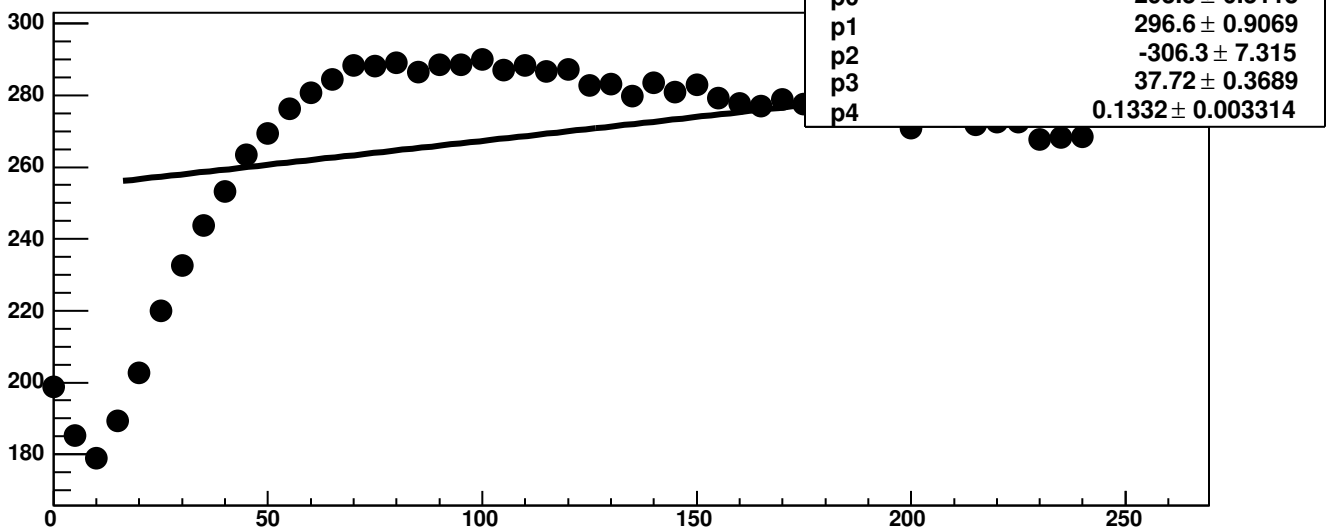
Chip 2, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold



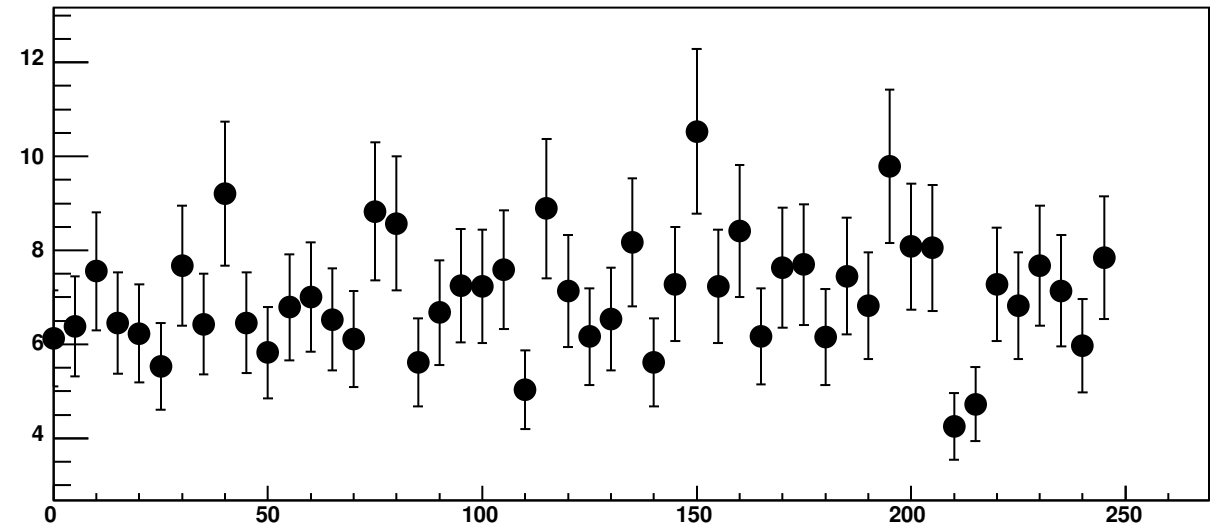
Chip 2, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold



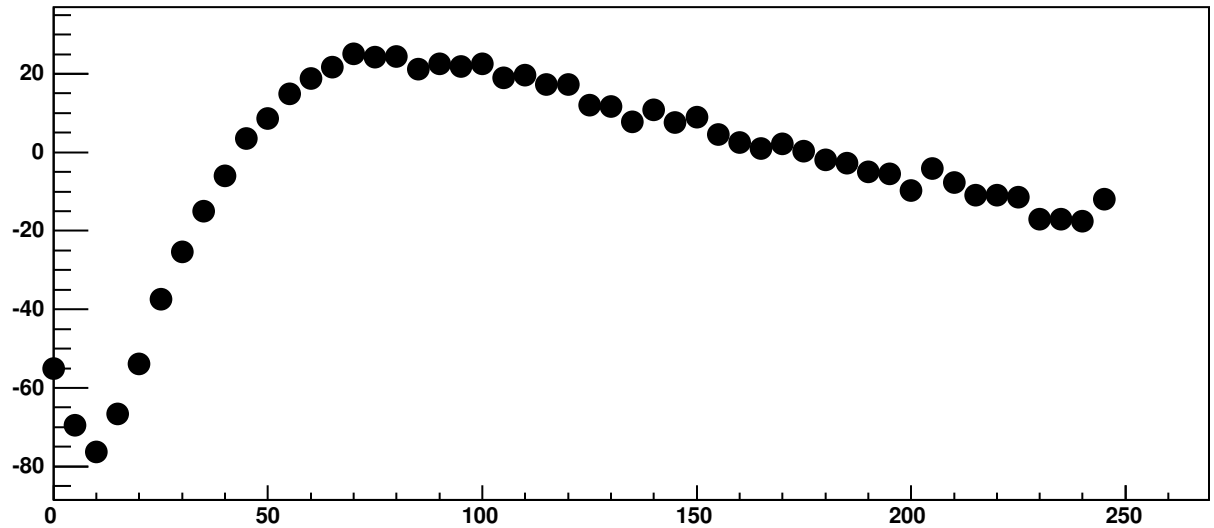
Chip 2, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold



Chip 2, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold

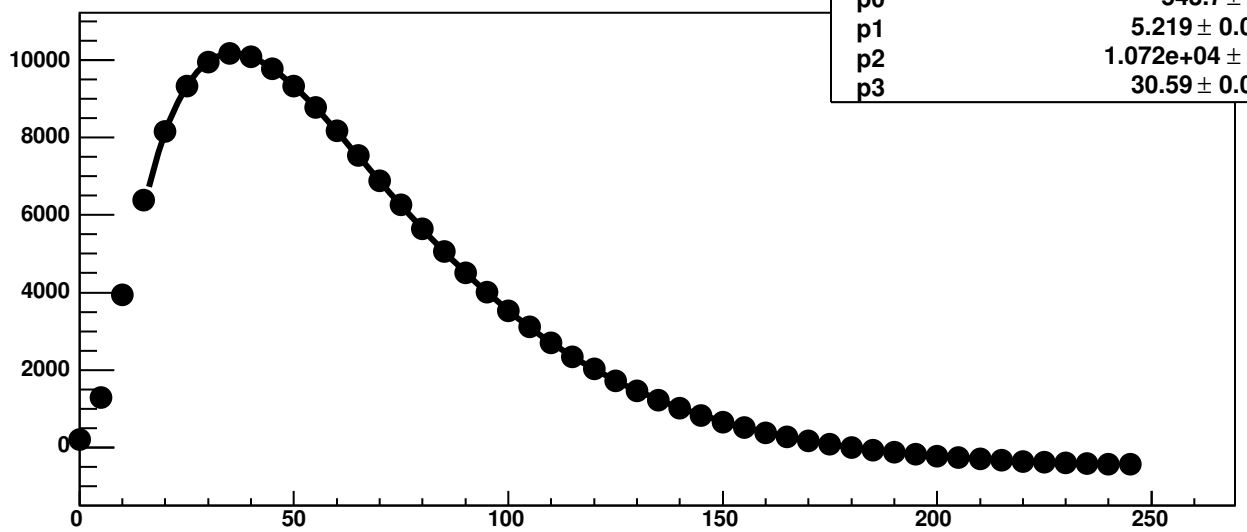


Chip 2, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold

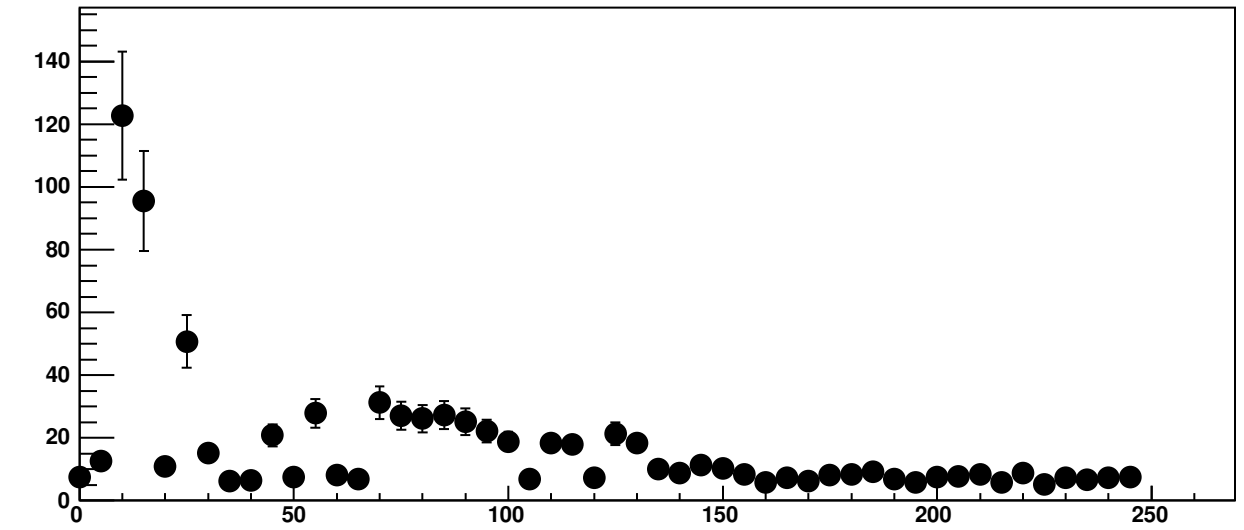


Chip 2, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold

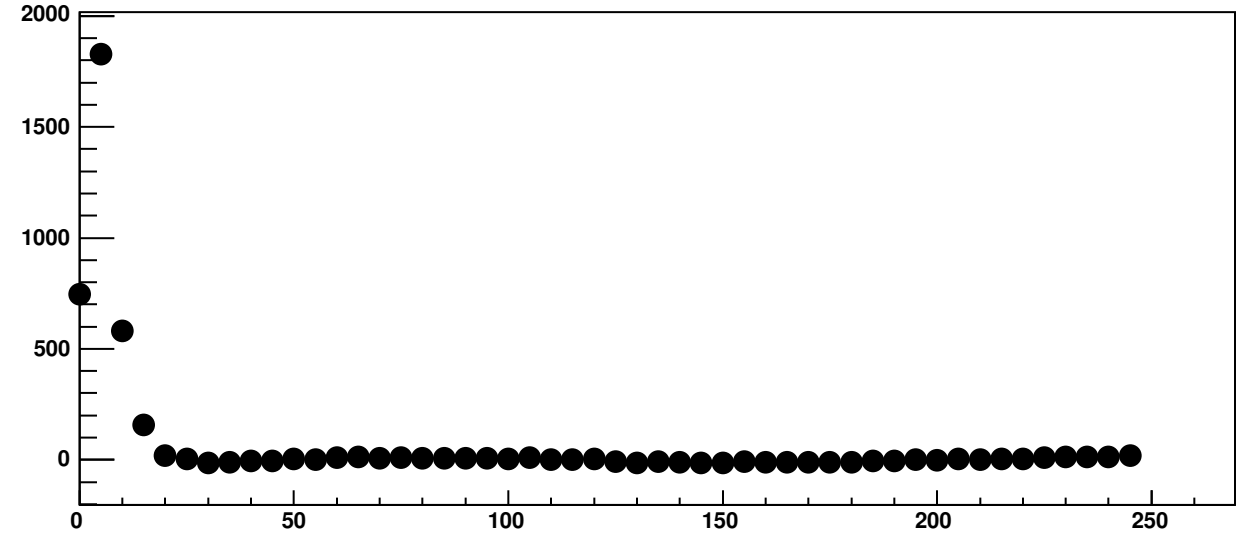
χ^2 / ndf	1036 / 42
p0	-543.7 ± 0.5533
p1	5.219 ± 0.009236
p2	1.072e+04 ± 0.8803
p3	30.59 ± 0.005879



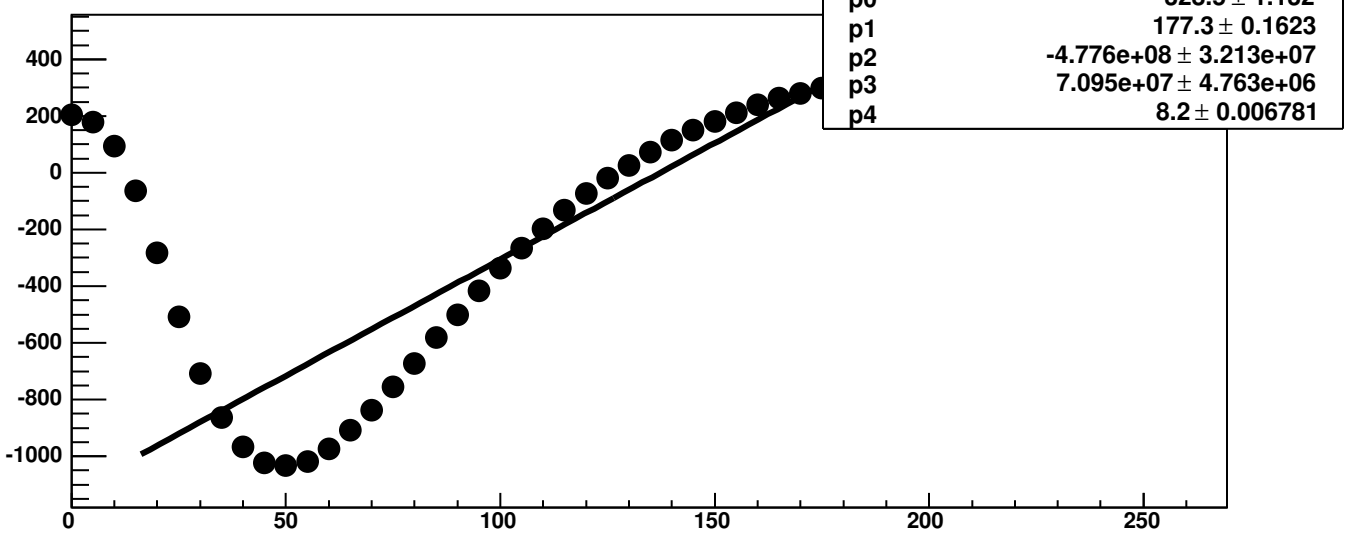
Chip 2, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold



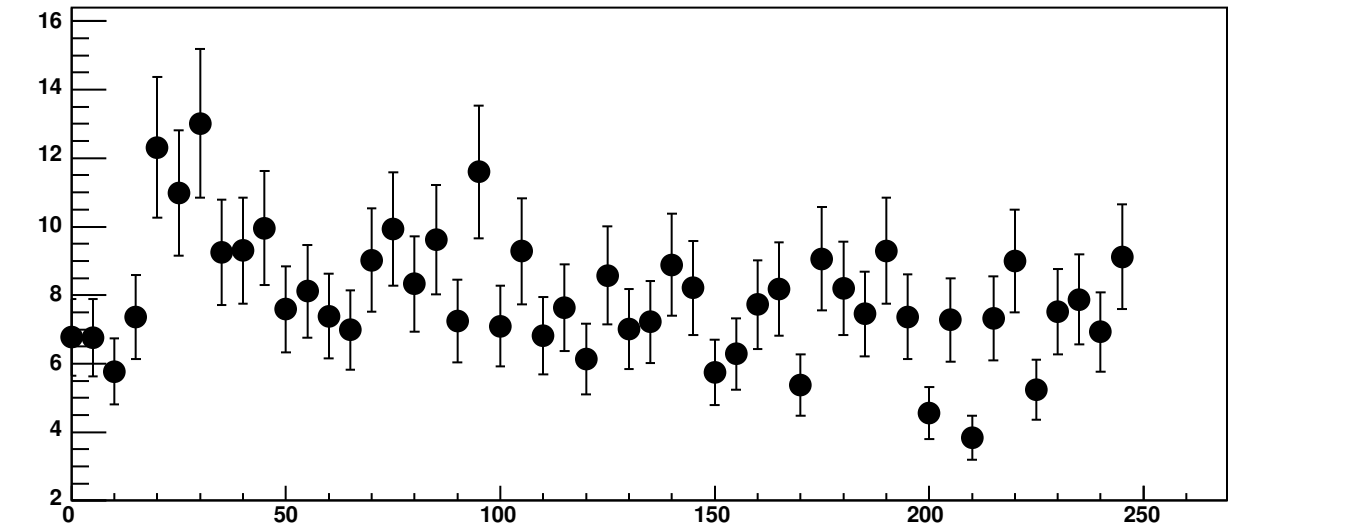
Chip 2, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold



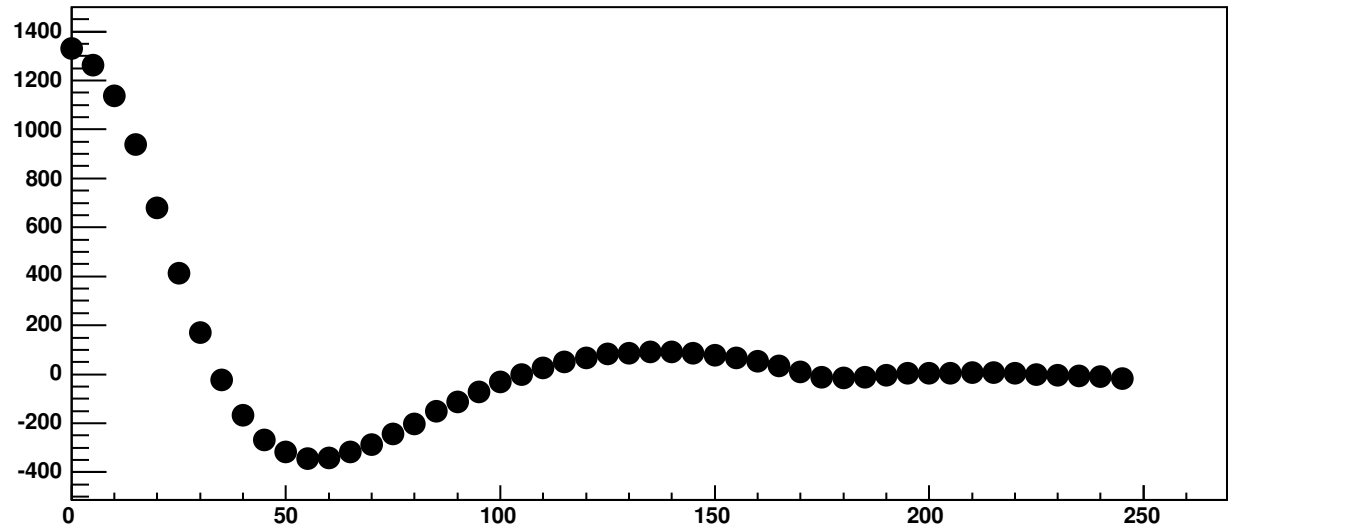
Chip 2, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold



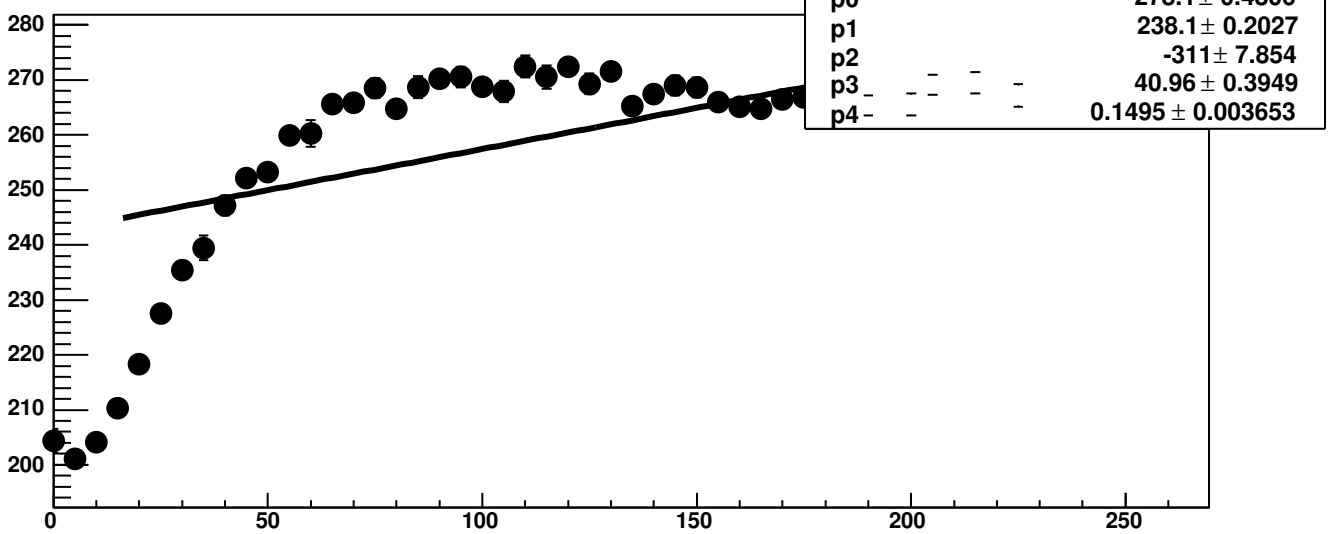
Chip 2, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



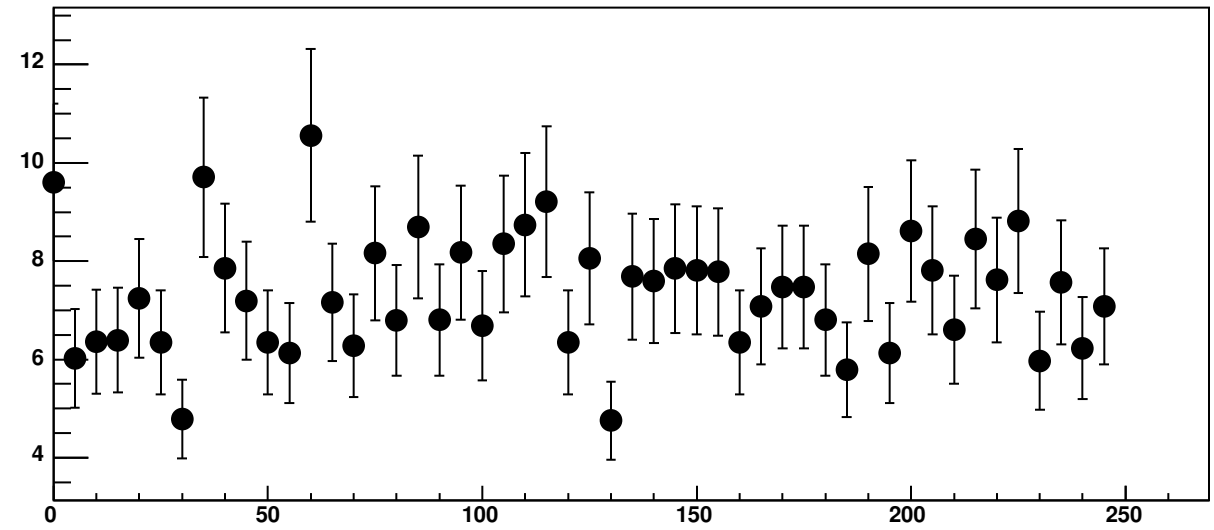
Chip 2, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold



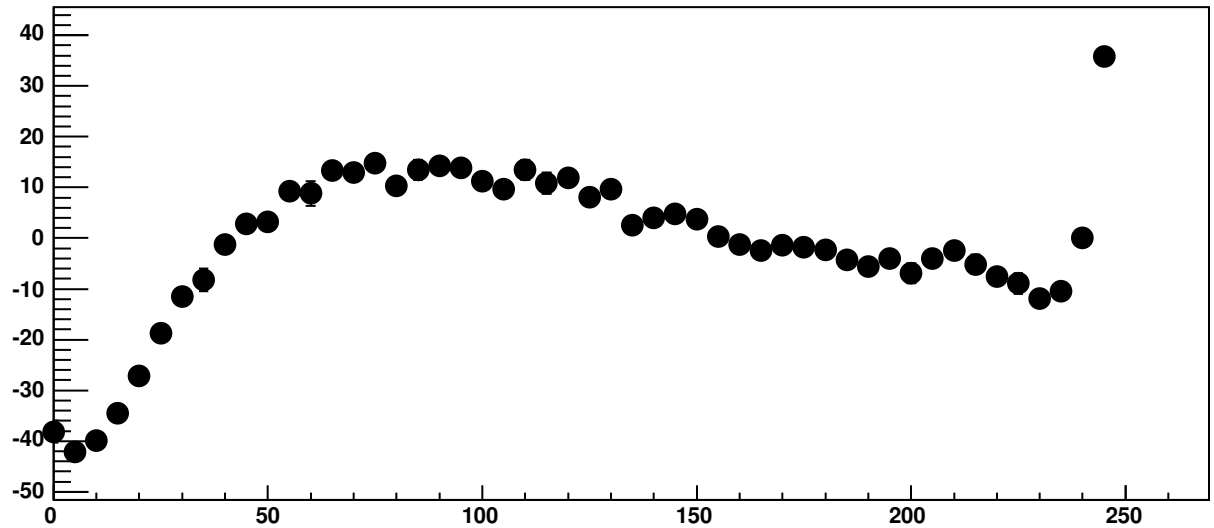
Chip 2, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

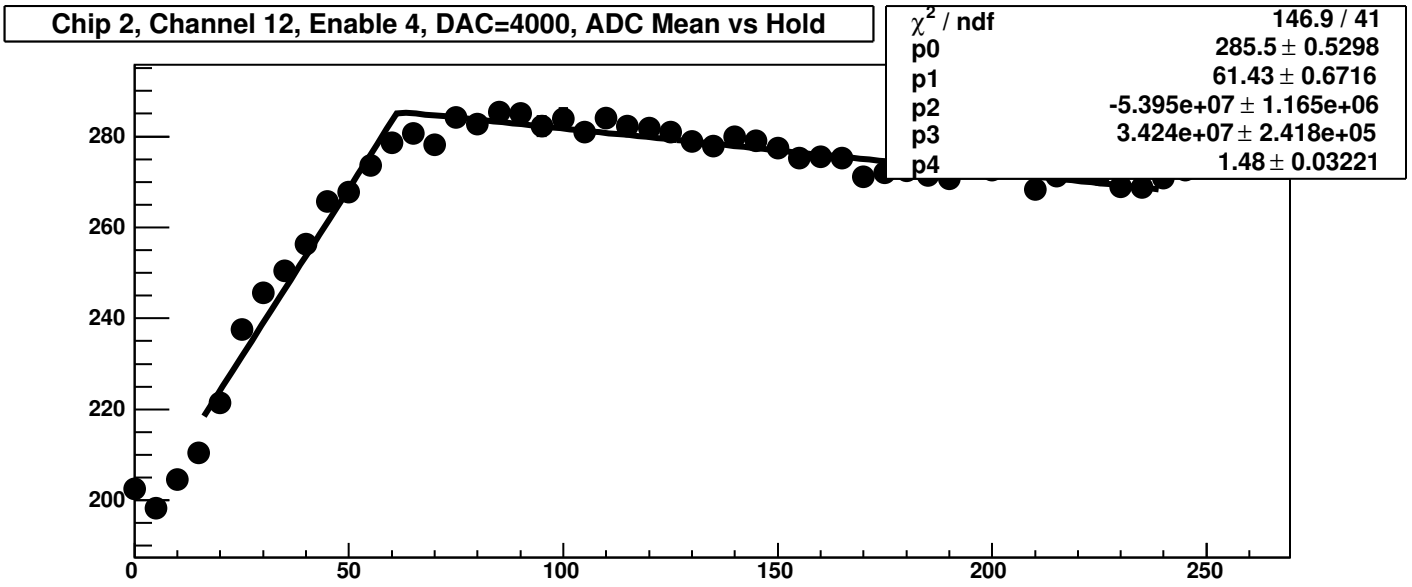


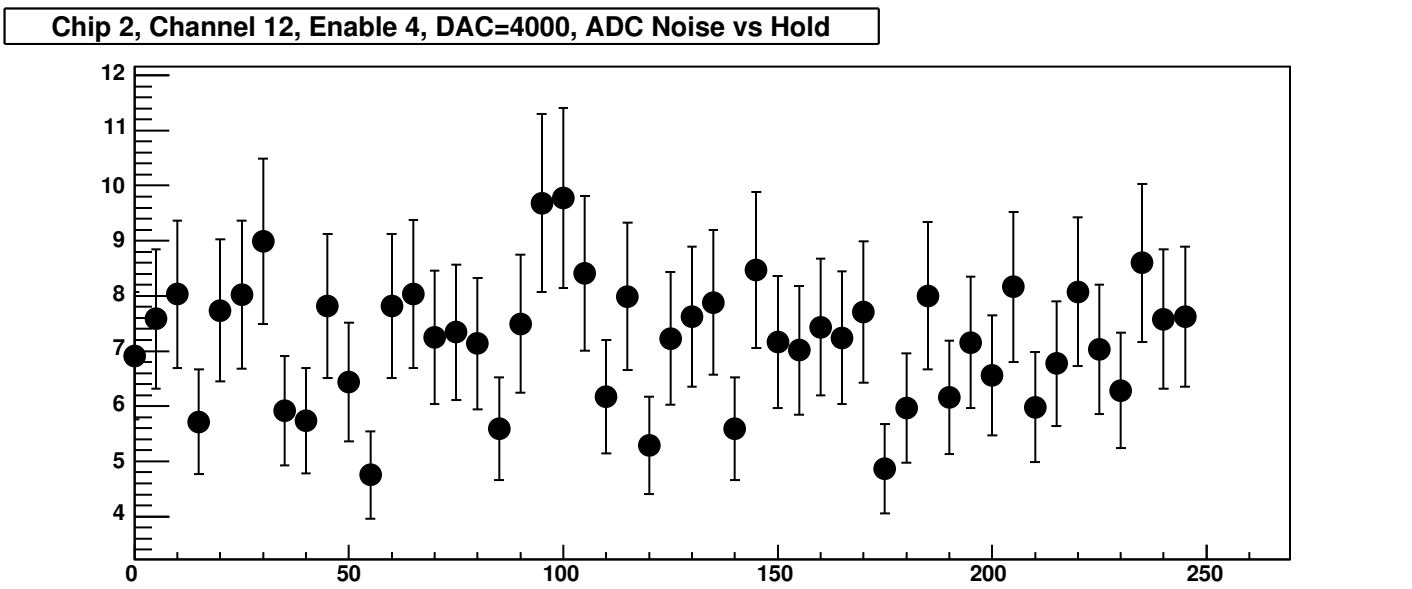
Chip 2, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold

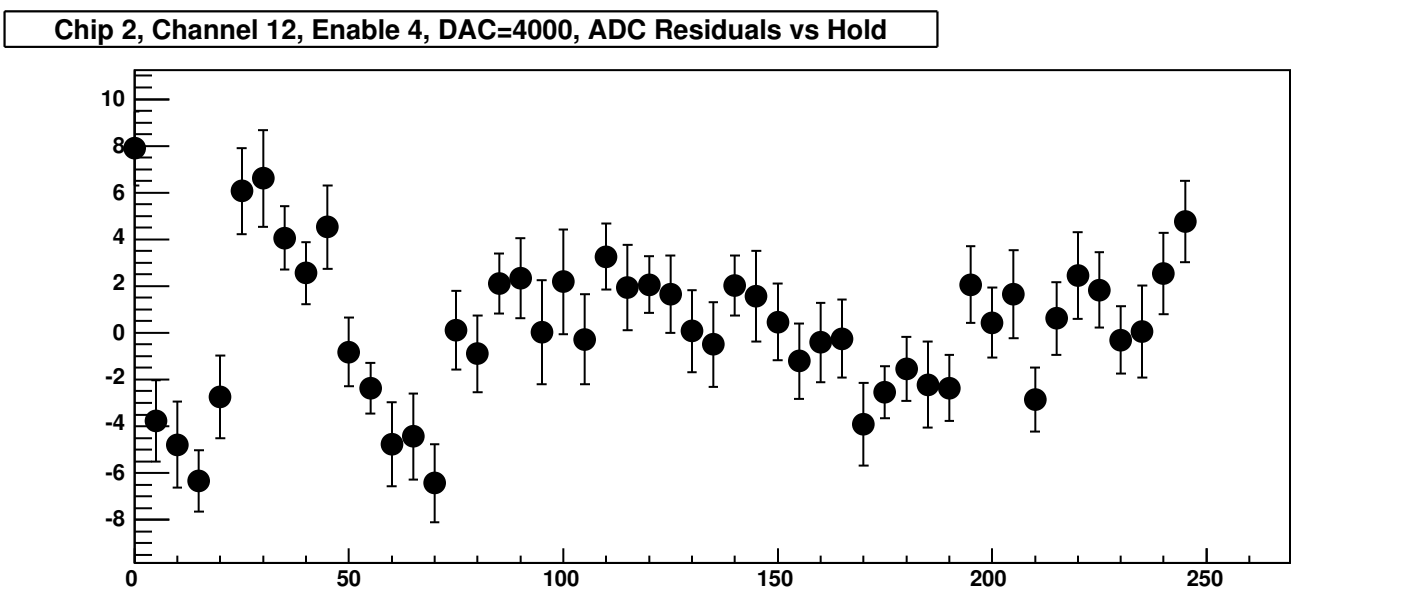


Chip 2, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

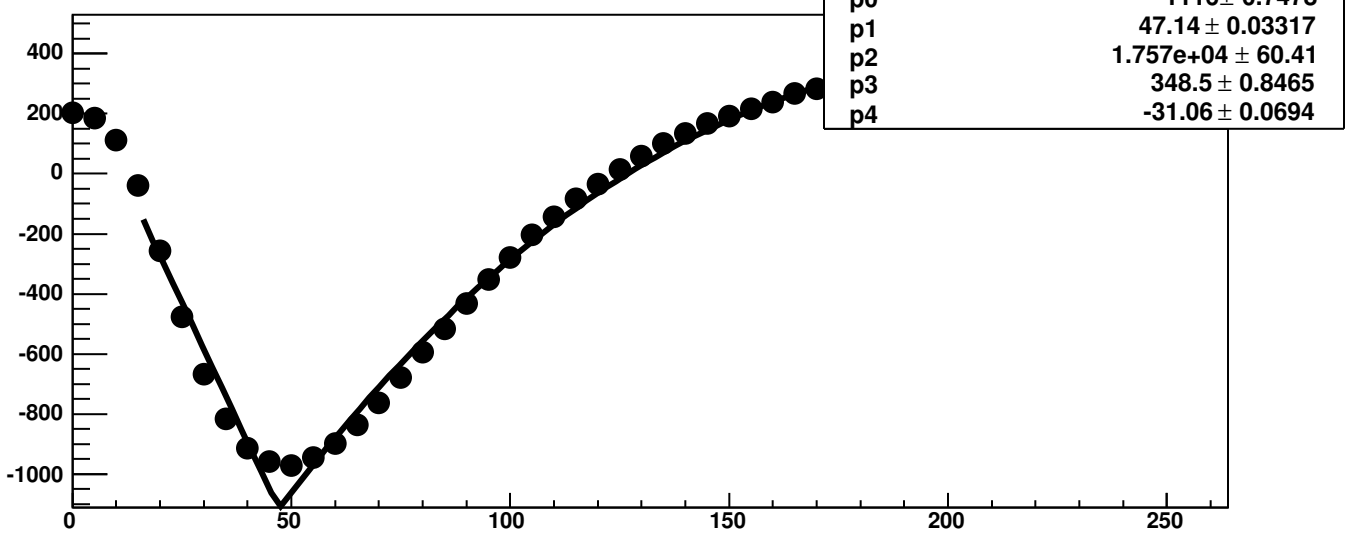



Chip 2, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 146.9 / 41
p0 285.5 ± 0.5298
p1 61.43 ± 0.6716
p2 -5.395e+07 ± 1.165e+06
p3 3.424e+07 ± 2.418e+05
p4 1.48 ± 0.03221
280
260
240
220
200
0
50
100
150
200
250

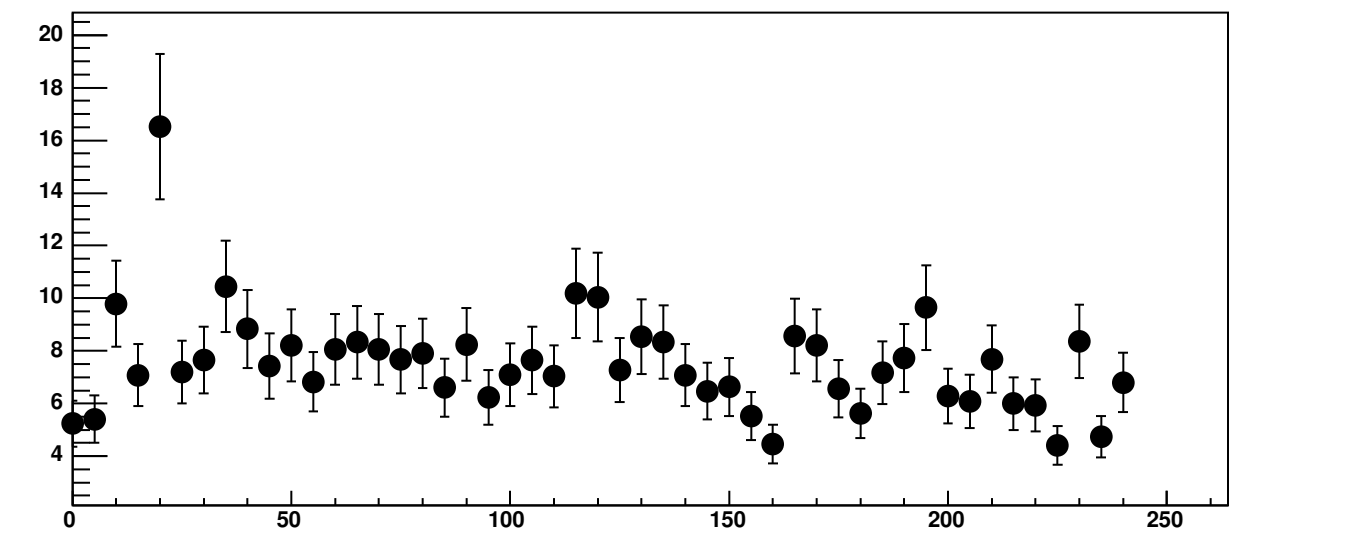

Chip 2, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 2, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold
10
8
6
4
2
0
-2
-4
-6
-8
0
50
100
150
200
250

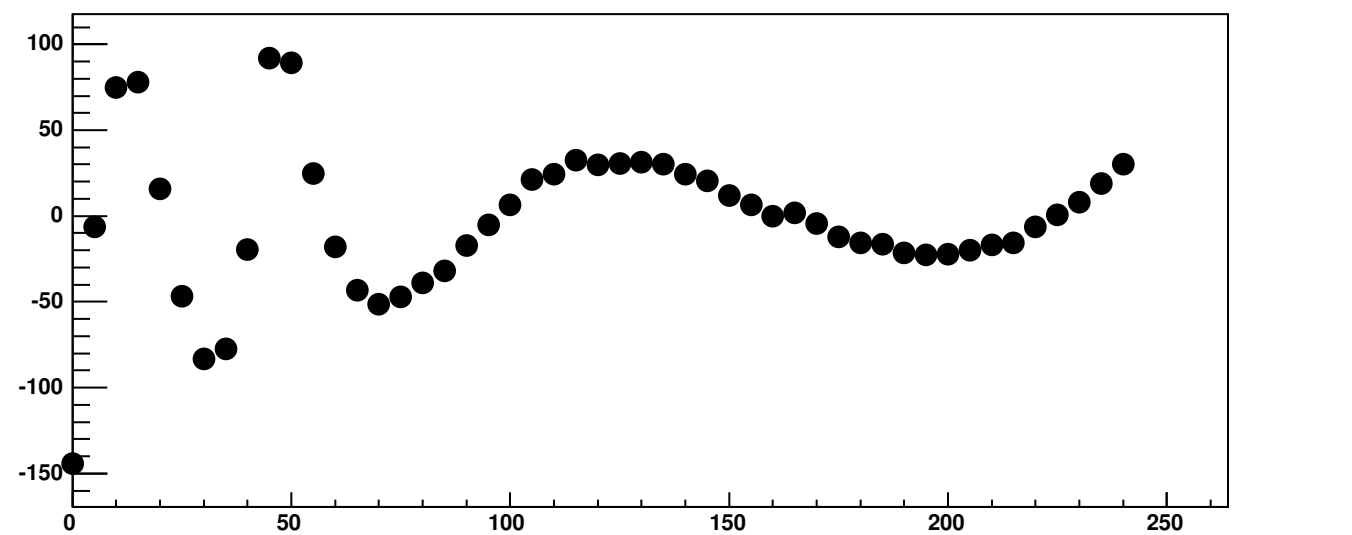
Chip 2, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold

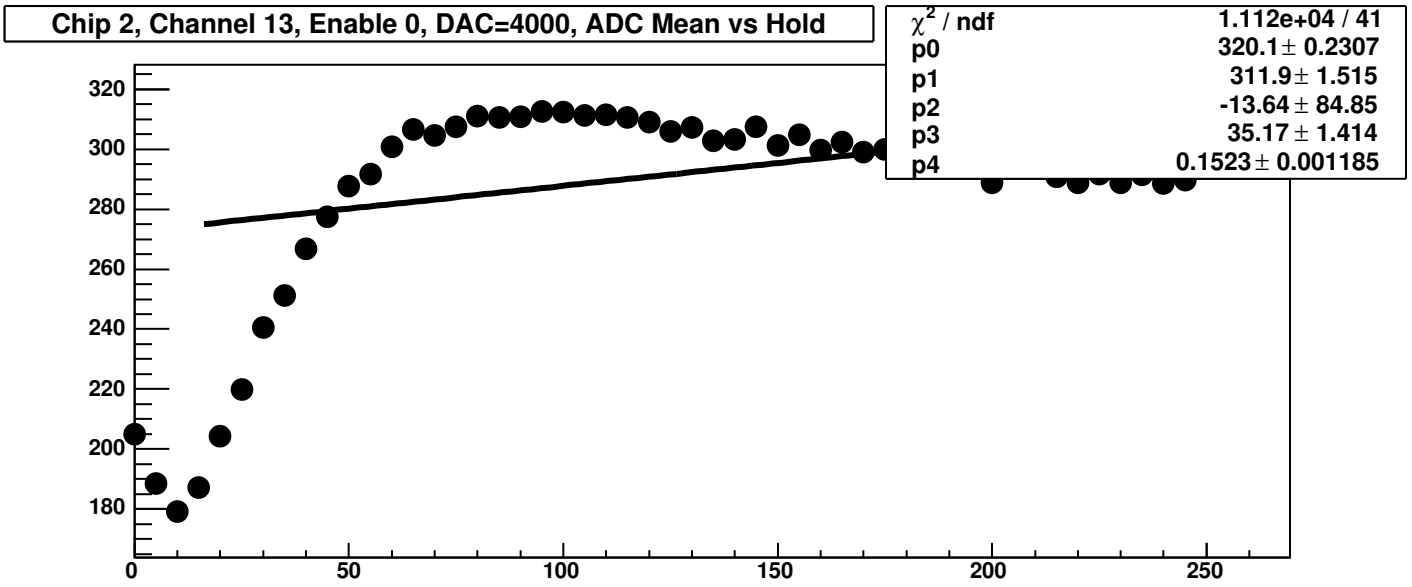


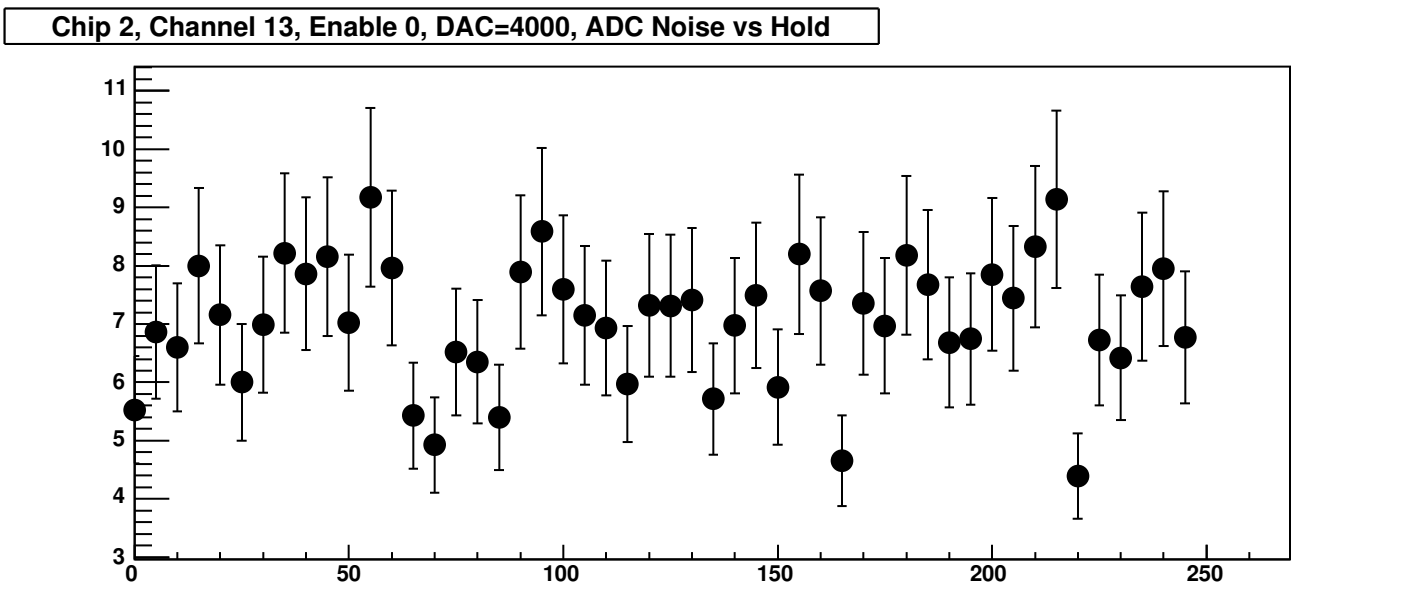
Chip 2, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold

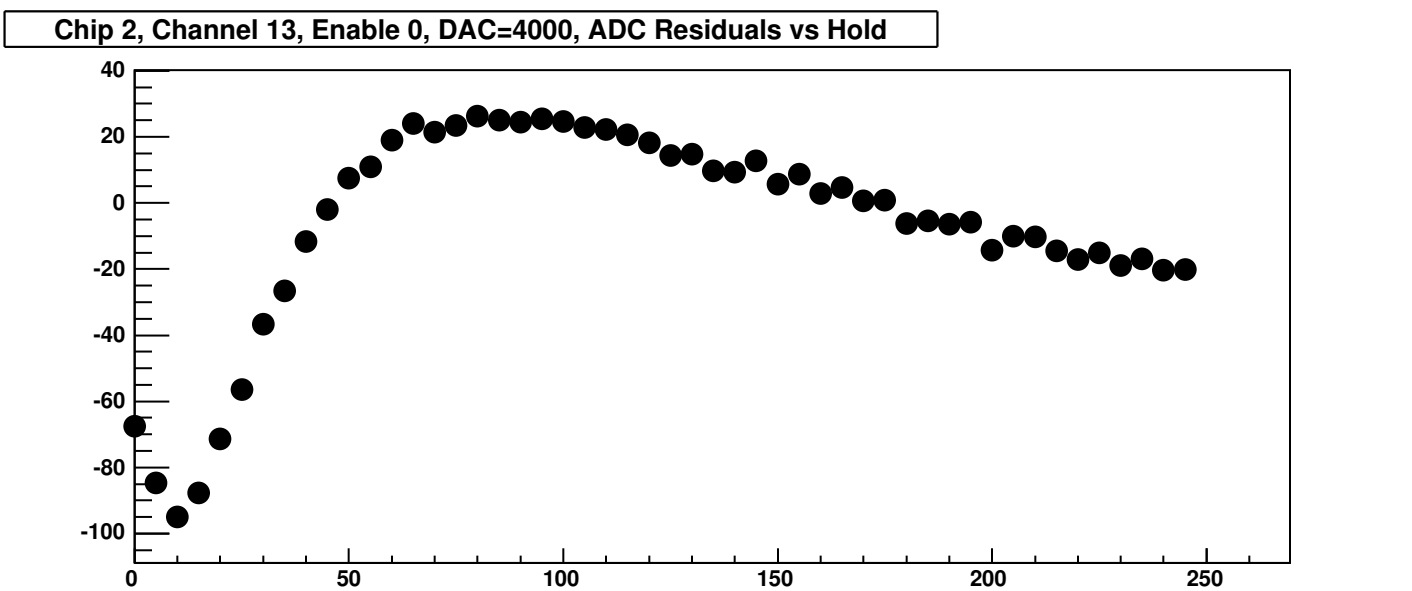


Chip 2, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold

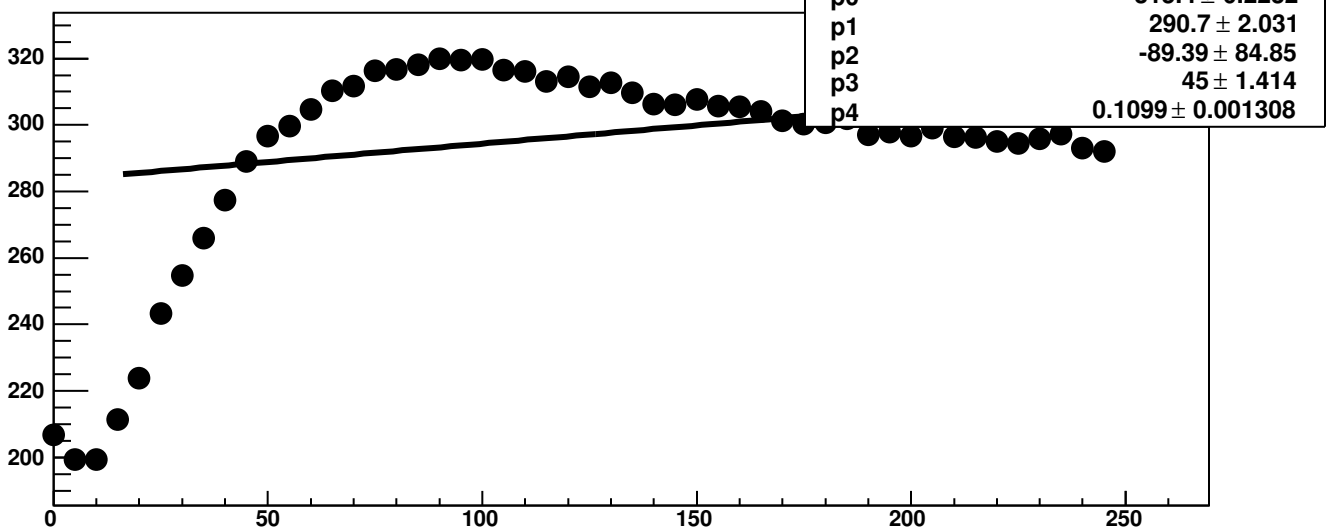



Chip 2, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.112e+04 / 41
p0 320.1 ± 0.2307
p1 311.9 ± 1.515
p2 -13.64 ± 84.85
p3 35.17 ± 1.414
p4 0.1523 ± 0.001185
320
300
280
260
240
220
200
180
0
50
100
150
200
250

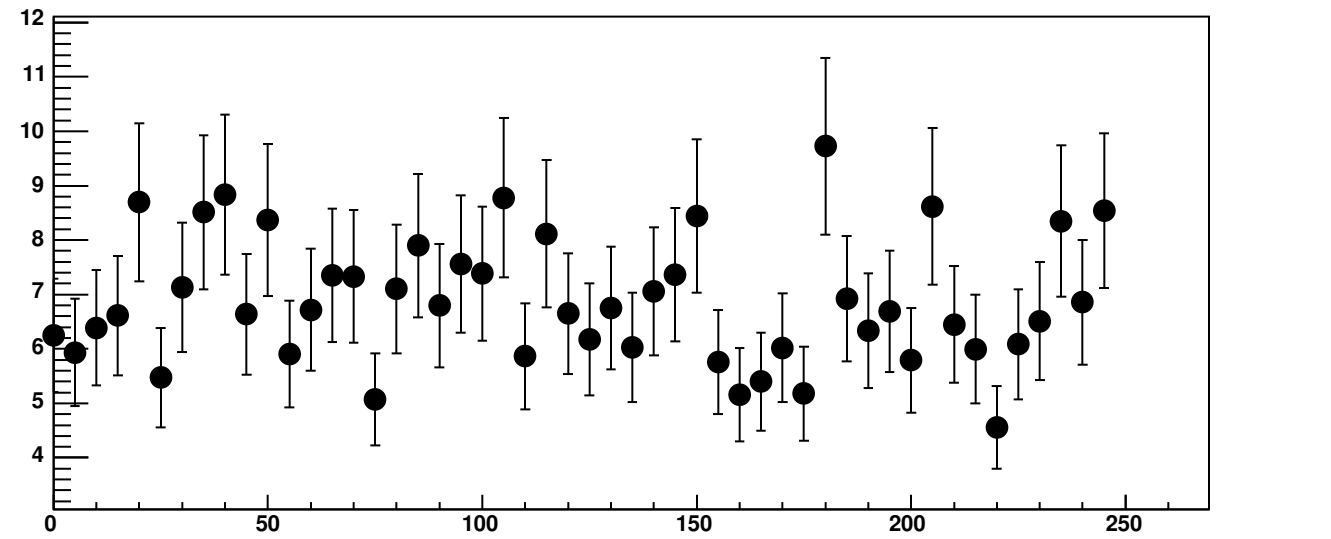

Chip 2, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 2, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
-100
0
50
100
150
200
250

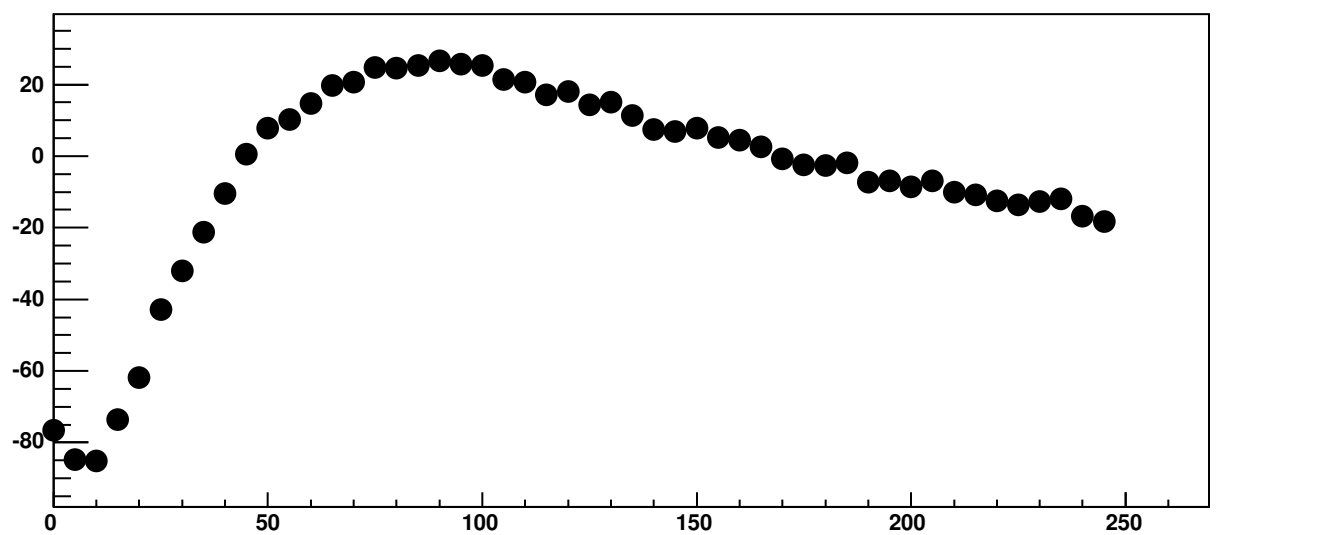
Chip 2, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold



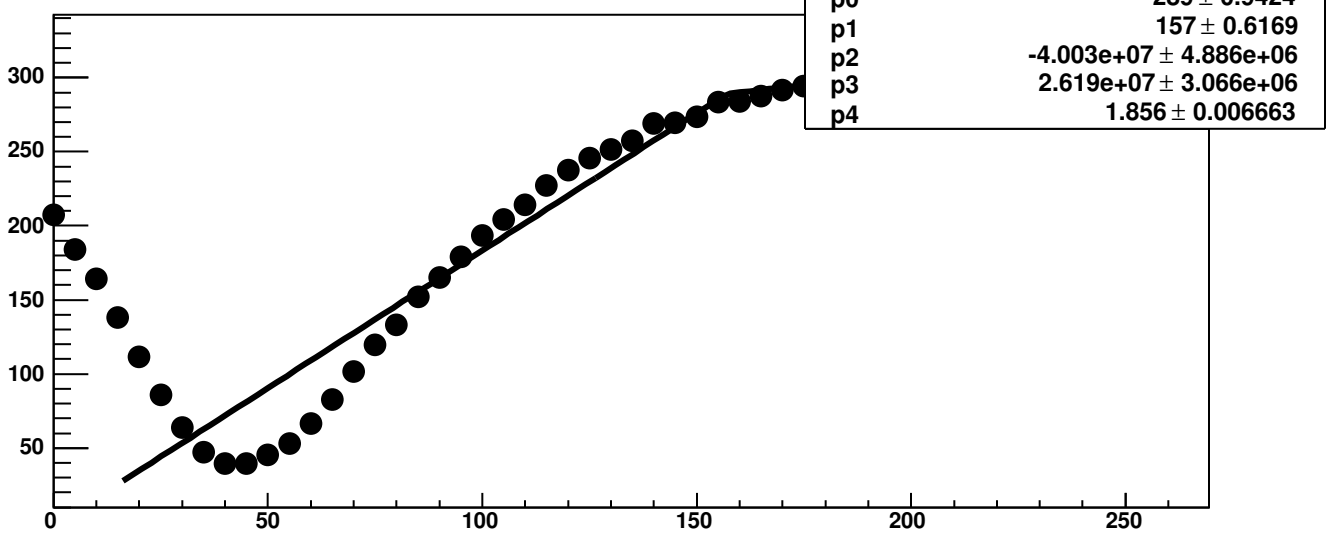
Chip 2, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold



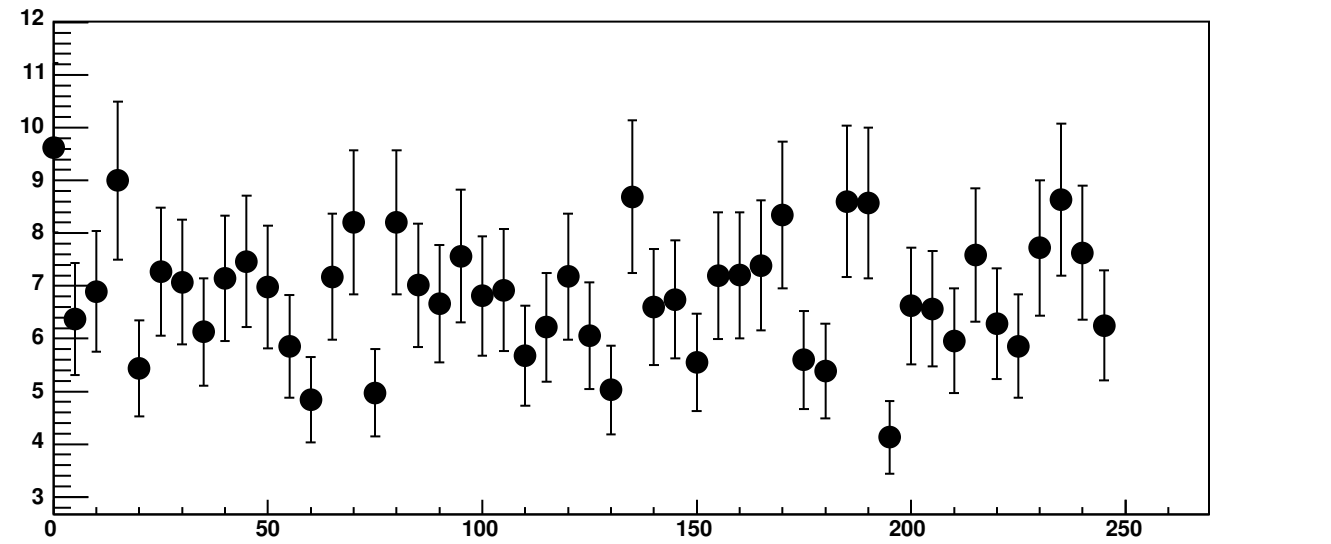
Chip 2, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold



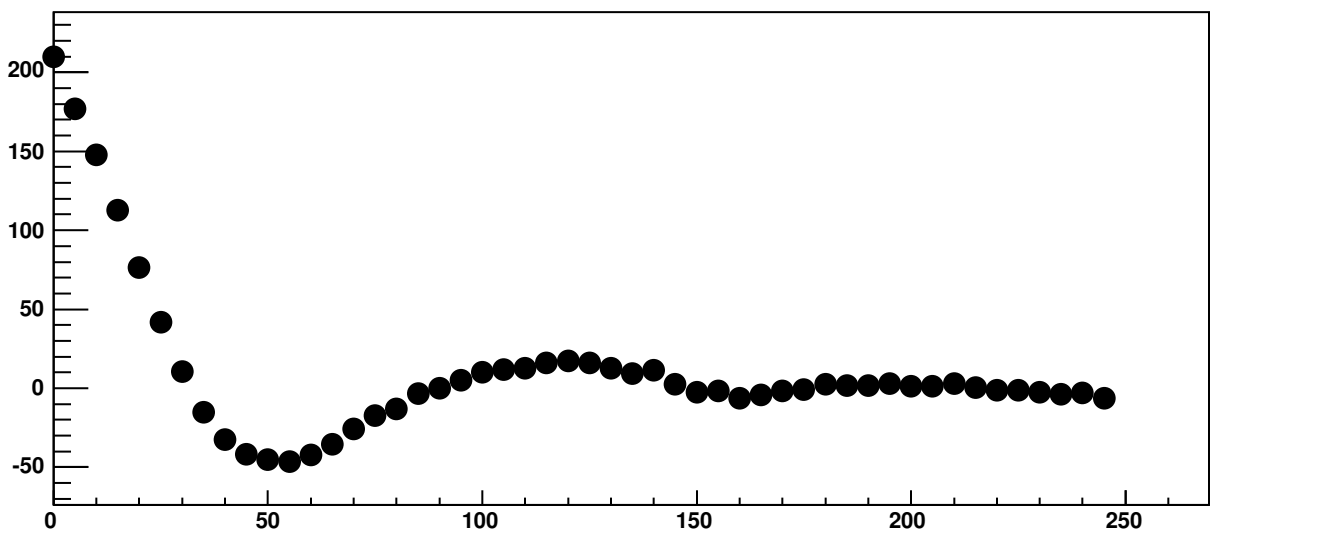
Chip 2, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold



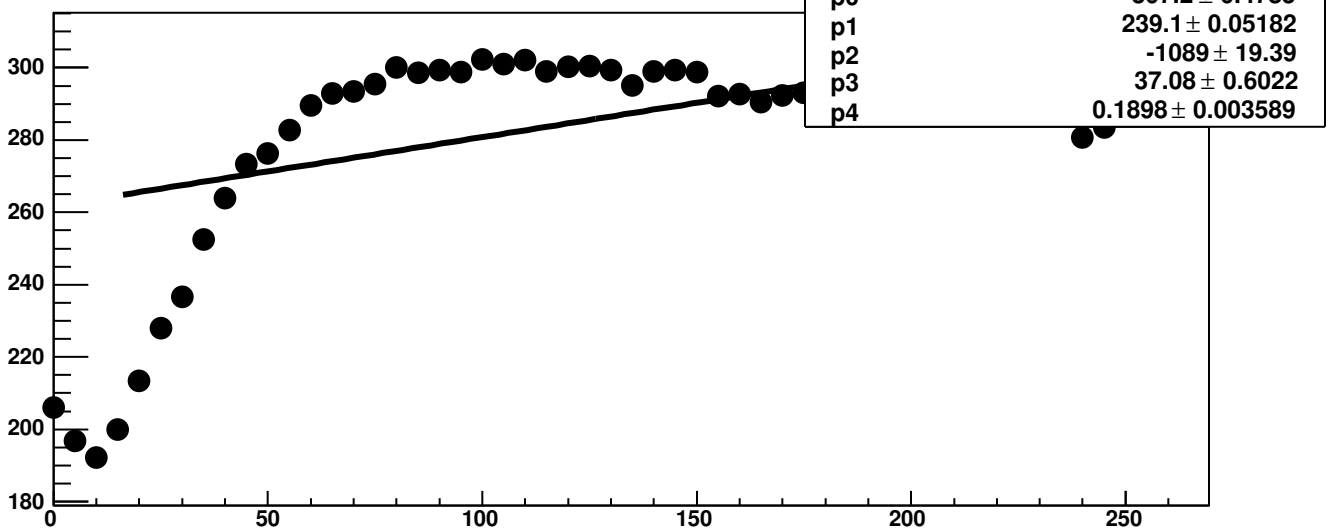
Chip 2, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



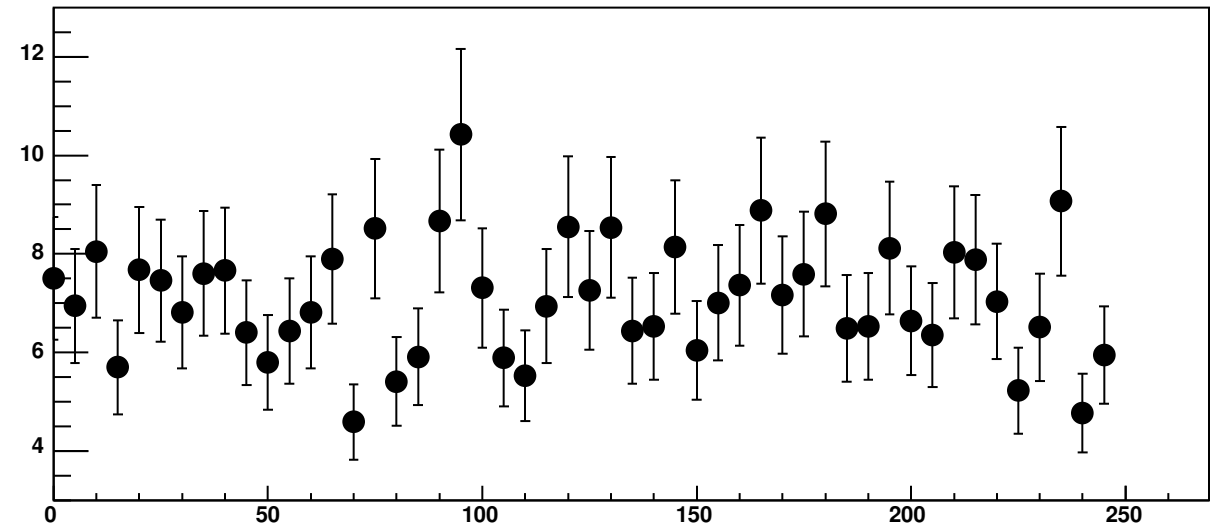
Chip 2, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold



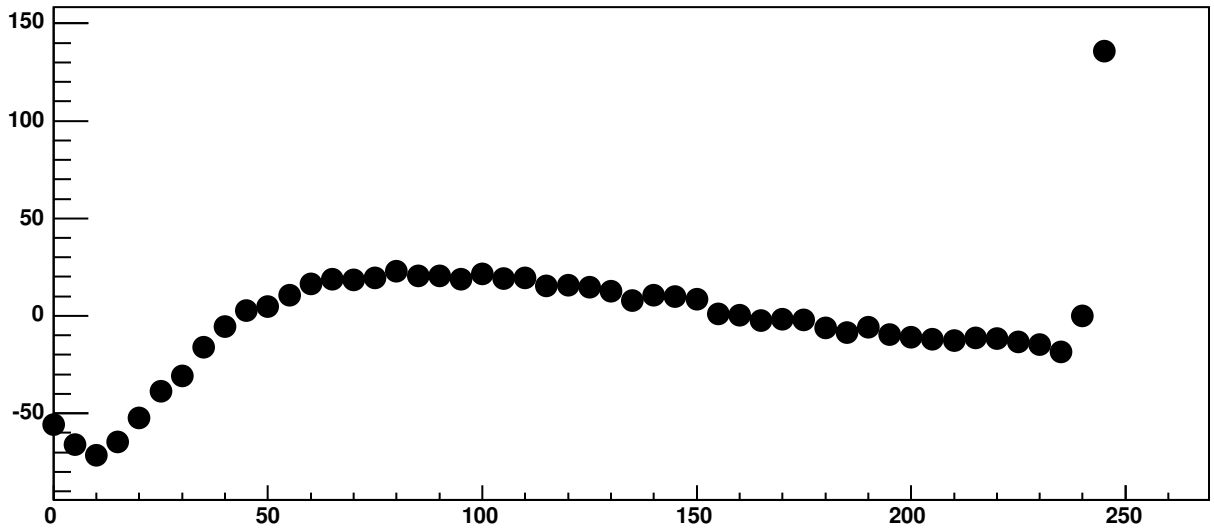
Chip 2, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold



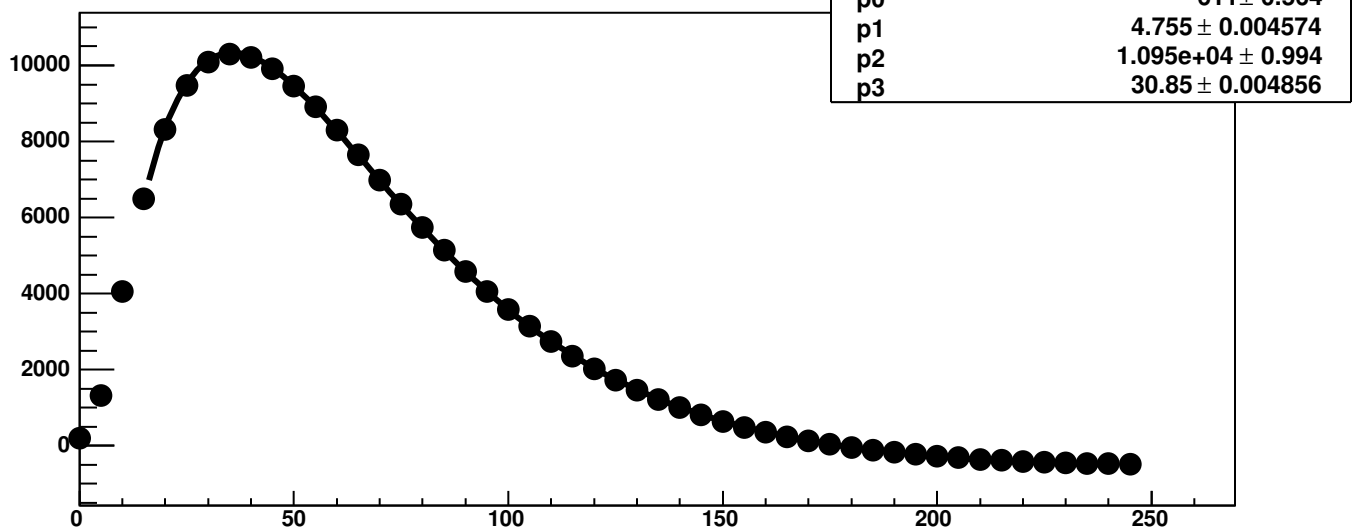
Chip 2, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



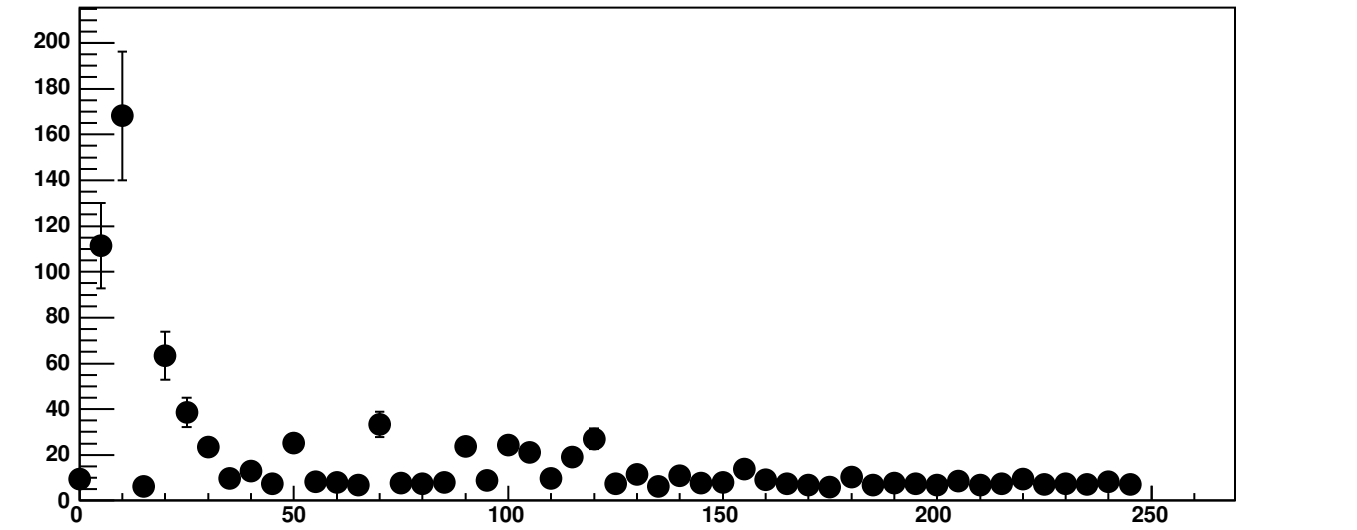
Chip 2, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold



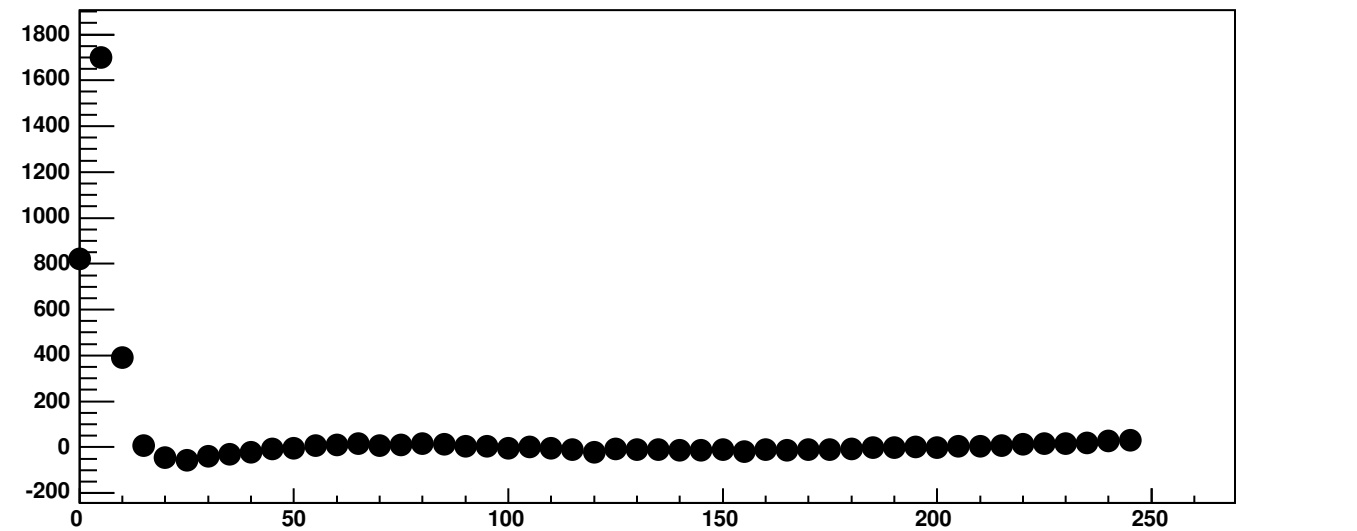
Chip 2, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold



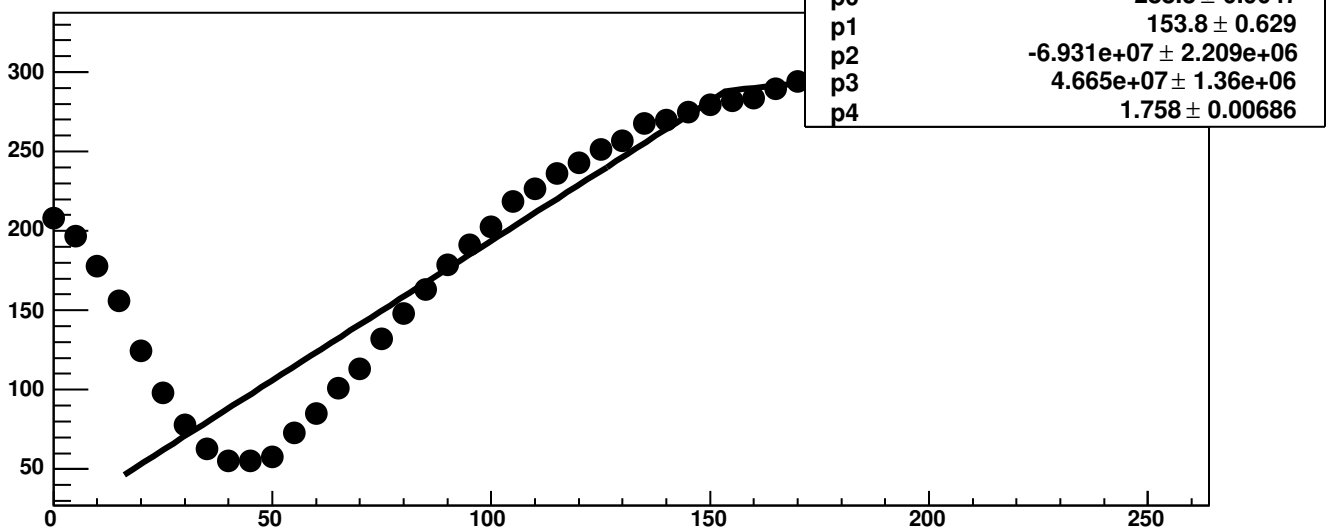
Chip 2, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold



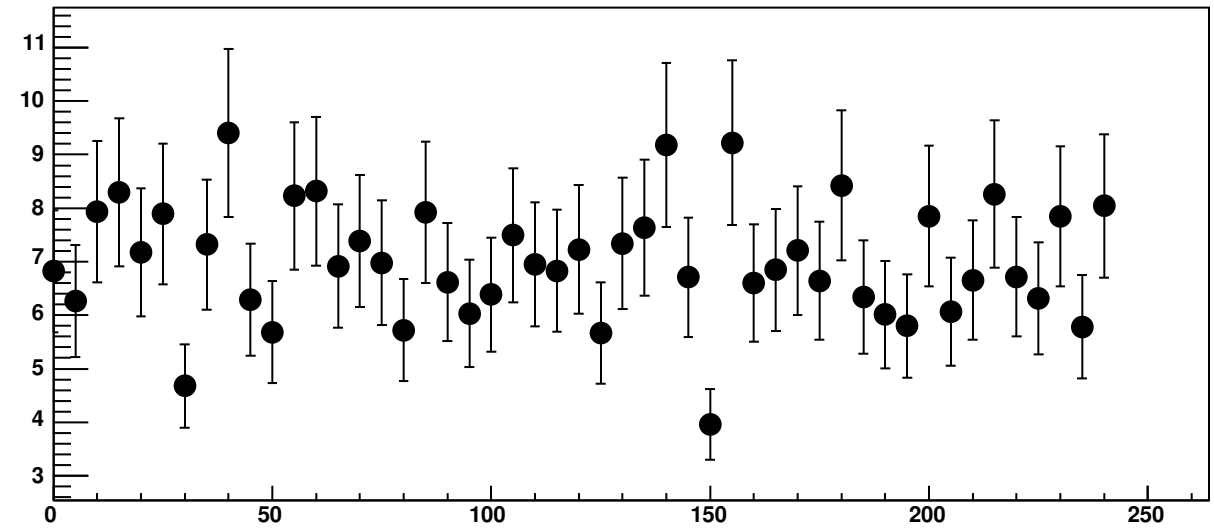
Chip 2, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold



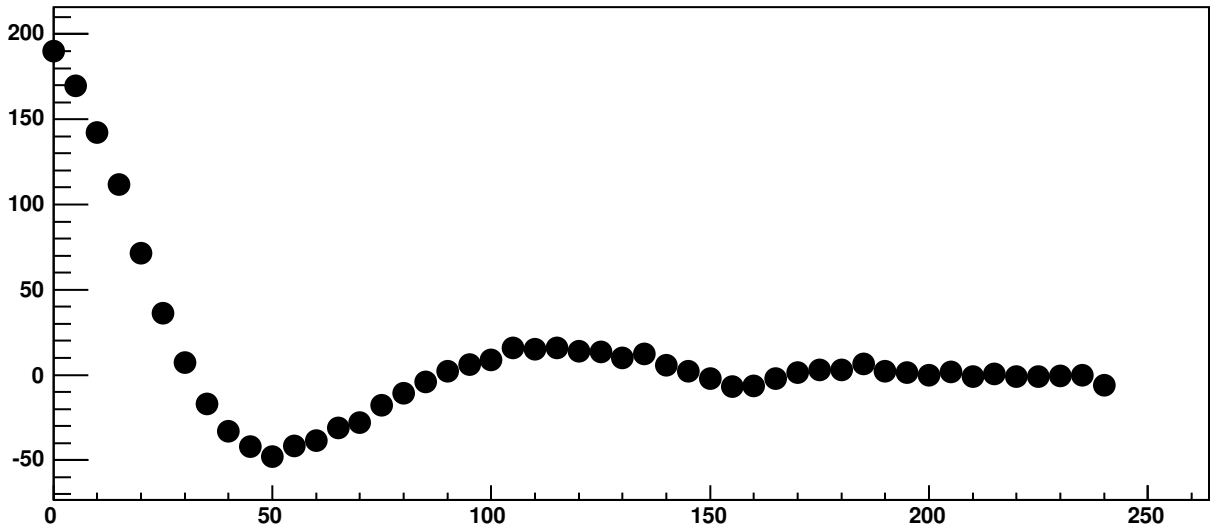
Chip 2, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold



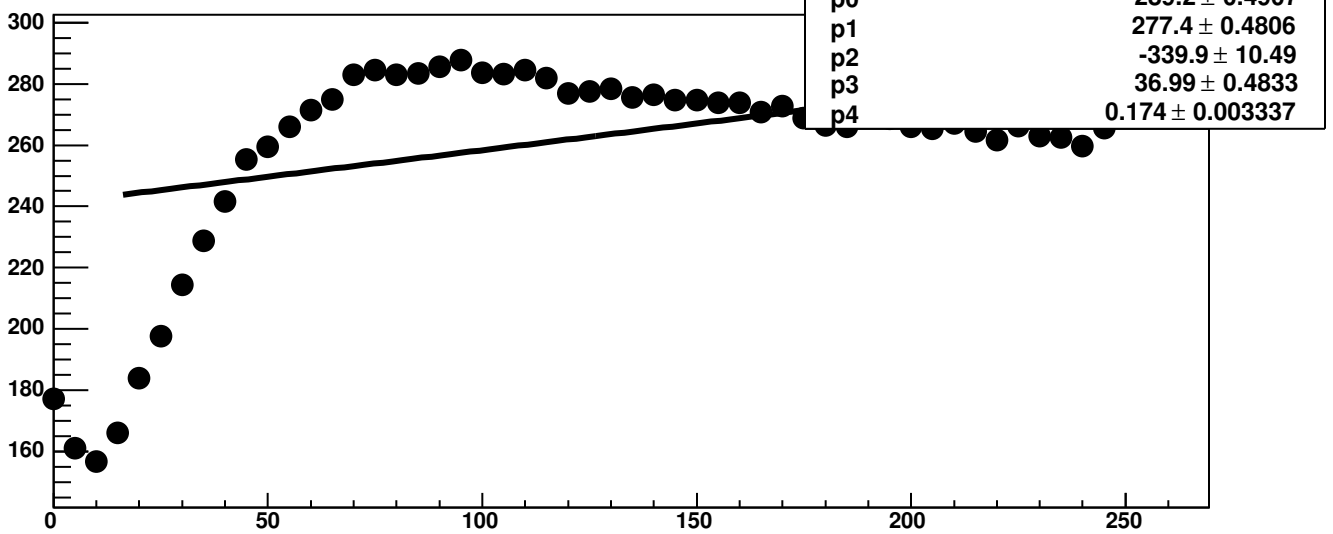
Chip 2, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



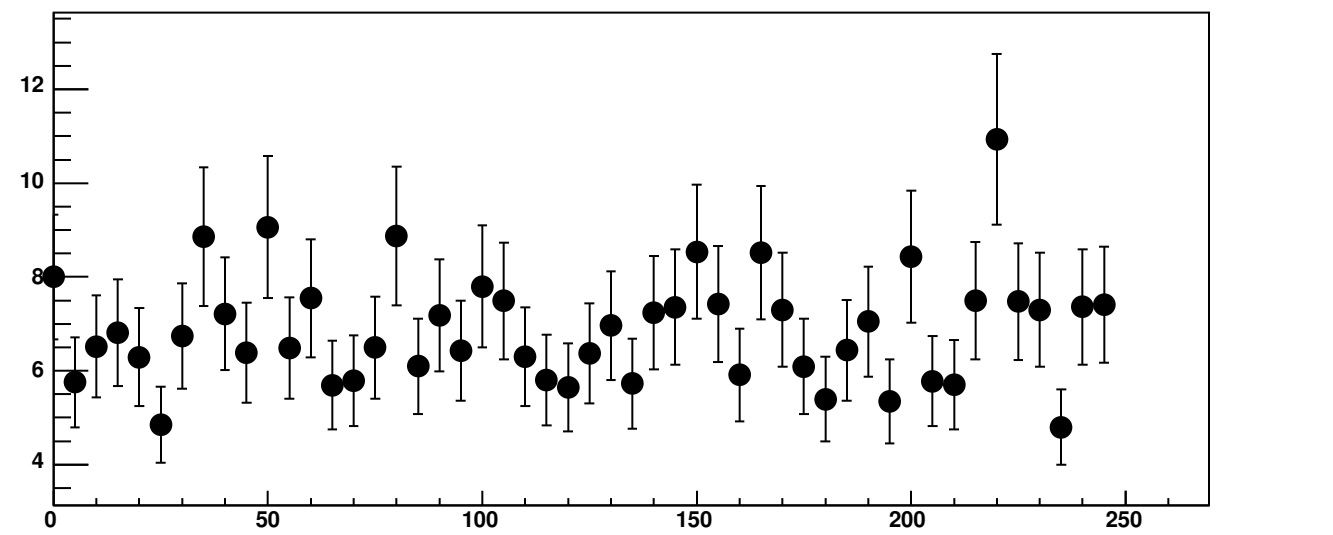
Chip 2, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold



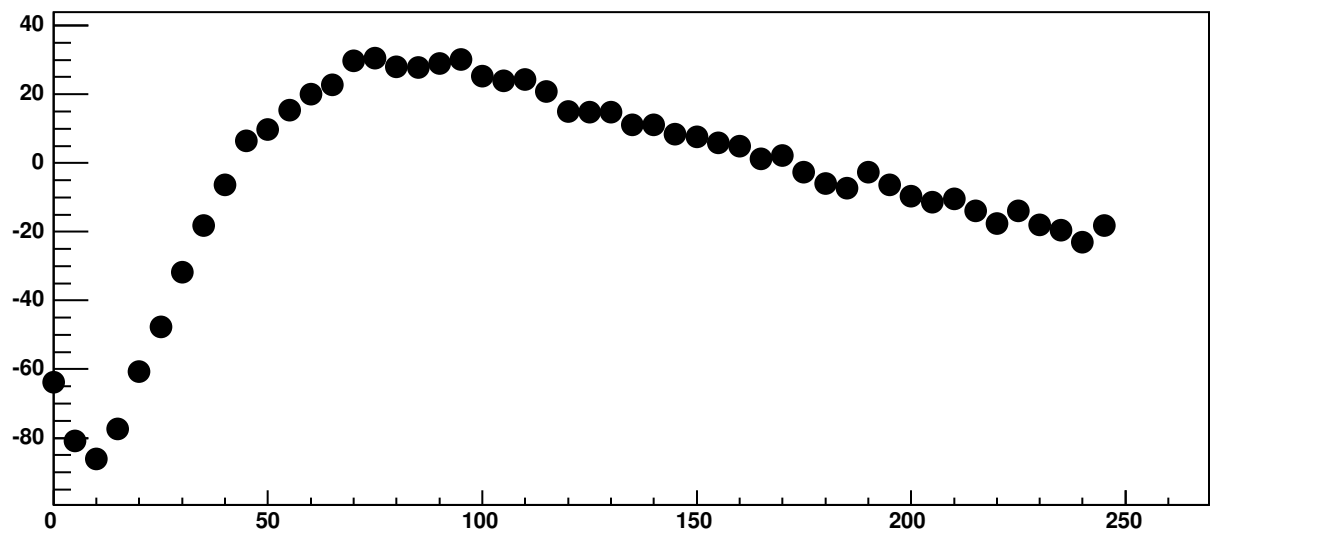
Chip 2, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold

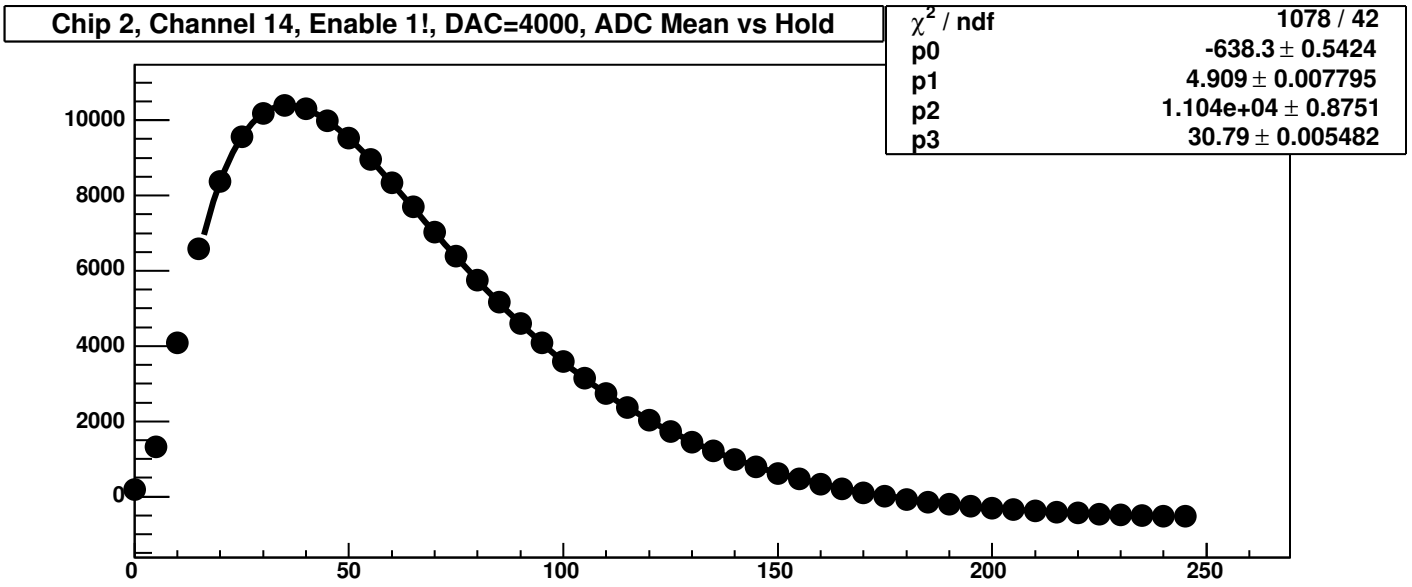


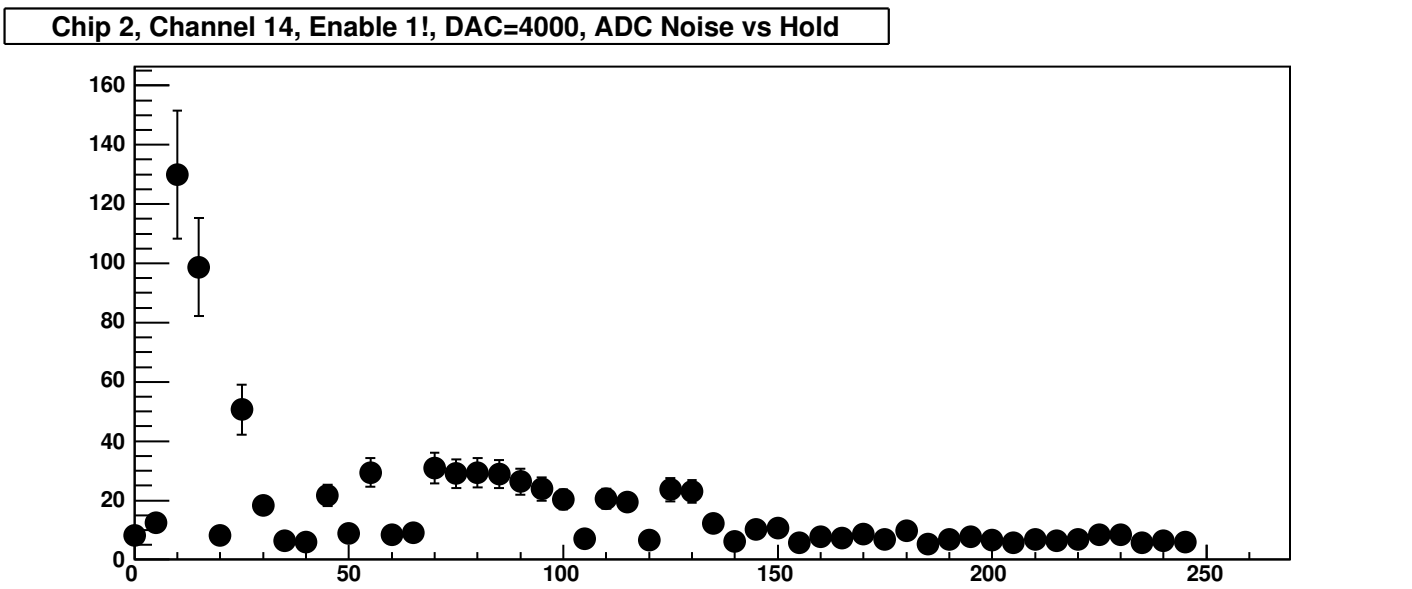
Chip 2, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold

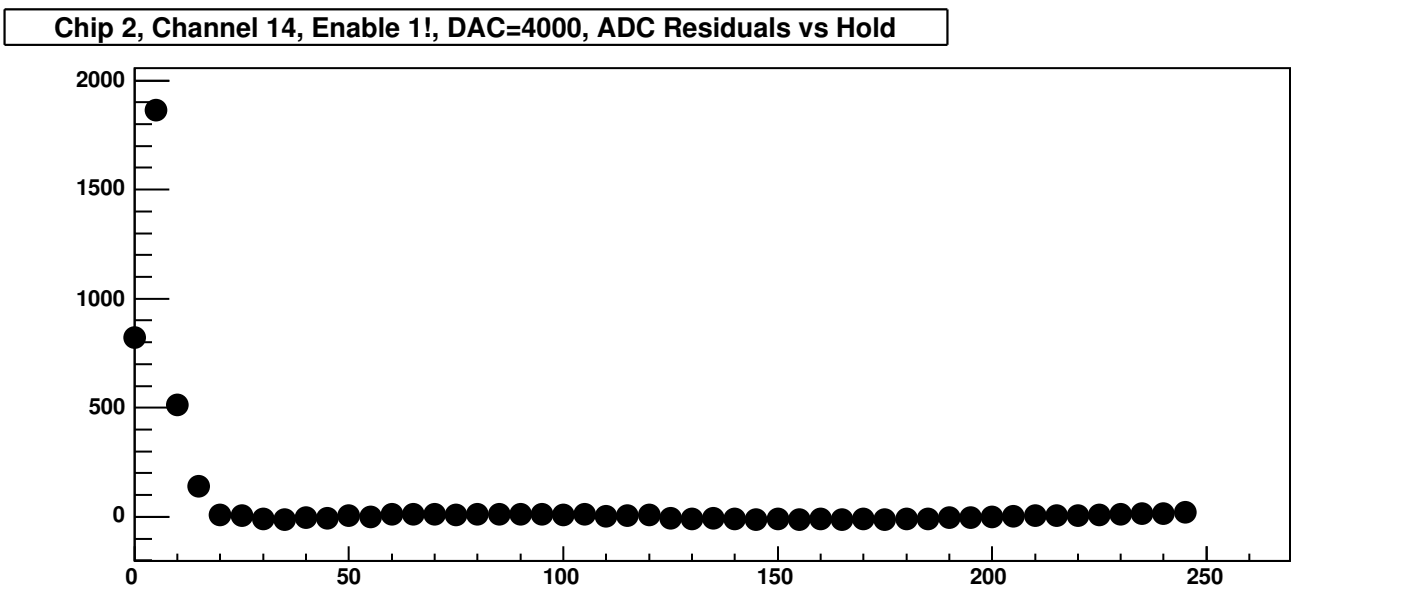


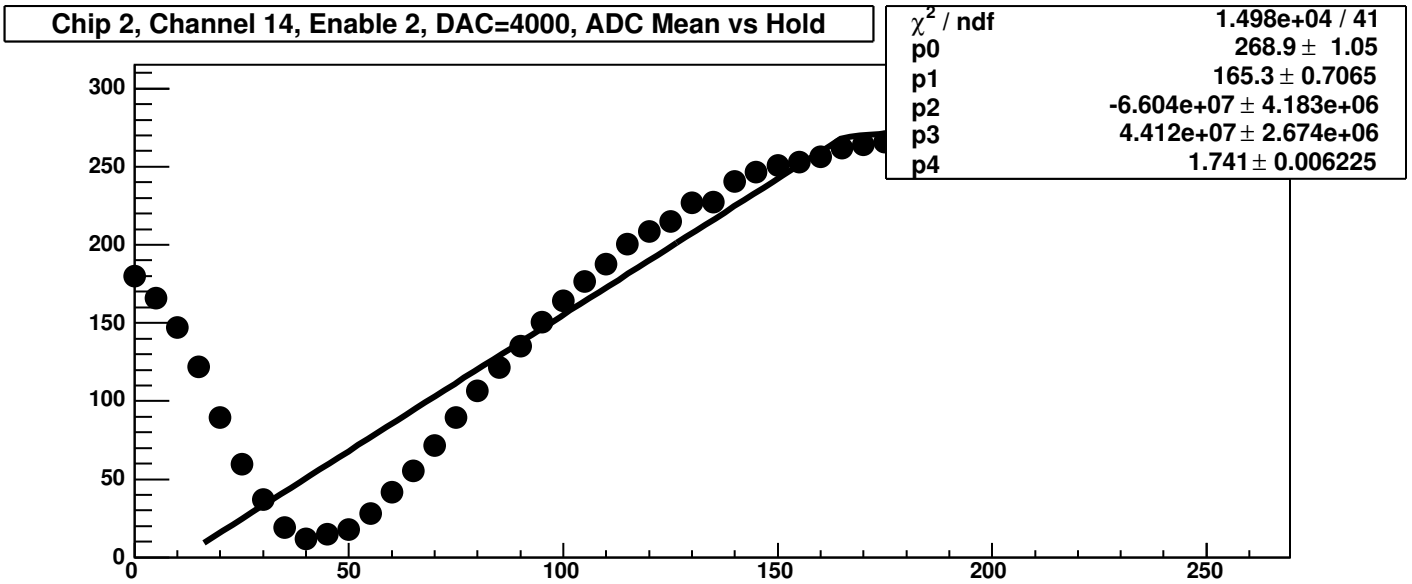
Chip 2, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold

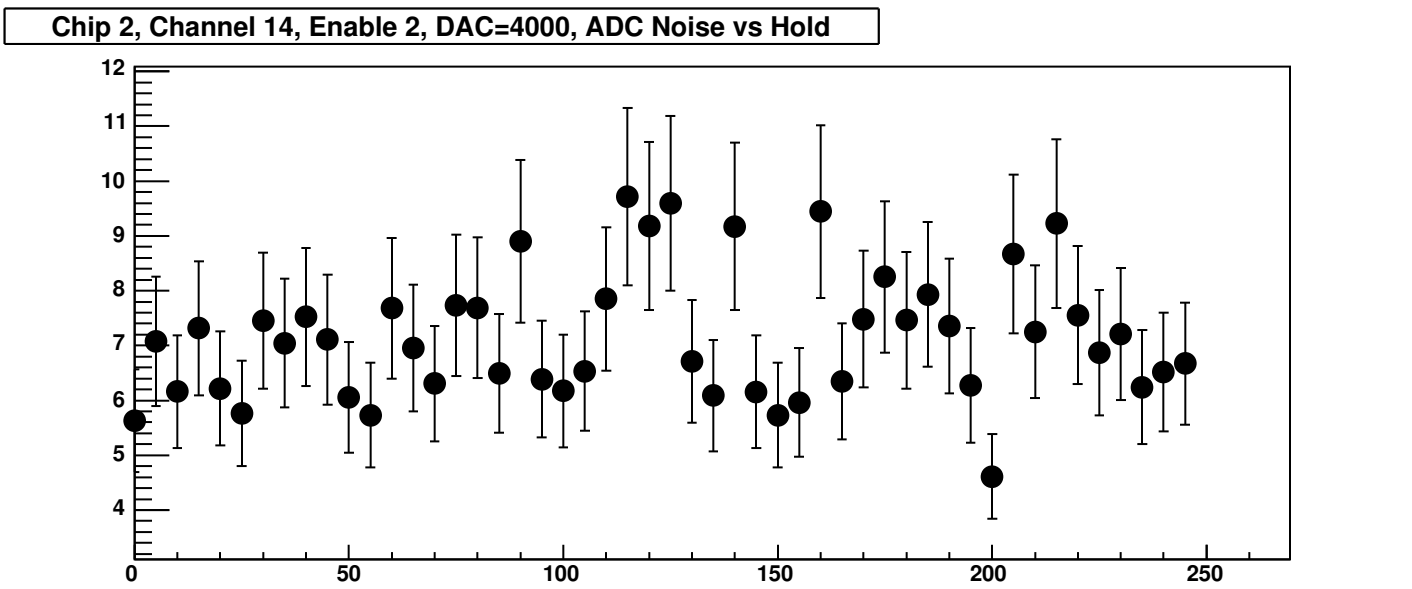


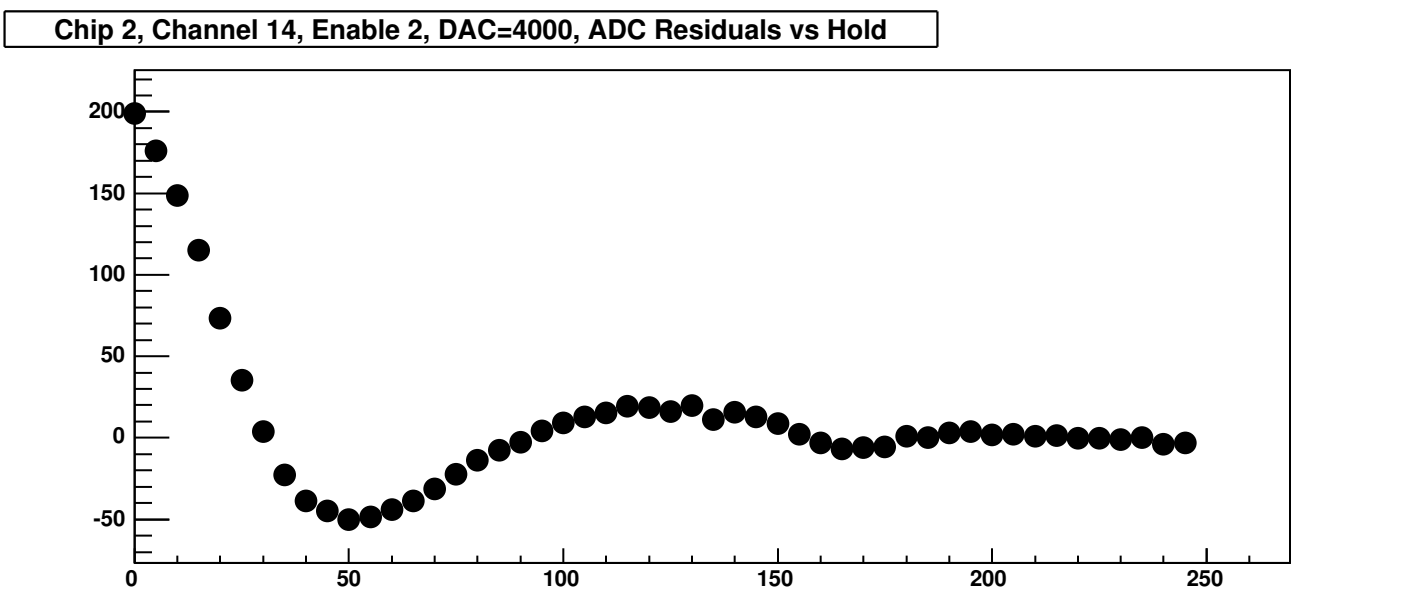

Chip 2, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1078 / 42
p0 -638.3 ± 0.5424
p1 4.909 ± 0.007795
p2 1.104e+04 ± 0.8751
p3 30.79 ± 0.005482
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 2, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

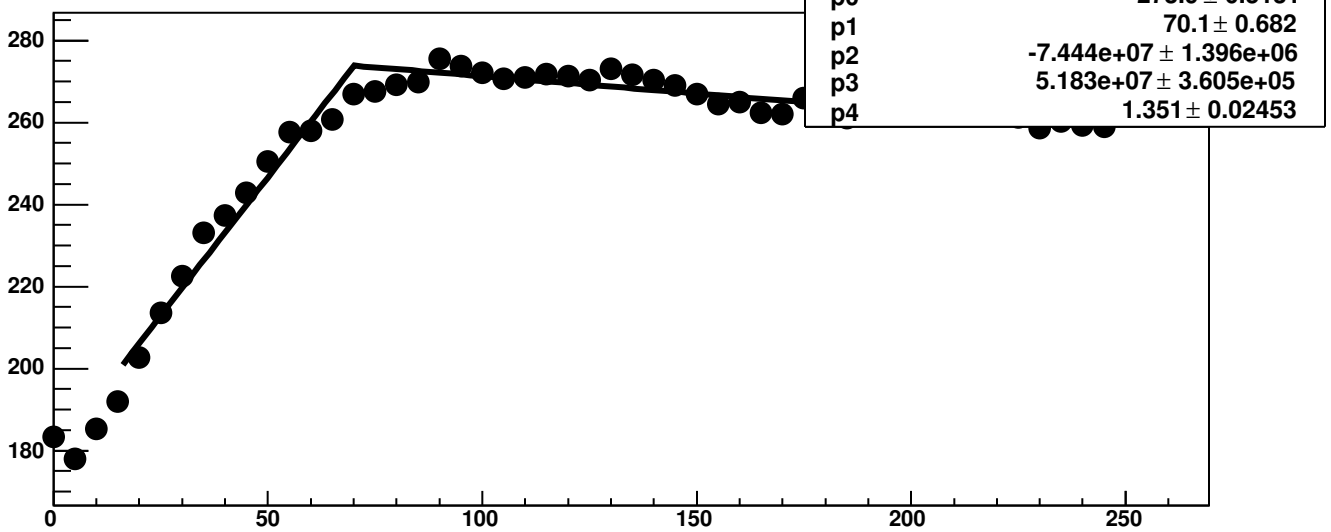

Chip 2, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 2, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.498e+04 / 41
p0 268.9 ± 1.05
p1 165.3 ± 0.7065
p2 -6.604e+07 ± 4.183e+06
p3 4.412e+07 ± 2.674e+06
p4 1.741 ± 0.006225
300
250
200
150
100
50
0
0
50
100
150
200
250

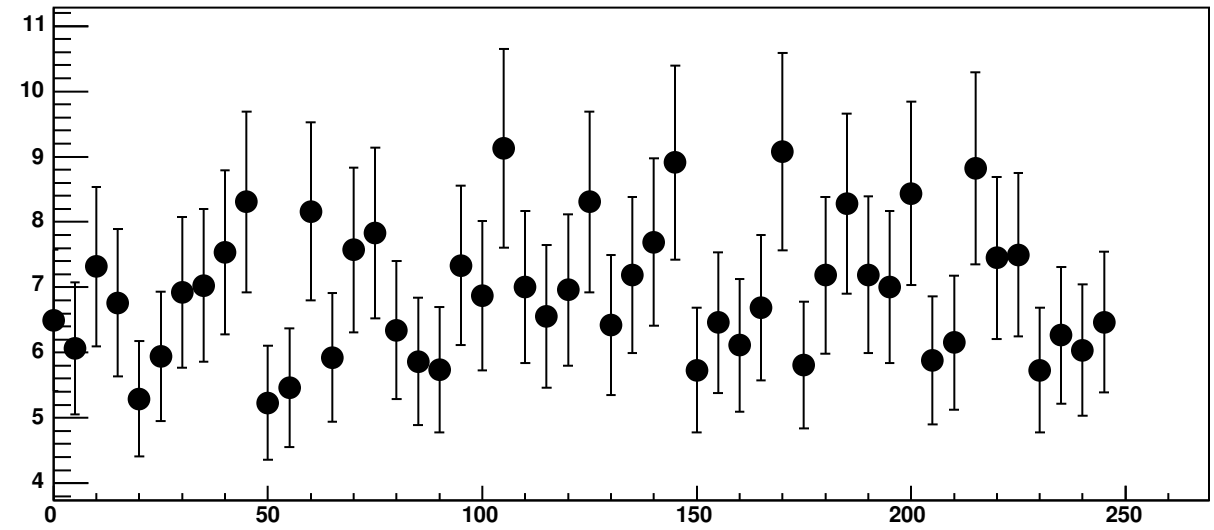

Chip 2, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 2, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold
200
150
100
50
0
-50
0
50
100
150
200
250

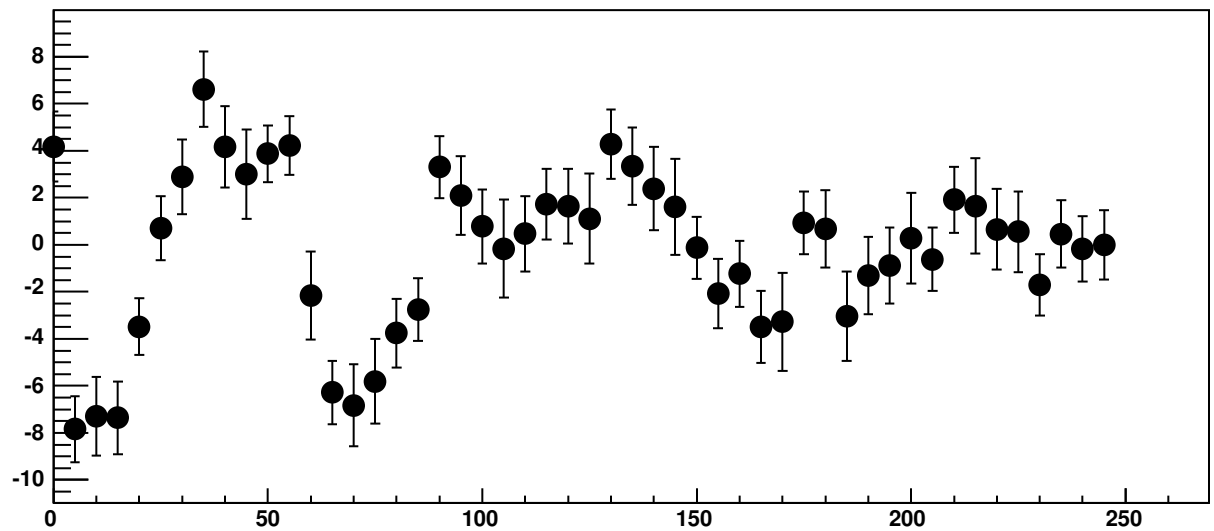
Chip 2, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold



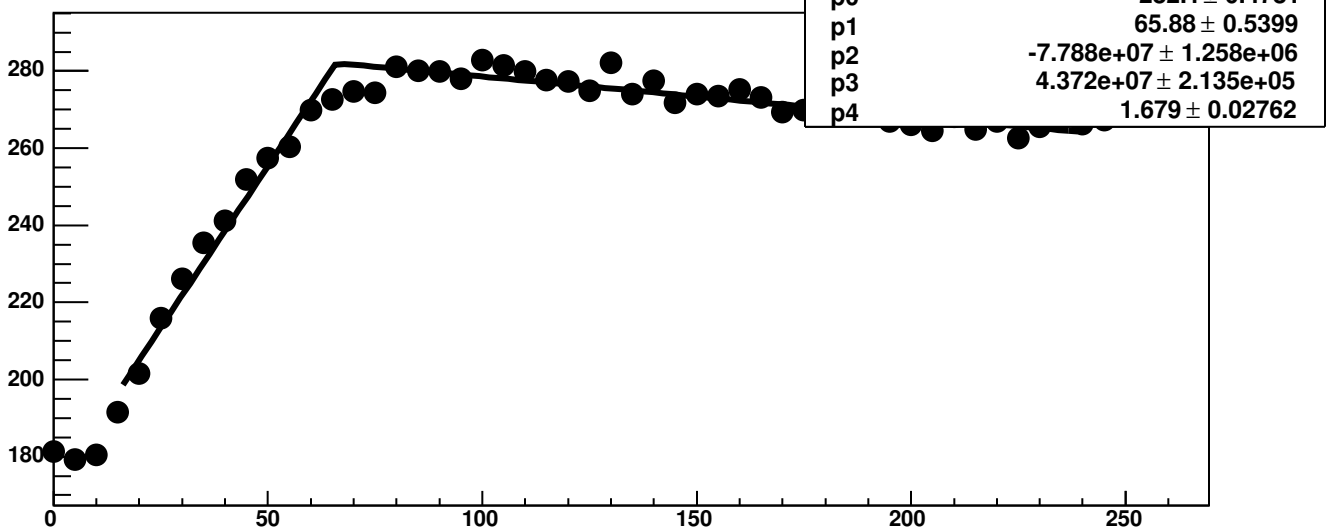
Chip 2, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



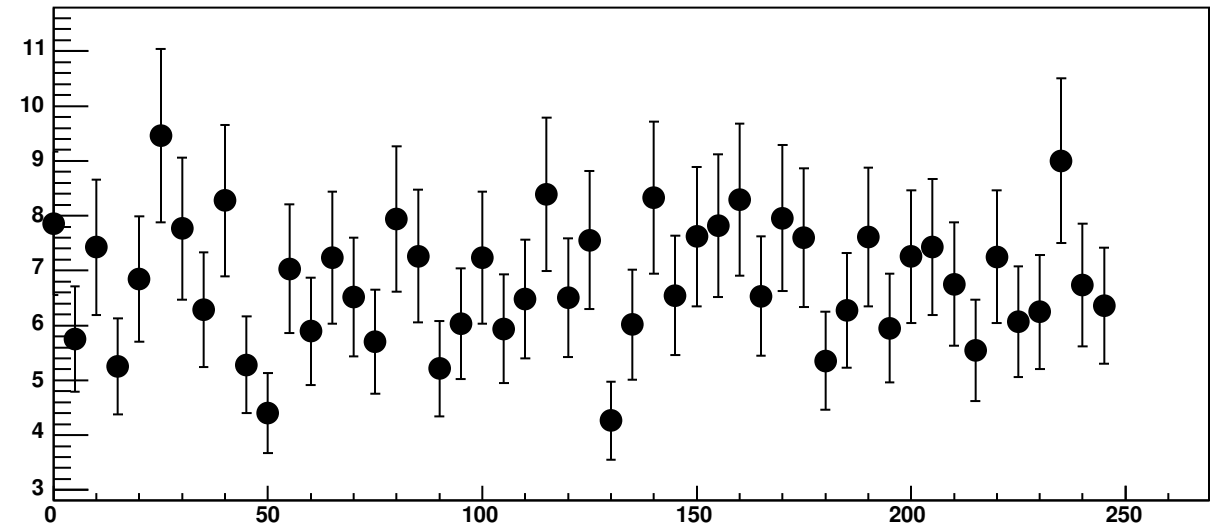
Chip 2, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold



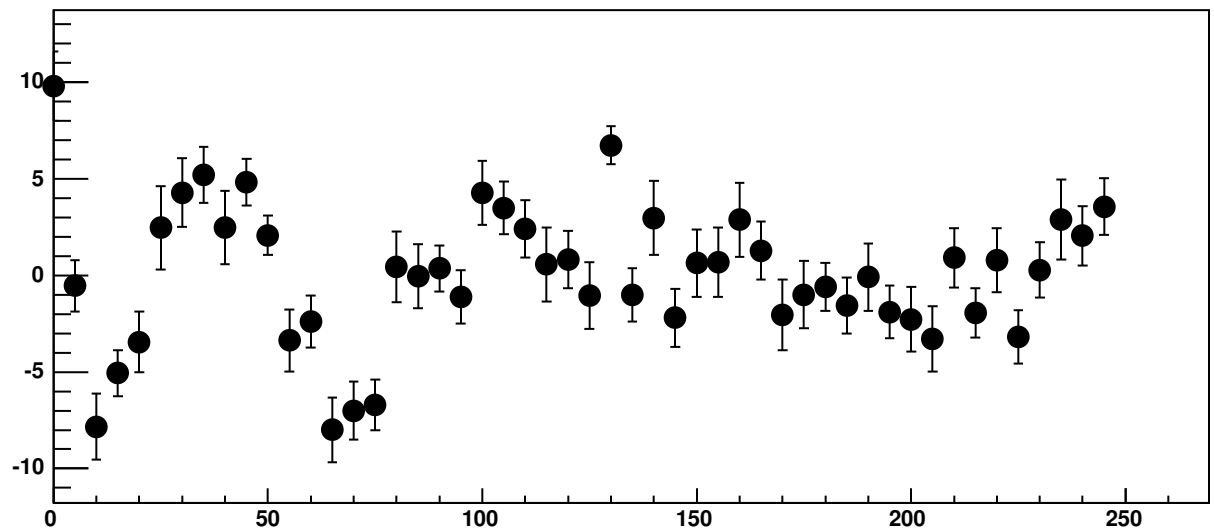
Chip 2, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold



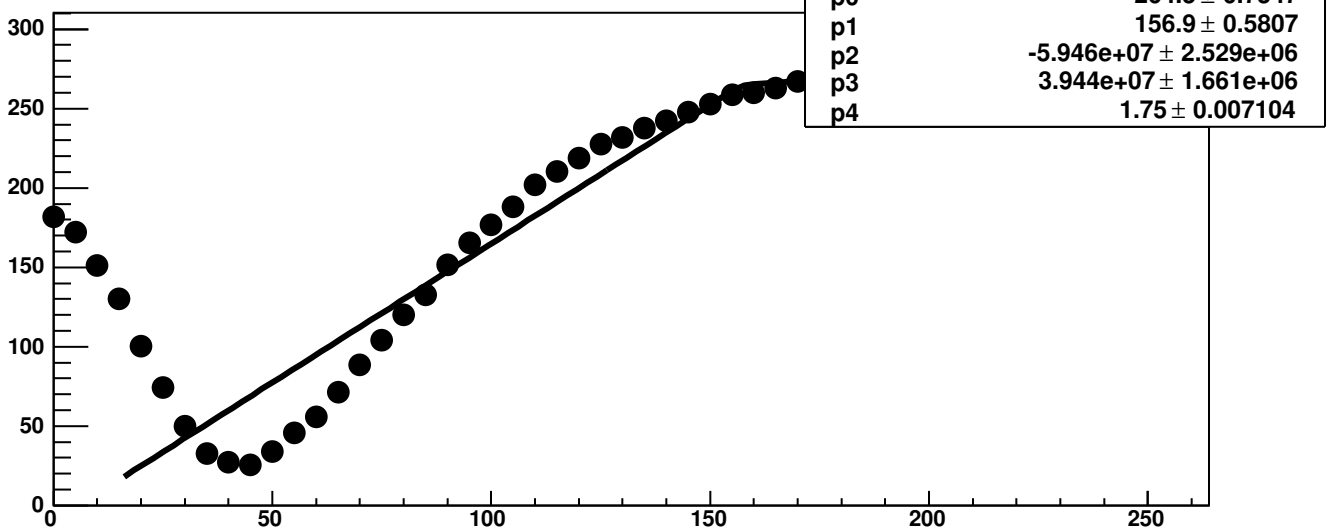
Chip 2, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold



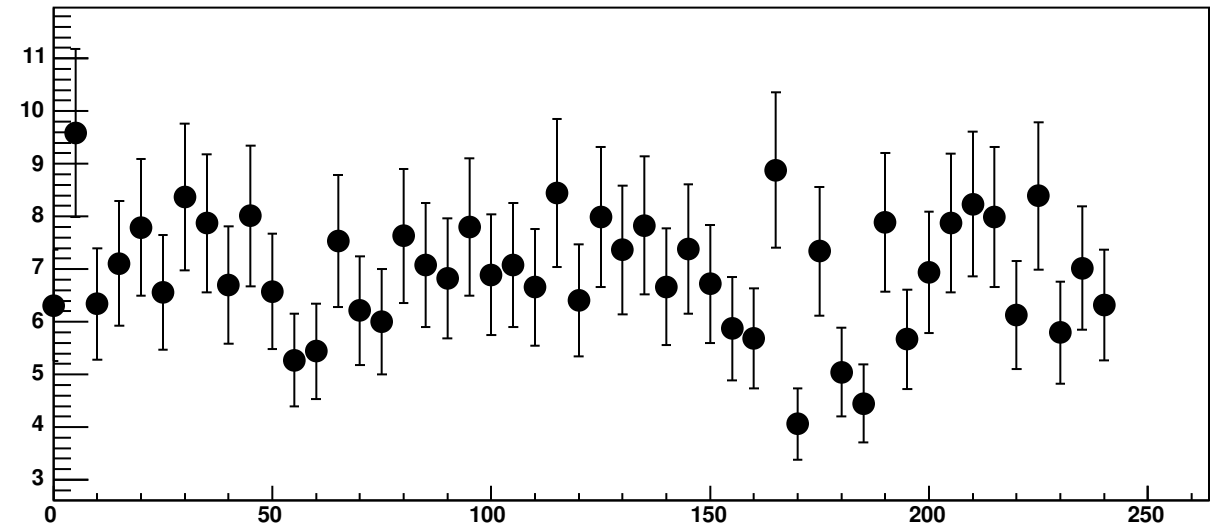
Chip 2, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold



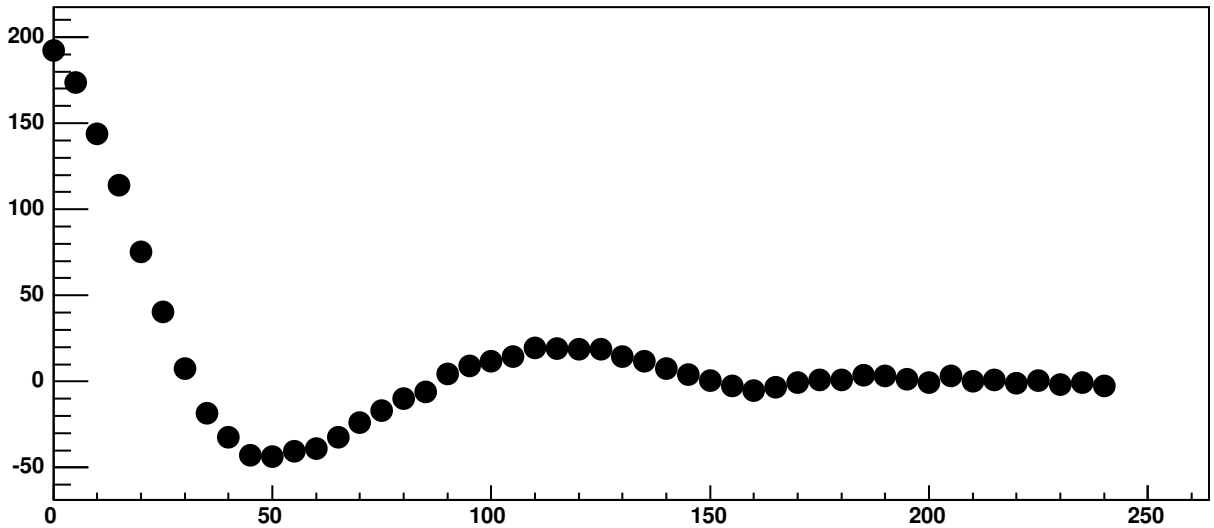
Chip 2, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold



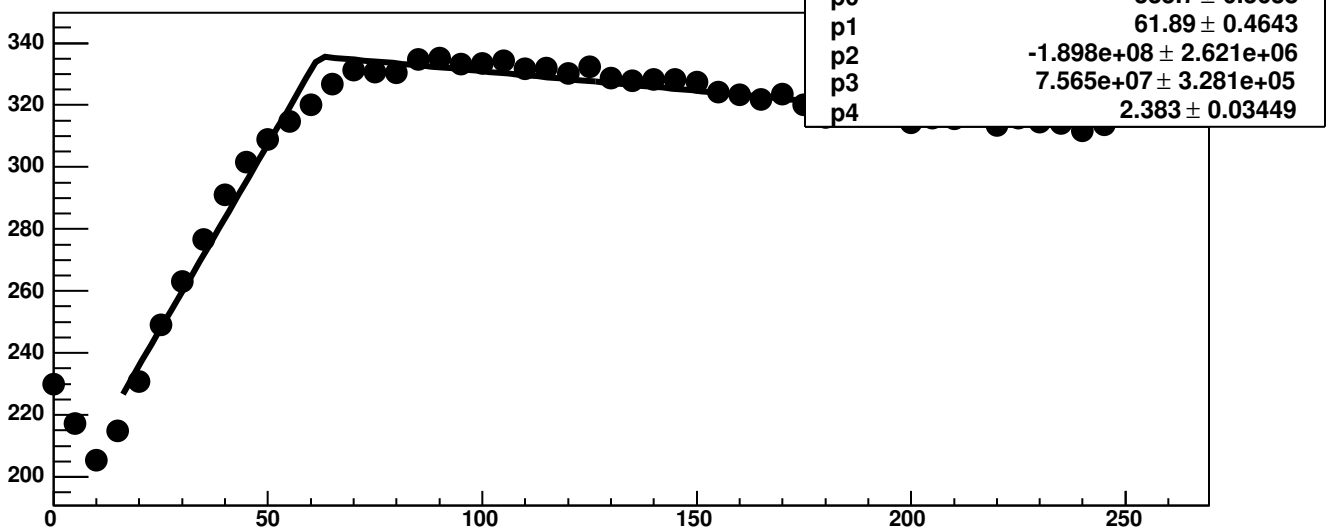
Chip 2, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold



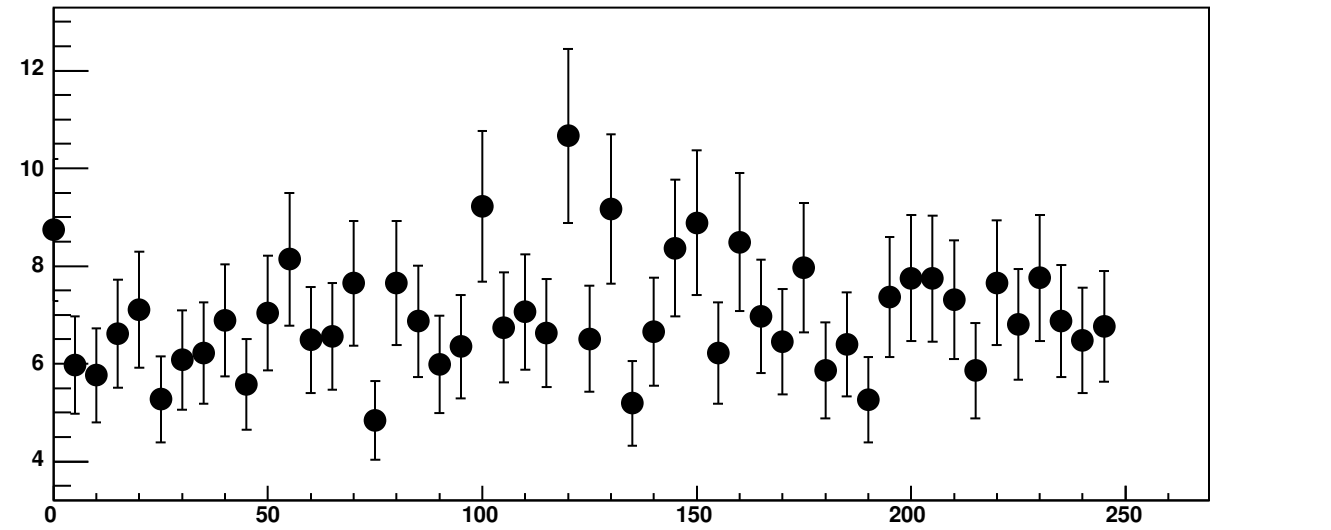
Chip 2, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold



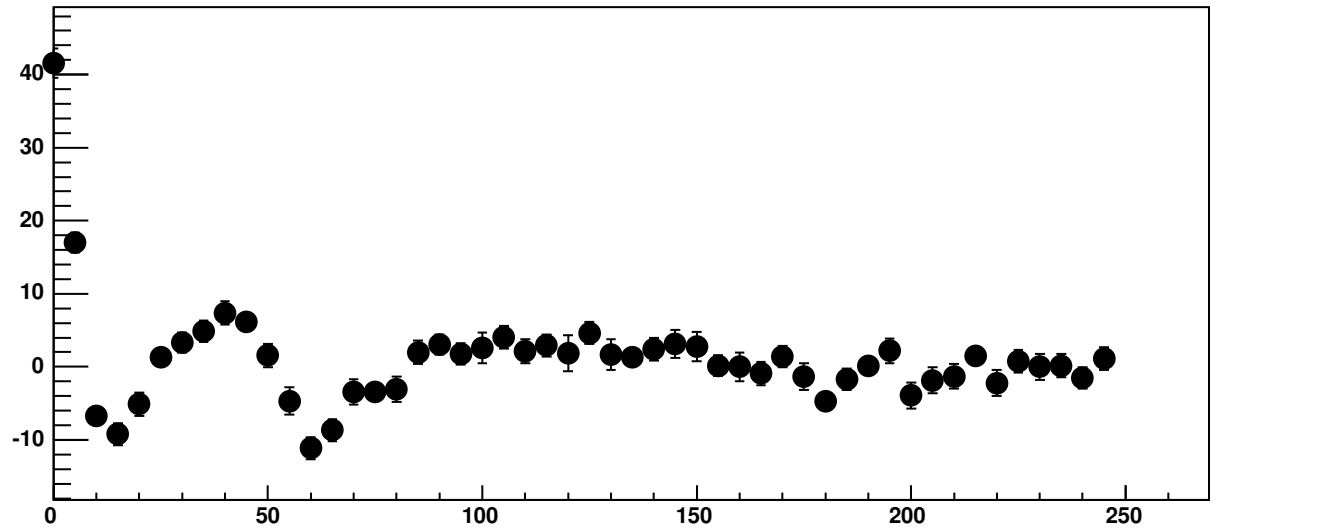
Chip 2, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold

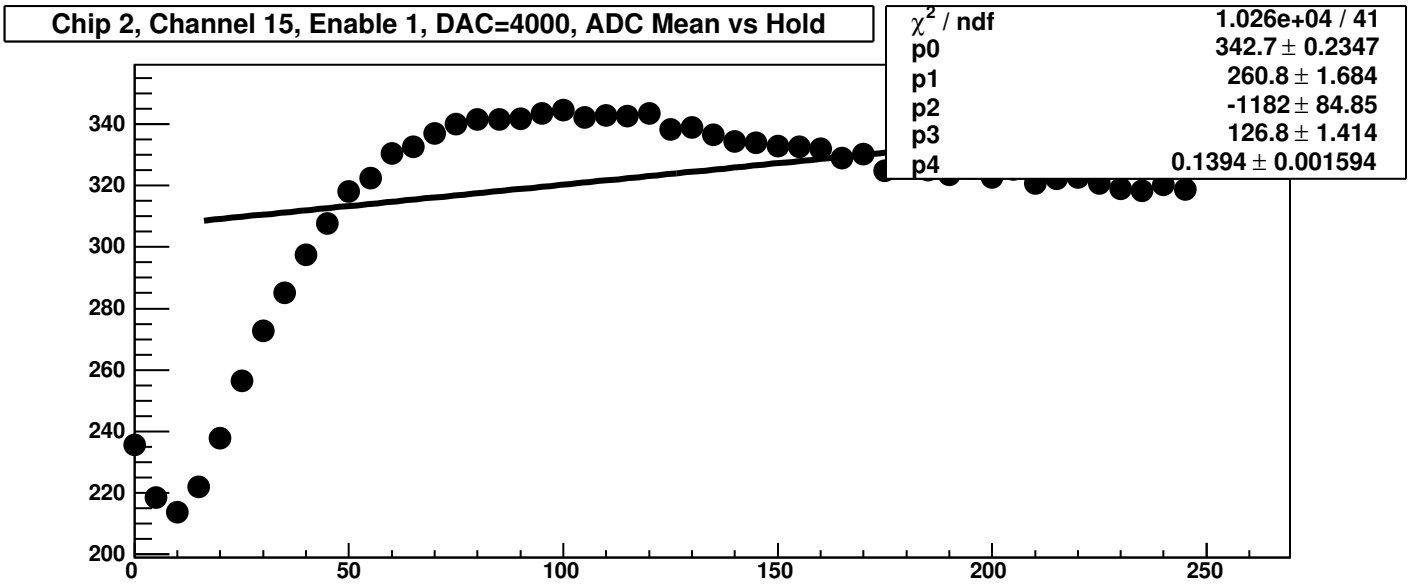


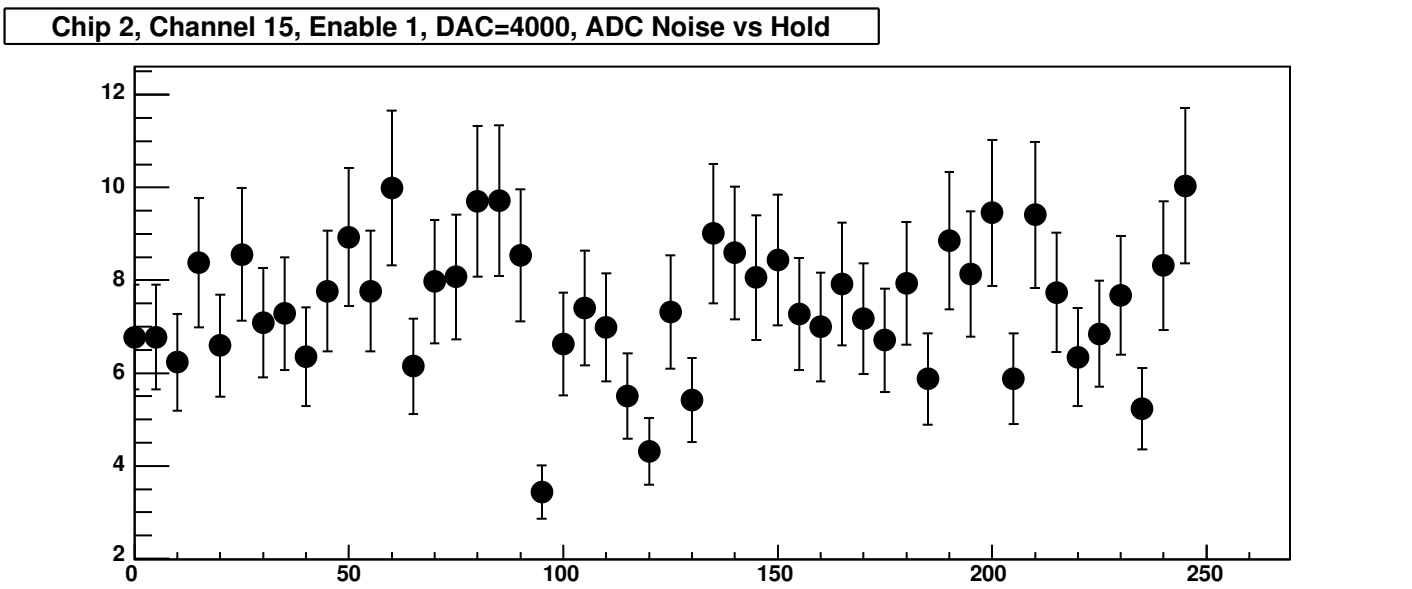
Chip 2, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold

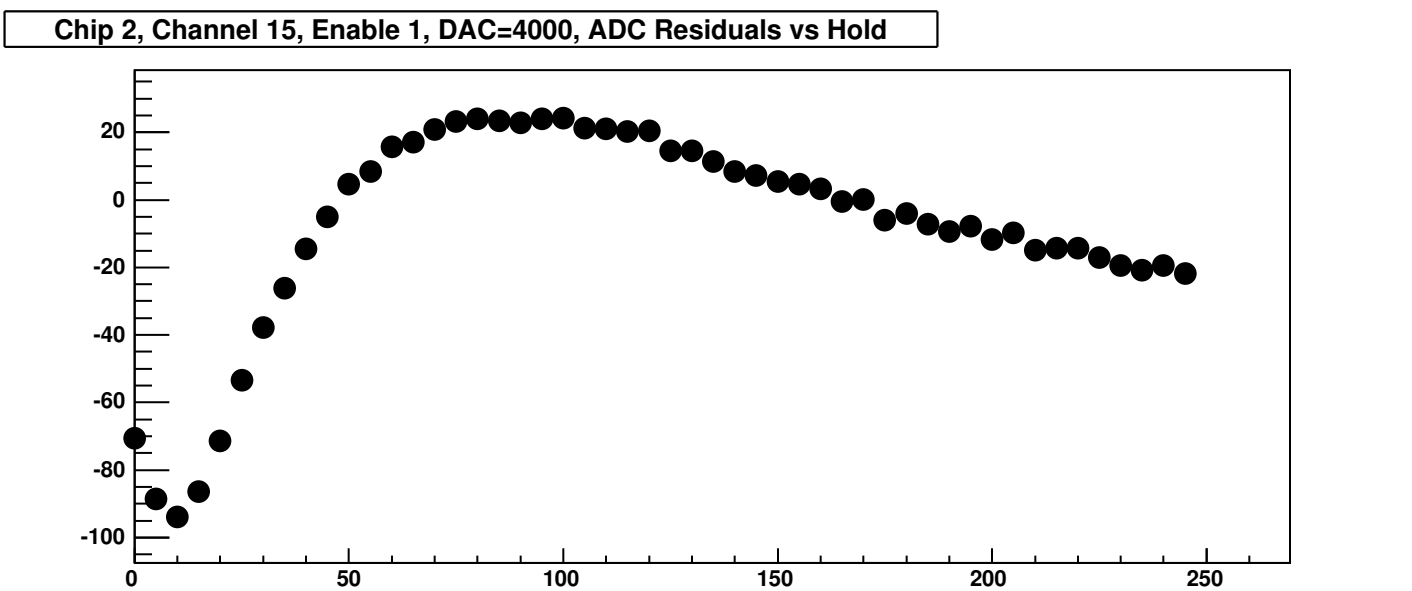


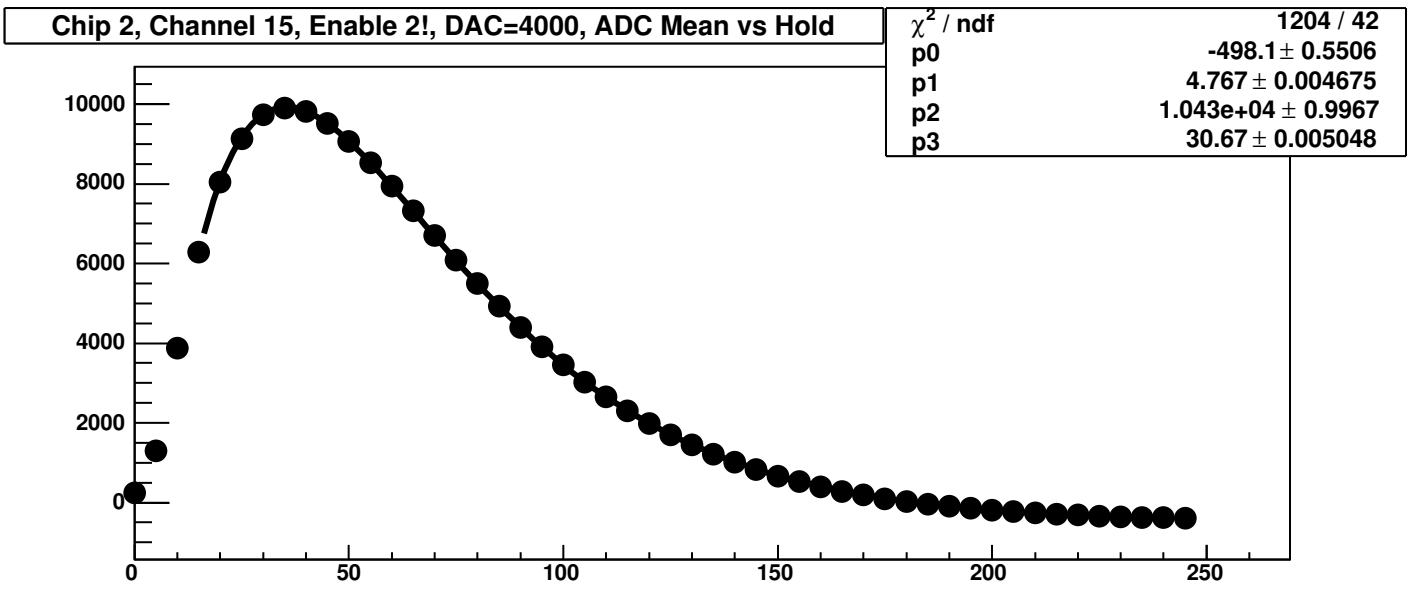
Chip 2, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold

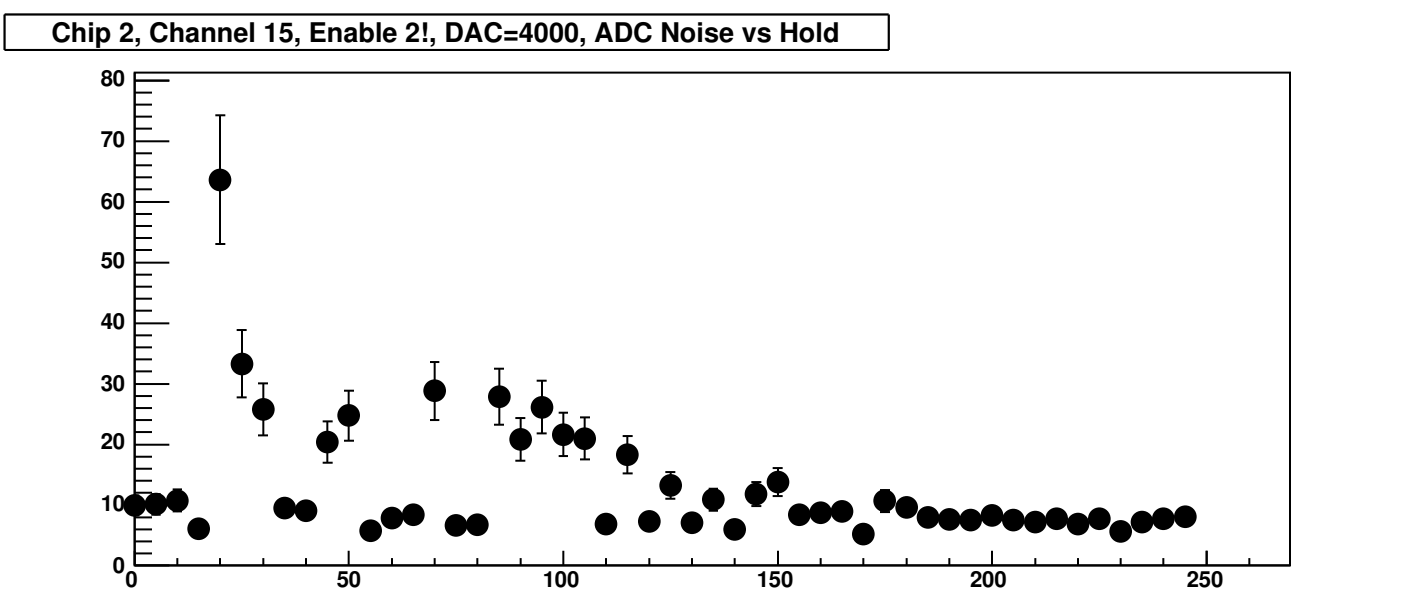


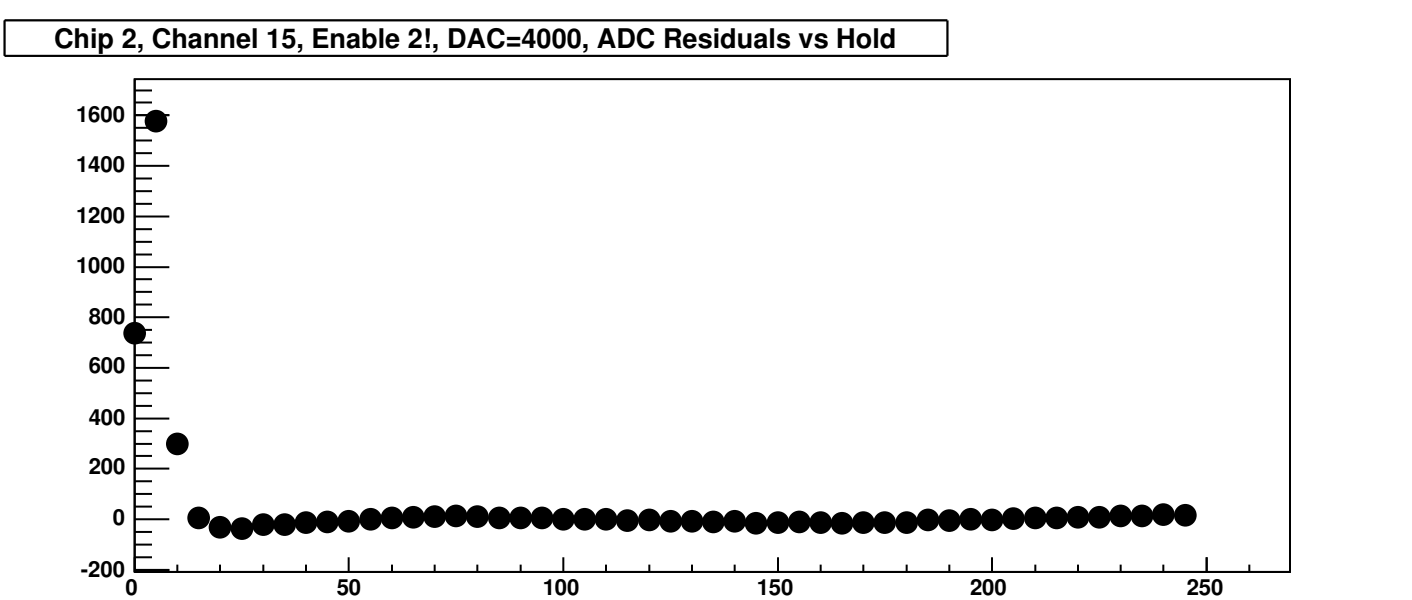

Chip 2, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.026e+04 / 41
p0 342.7 ± 0.2347
p1 260.8 ± 1.684
p2 -1182 ± 84.85
p3 126.8 ± 1.414
p4 0.1394 ± 0.001594
340
320
300
280
260
240
220
200
0
50
100
150
200
250


Chip 2, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
2
0
50
100
150
200
250

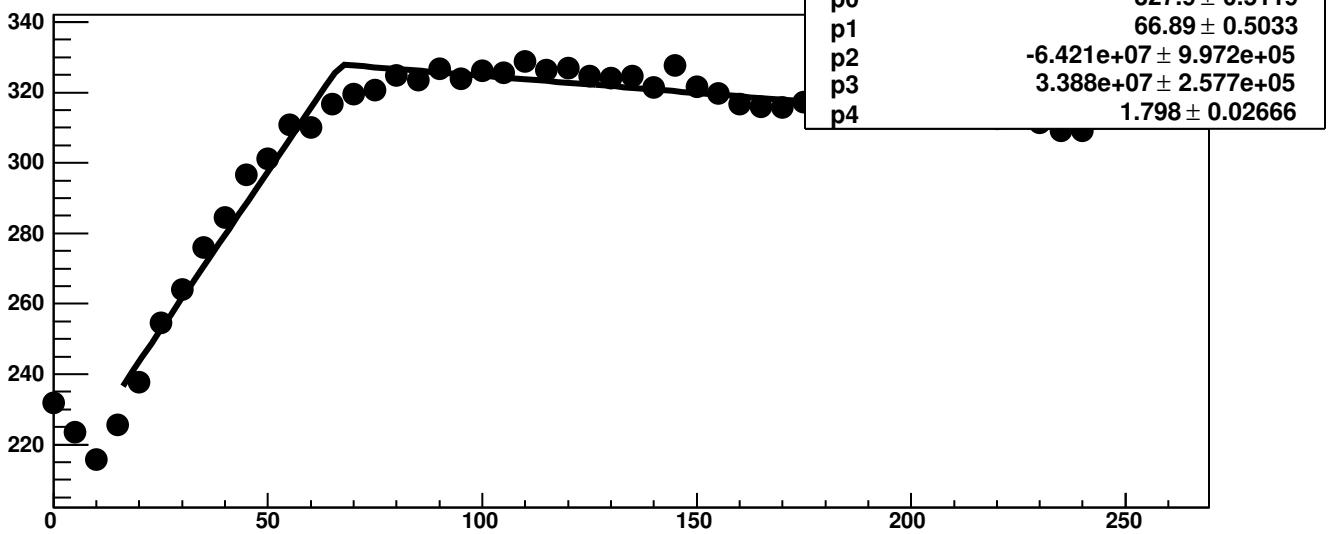

Chip 2, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
-80
-100
0
50
100
150
200
250


Chip 2, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1204 / 42
p0 -498.1 ± 0.5506
p1 4.767 ± 0.004675
p2 1.043e+04 ± 0.9967
p3 30.67 ± 0.005048
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

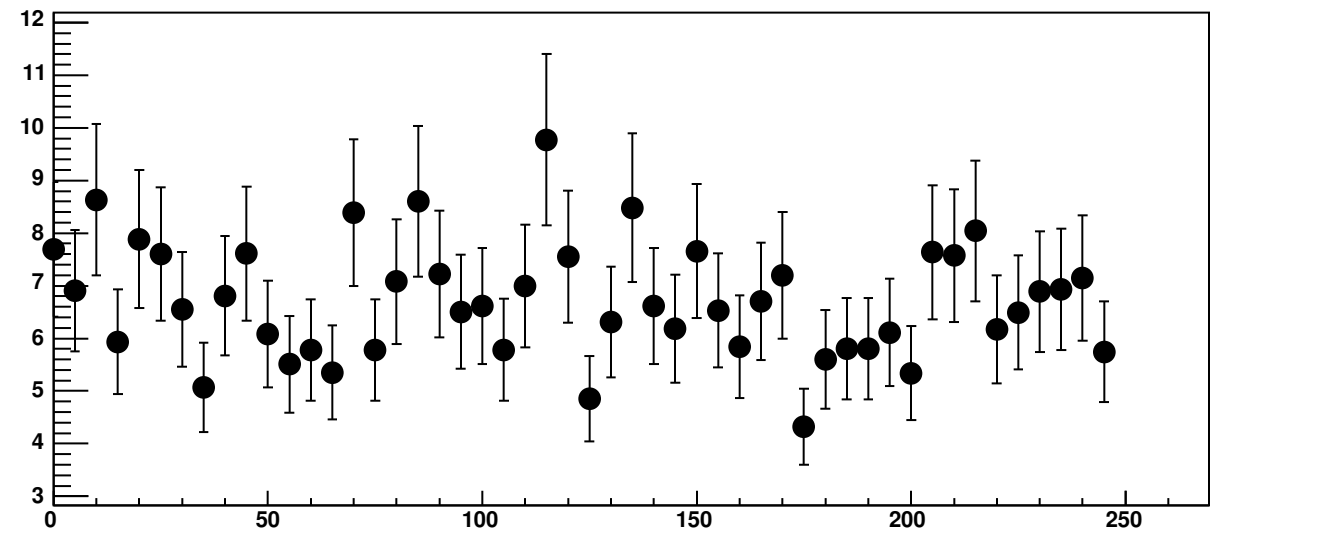

Chip 2, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250


Chip 2, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

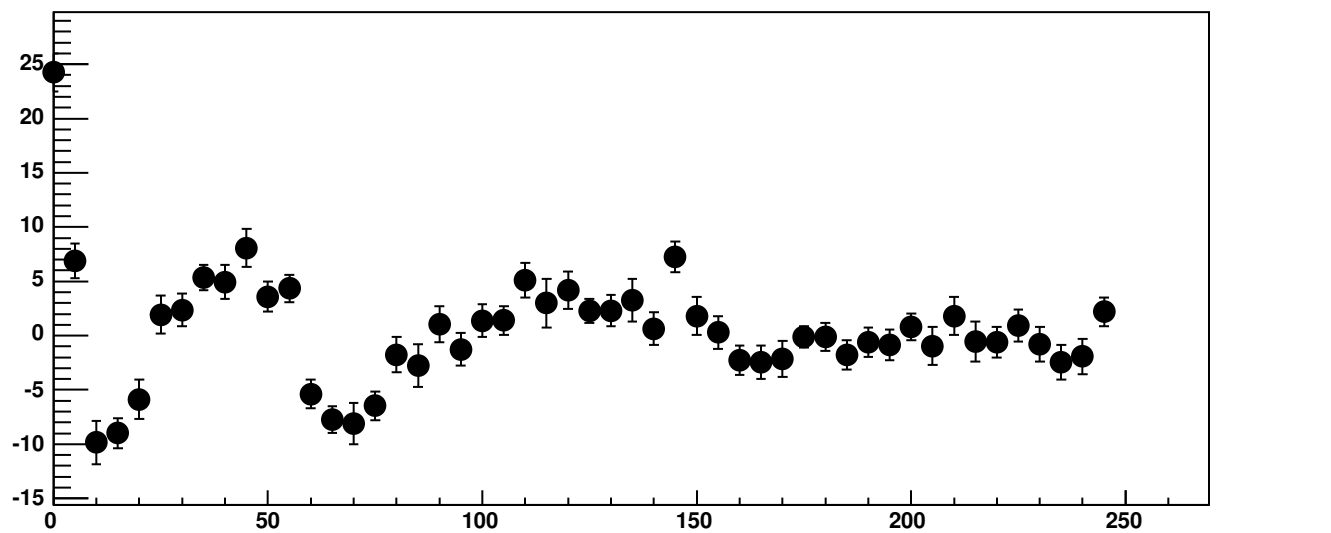
Chip 2, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold



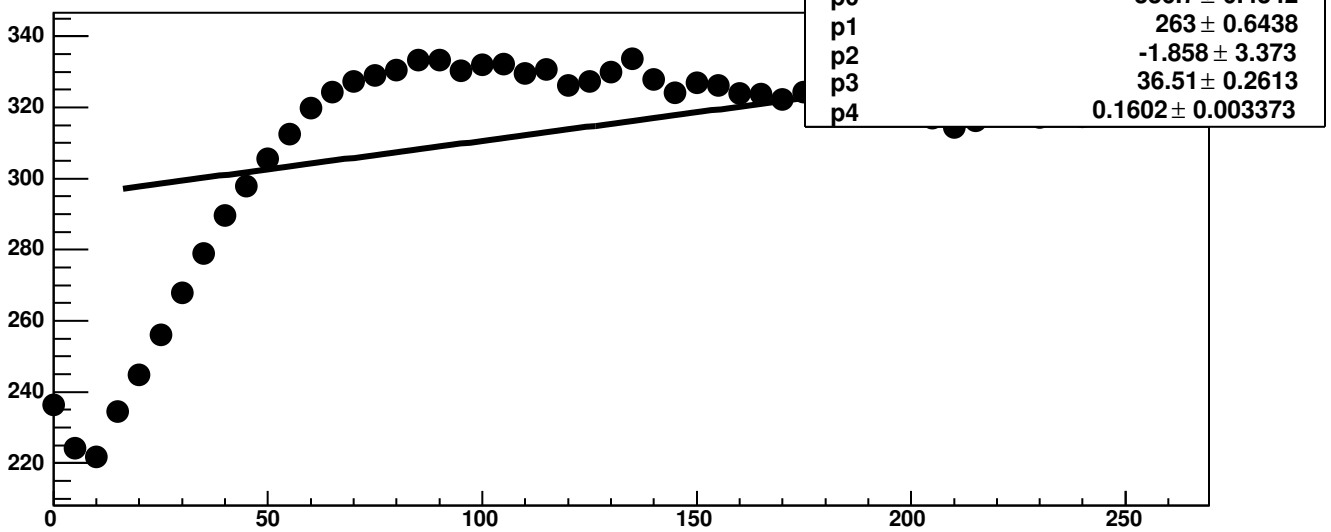
Chip 2, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



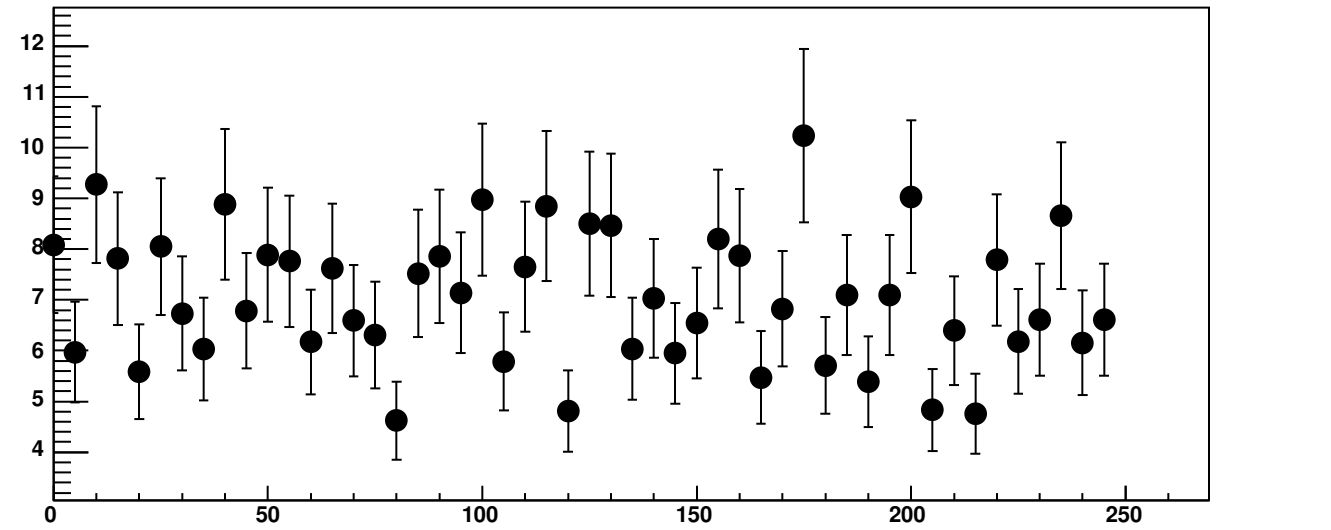
Chip 2, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold



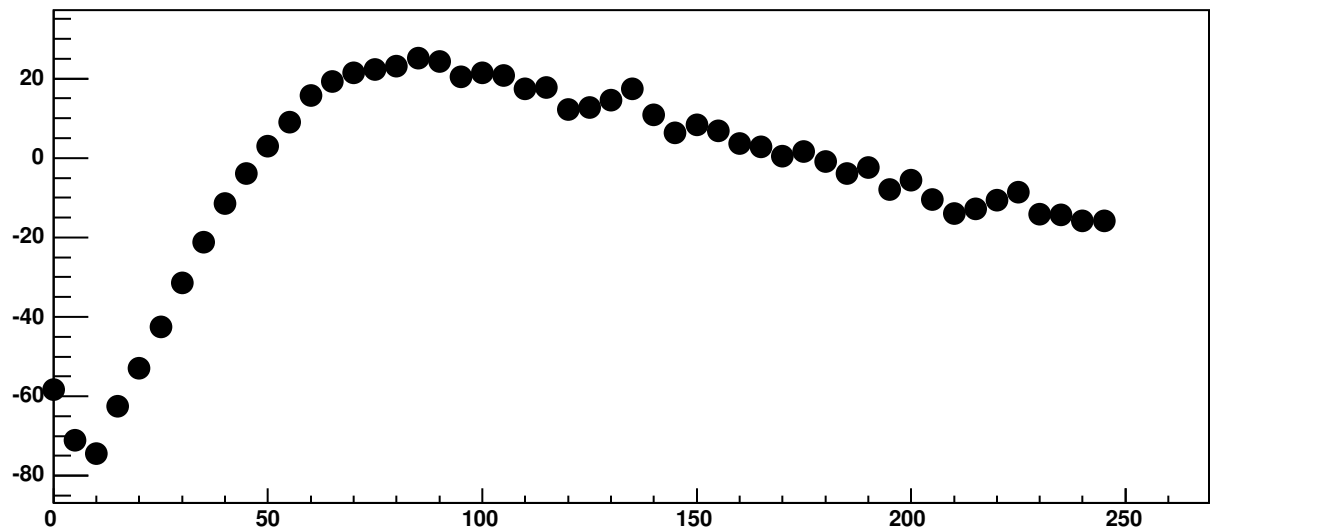
Chip 2, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold



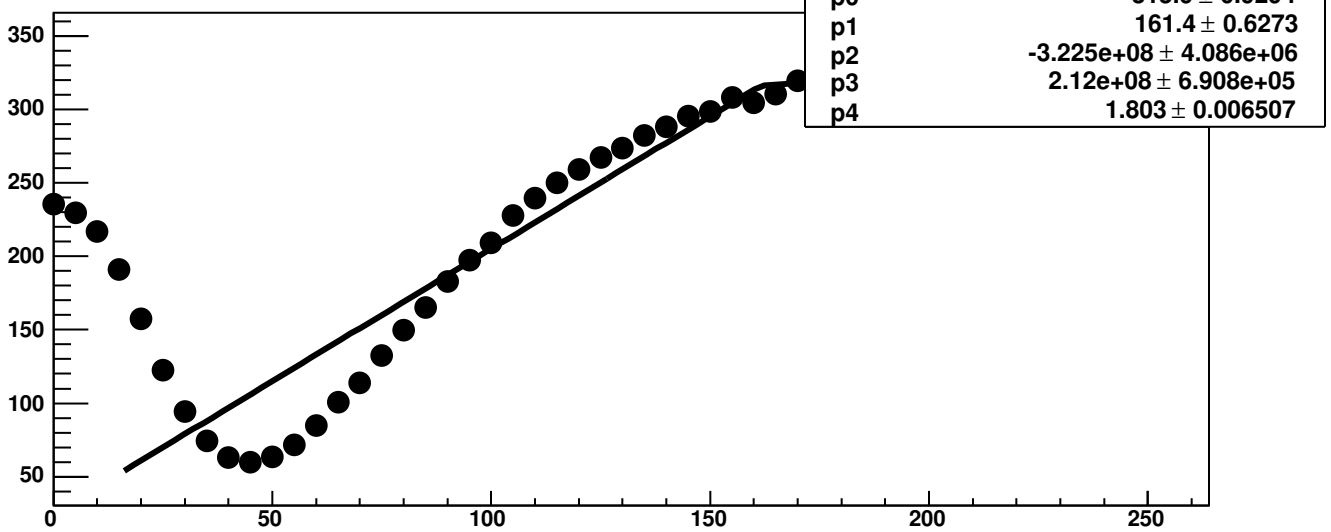
Chip 2, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



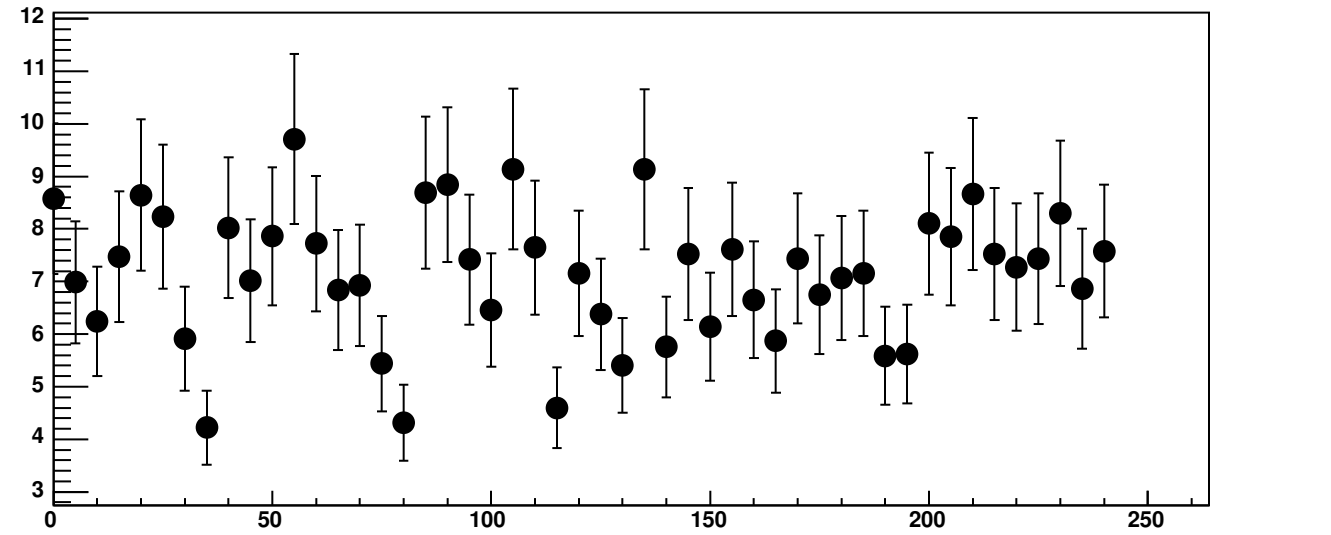
Chip 2, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold



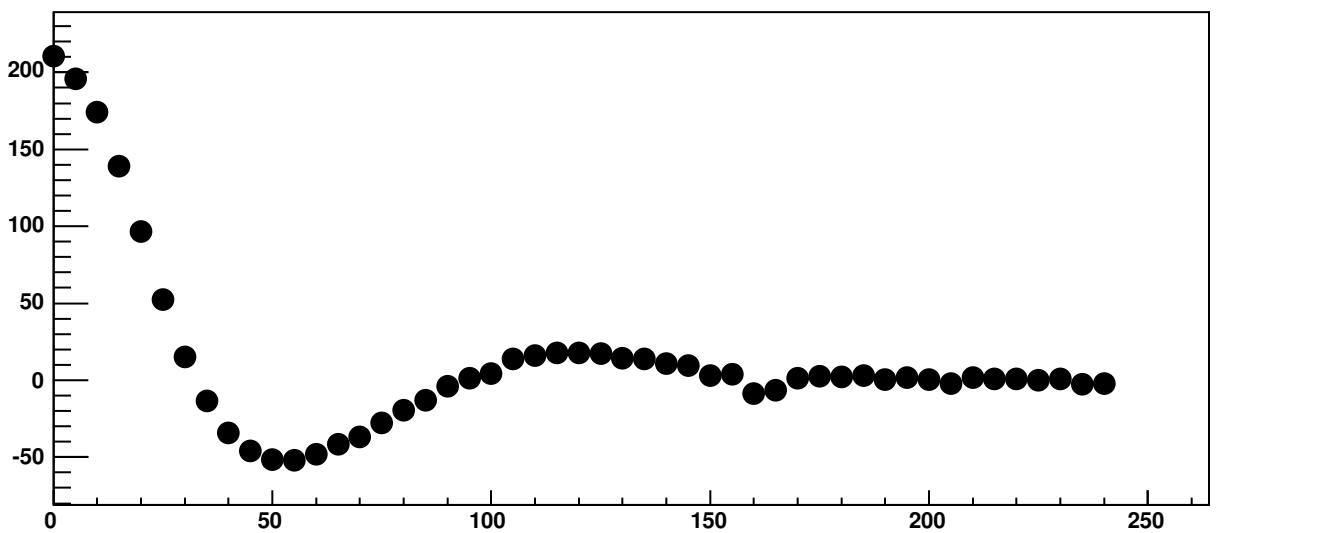
Chip 2, Channel 15, Enable 5, DAC=4000, ADC Mean vs Hold



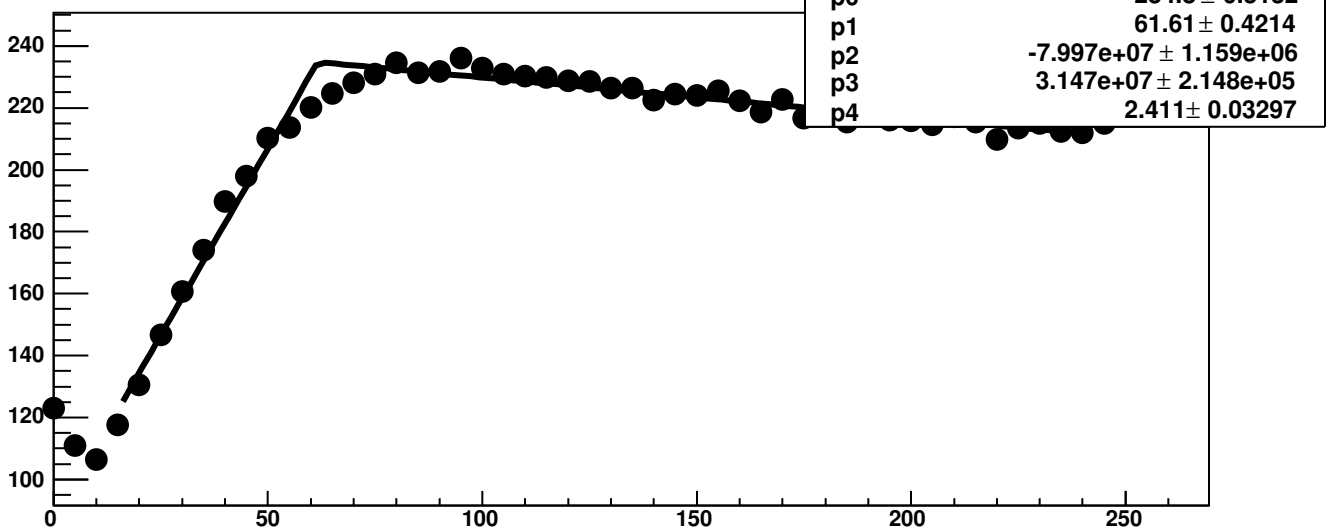
Chip 2, Channel 15, Enable 5, DAC=4000, ADC Noise vs Hold



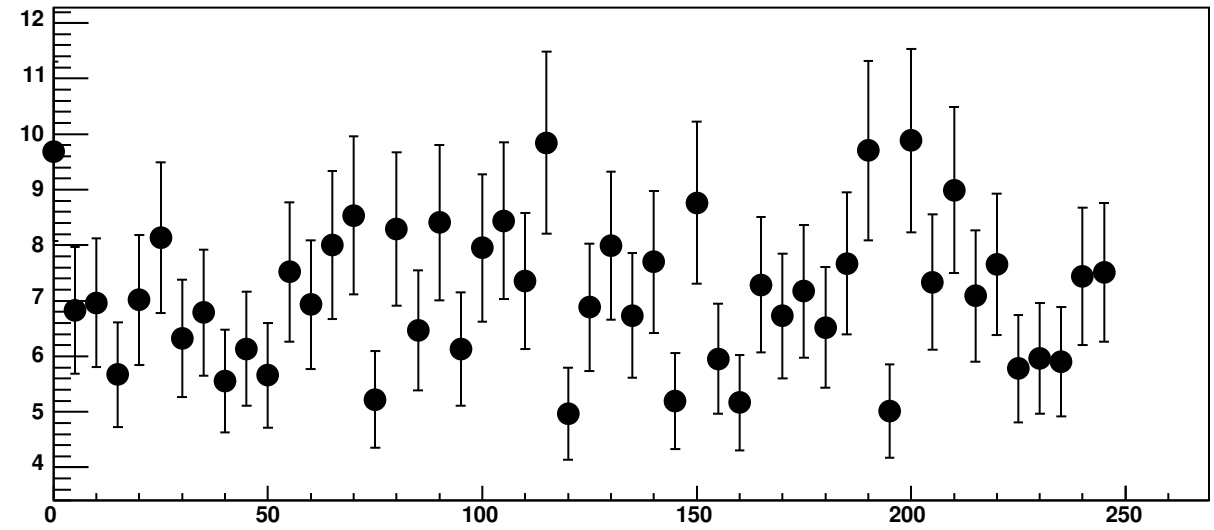
Chip 2, Channel 15, Enable 5, DAC=4000, ADC Residuals vs Hold



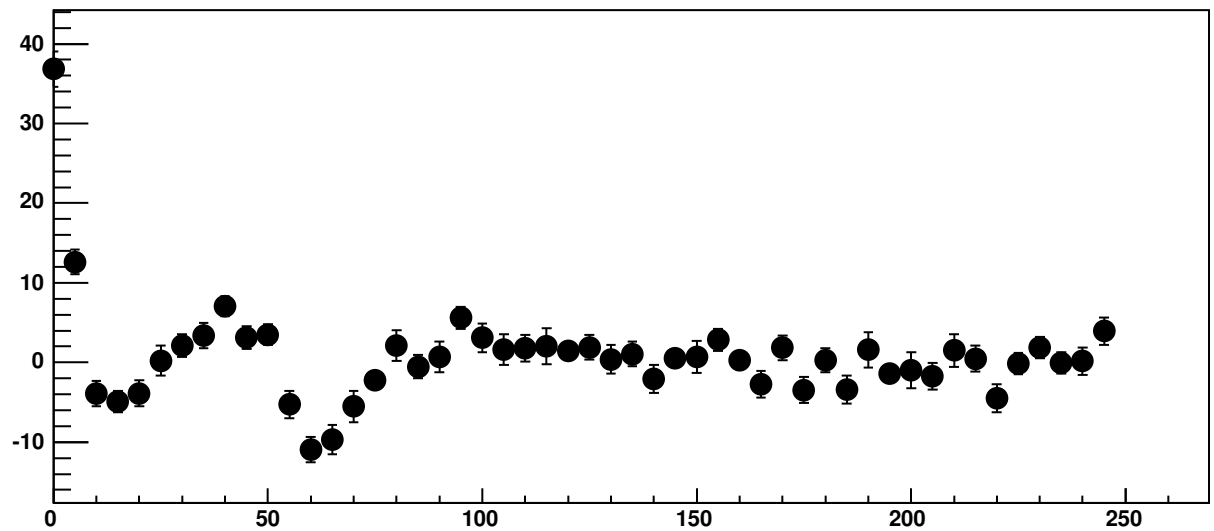
Chip 2, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold



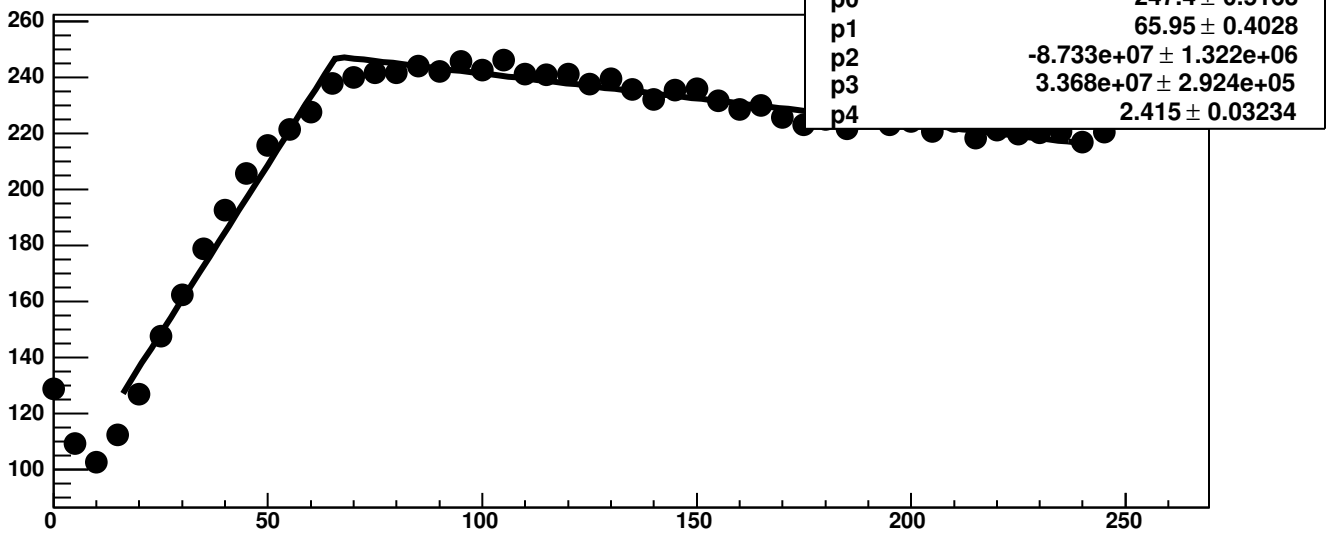
Chip 2, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



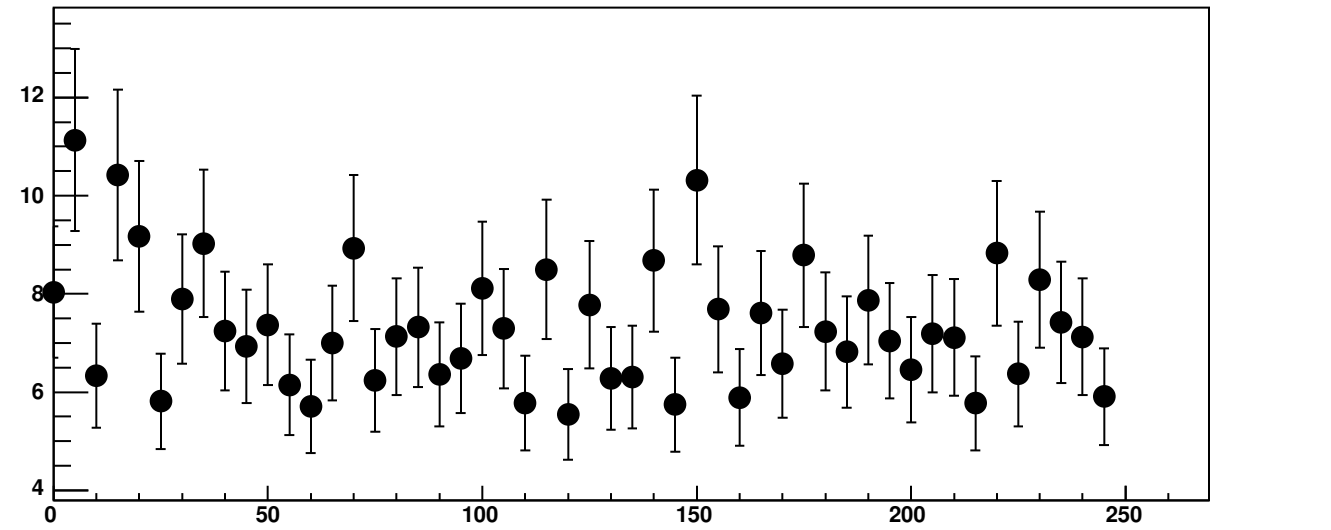
Chip 2, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold



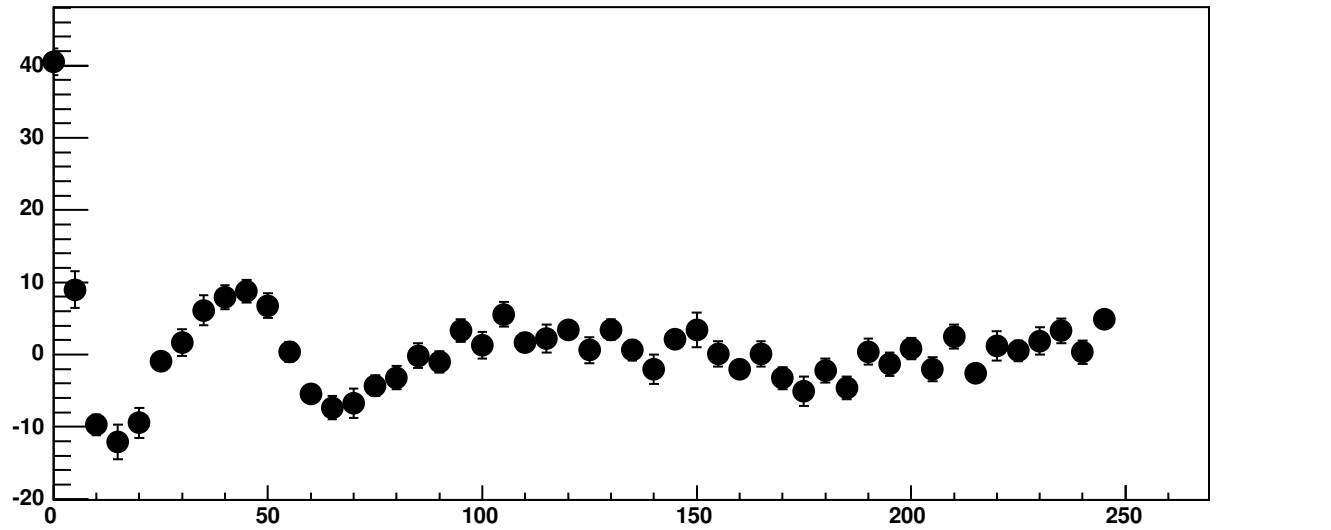
Chip 2, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold



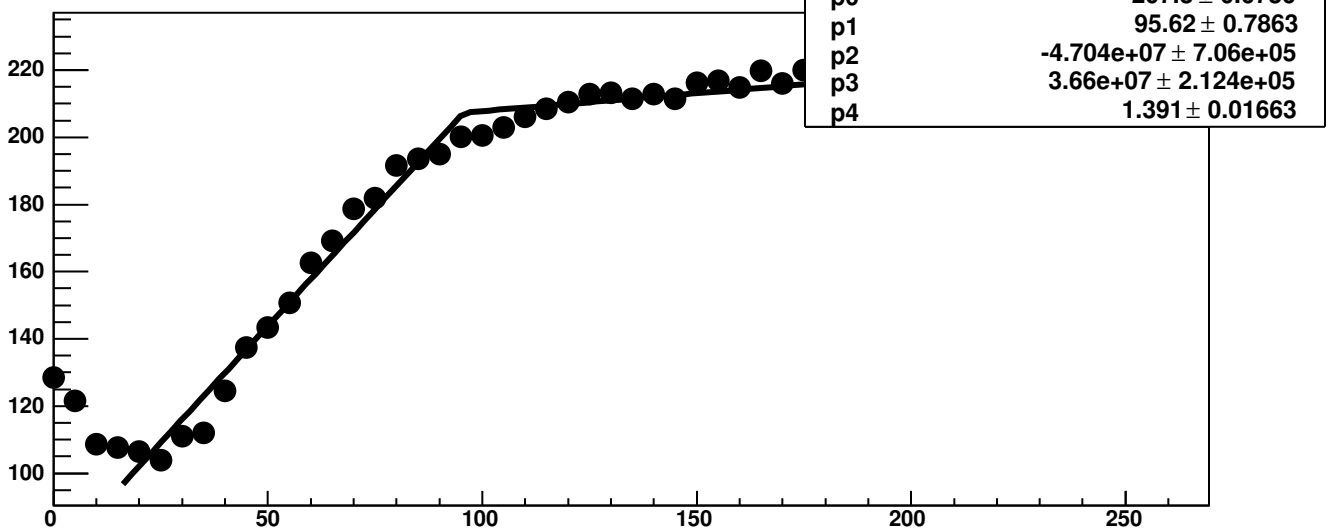
Chip 2, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



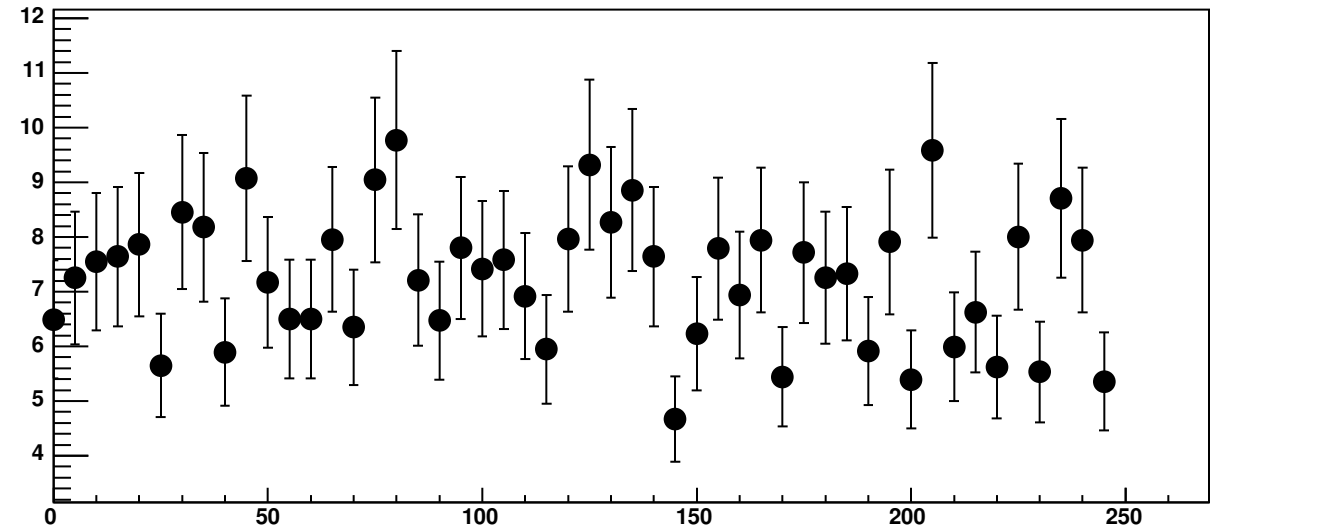
Chip 2, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold



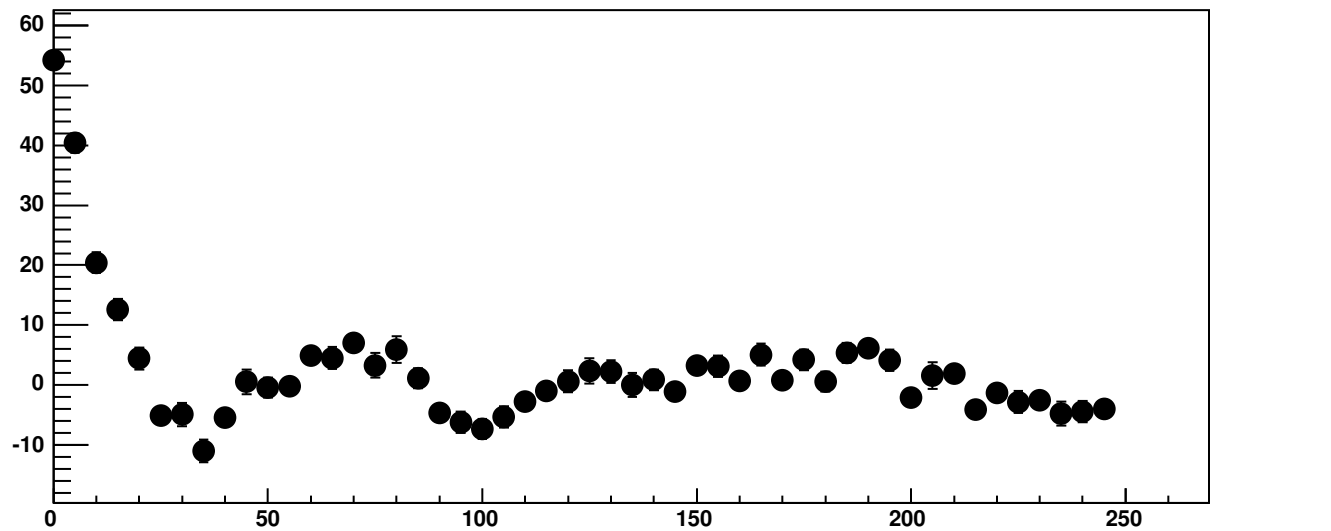
Chip 2, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold



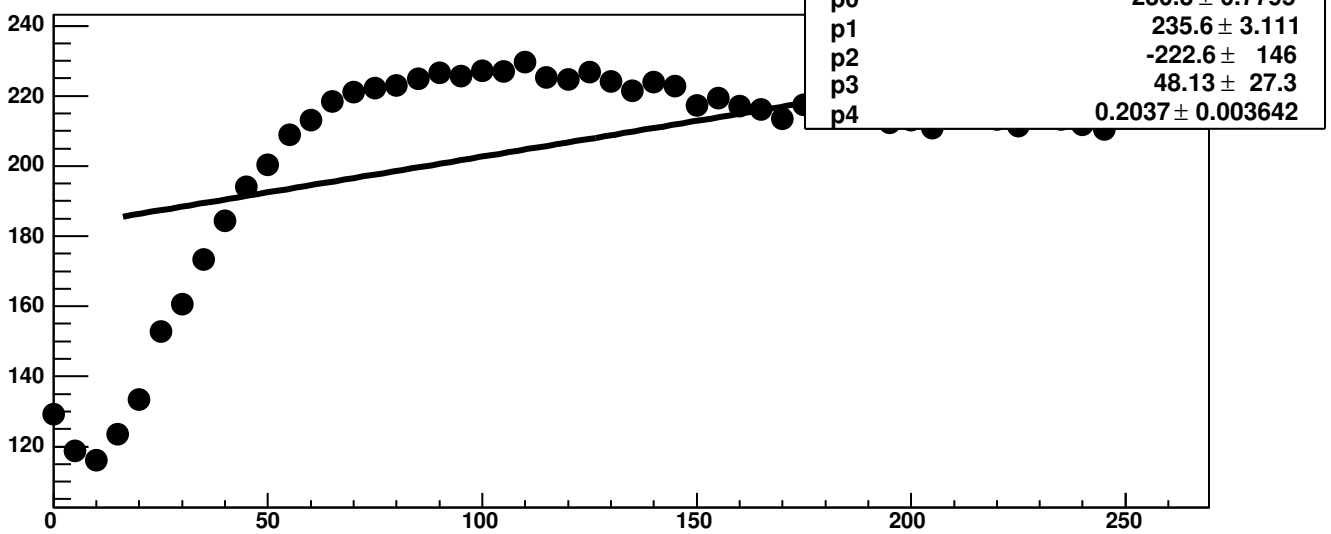
Chip 2, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold



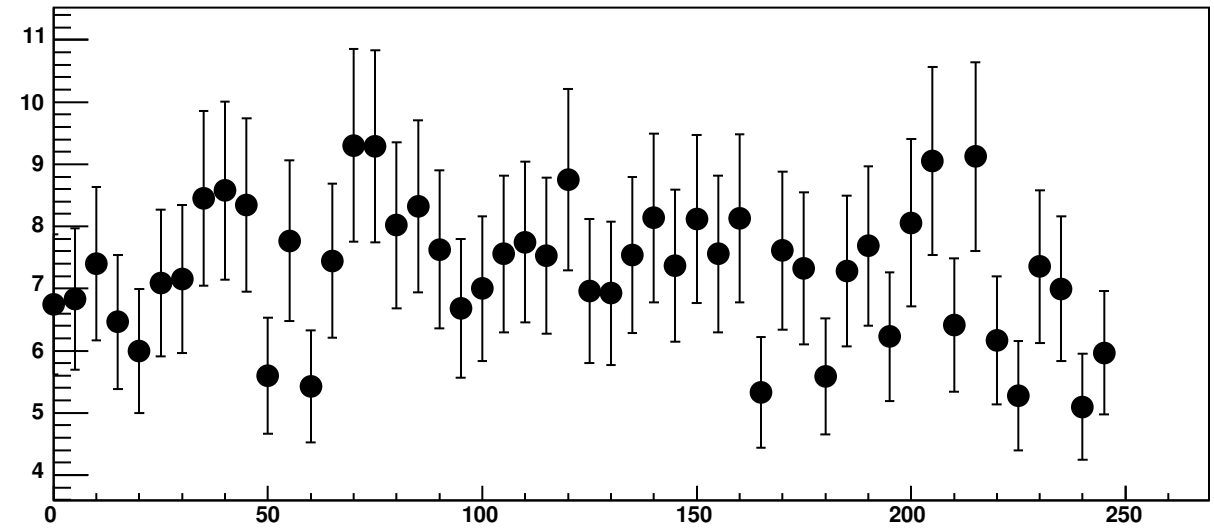
Chip 2, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold



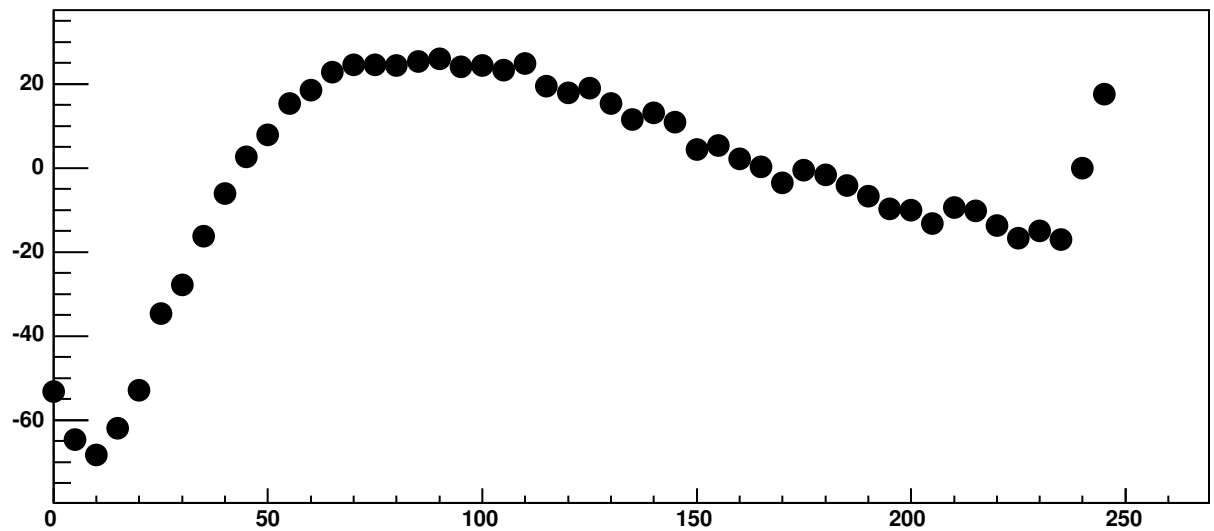
Chip 2, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold



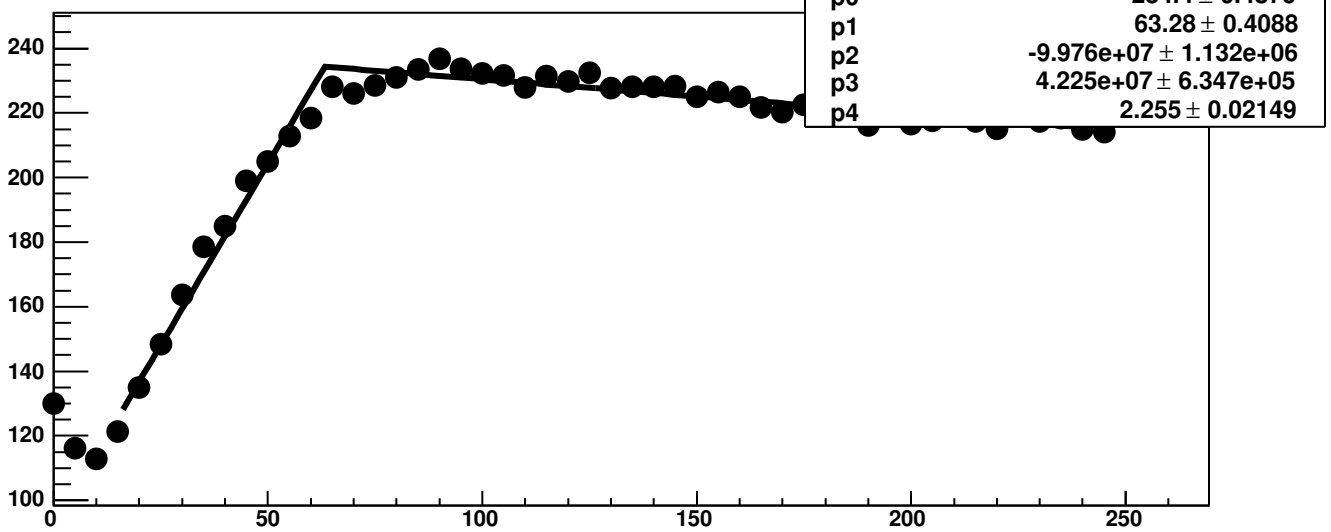
Chip 2, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



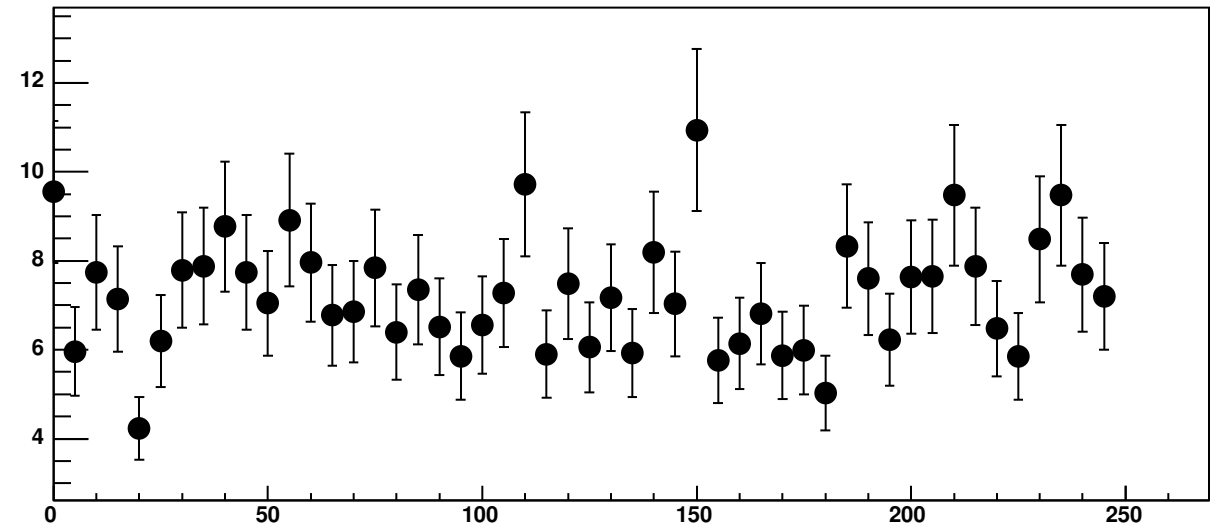
Chip 2, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold



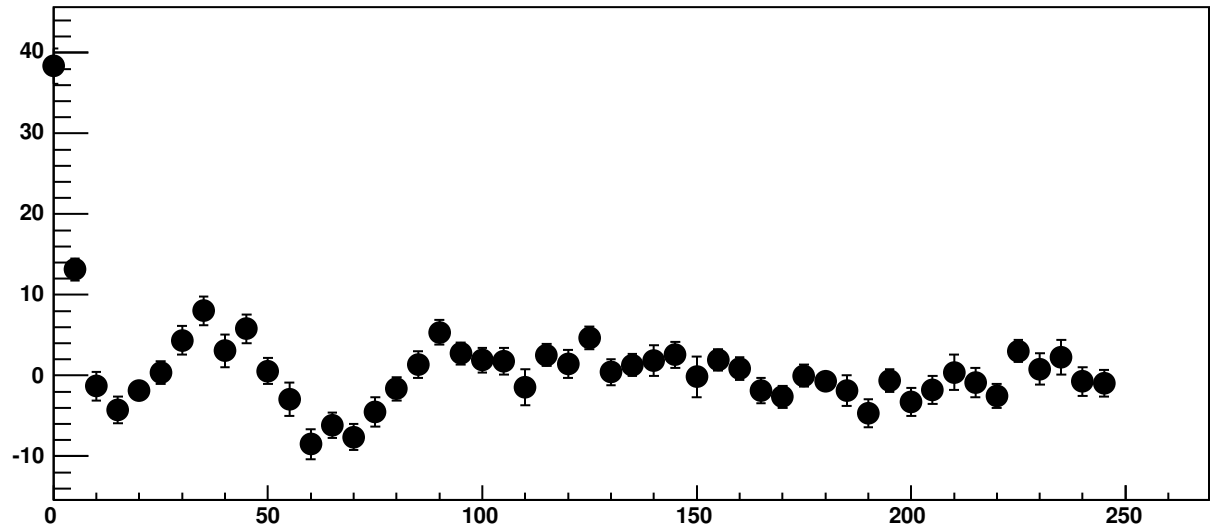
Chip 2, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold



Chip 2, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold

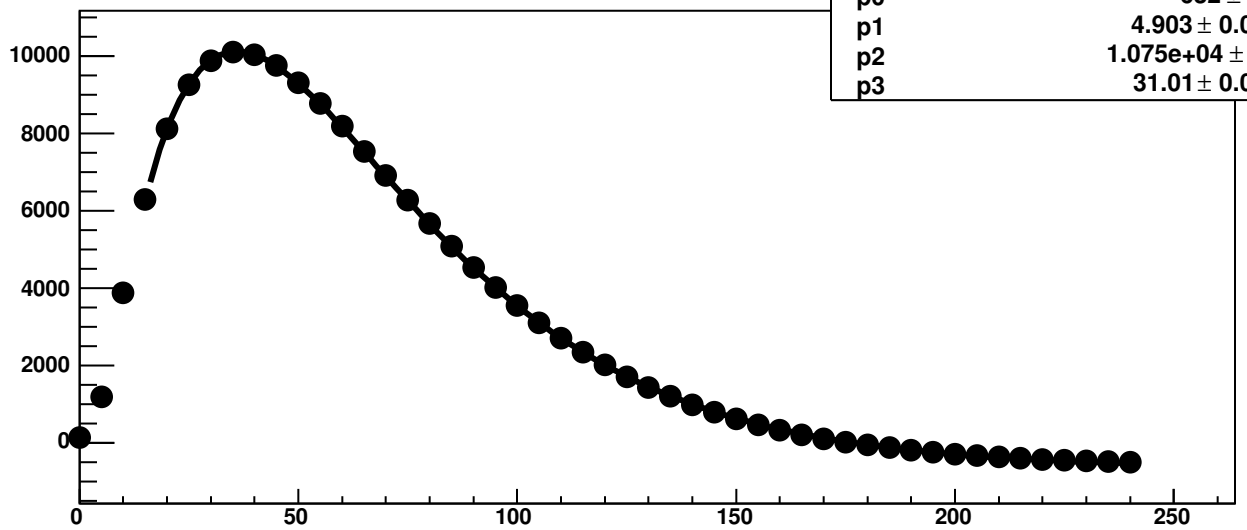


Chip 2, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

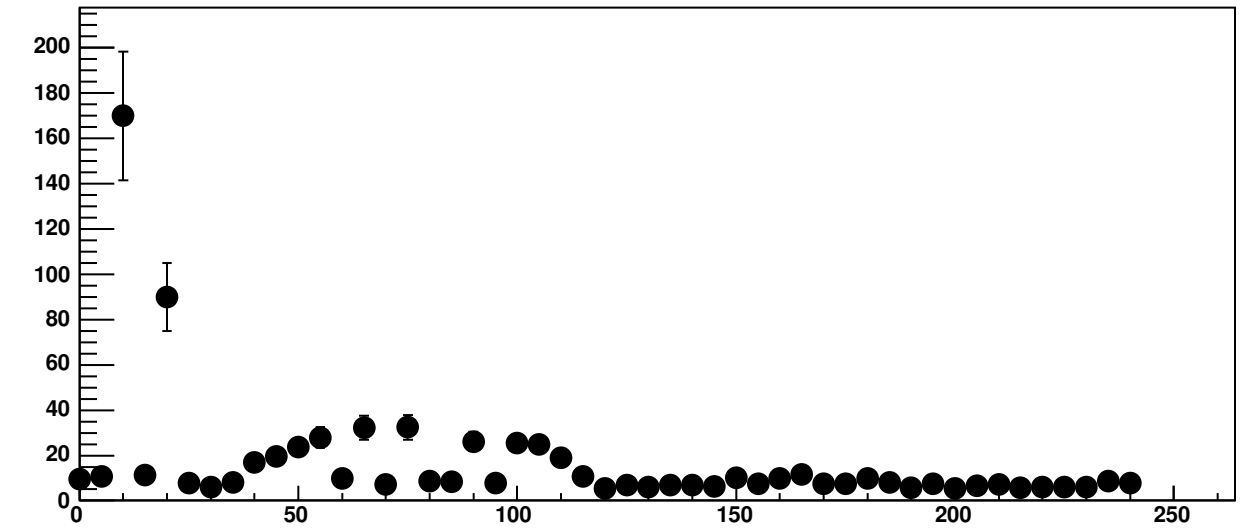


Chip 2, Channel 16, Enable 5!, DAC=4000, ADC Mean vs Hold

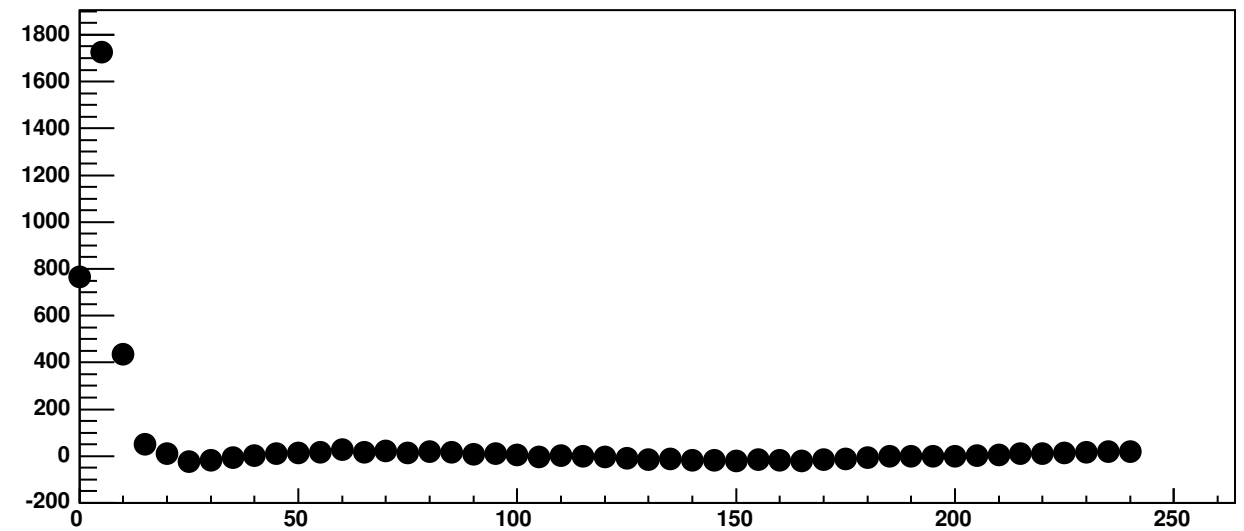
χ^2 / ndf	2626 / 42
p0	-632 ± 0.5107
p1	4.903 ± 0.006546
p2	1.075e+04 ± 0.9221
p3	31.01 ± 0.004583



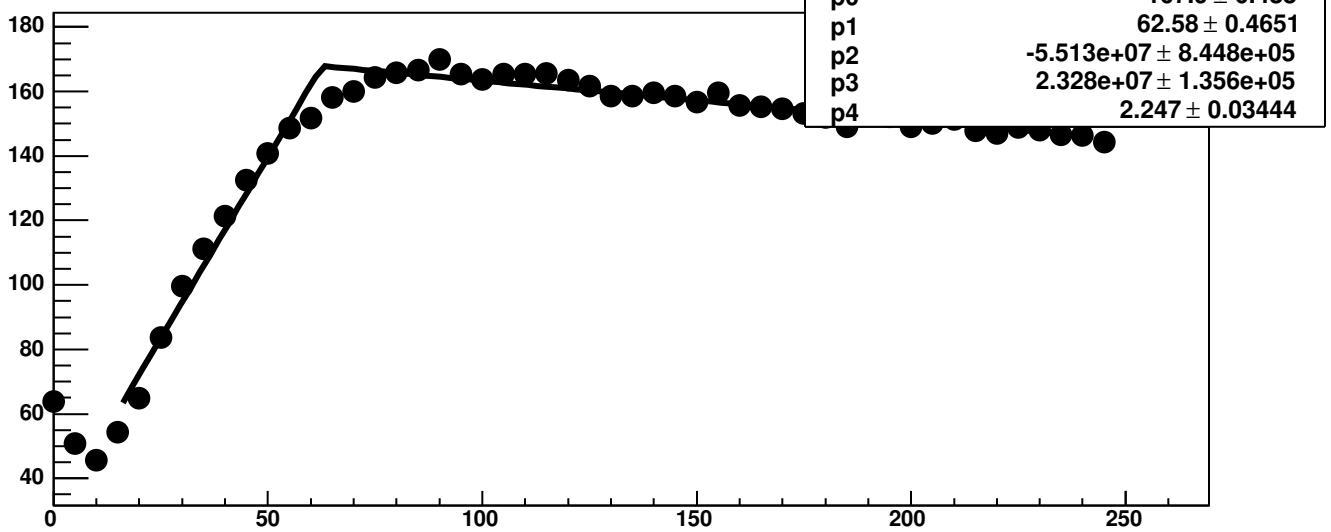
Chip 2, Channel 16, Enable 5!, DAC=4000, ADC Noise vs Hold



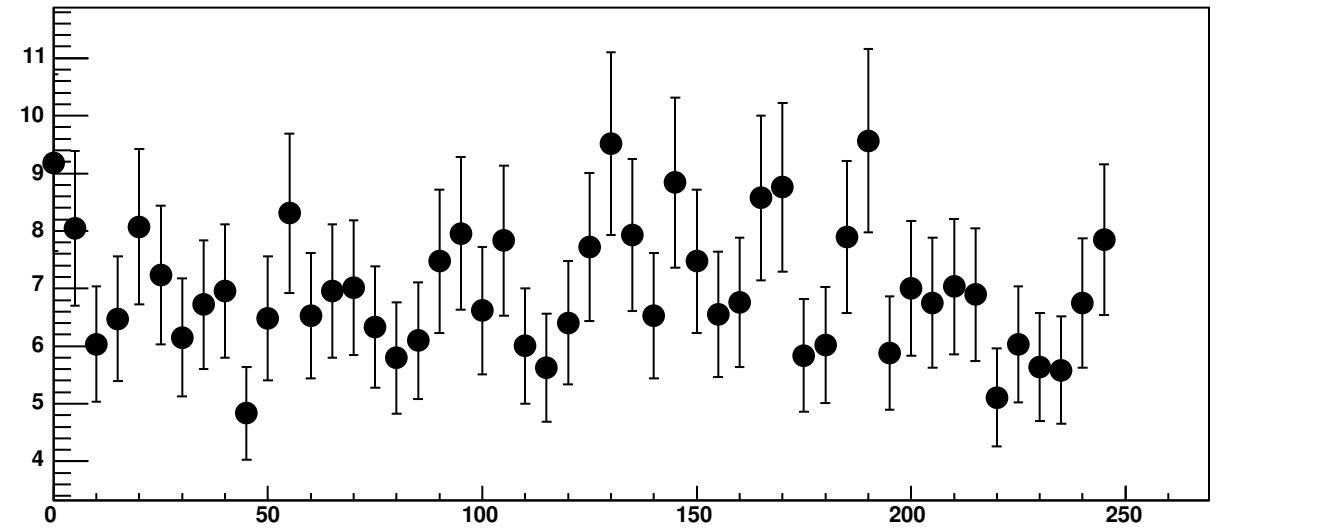
Chip 2, Channel 16, Enable 5!, DAC=4000, ADC Residuals vs Hold



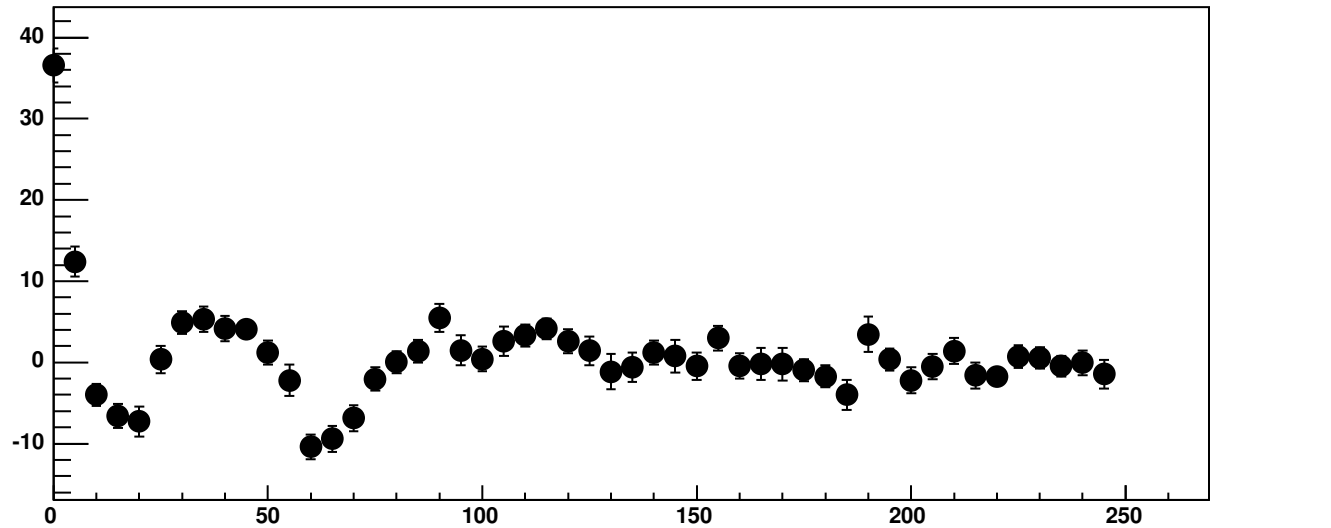
Chip 2, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold



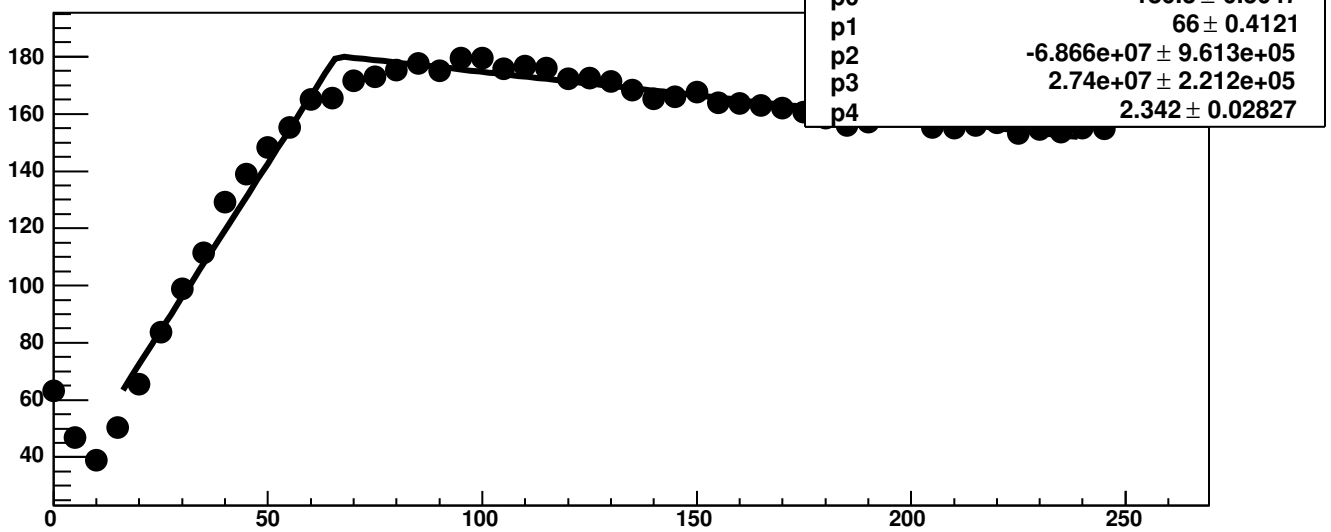
Chip 2, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



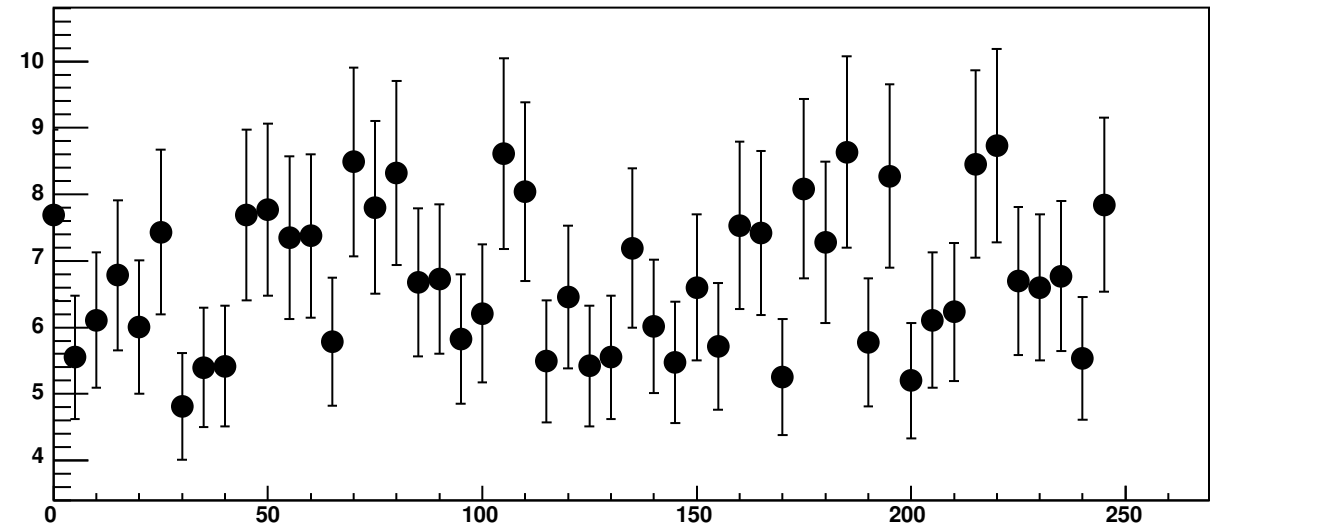
Chip 2, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold



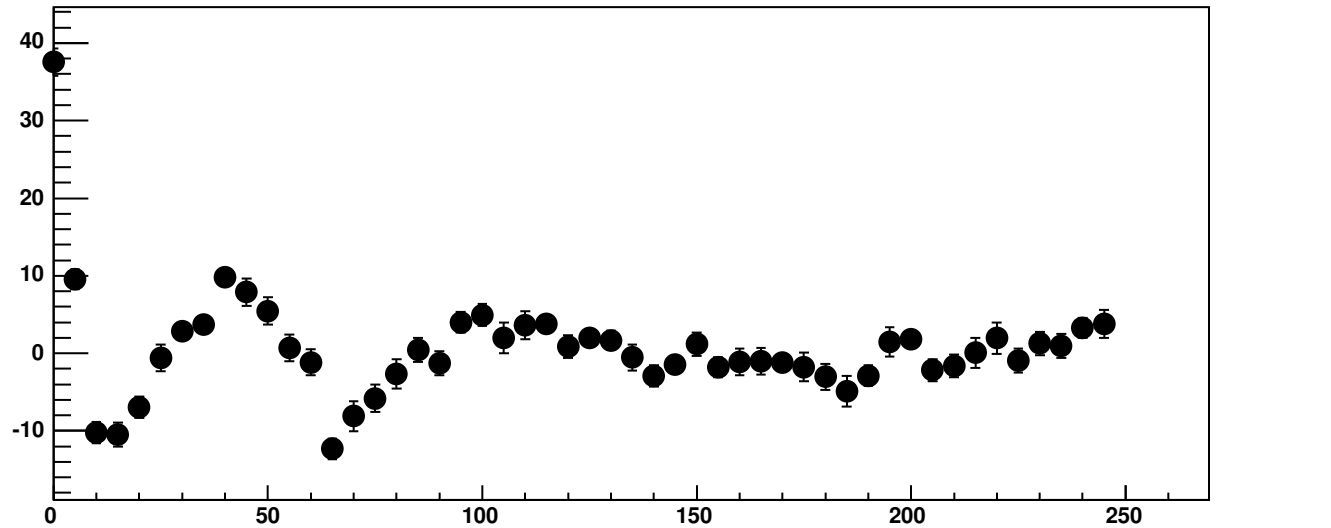
Chip 2, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 2, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold

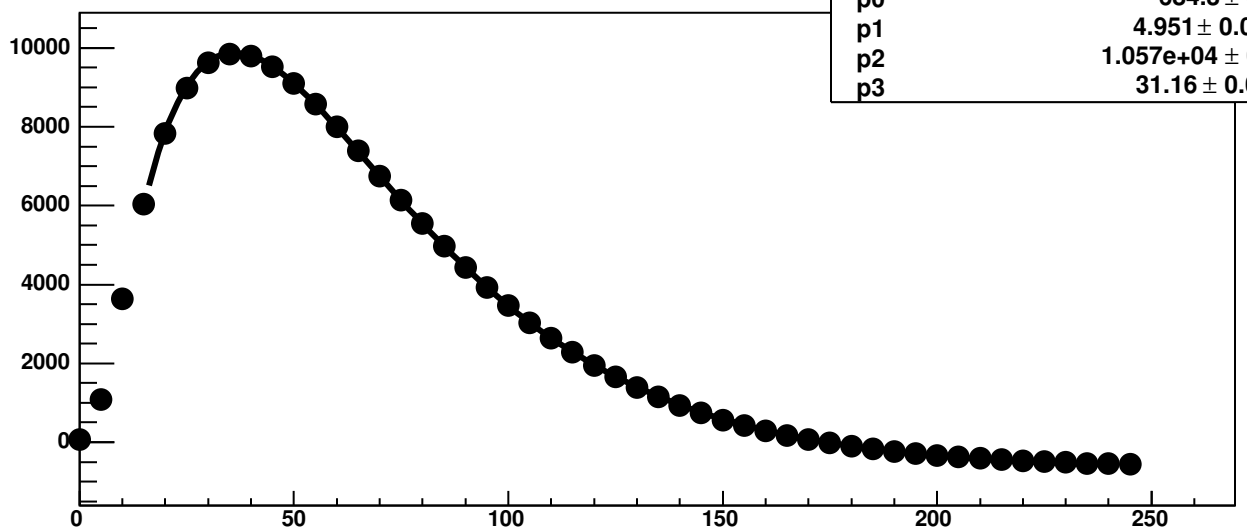


Chip 2, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold

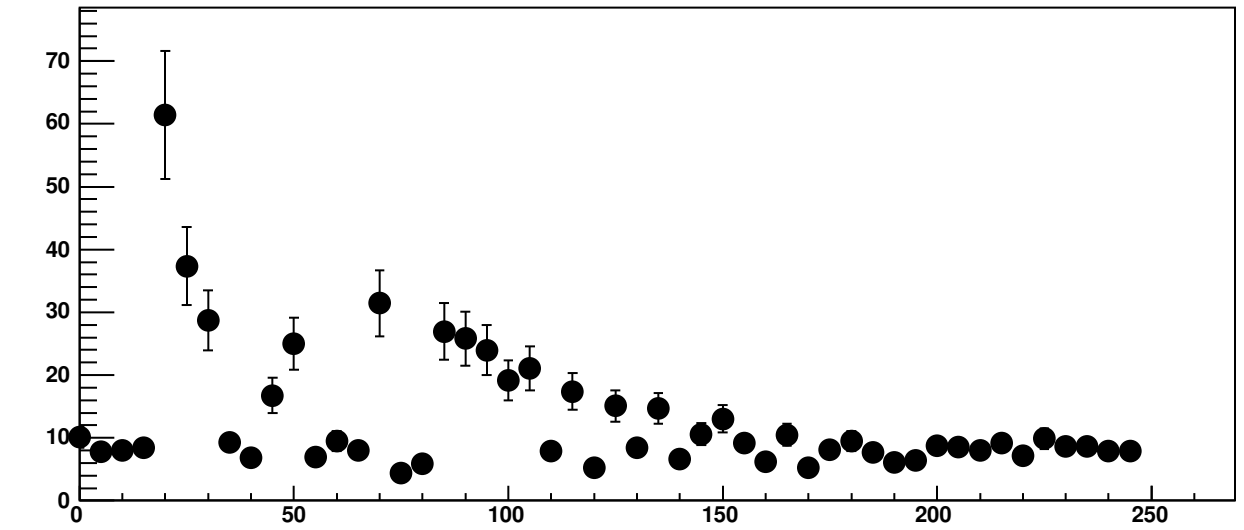


Chip 2, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold

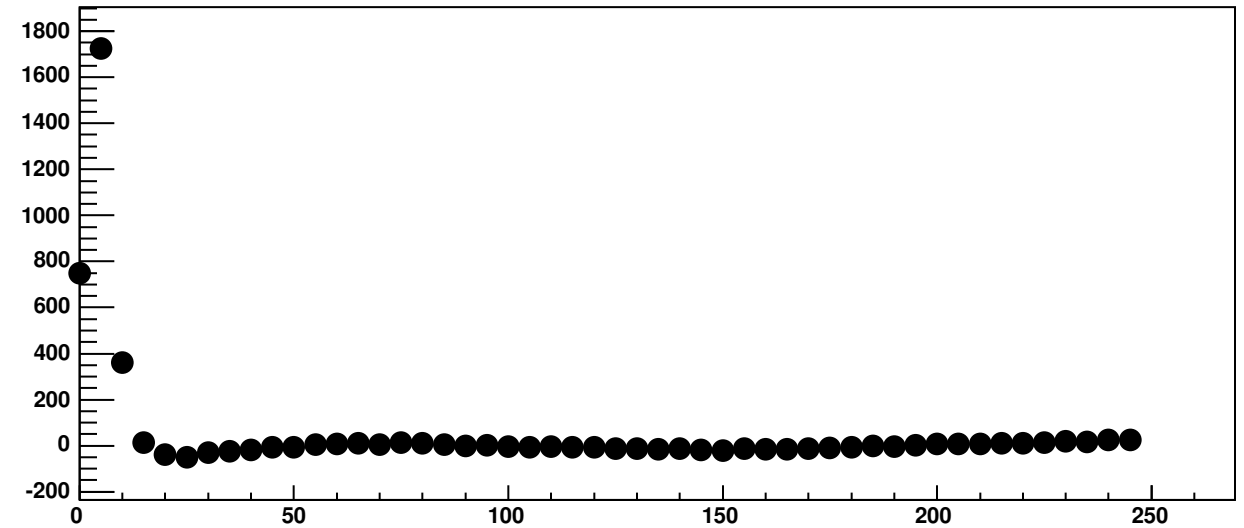
χ^2 / ndf	1784 / 42
p0	-684.3 ± 0.5815
p1	4.951 ± 0.005407
p2	1.057e+04 ± 0.9576
p3	31.16 ± 0.004841



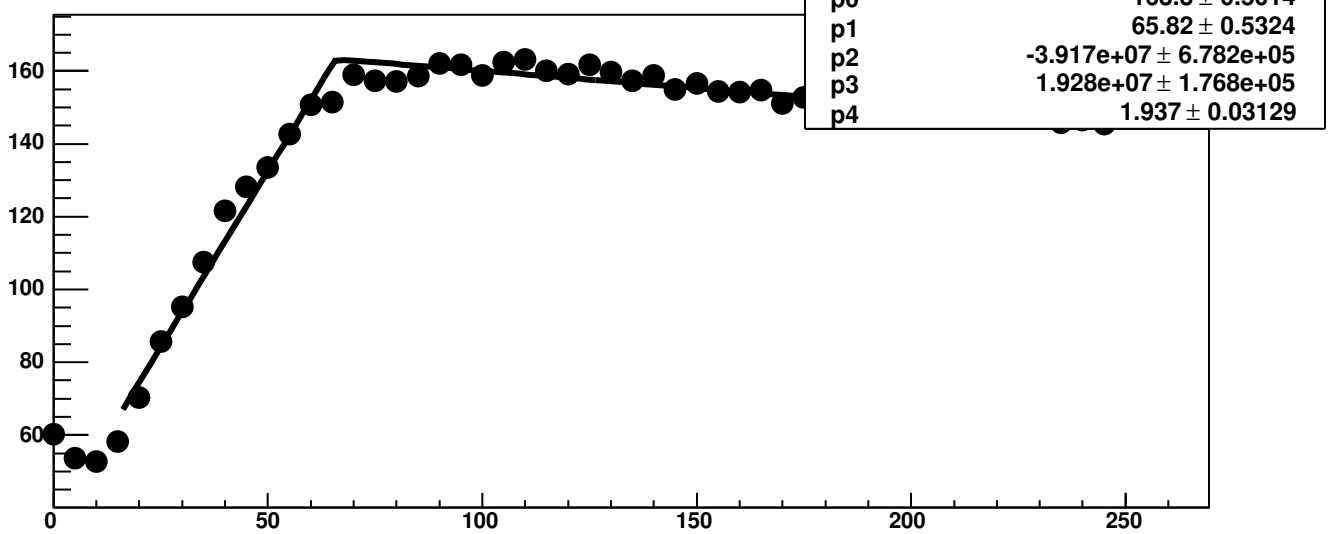
Chip 2, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold



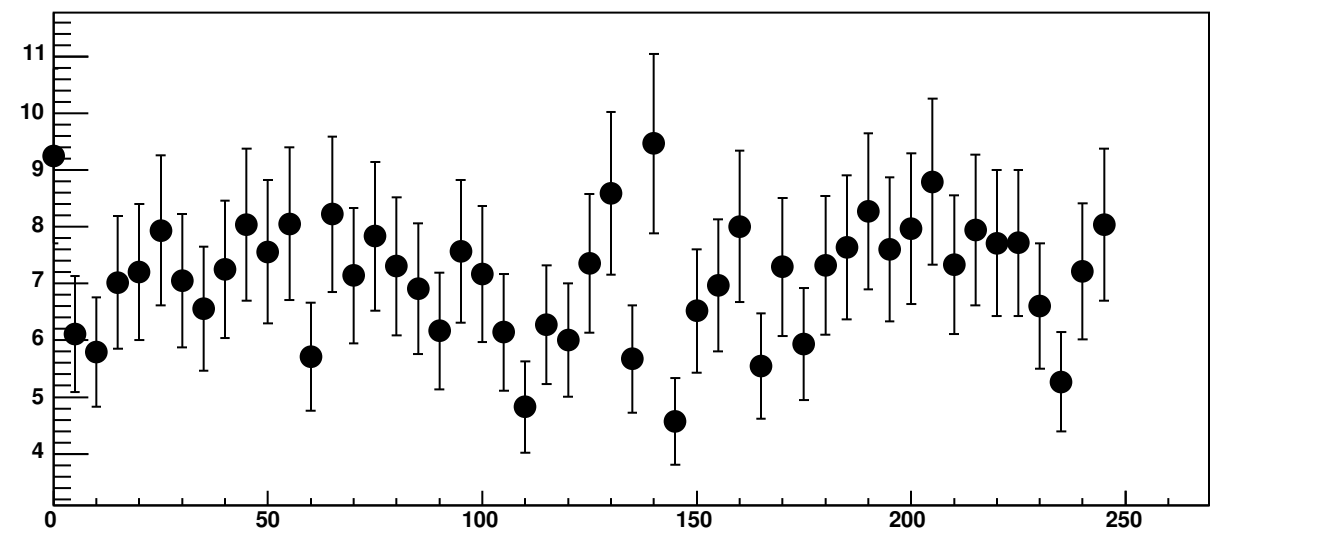
Chip 2, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold



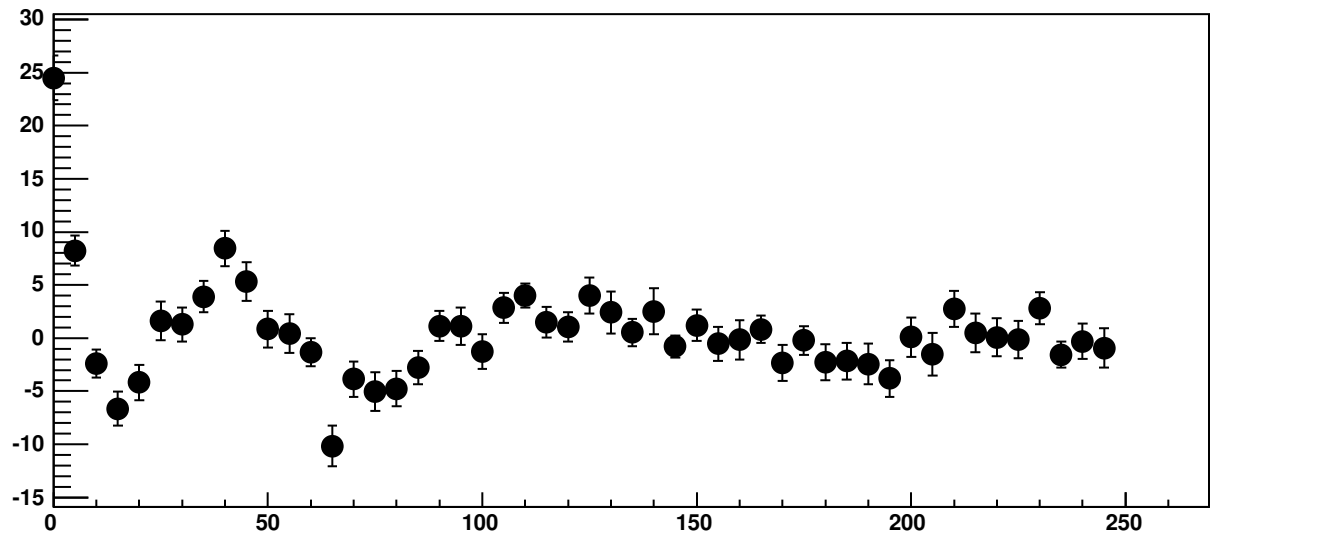
Chip 2, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



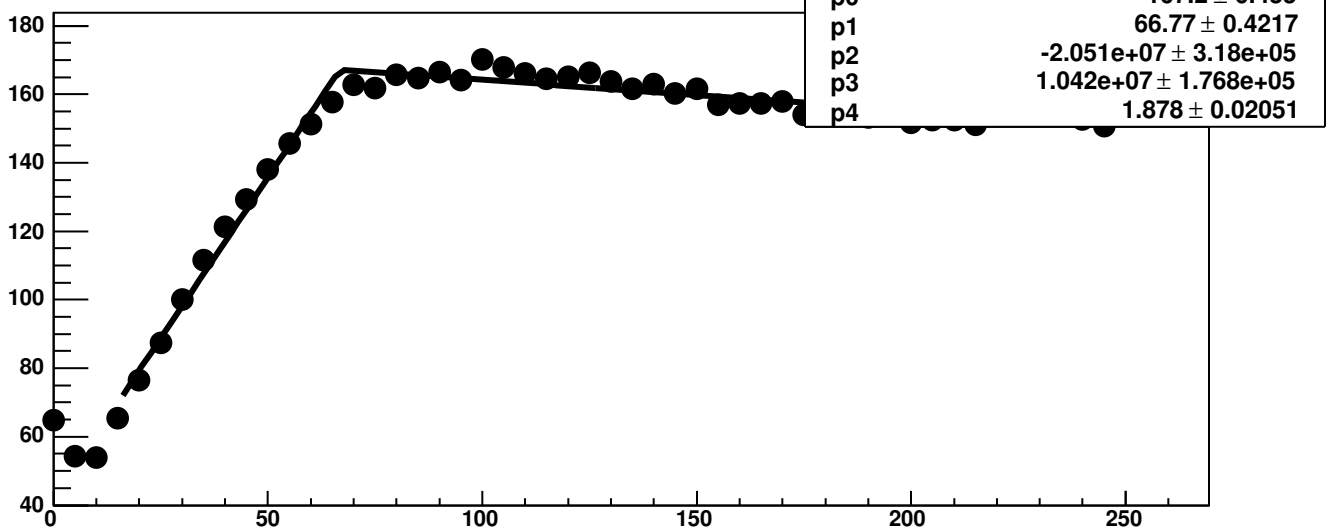
Chip 2, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



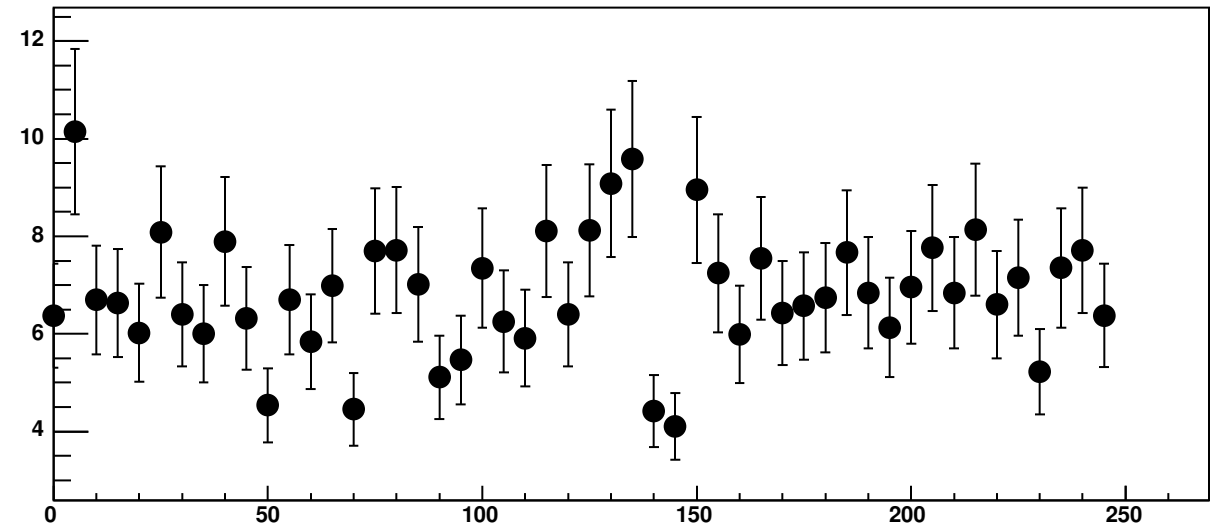
Chip 2, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold



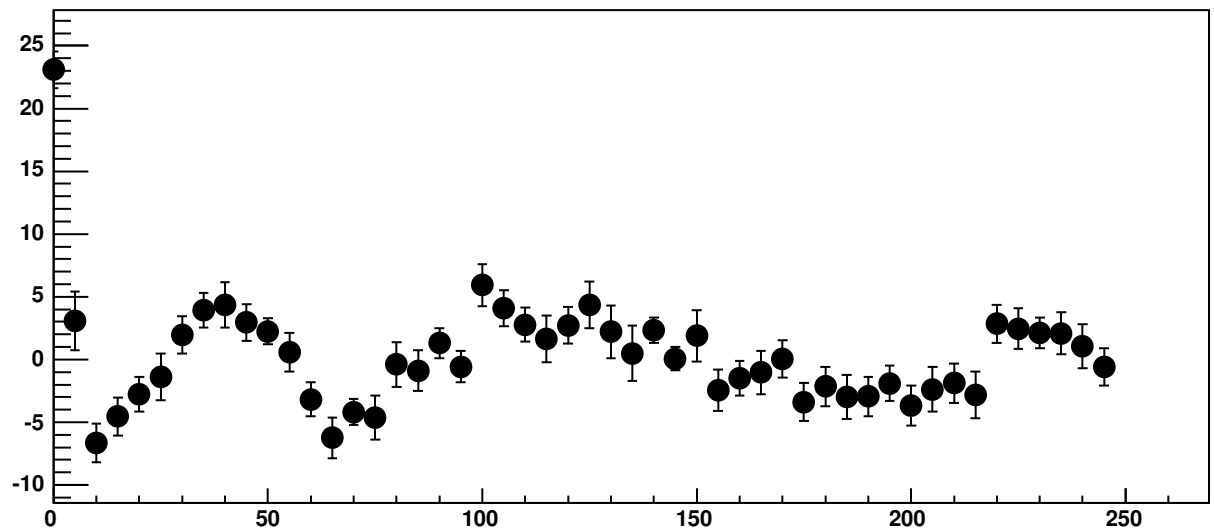
Chip 2, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



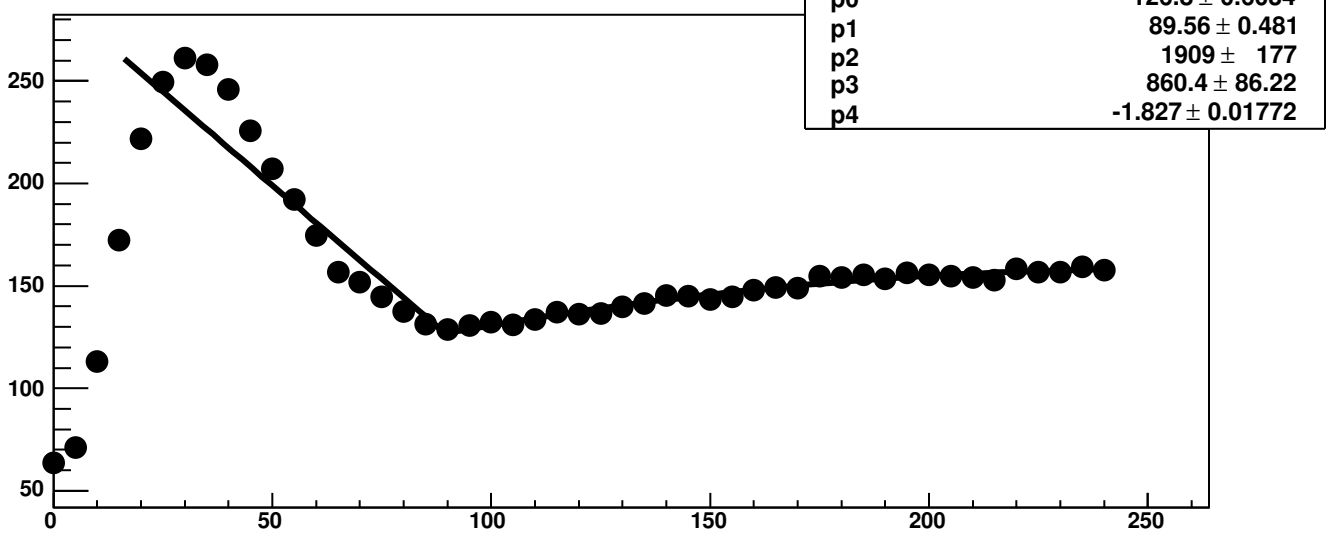
Chip 2, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



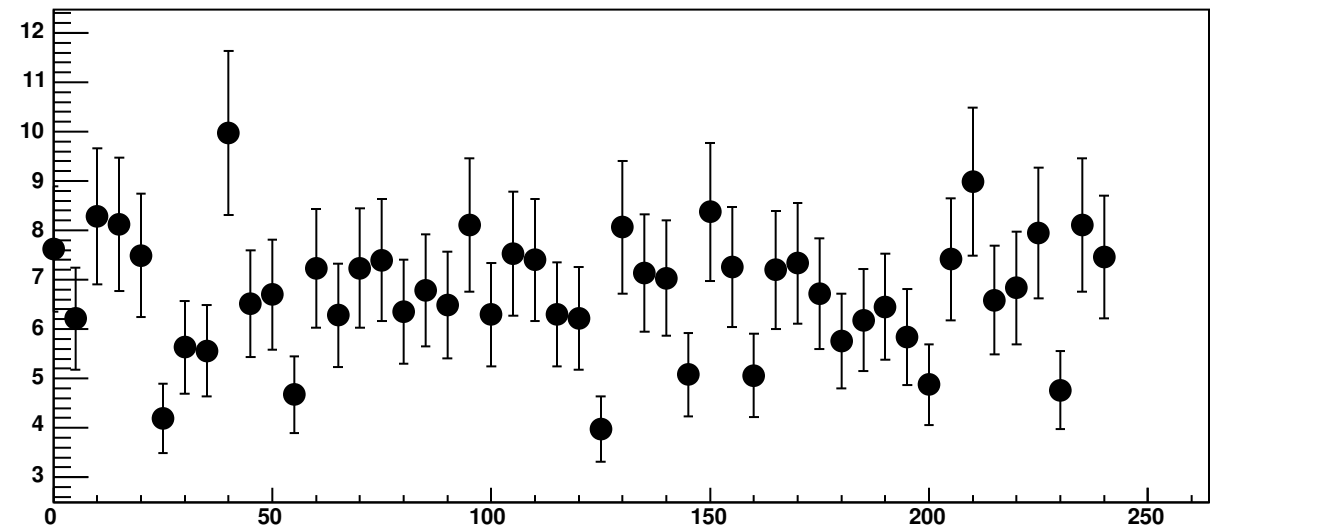
Chip 2, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold



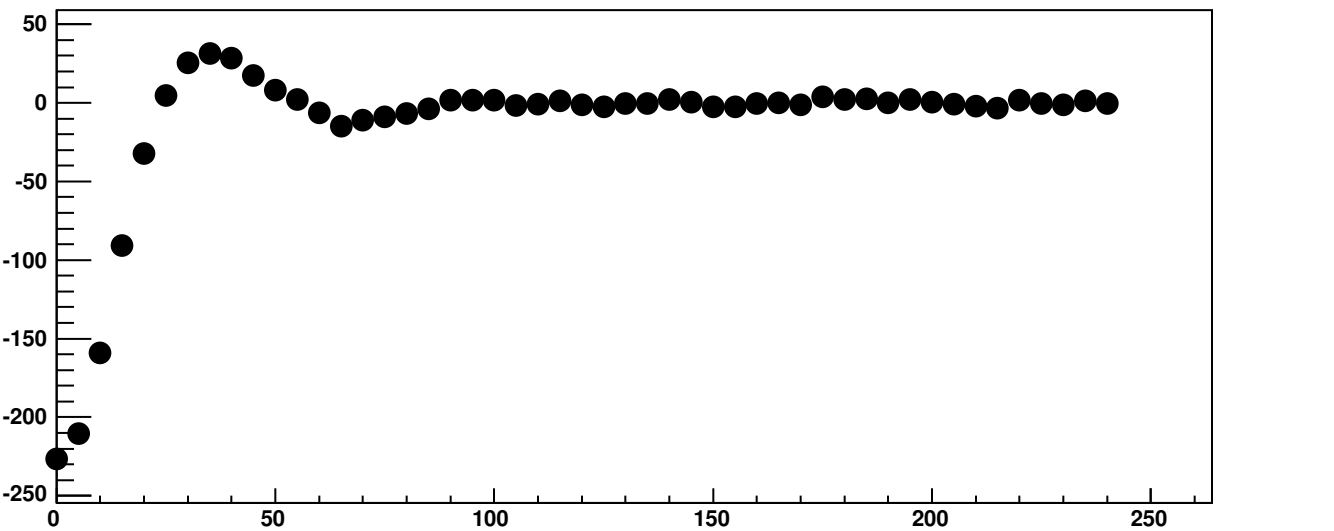
Chip 2, Channel 17, Enable 5, DAC=4000, ADC Mean vs Hold

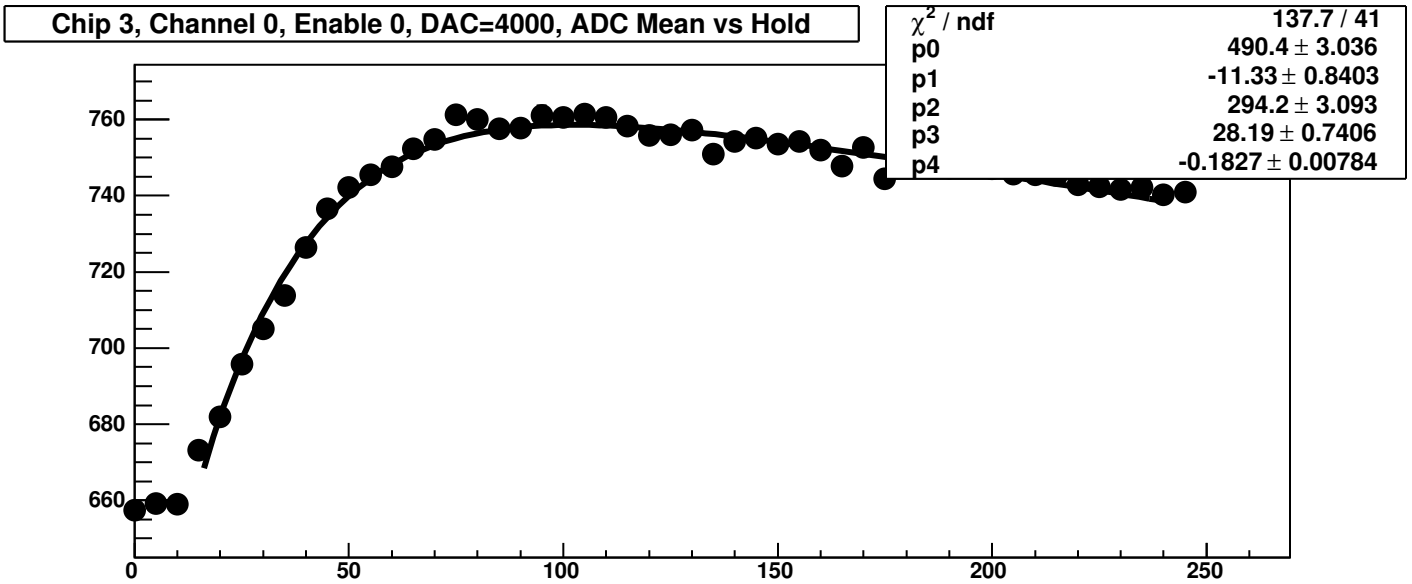


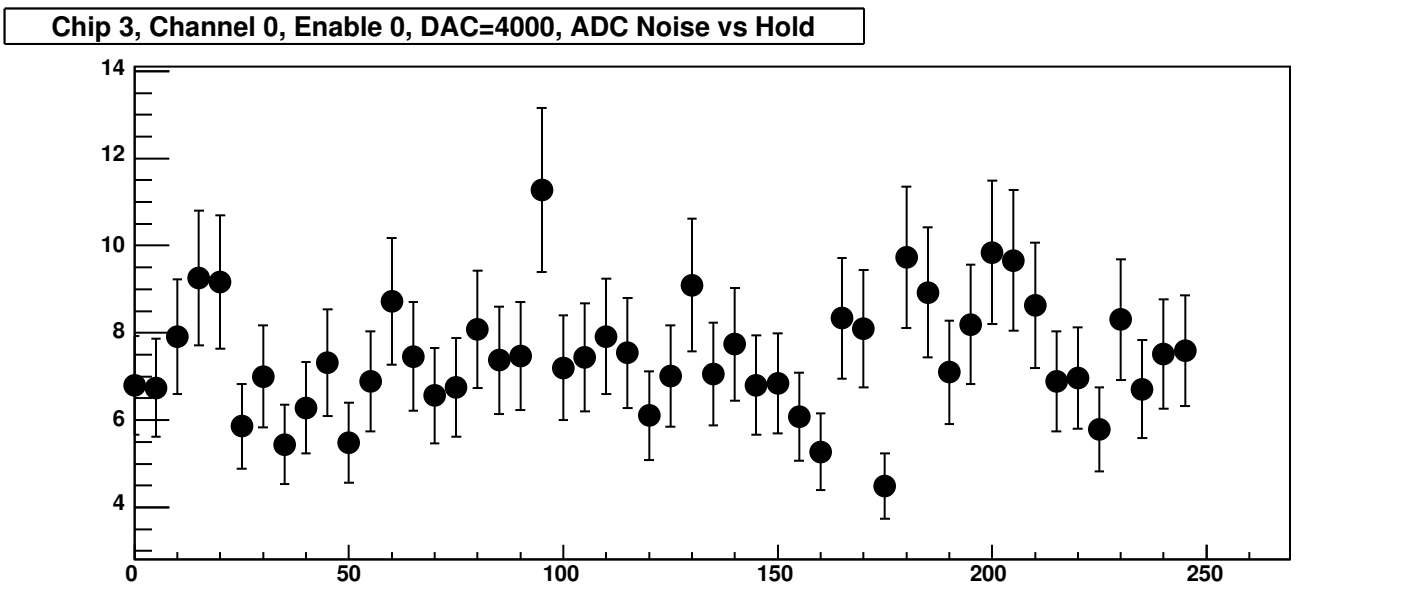
Chip 2, Channel 17, Enable 5, DAC=4000, ADC Noise vs Hold

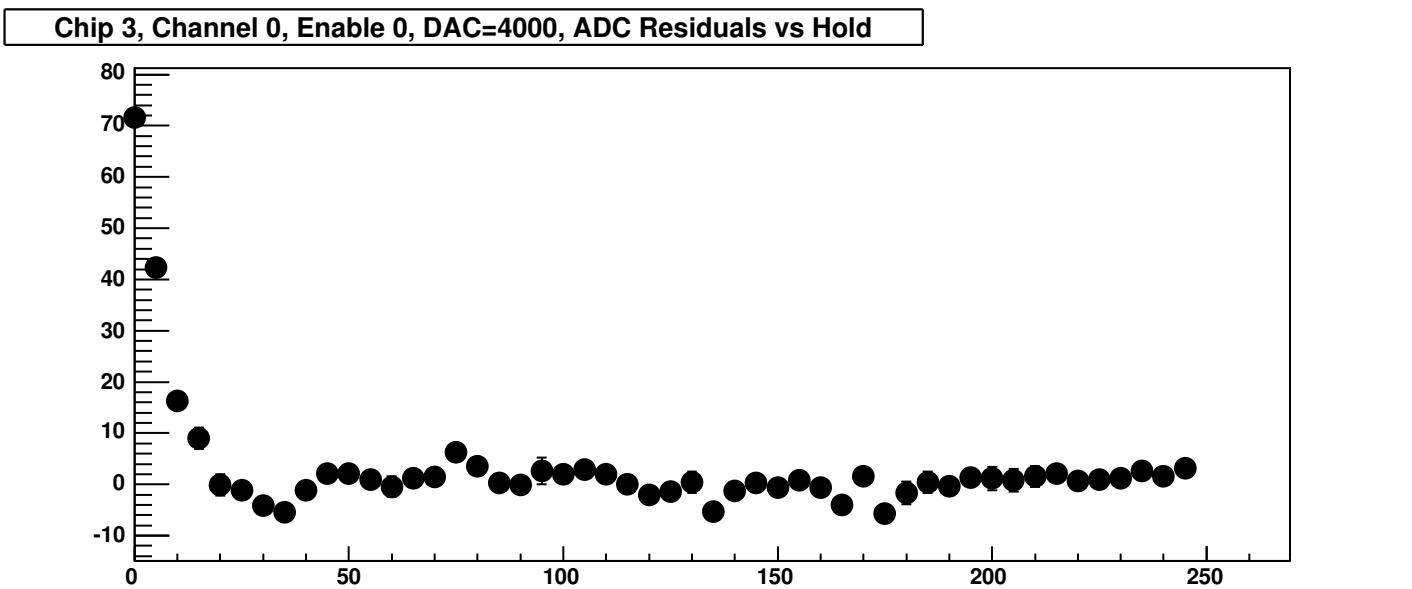


Chip 2, Channel 17, Enable 5, DAC=4000, ADC Residuals vs Hold

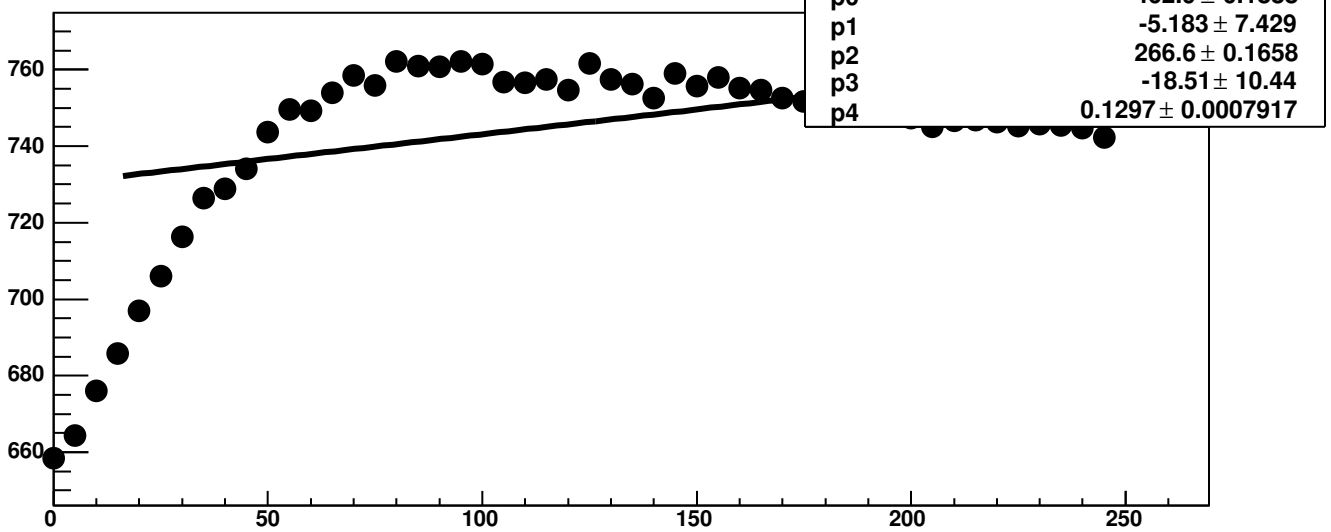



Chip 3, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 137.7 / 41
p0 490.4 ± 3.036
p1 -11.33 ± 0.8403
p2 294.2 ± 3.093
p3 28.19 ± 0.7406
p4 -0.1827 ± 0.00784
760
740
720
700
680
660
0
50
100
150
200
250

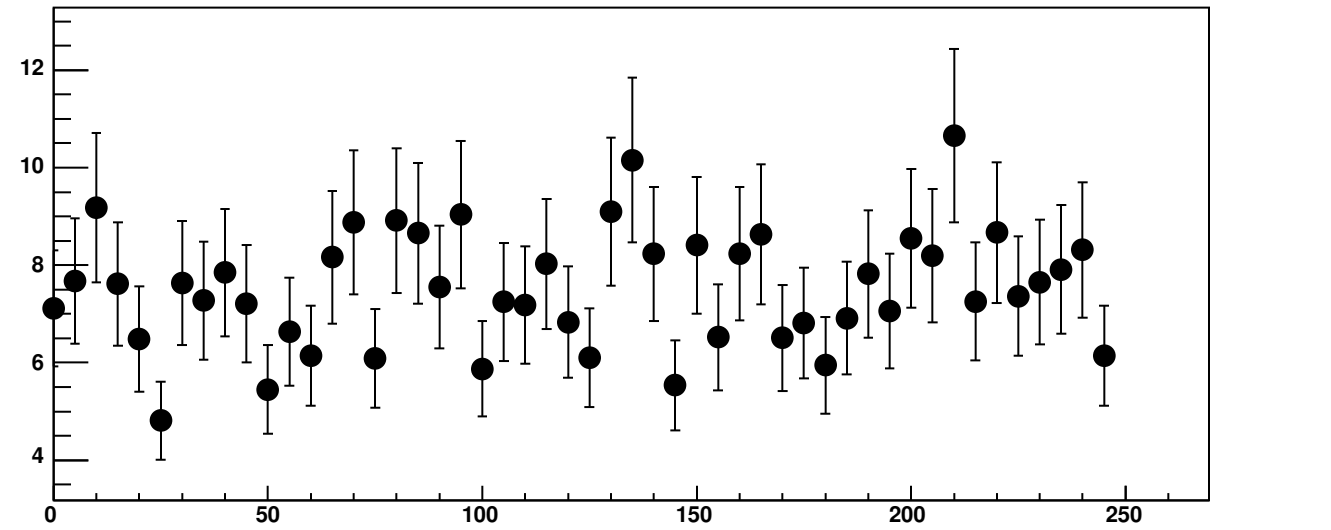

Chip 3, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 3, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold
80
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

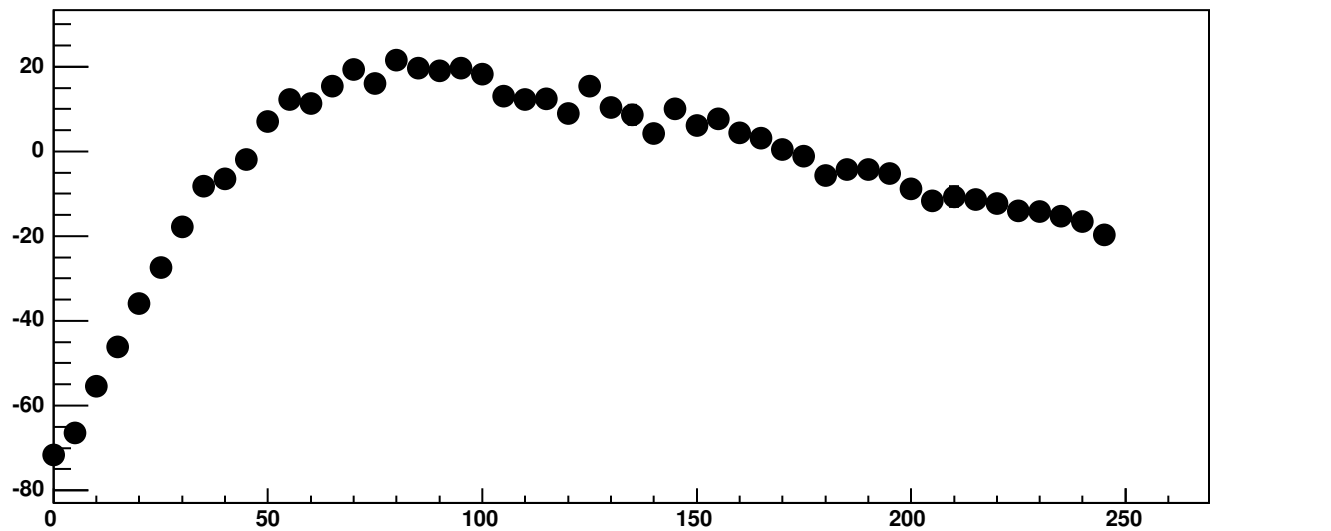
Chip 3, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold



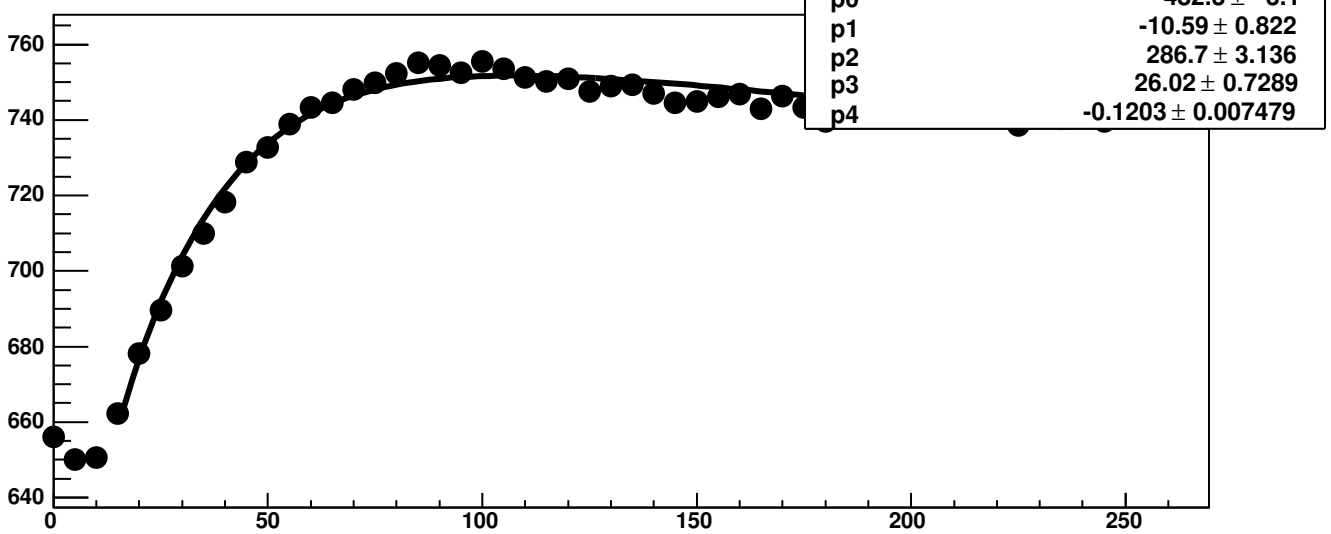
Chip 3, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



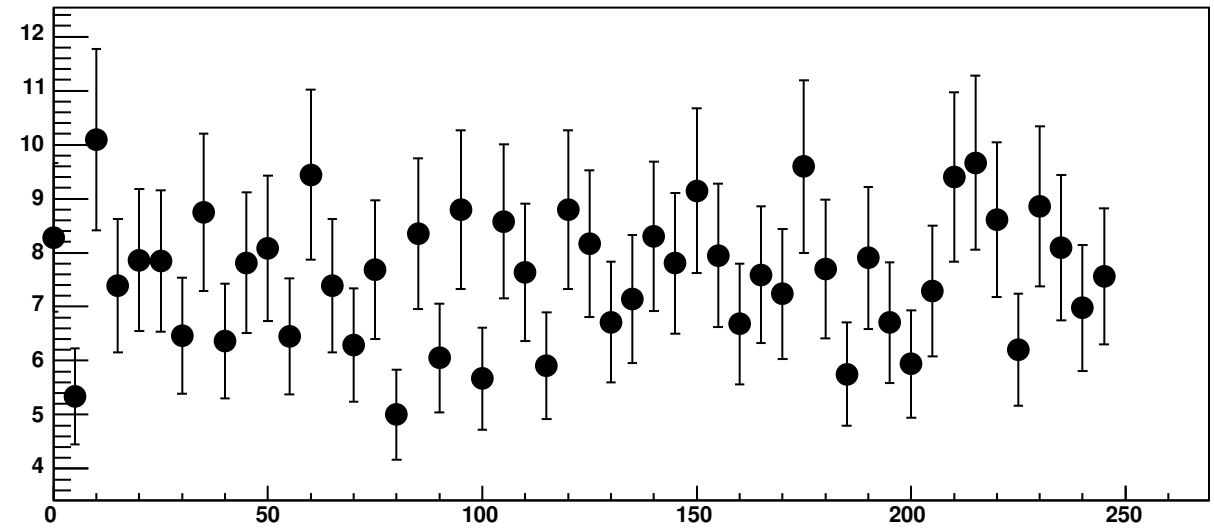
Chip 3, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold



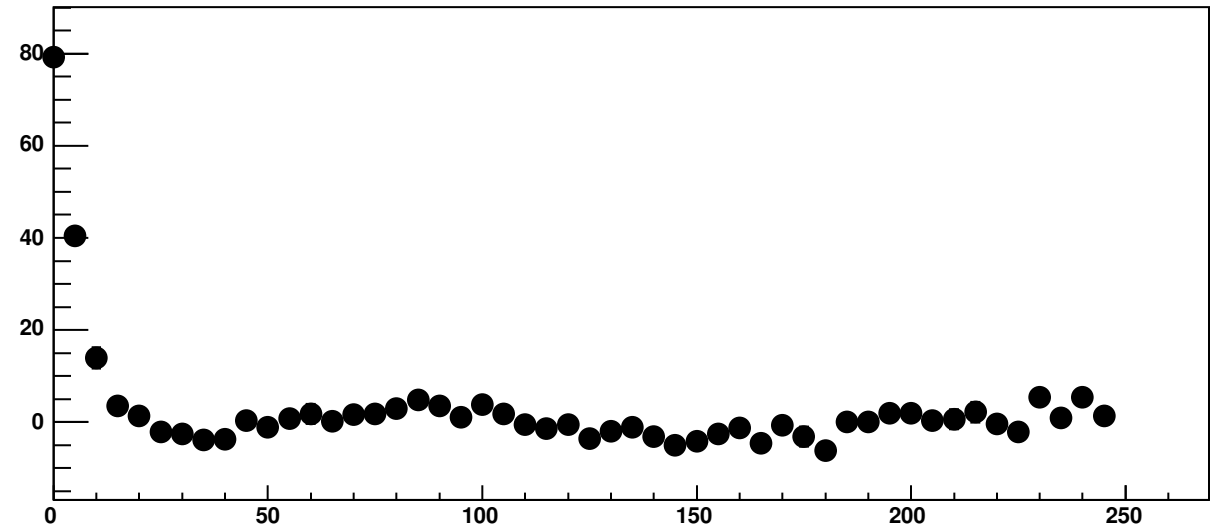
Chip 3, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold



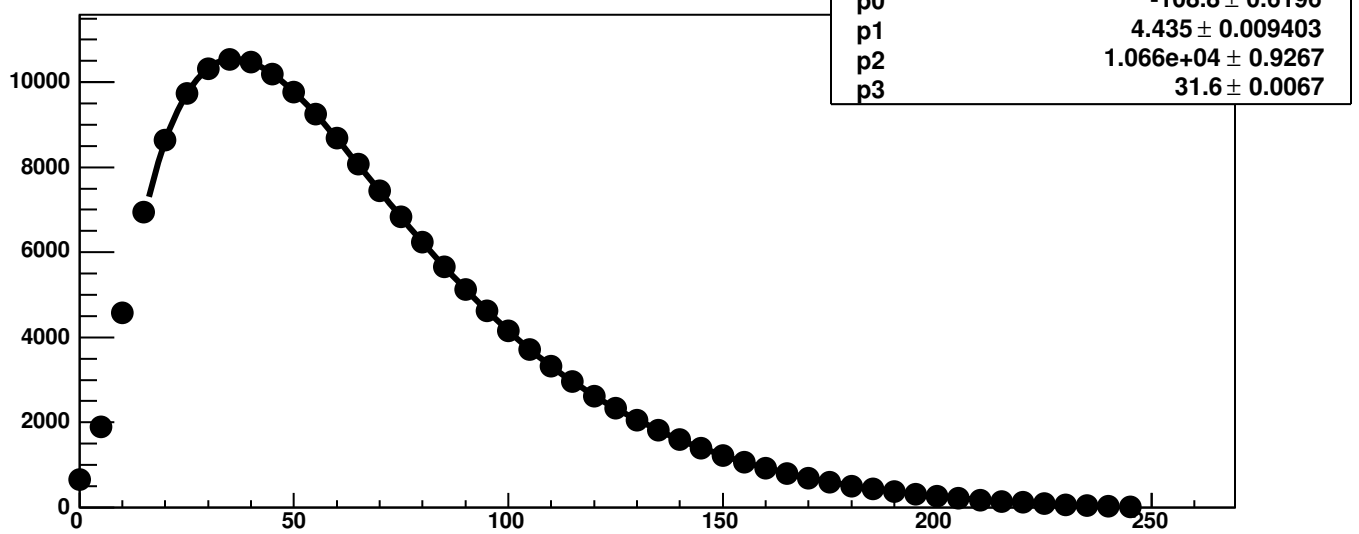
Chip 3, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



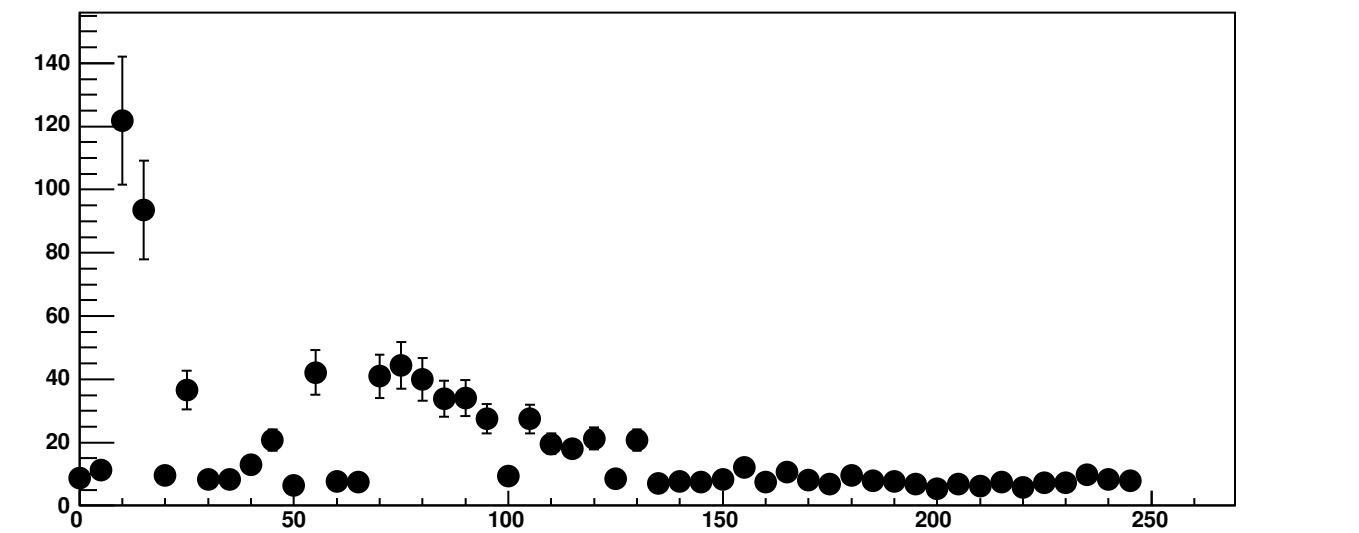
Chip 3, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold



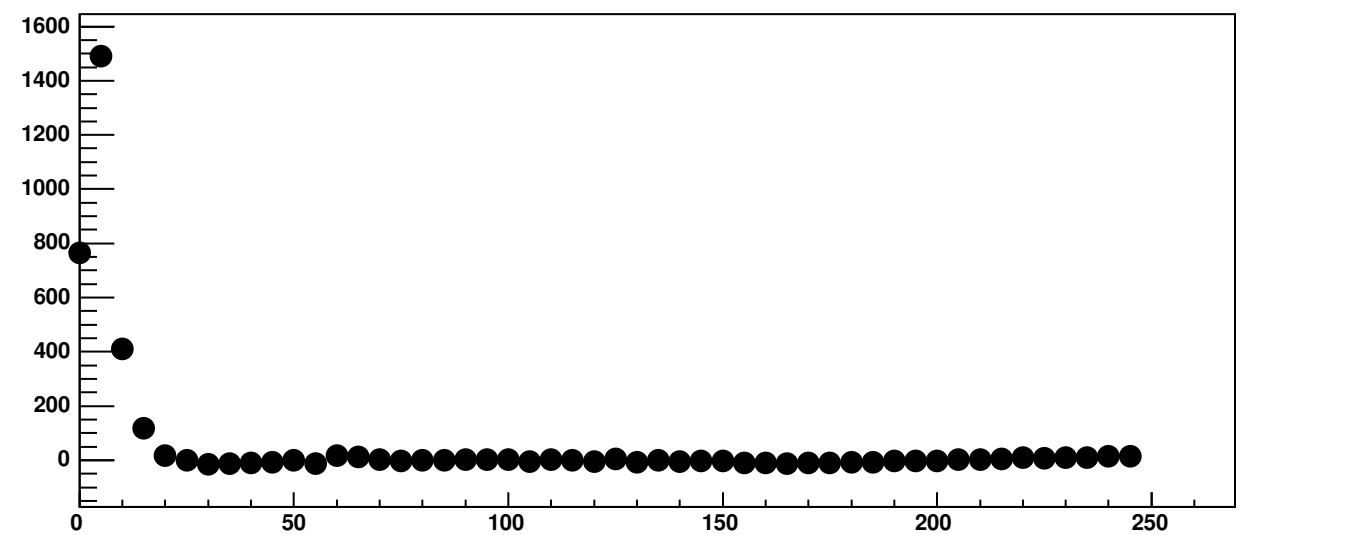
Chip 3, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold



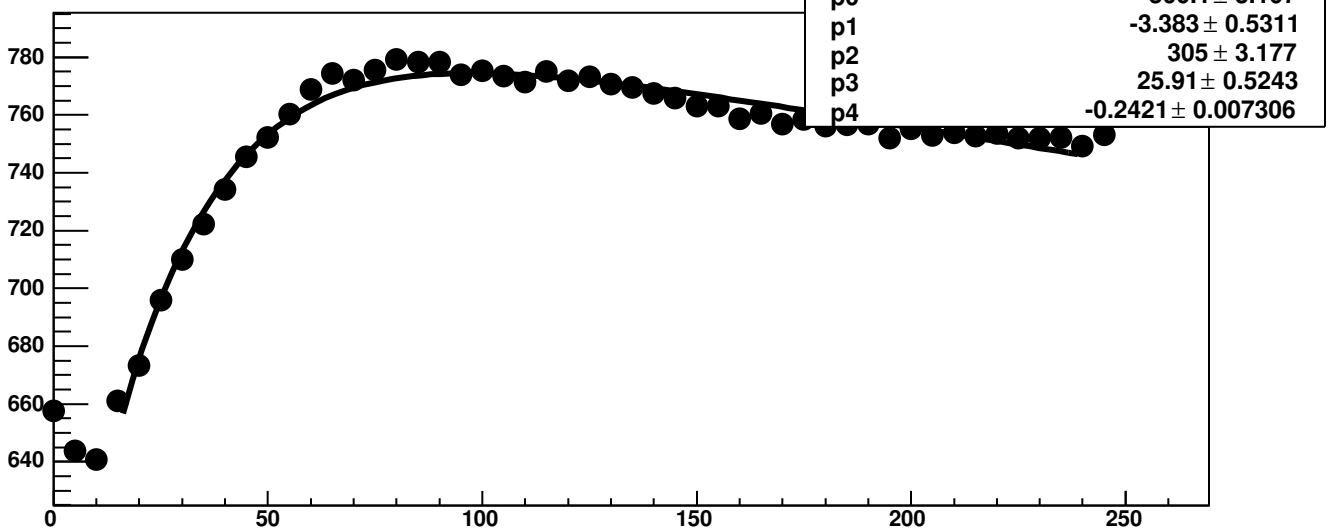
Chip 3, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold



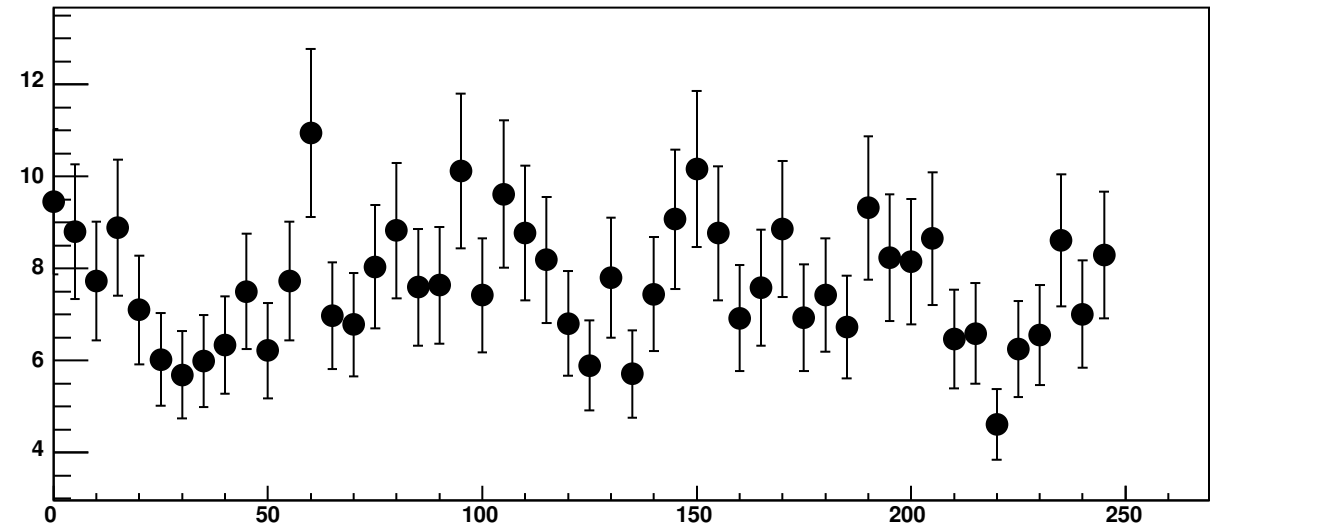
Chip 3, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold



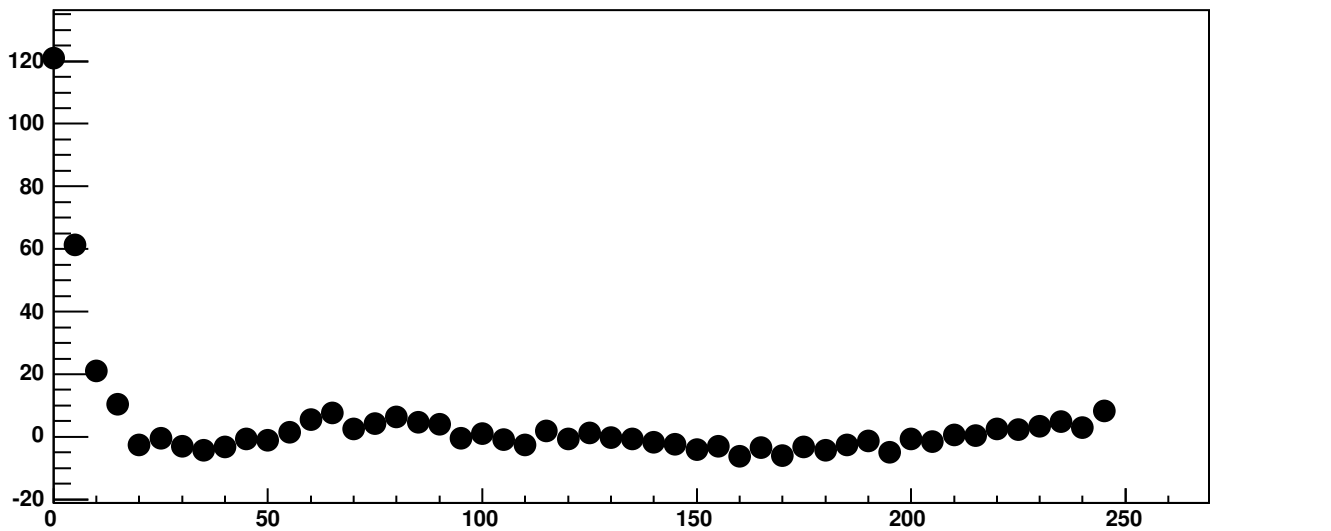
Chip 3, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold



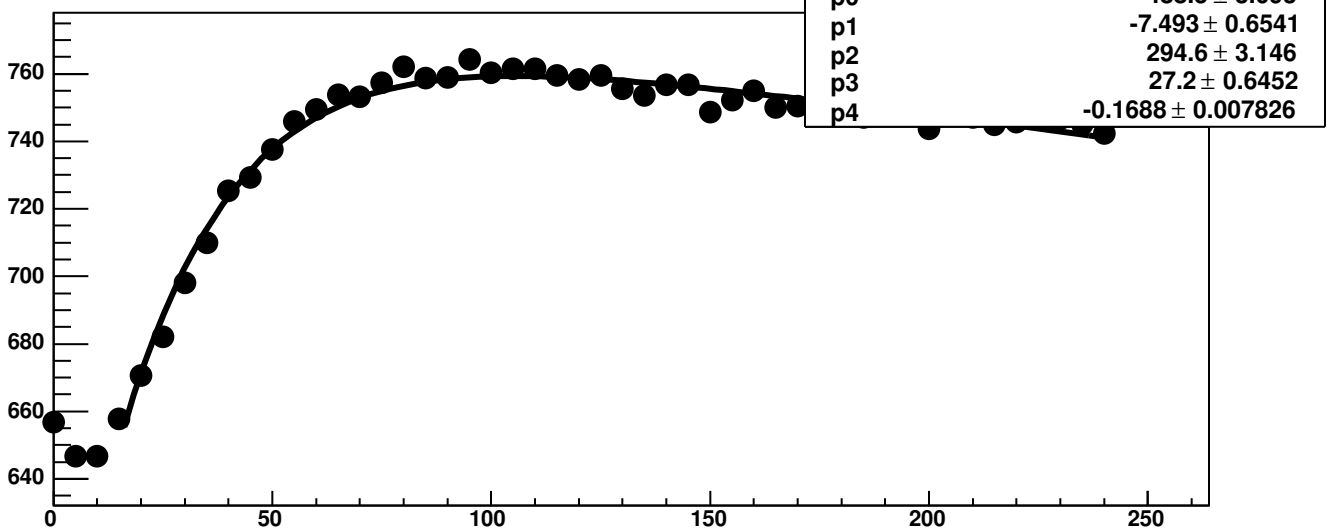
Chip 3, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



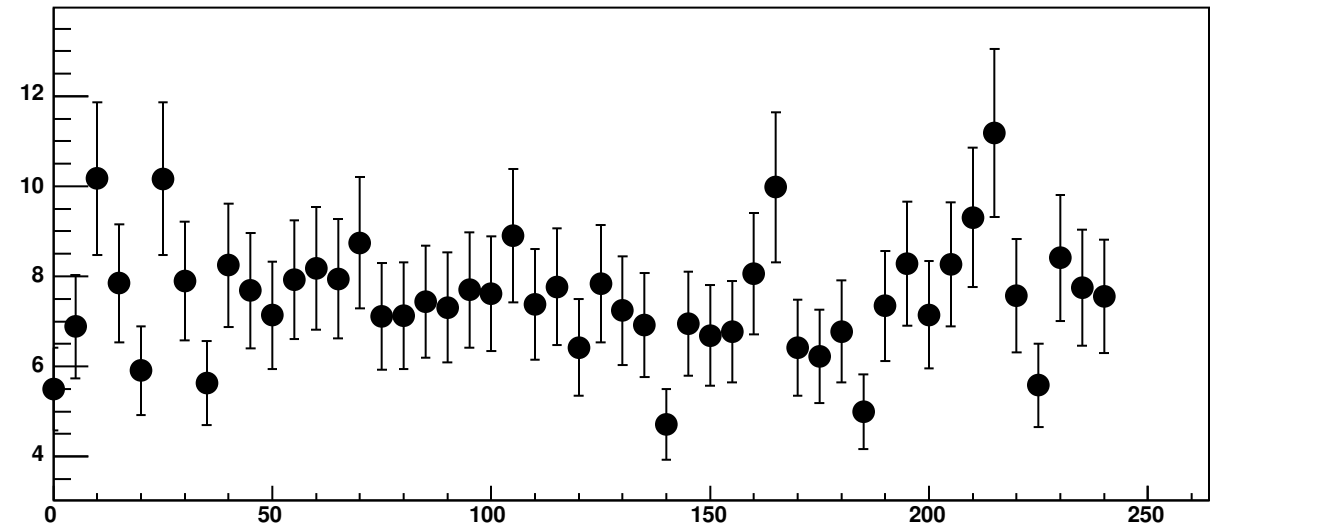
Chip 3, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold



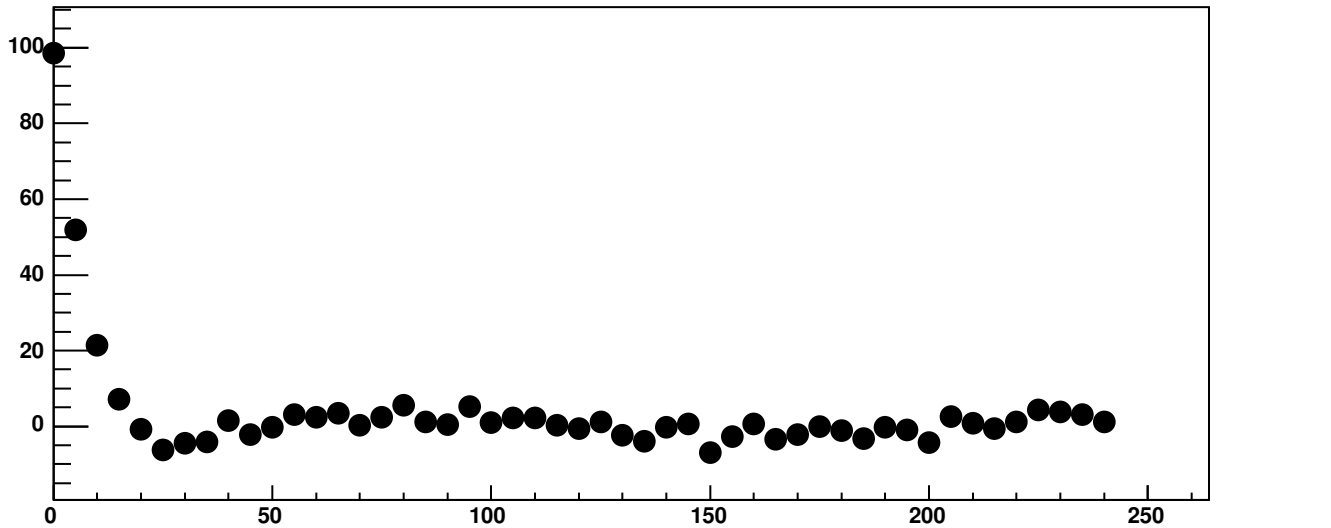
Chip 3, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold



Chip 3, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold

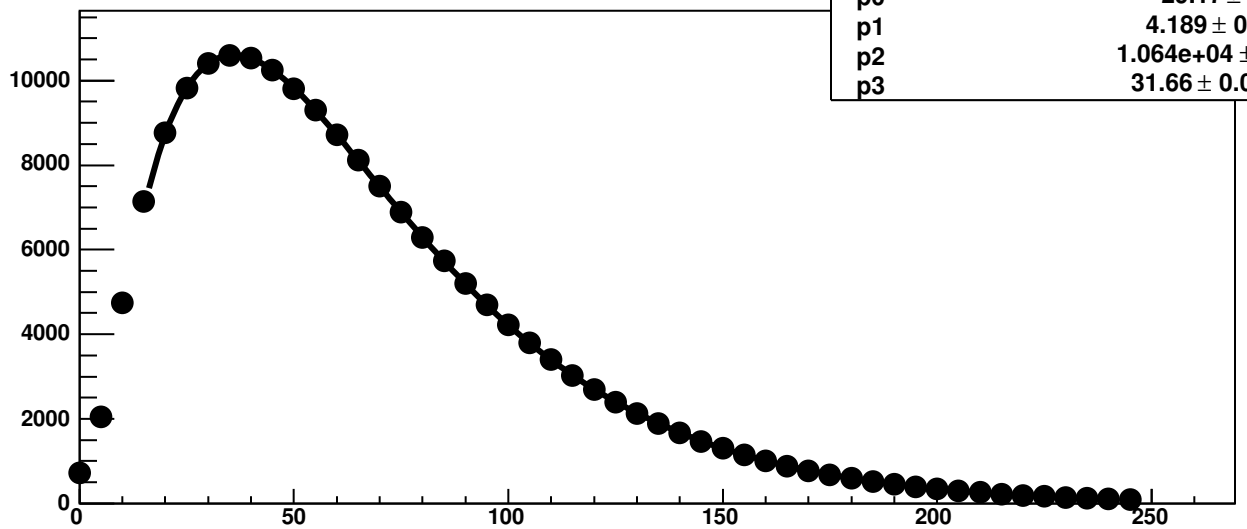


Chip 3, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold

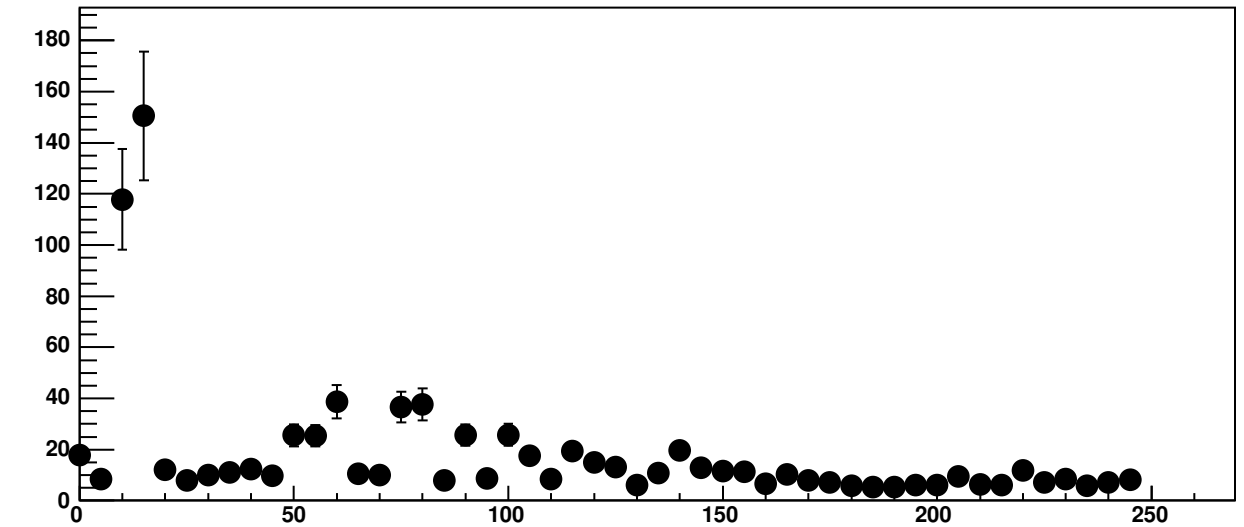


Chip 3, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold

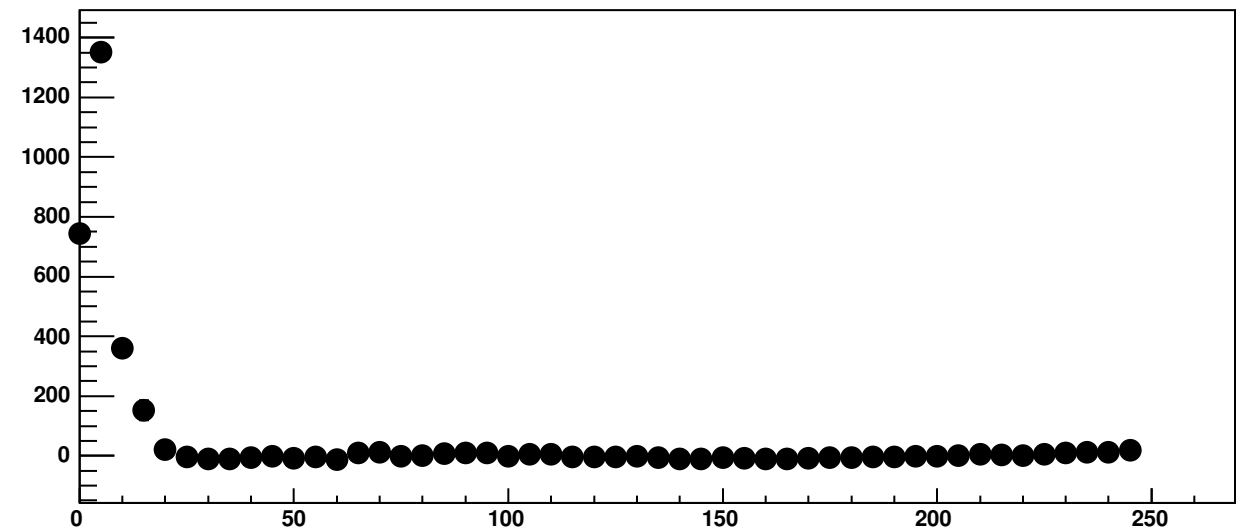
χ^2 / ndf	571.8 / 42
p0	-29.17 ± 0.5823
p1	4.189 ± 0.01084
p2	1.064e+04 ± 1.026
p3	31.66 ± 0.006823

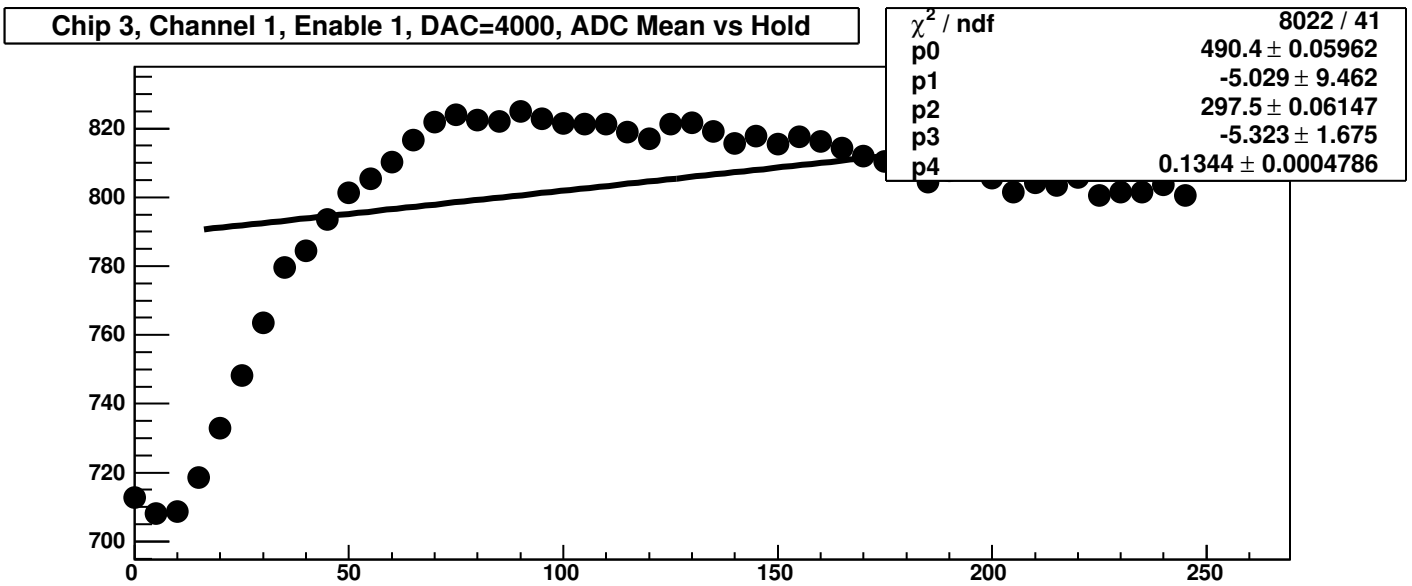


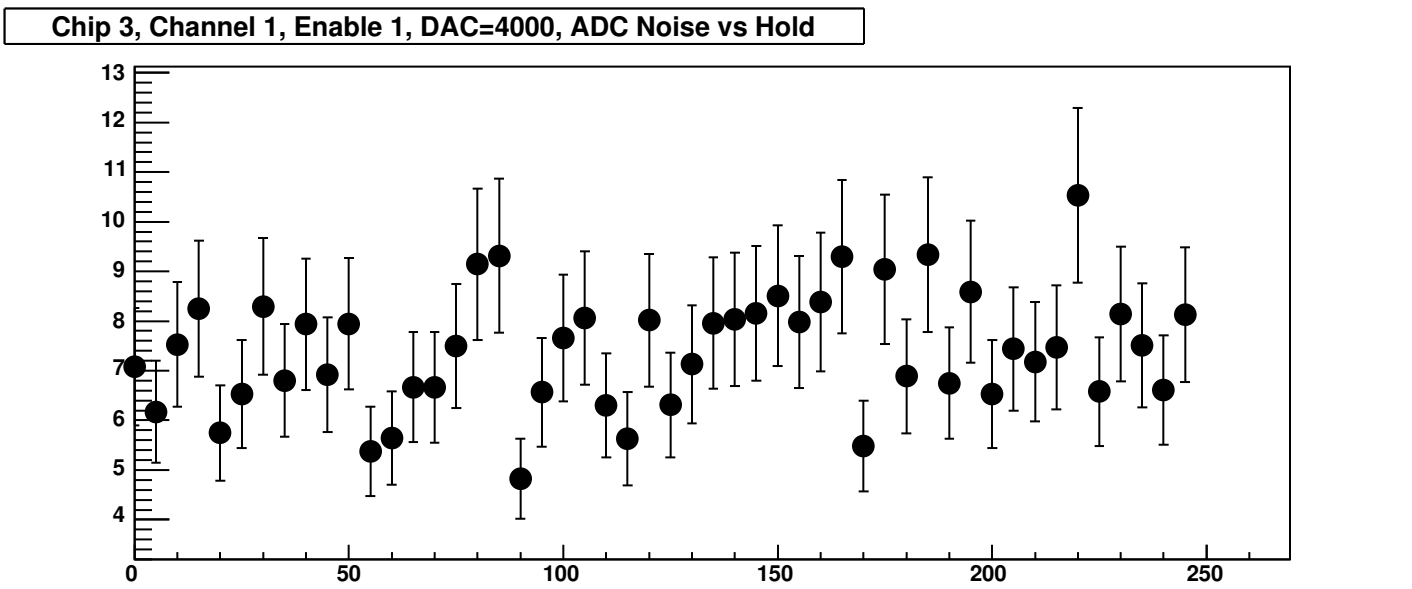
Chip 3, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold

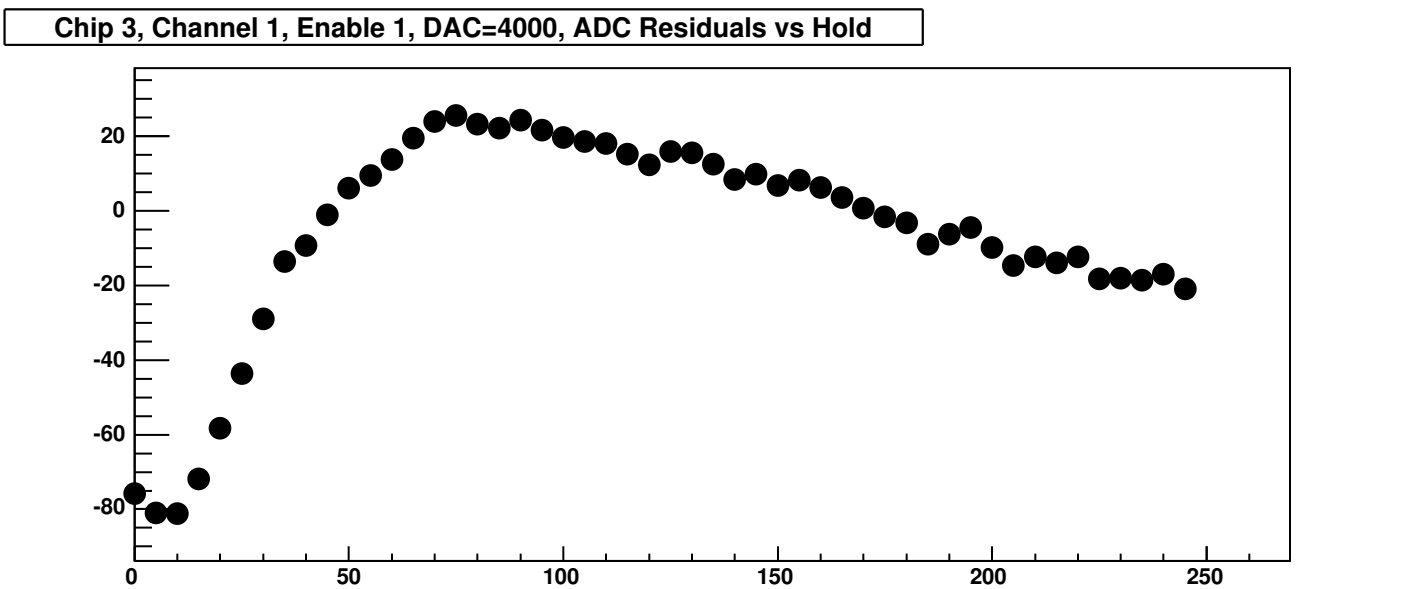


Chip 3, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold

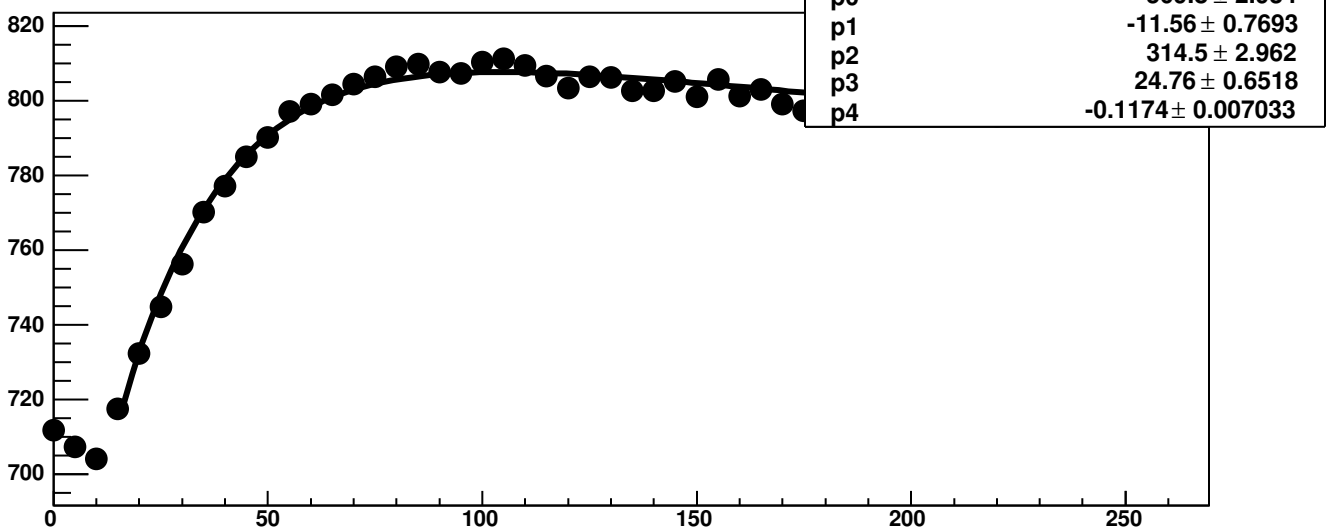



Chip 3, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 8022 / 41
p0 490.4 ± 0.05962
p1 -5.029 ± 9.462
p2 297.5 ± 0.06147
p3 -5.323 ± 1.675
p4 0.1344 ± 0.0004786
820
800
780
760
740
720
700
0
50
100
150
200
250

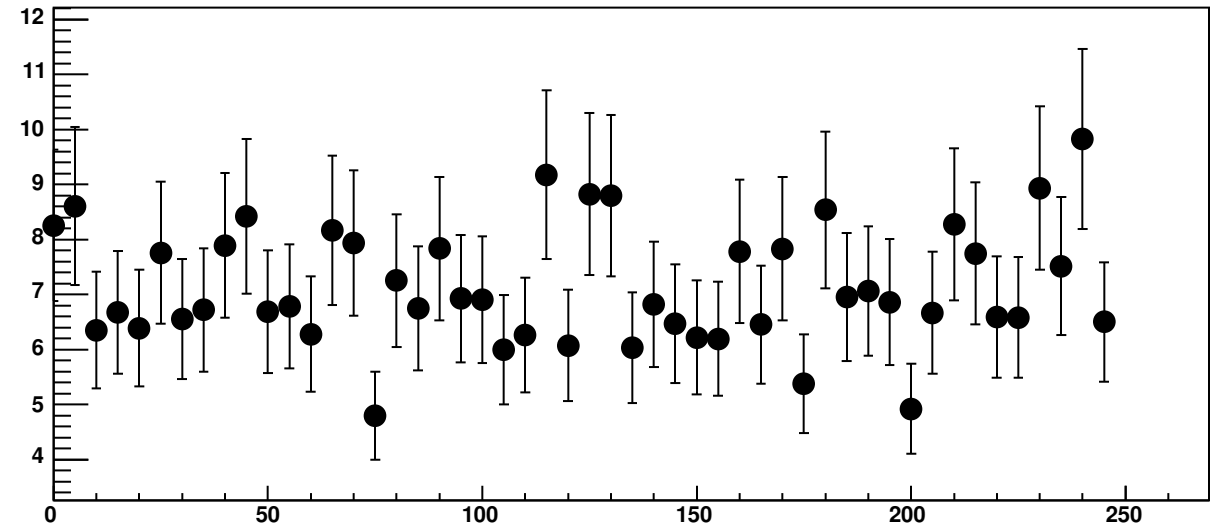

Chip 3, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 3, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
-80
0
50
100
150
200
250

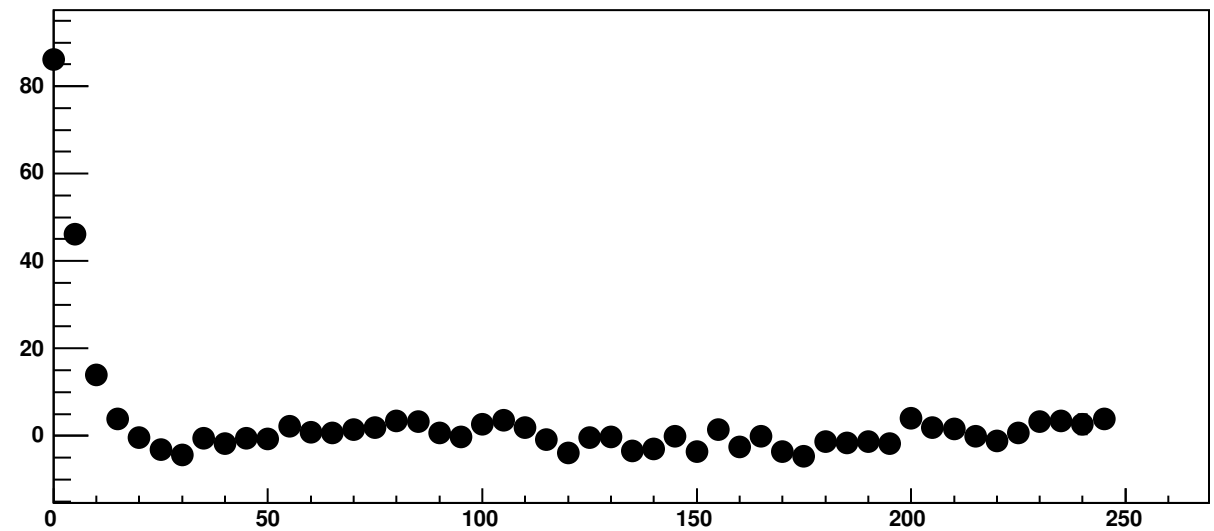
Chip 3, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

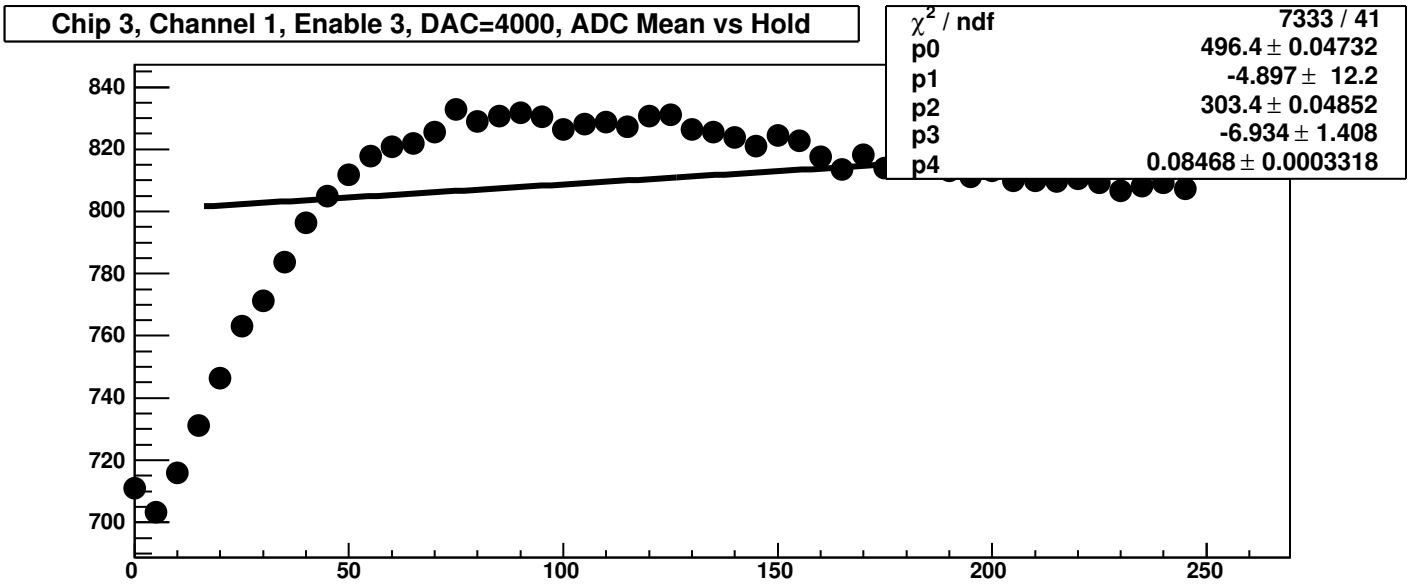


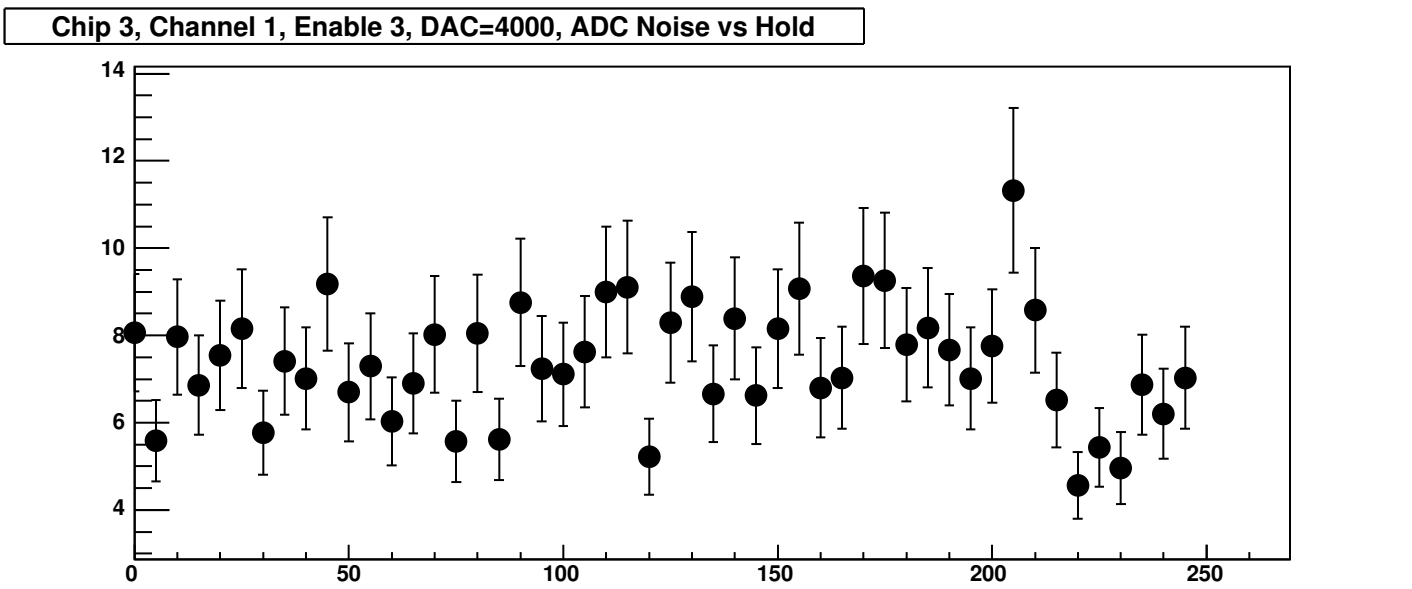
Chip 3, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold

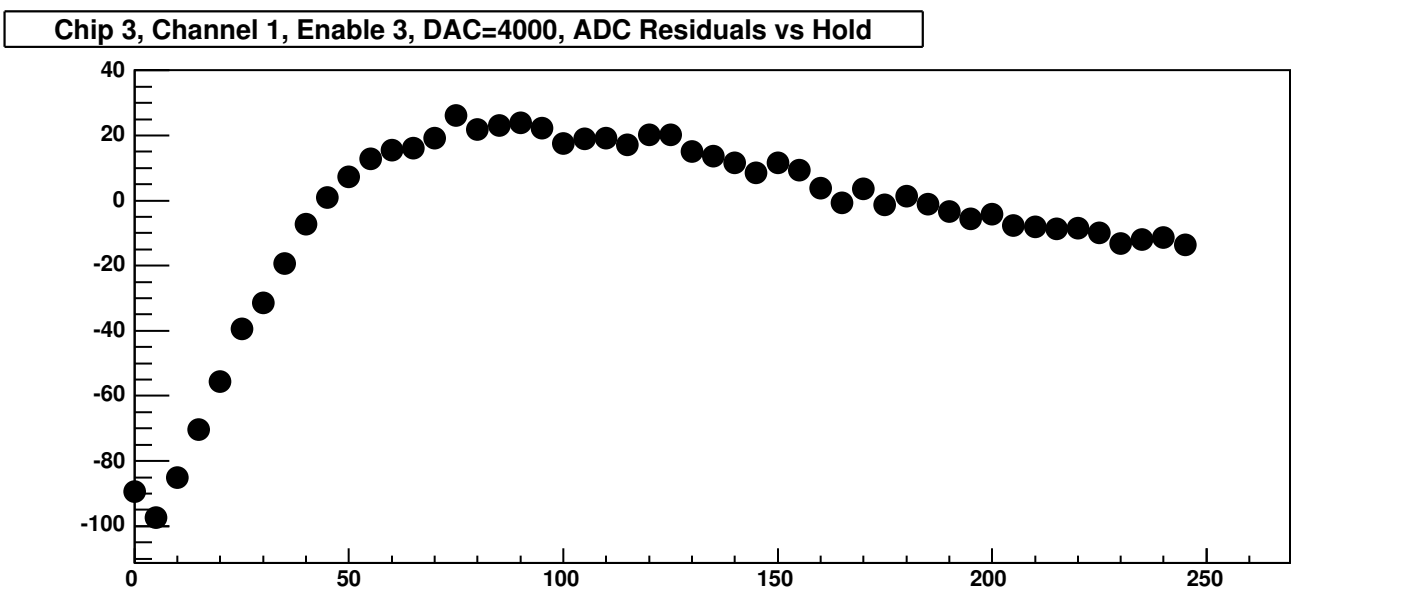


Chip 3, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

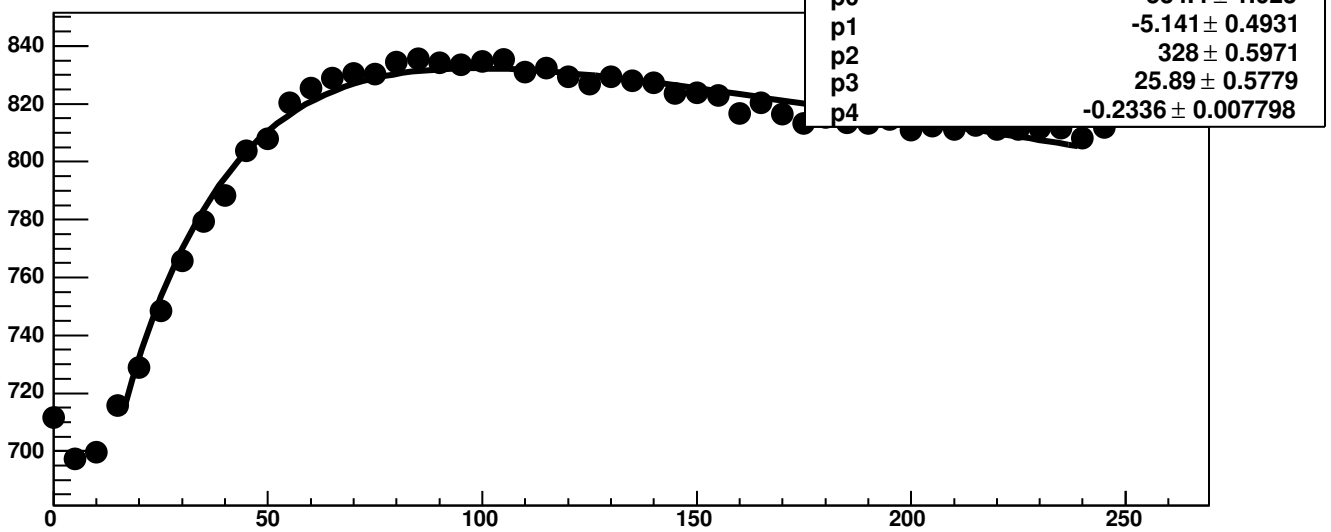



Chip 3, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 7333 / 41
p0 496.4 ± 0.04732
p1 -4.897 ± 12.2
p2 303.4 ± 0.04852
p3 -6.934 ± 1.408
p4 0.08468 ± 0.0003318
840
820
800
780
760
740
720
700
0
50
100
150
200
250

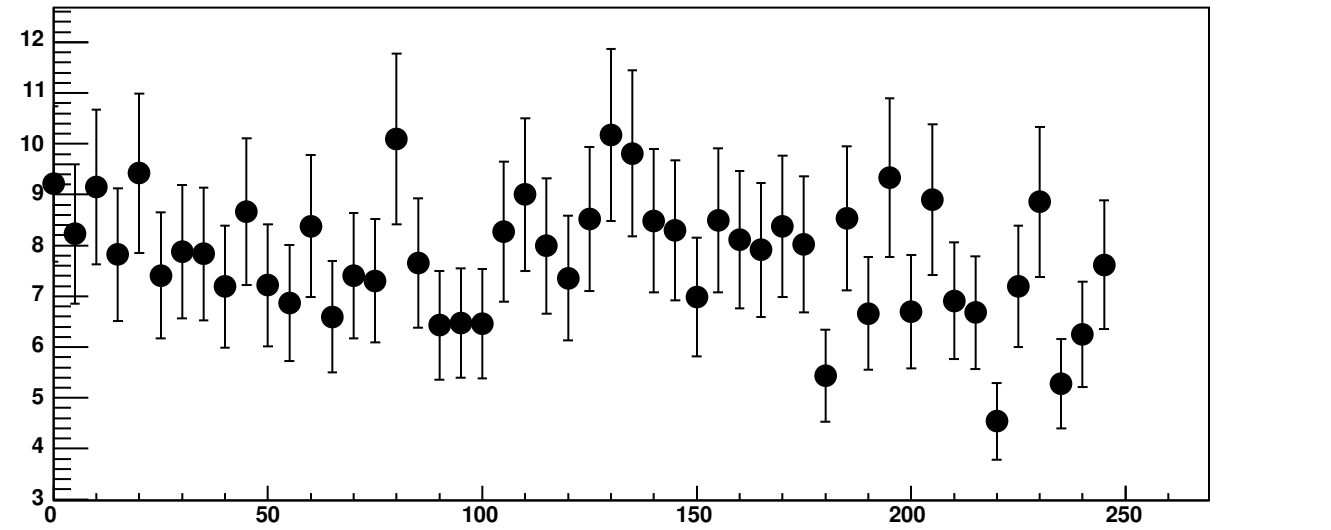

Chip 3, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 3, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
-100
0
50
100
150
200
250

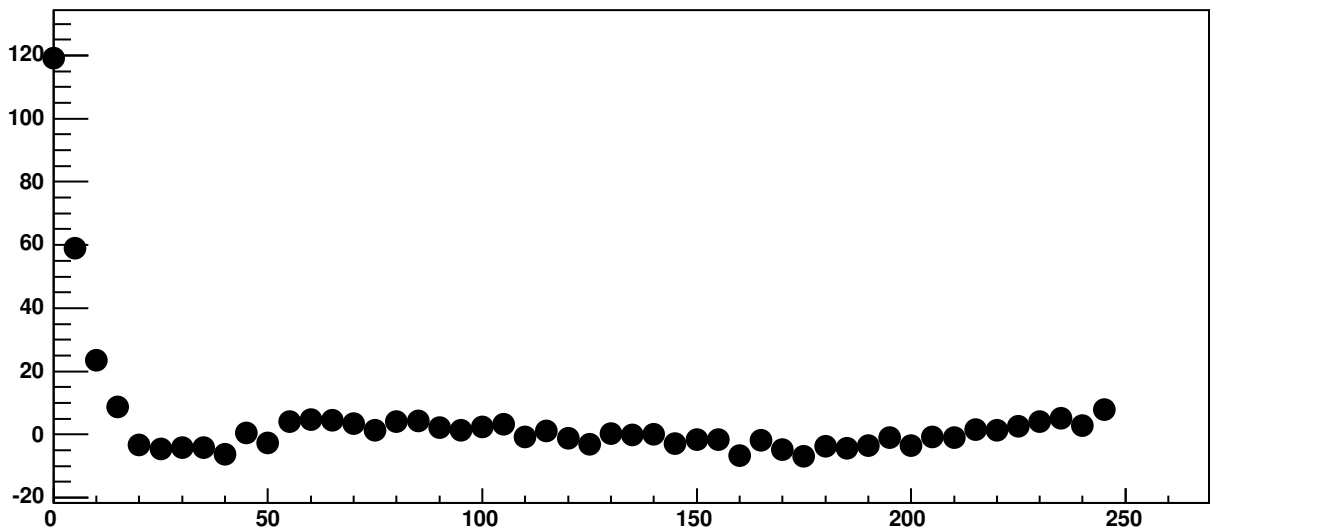
Chip 3, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold



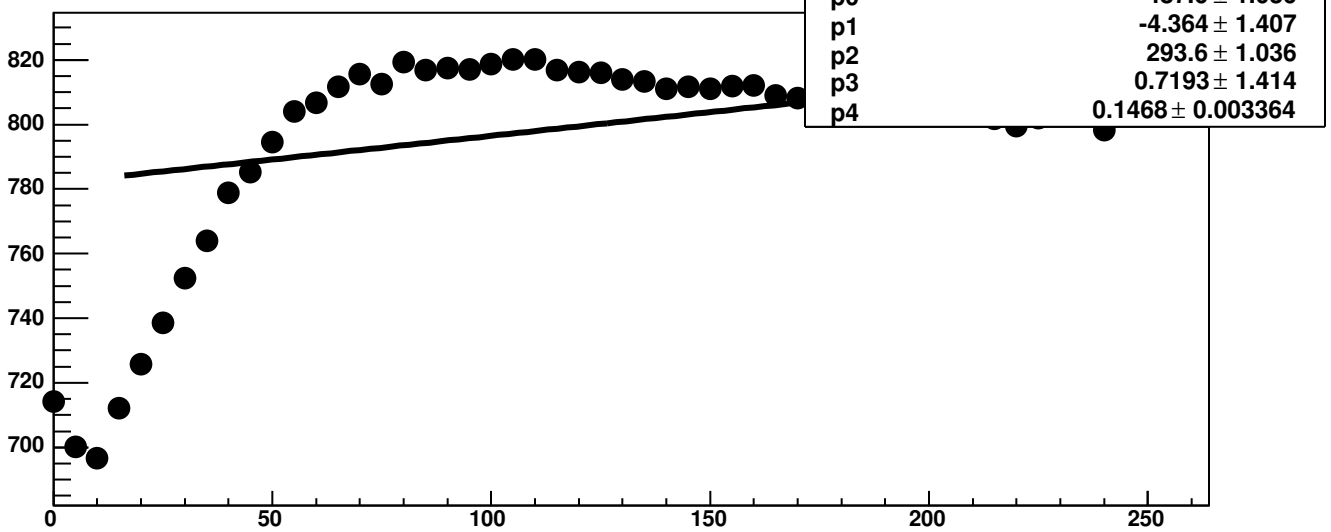
Chip 3, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



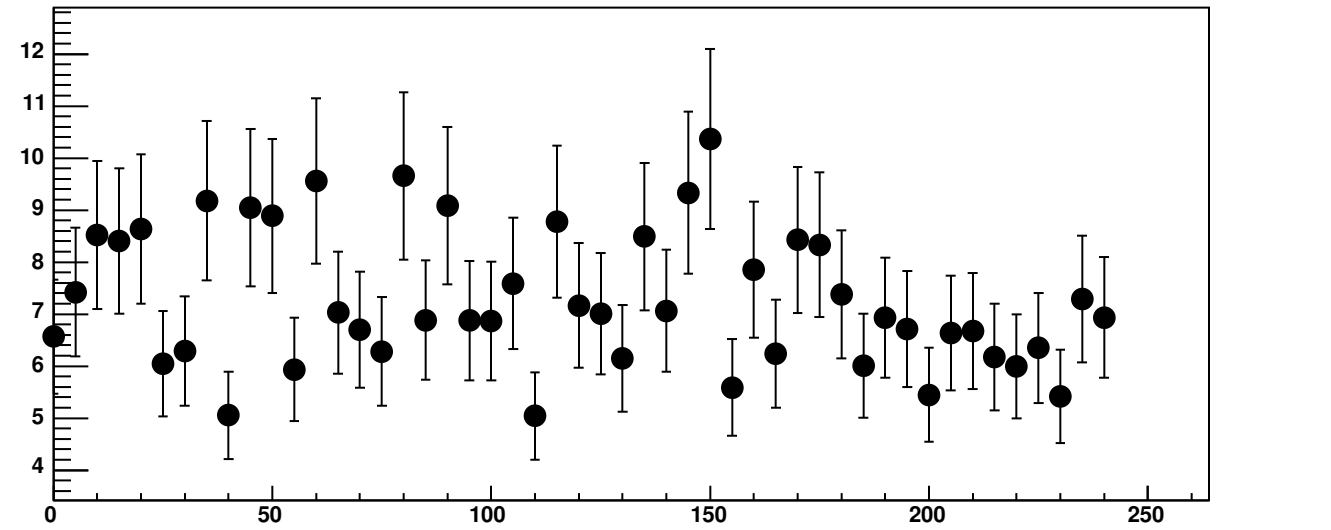
Chip 3, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold



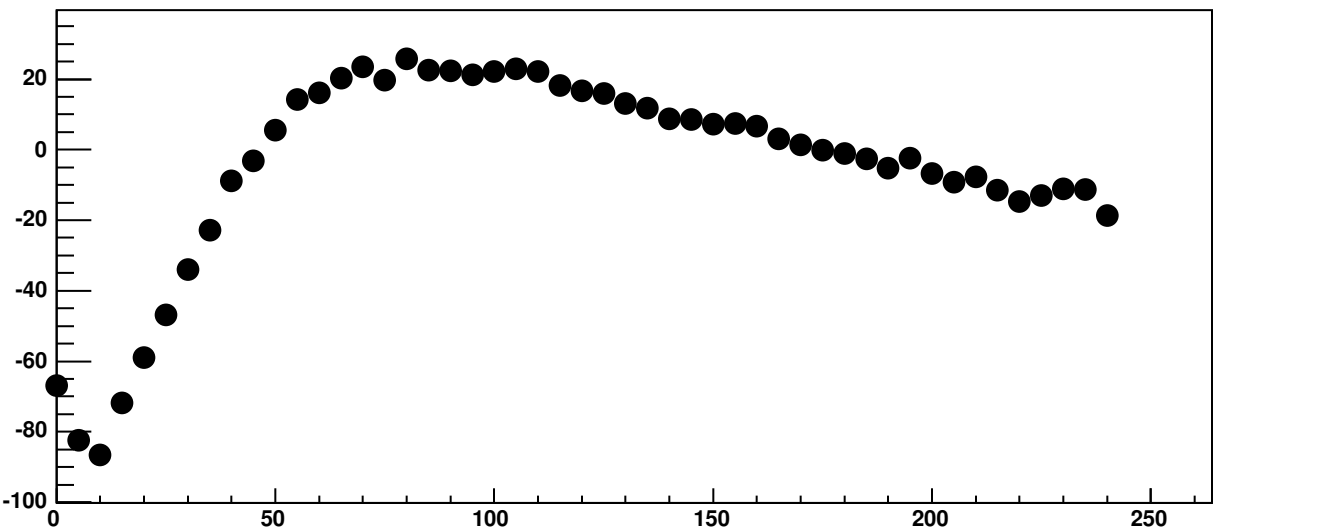
Chip 3, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

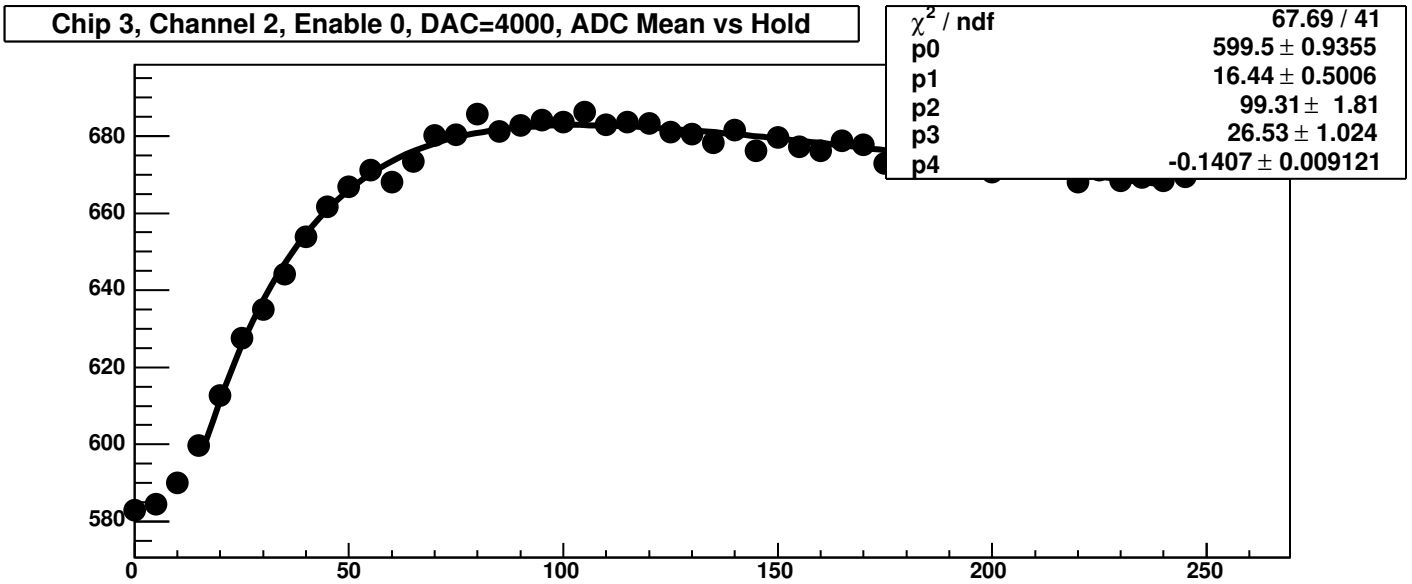


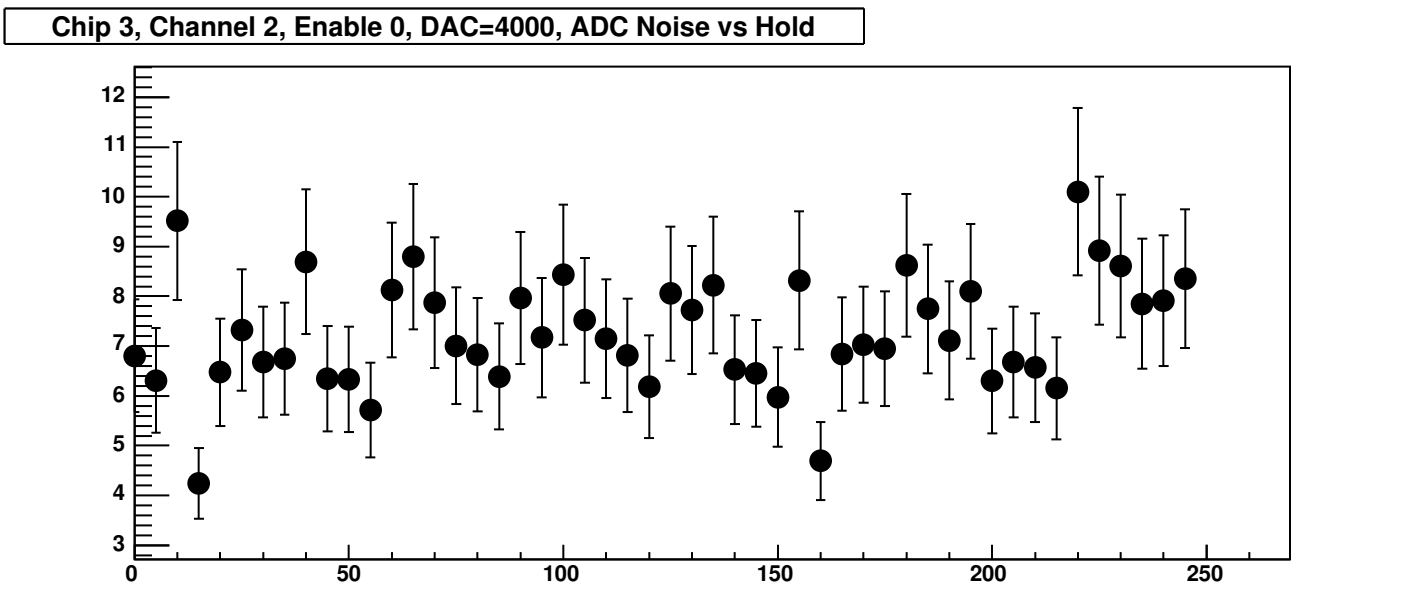
Chip 3, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold

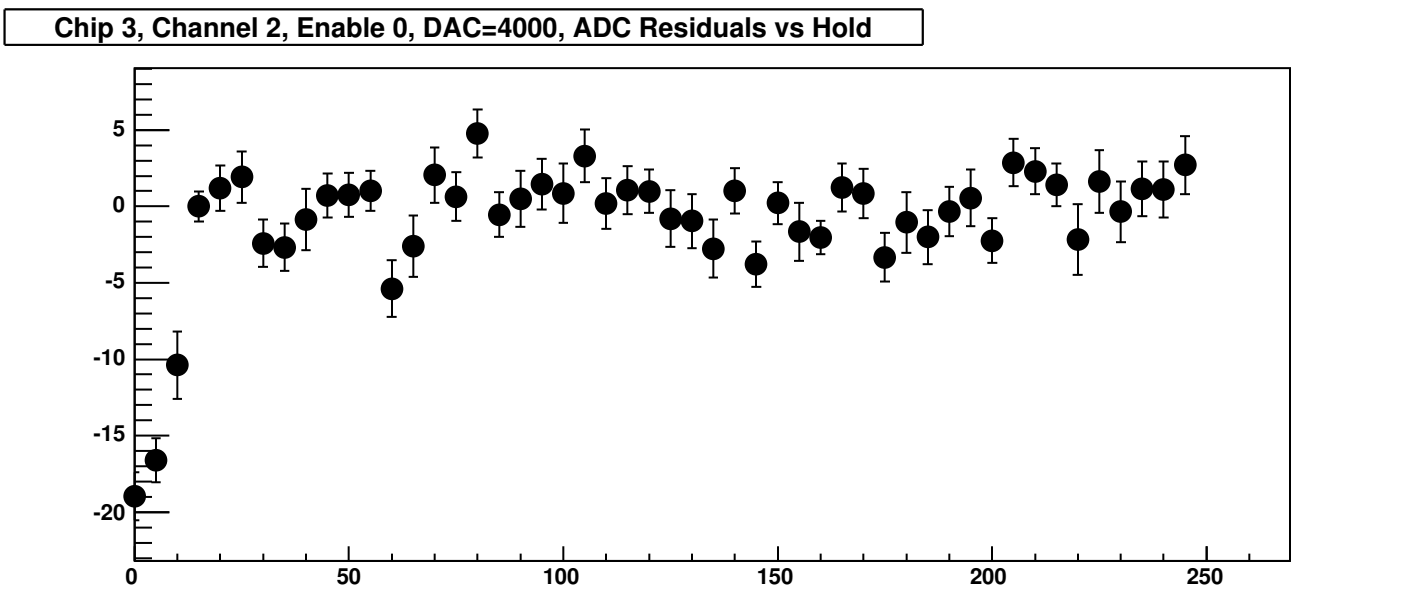


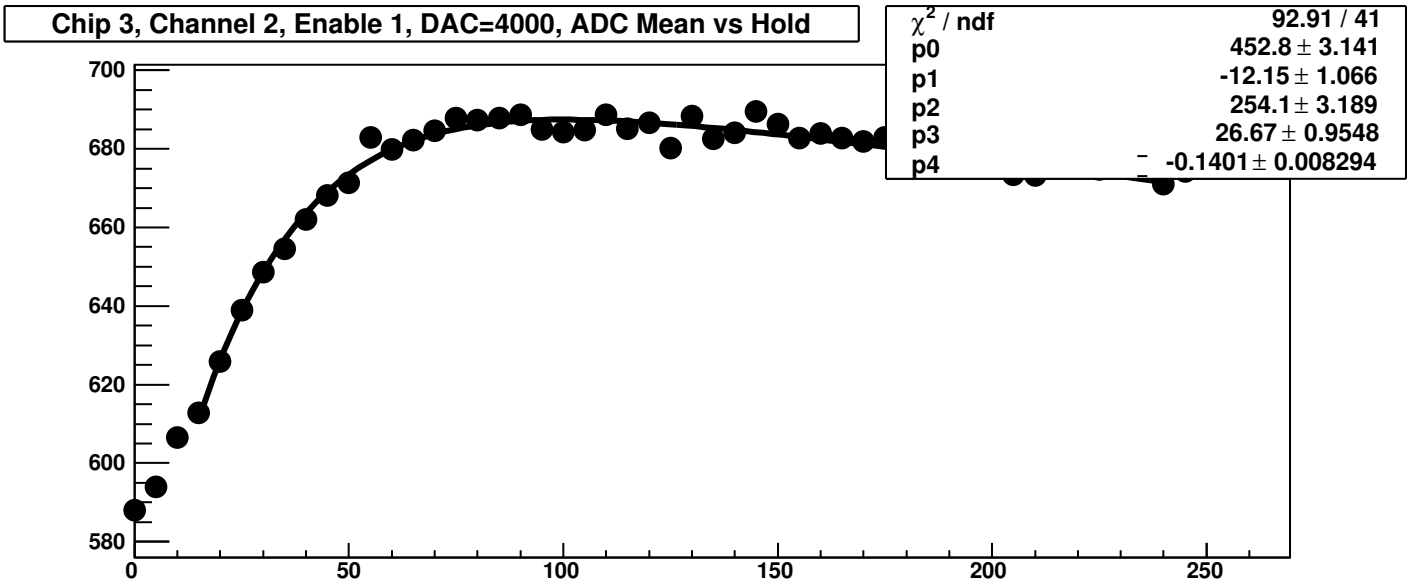
Chip 3, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold

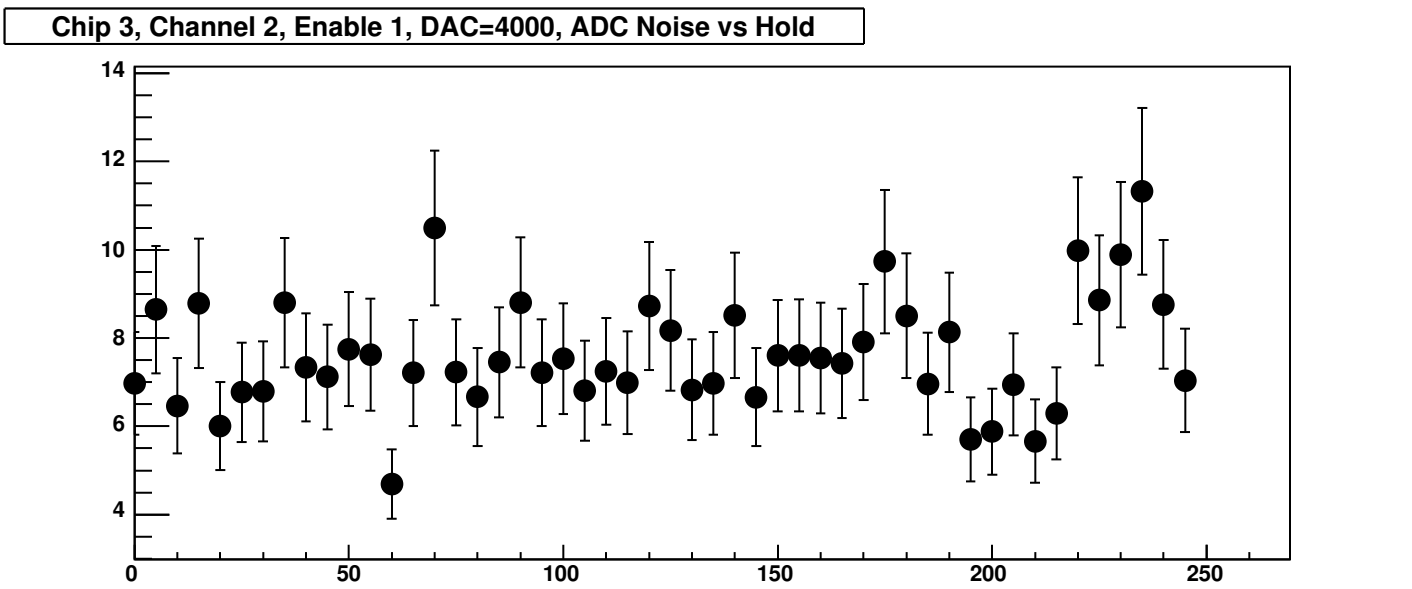


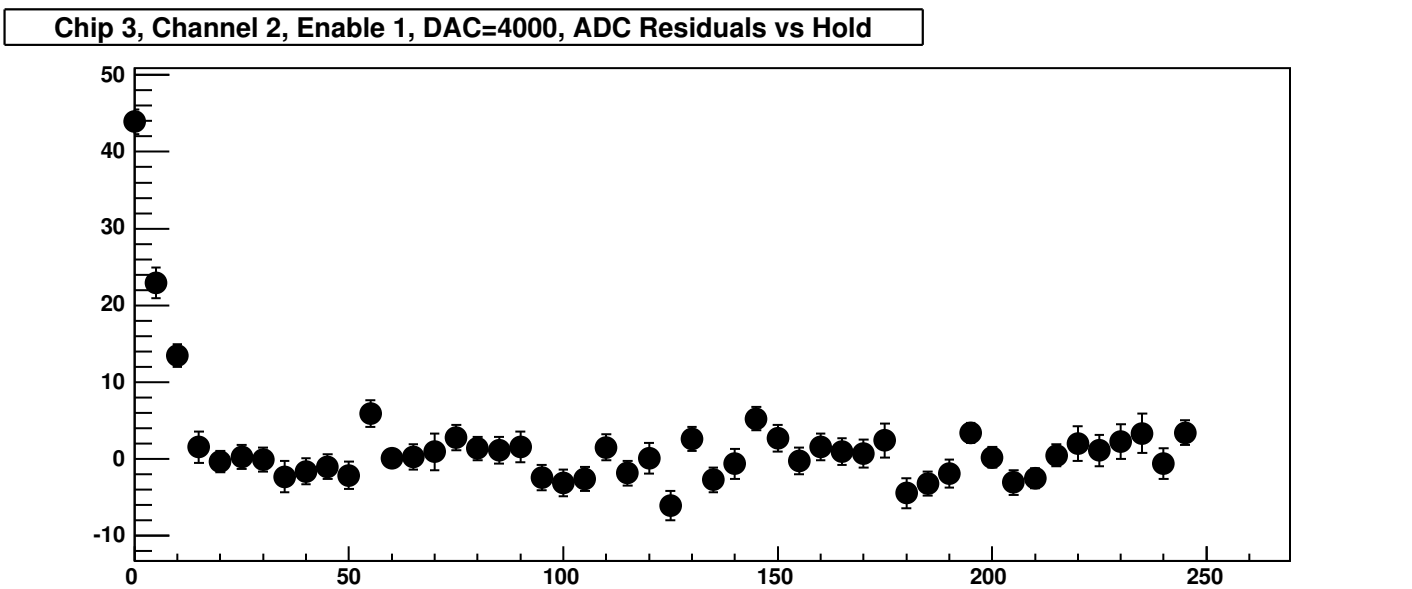

Chip 3, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 67.69 / 41
p0 599.5 ± 0.9355
p1 16.44 ± 0.5006
p2 99.31 ± 1.81
p3 26.53 ± 1.024
p4 -0.1407 ± 0.009121
680
660
640
620
600
580
0
50
100
150
200
250


Chip 3, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

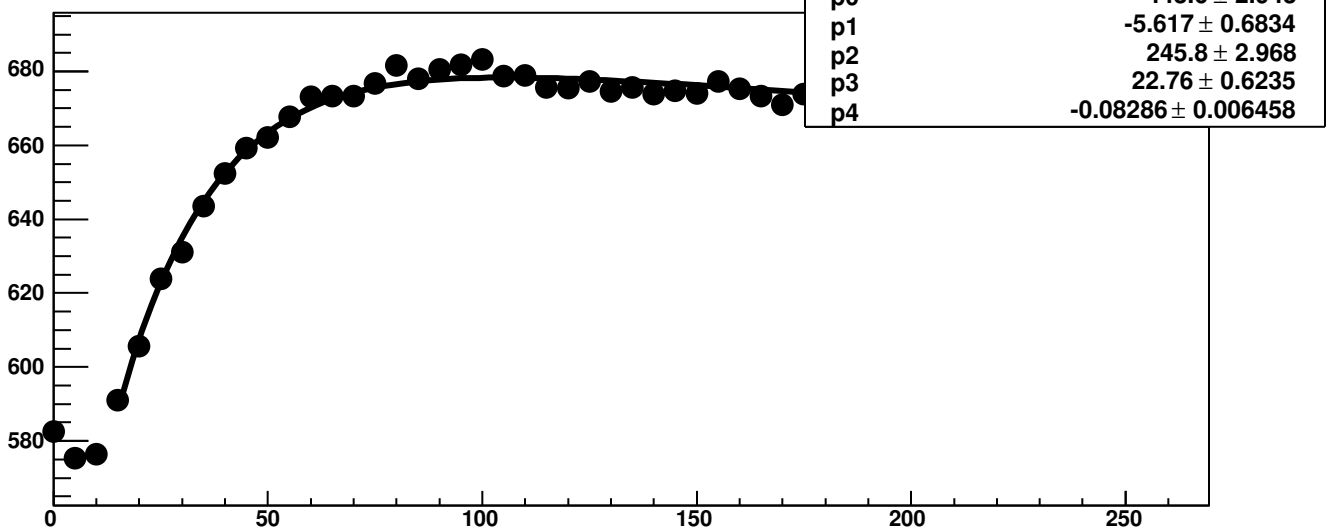

Chip 3, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold
5
0
-5
-10
-15
-20
0
50
100
150
200
250


Chip 3, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 92.91 / 41
p0 452.8 ± 3.141
p1 -12.15 ± 1.066
p2 254.1 ± 3.189
p3 26.67 ± 0.9548
p4 -0.1401 ± 0.008294
700
680
660
640
620
600
580
0
50
100
150
200
250

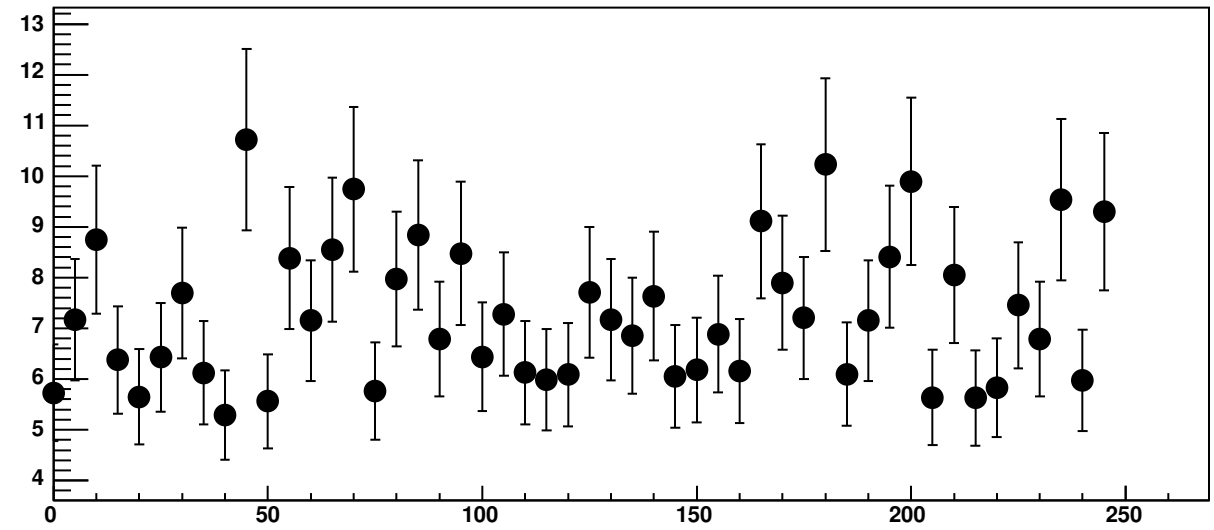

Chip 3, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 3, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250

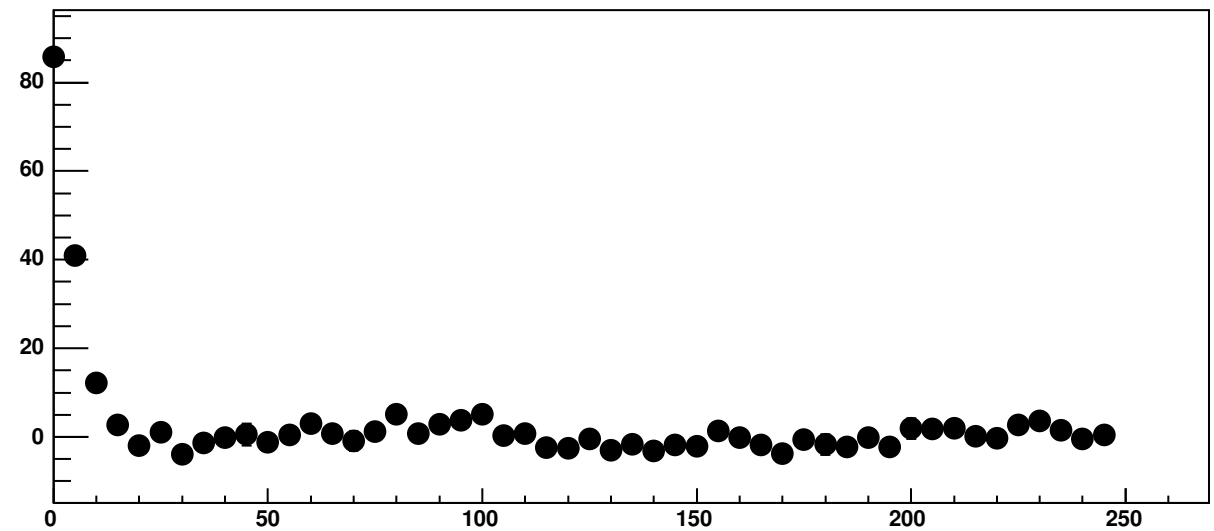
Chip 3, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold



Chip 3, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold

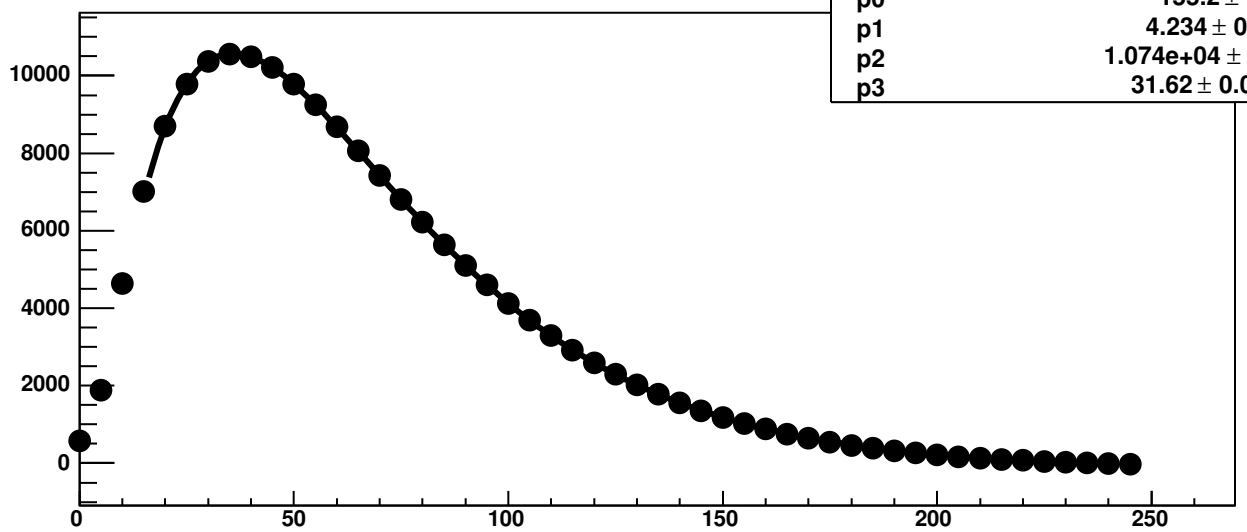


Chip 3, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

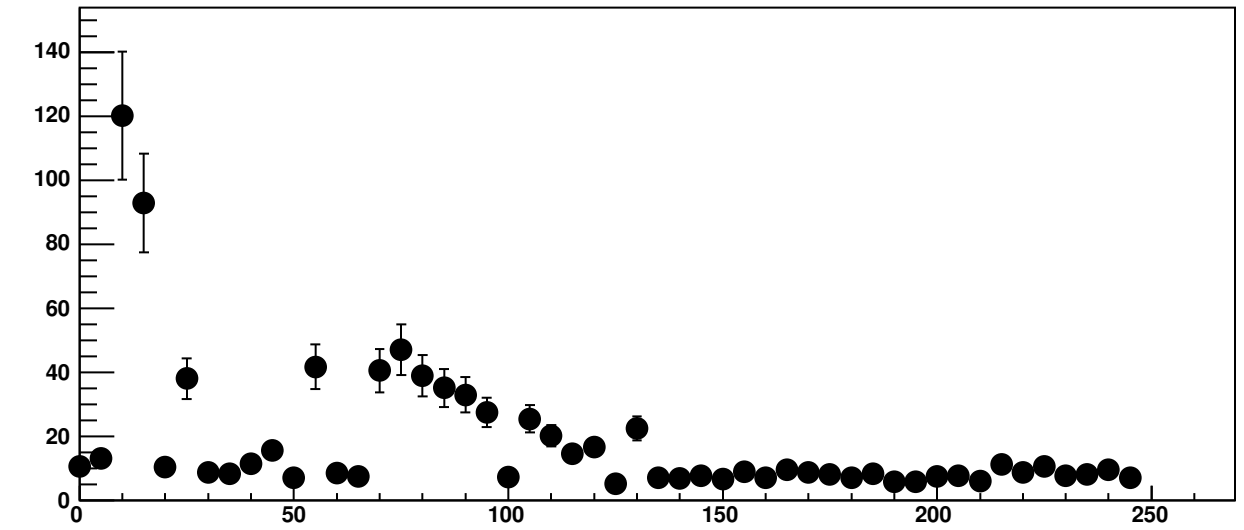


Chip 3, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold

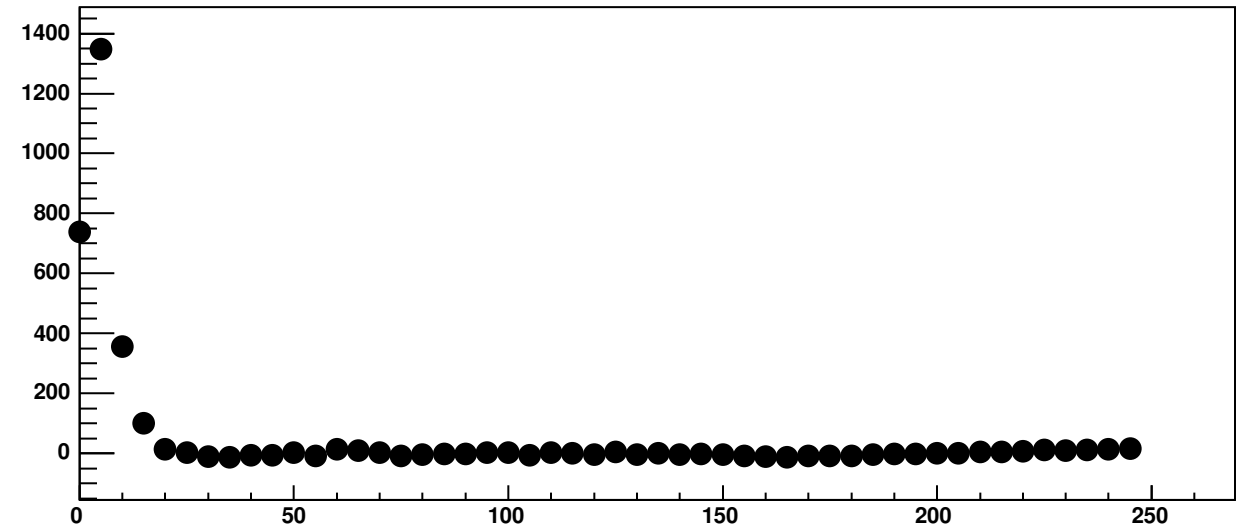
χ^2 / ndf	555.6 / 42
p0	-155.2 ± 0.6586
p1	4.234 ± 0.00982
p2	1.074e+04 ± 0.9722
p3	31.62 ± 0.006404

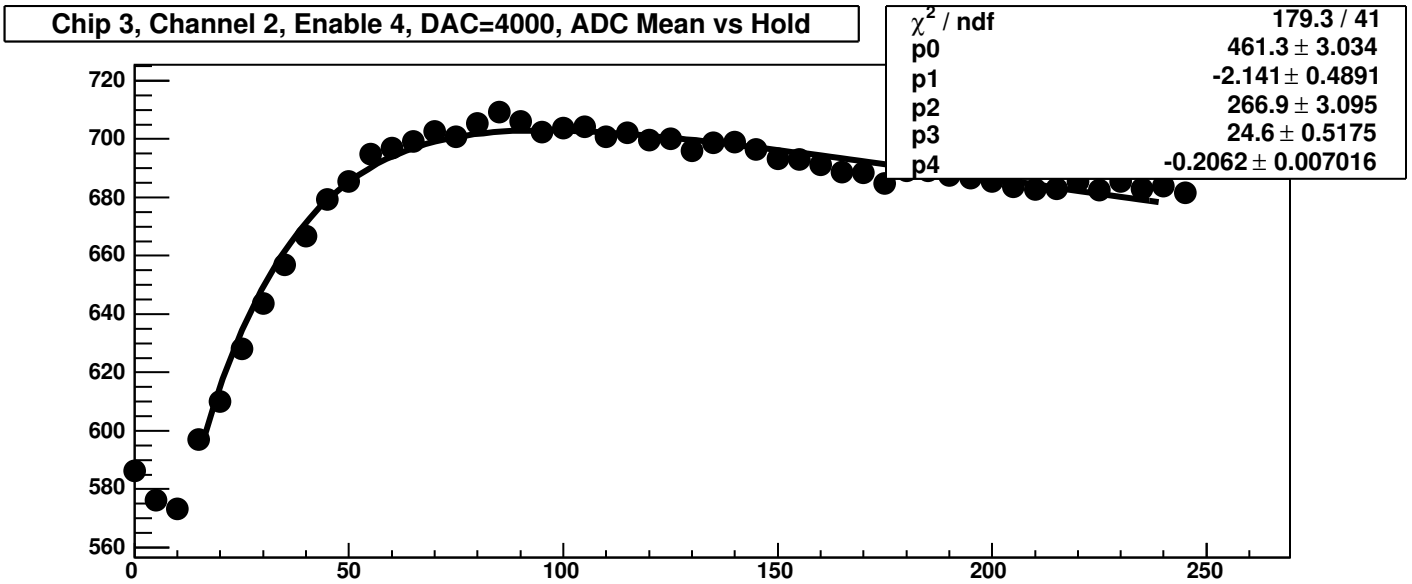


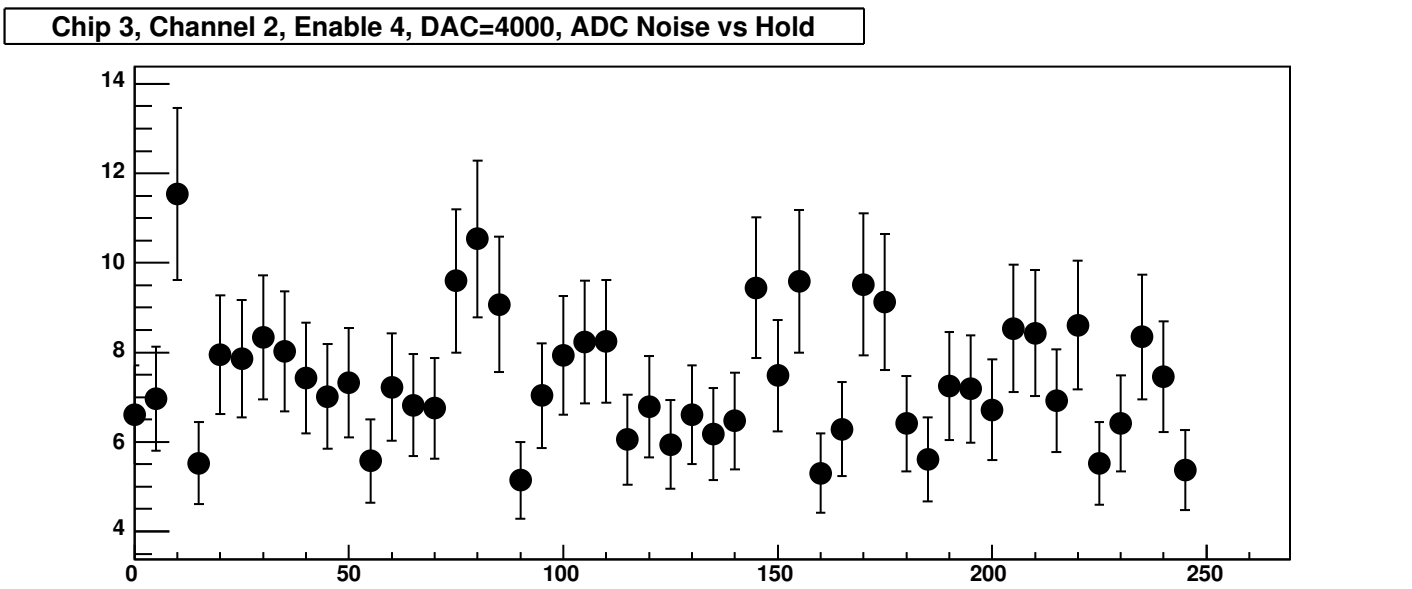
Chip 3, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold

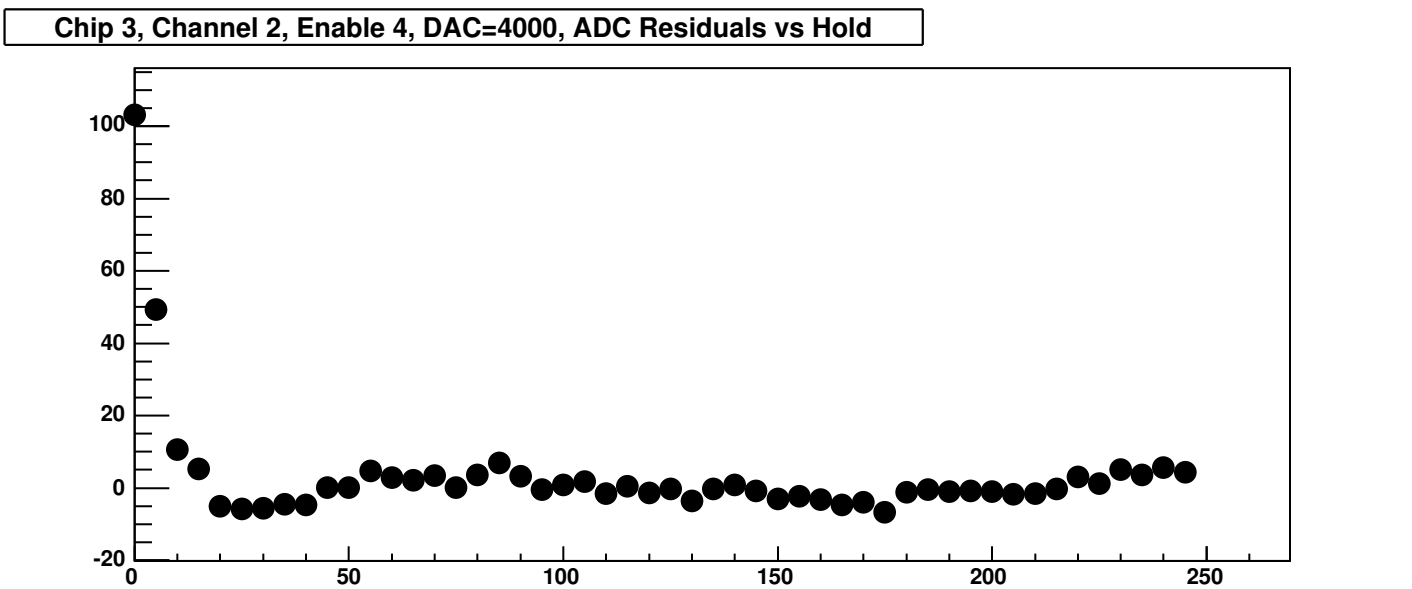


Chip 3, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold

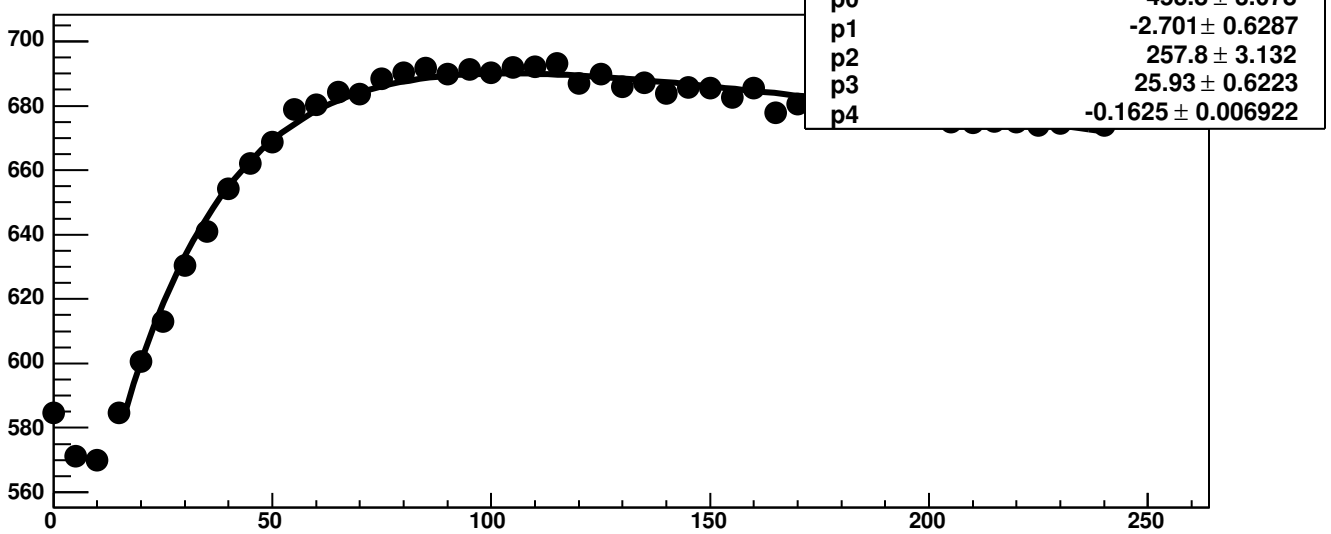



Chip 3, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 179.3 / 41
p0 461.3 ± 3.034
p1 -2.141 ± 0.4891
p2 266.9 ± 3.095
p3 24.6 ± 0.5175
p4 -0.2062 ± 0.007016
720
700
680
660
640
620
600
580
560
0
50
100
150
200
250

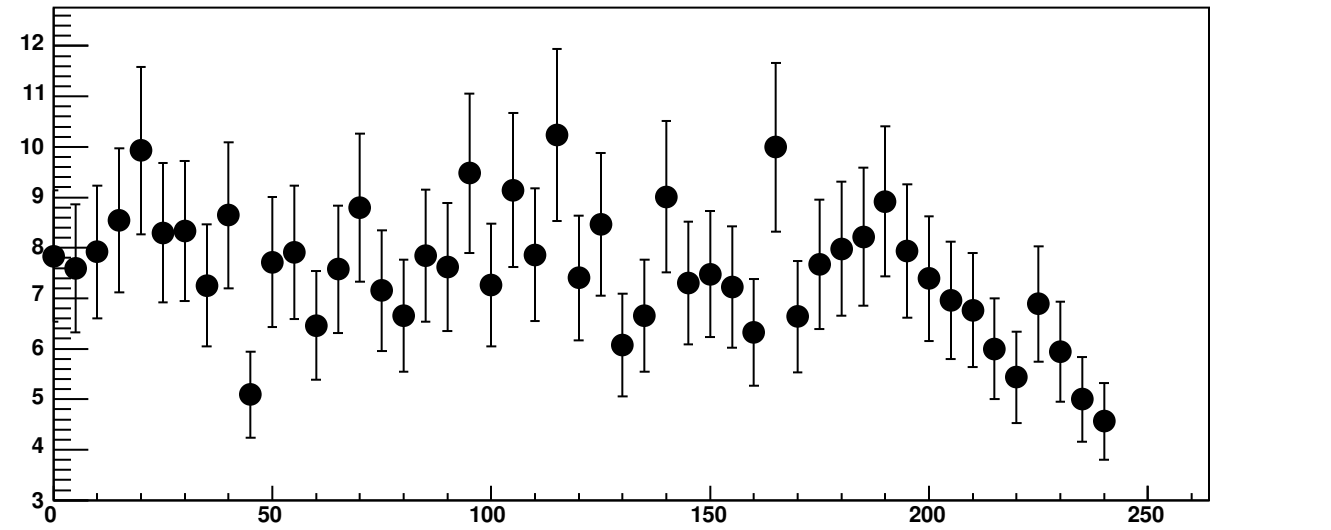

Chip 3, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250


Chip 3, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold
100
80
60
40
20
0
-20
0
50
100
150
200
250

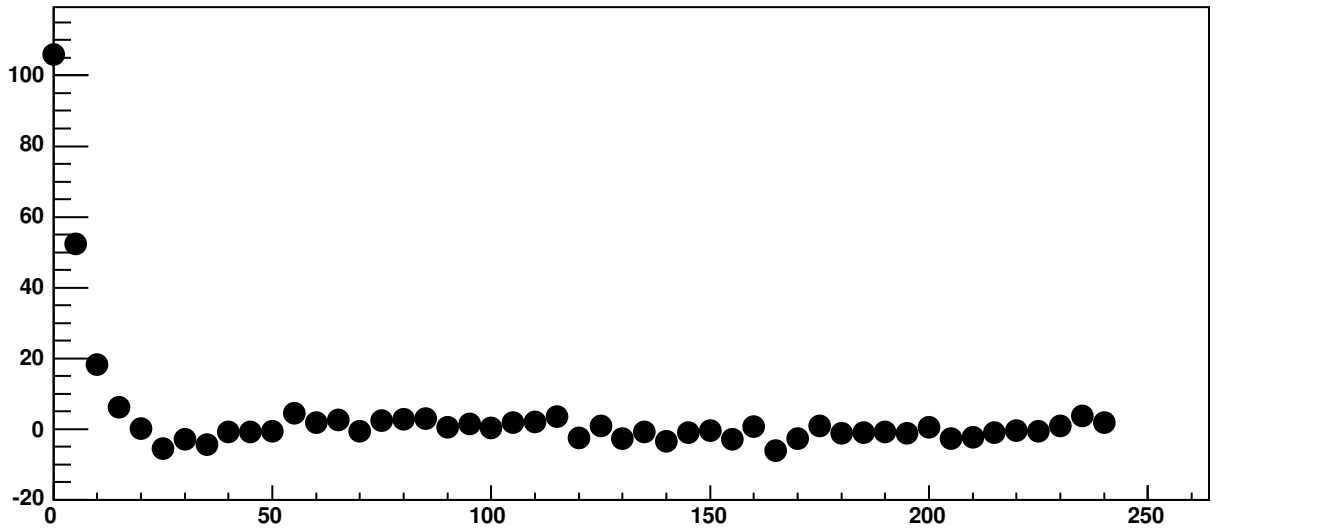
Chip 3, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold



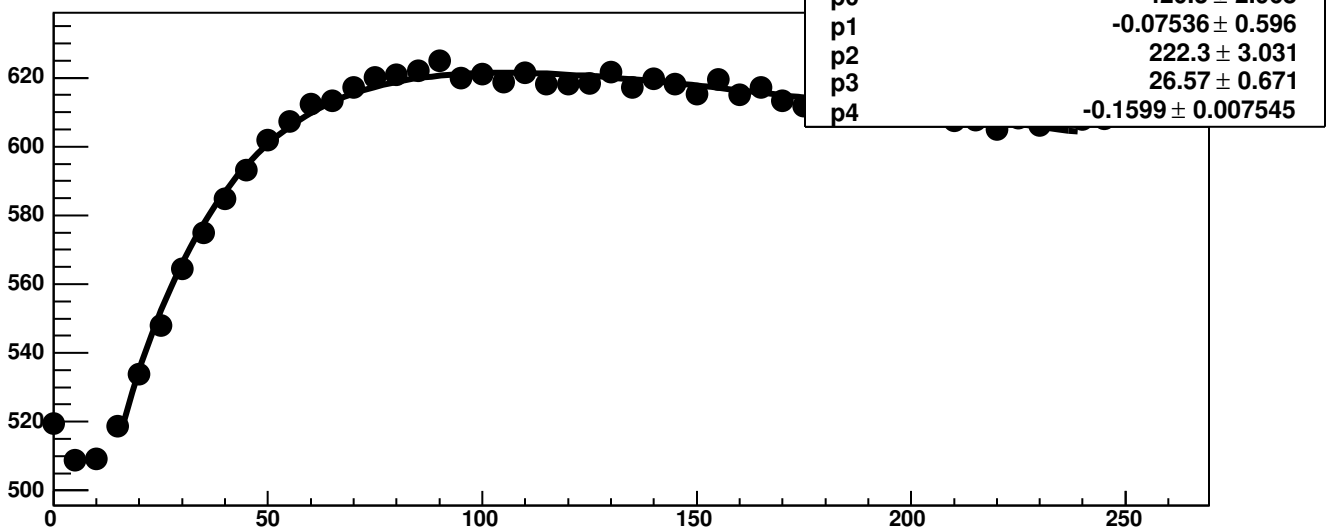
Chip 3, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold



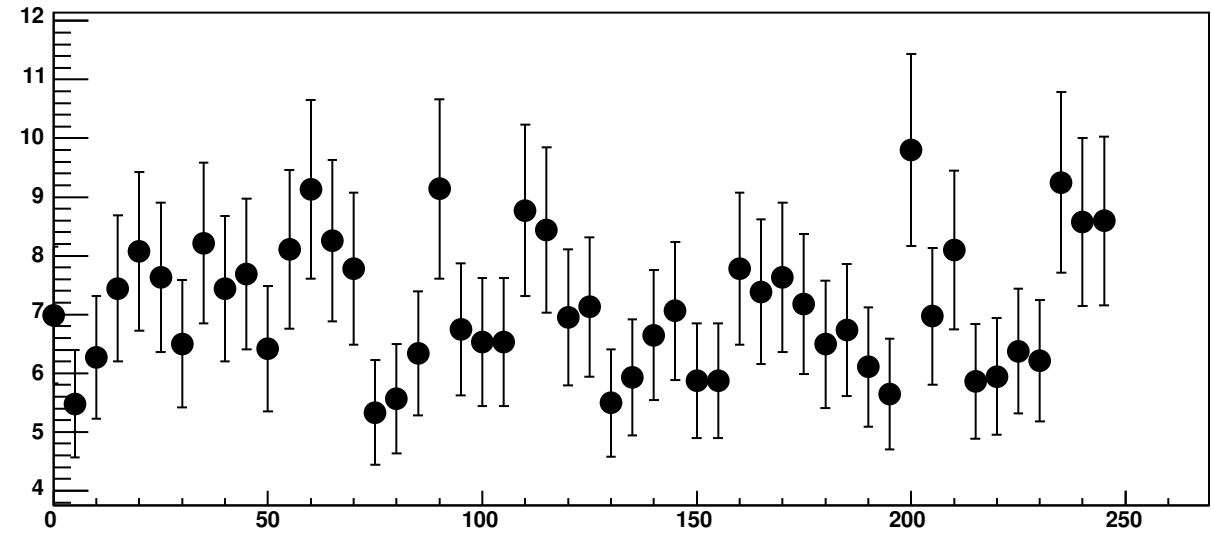
Chip 3, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold



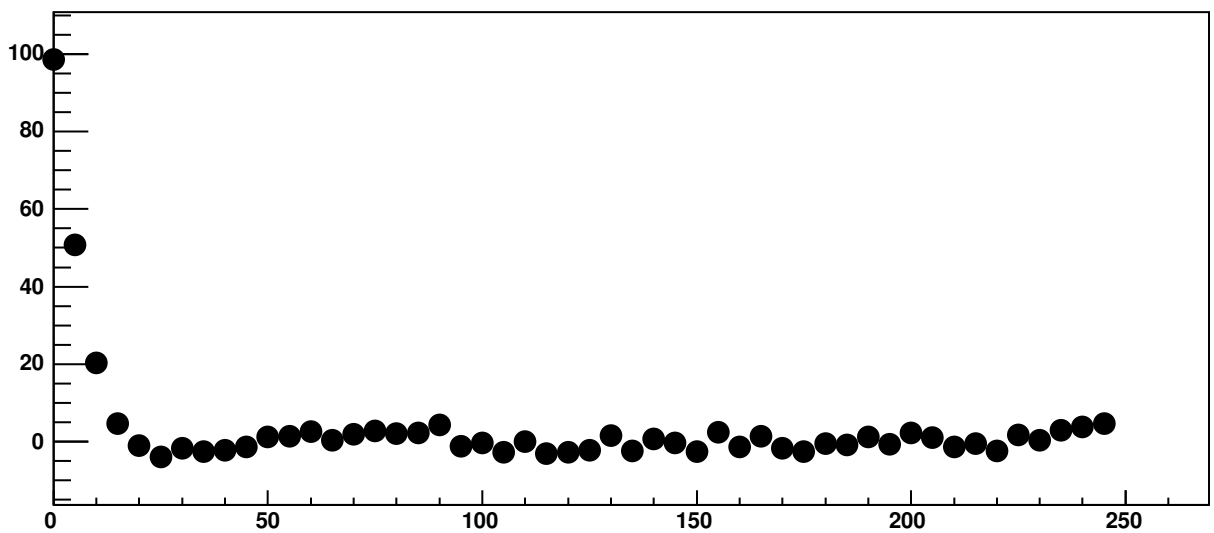
Chip 3, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold



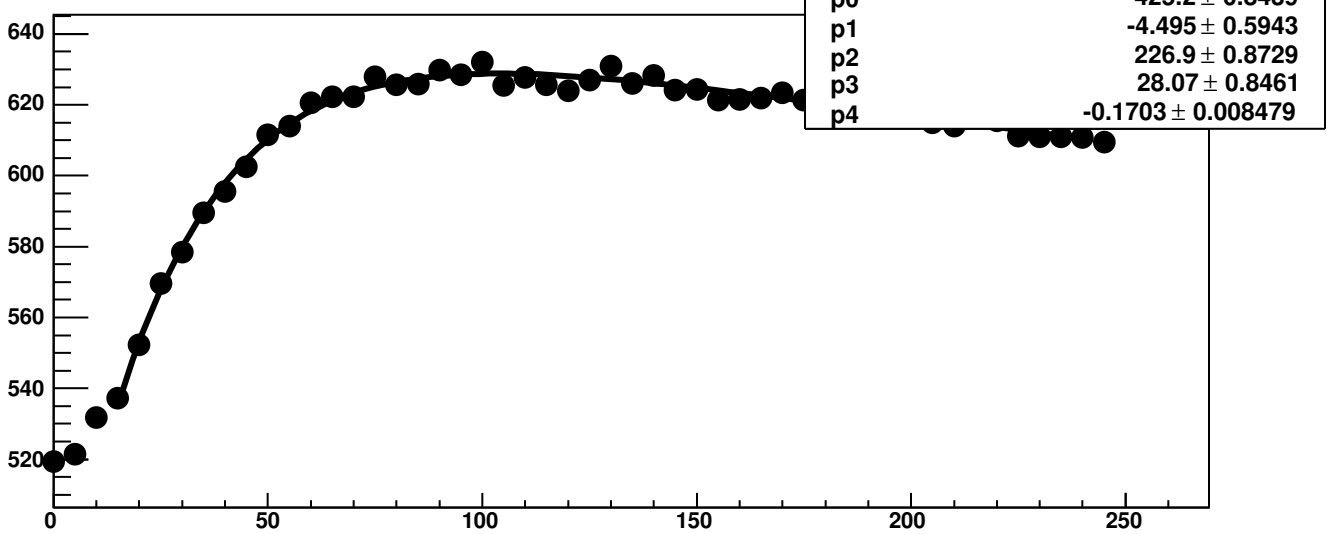
Chip 3, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold



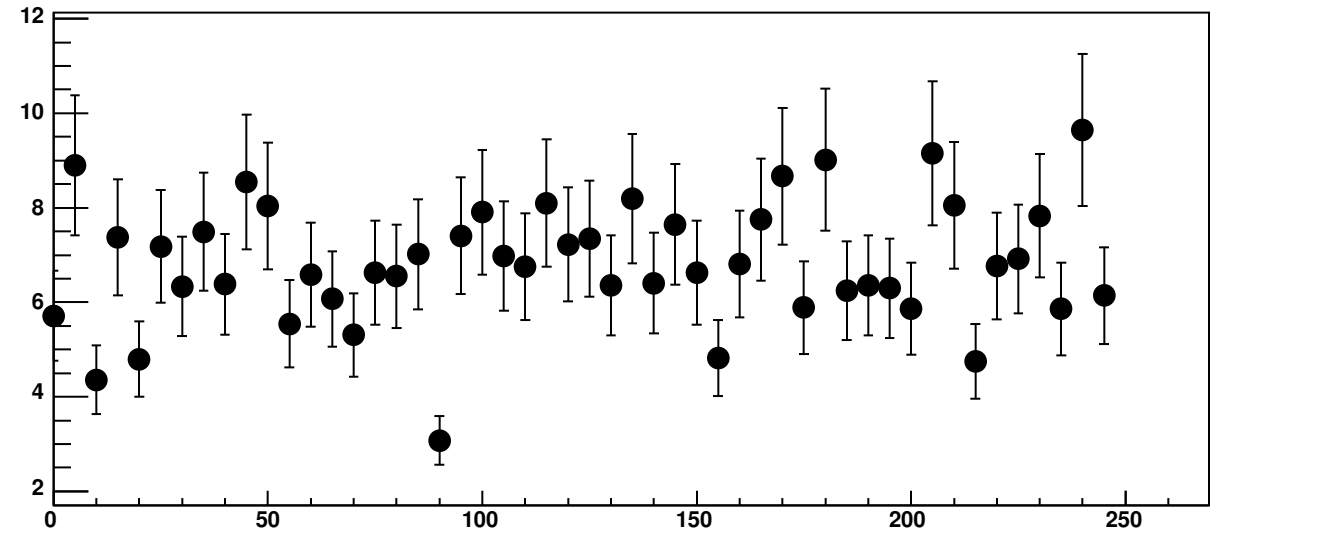
Chip 3, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold



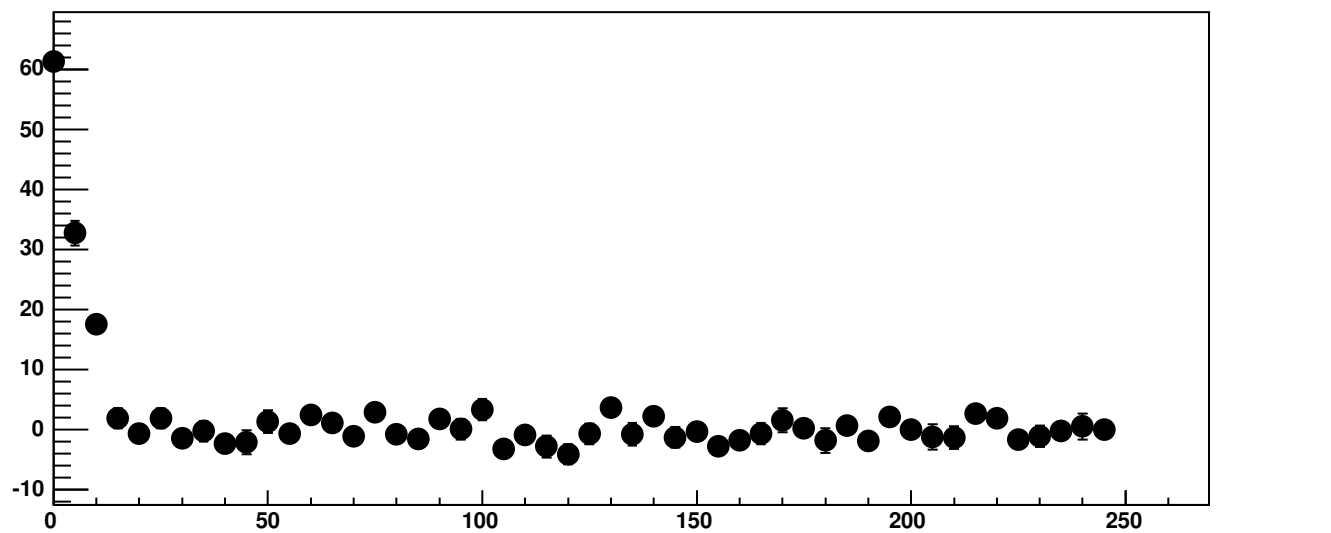
Chip 3, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold



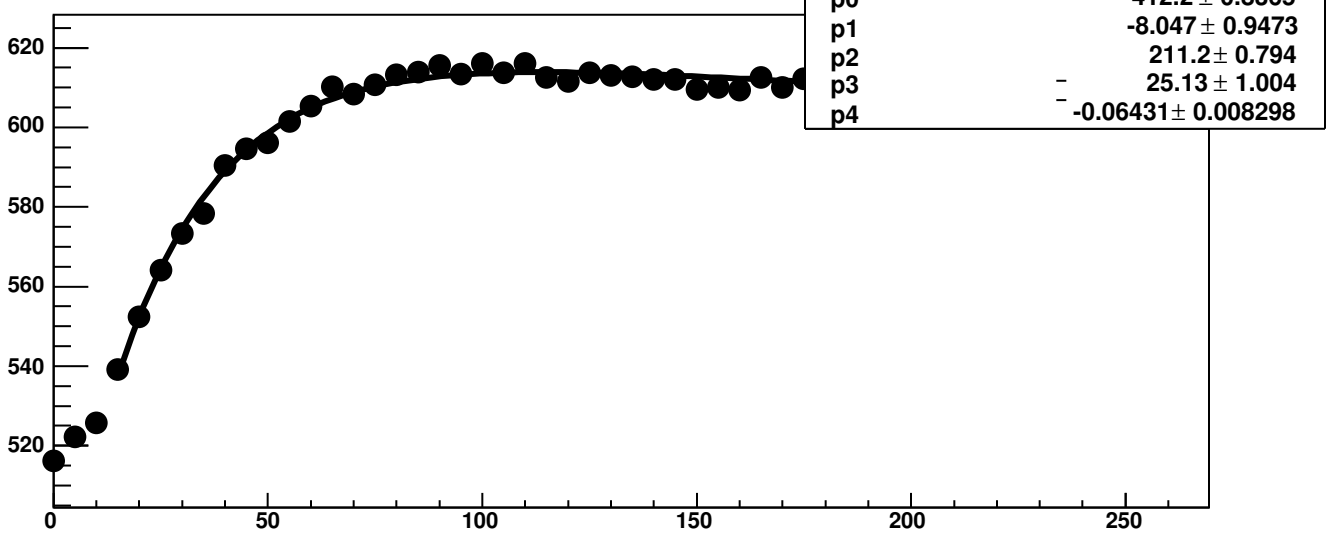
Chip 3, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold



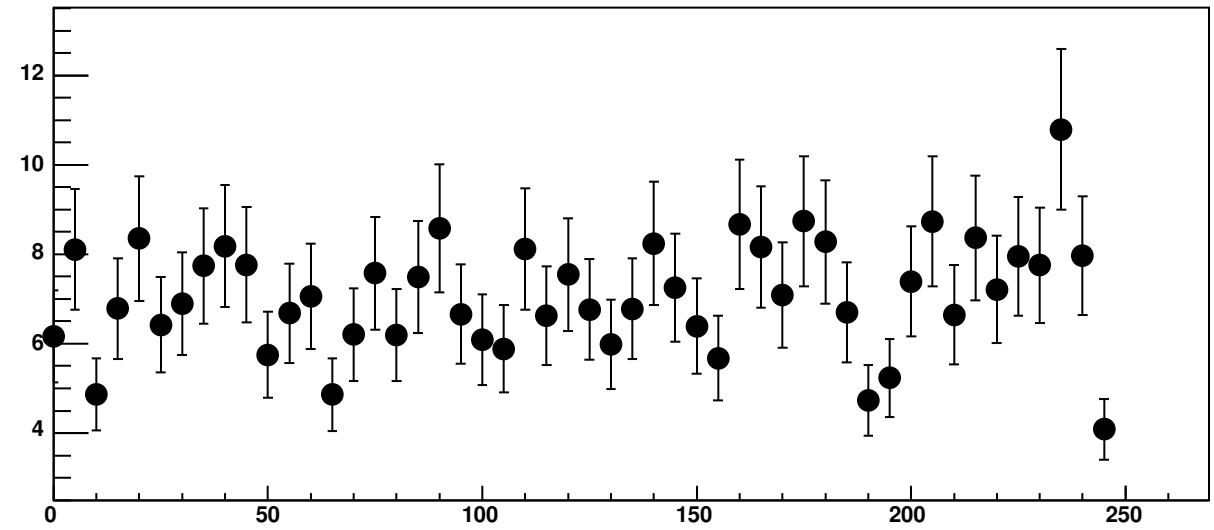
Chip 3, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold



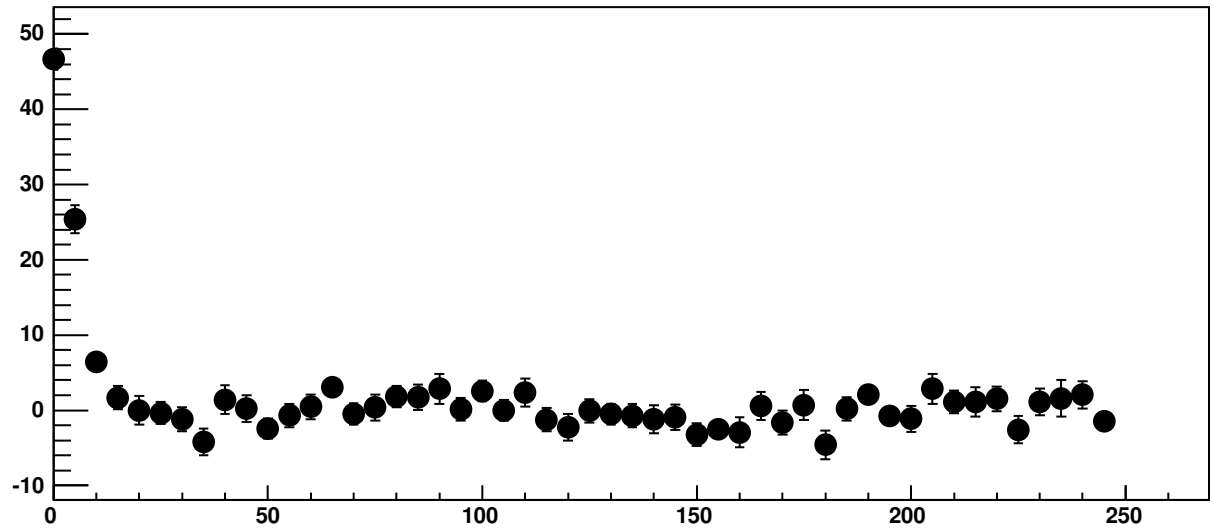
Chip 3, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

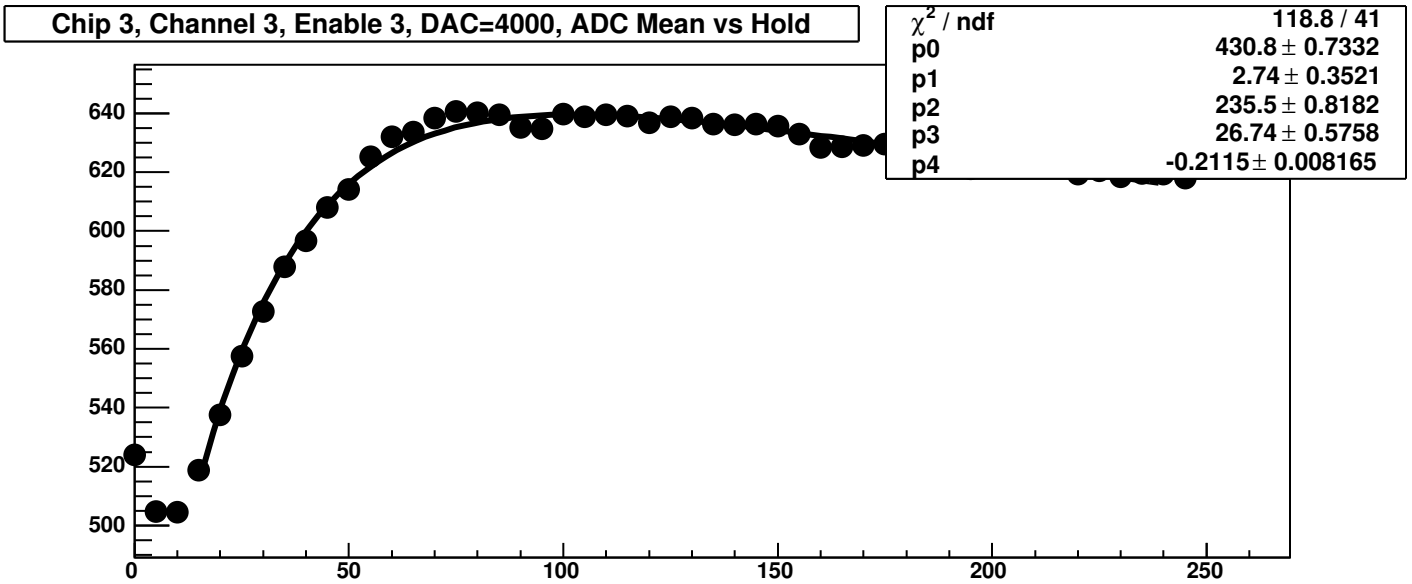


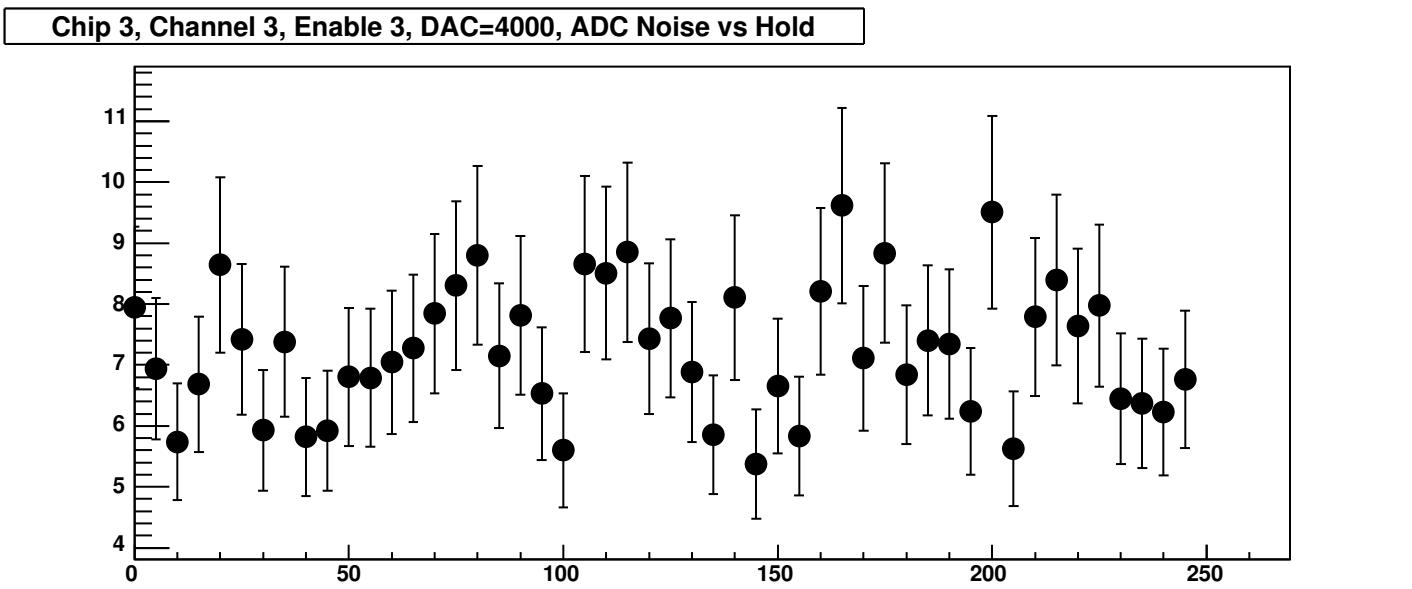
Chip 3, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold

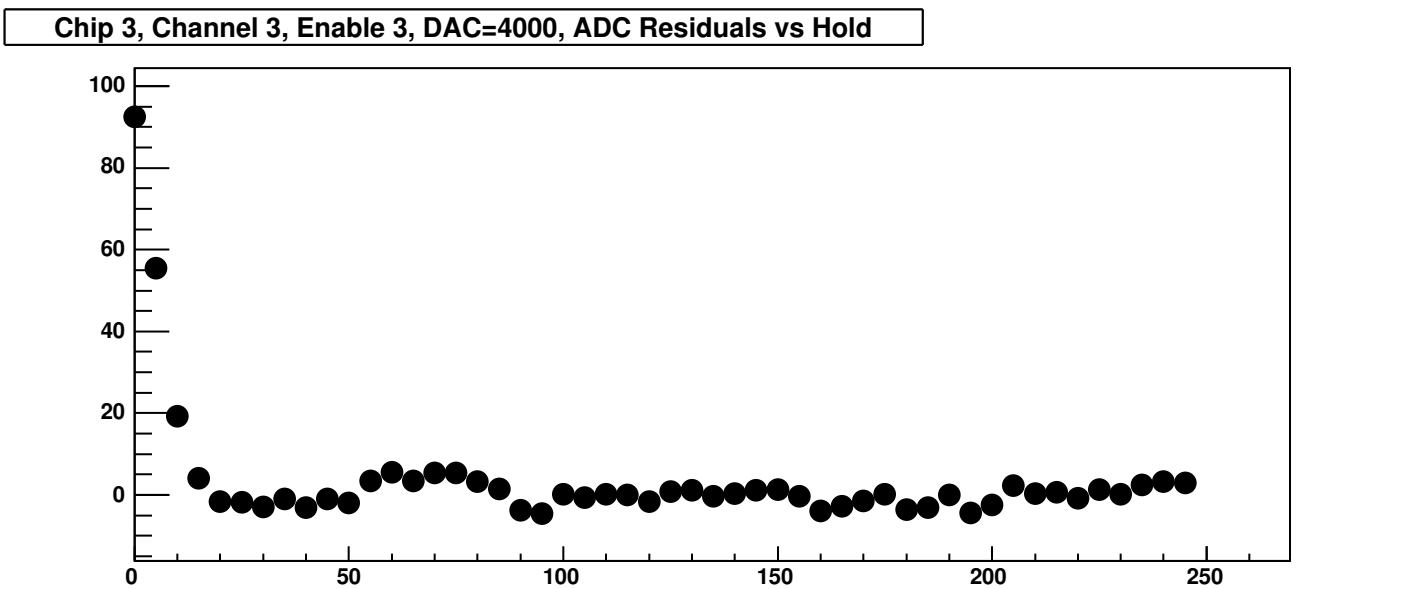


Chip 3, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

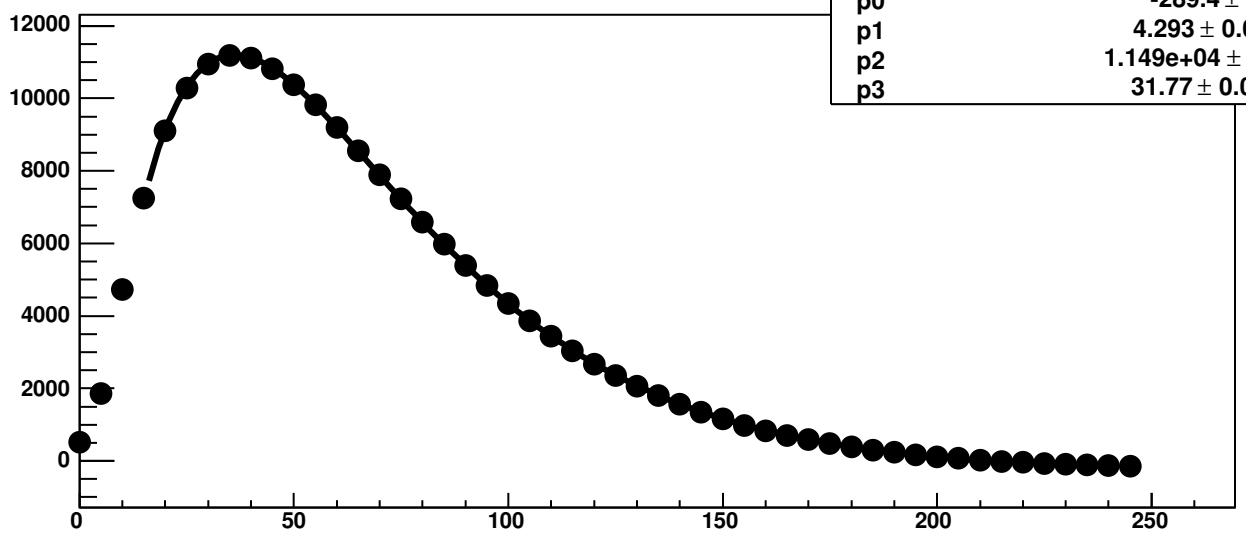



Chip 3, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 118.8 / 41
p0 430.8 ± 0.7332
p1 2.74 ± 0.3521
p2 235.5 ± 0.8182
p3 26.74 ± 0.5758
p4 -0.2115 ± 0.008165
640
620
600
580
560
540
520
500
0
50
100
150
200
250

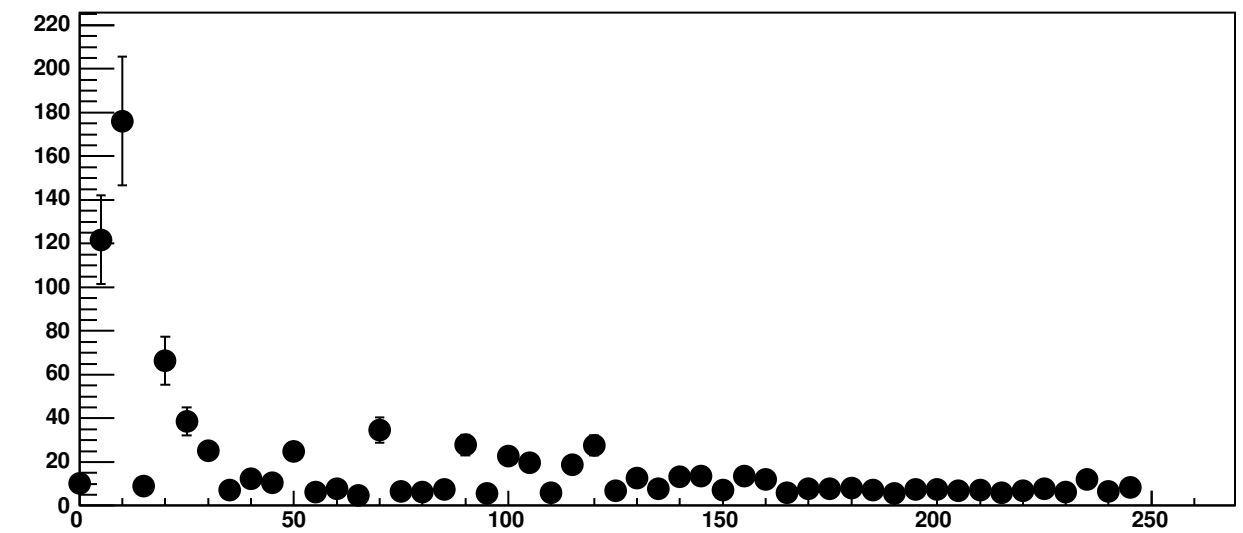

Chip 3, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 3, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold
100
80
60
40
20
0
0
50
100
150
200
250

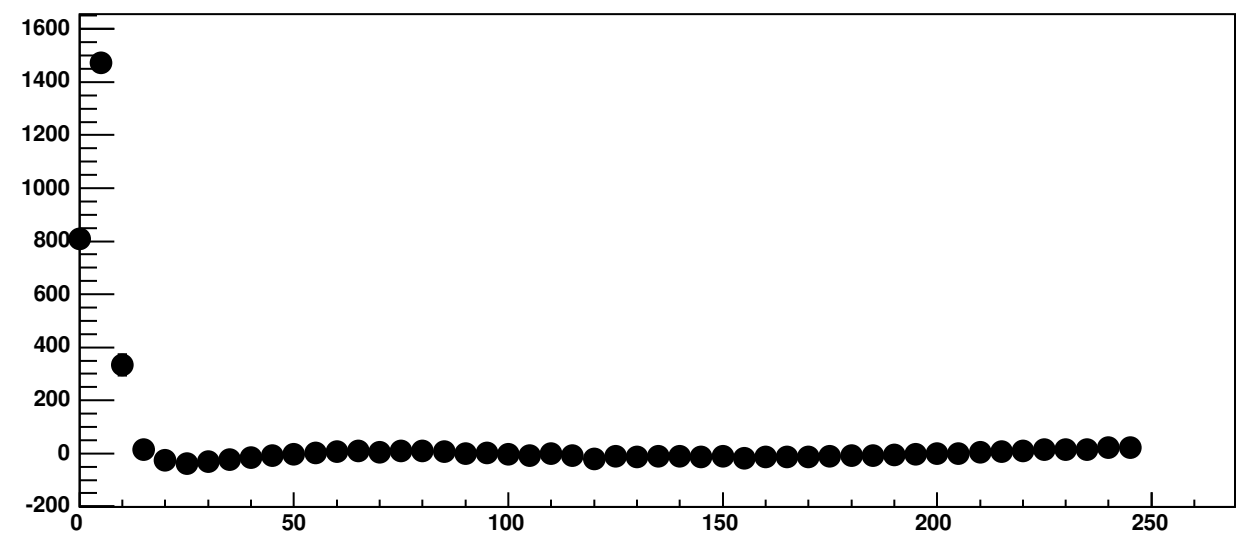
Chip 3, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold

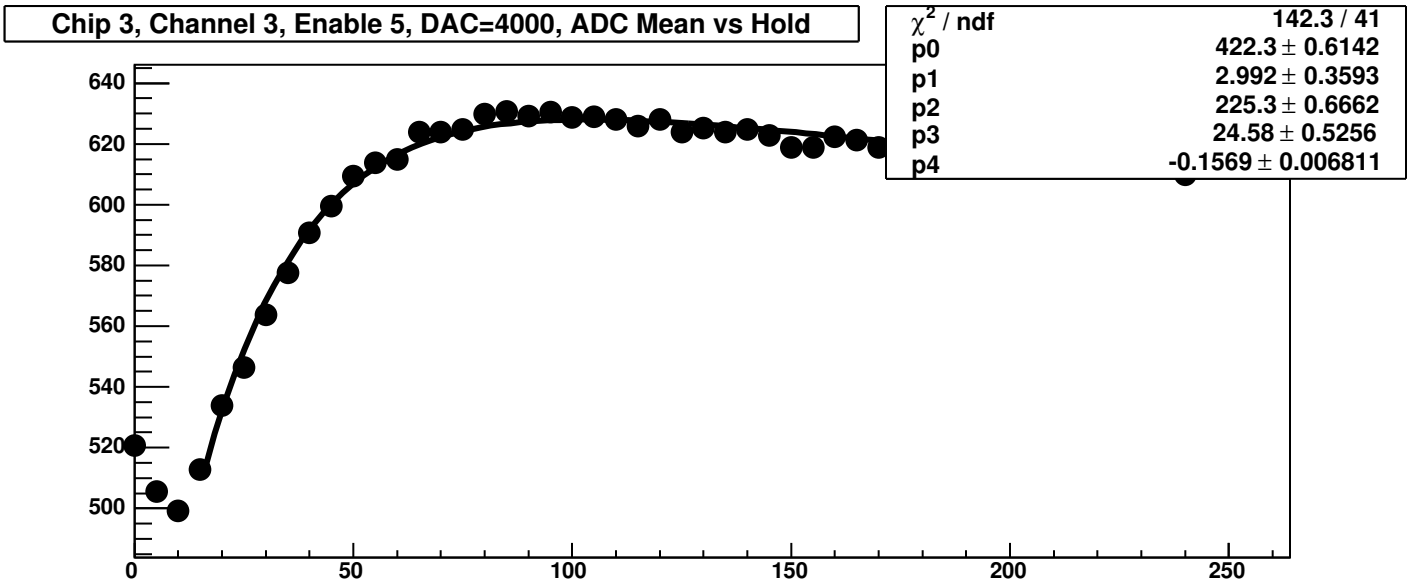


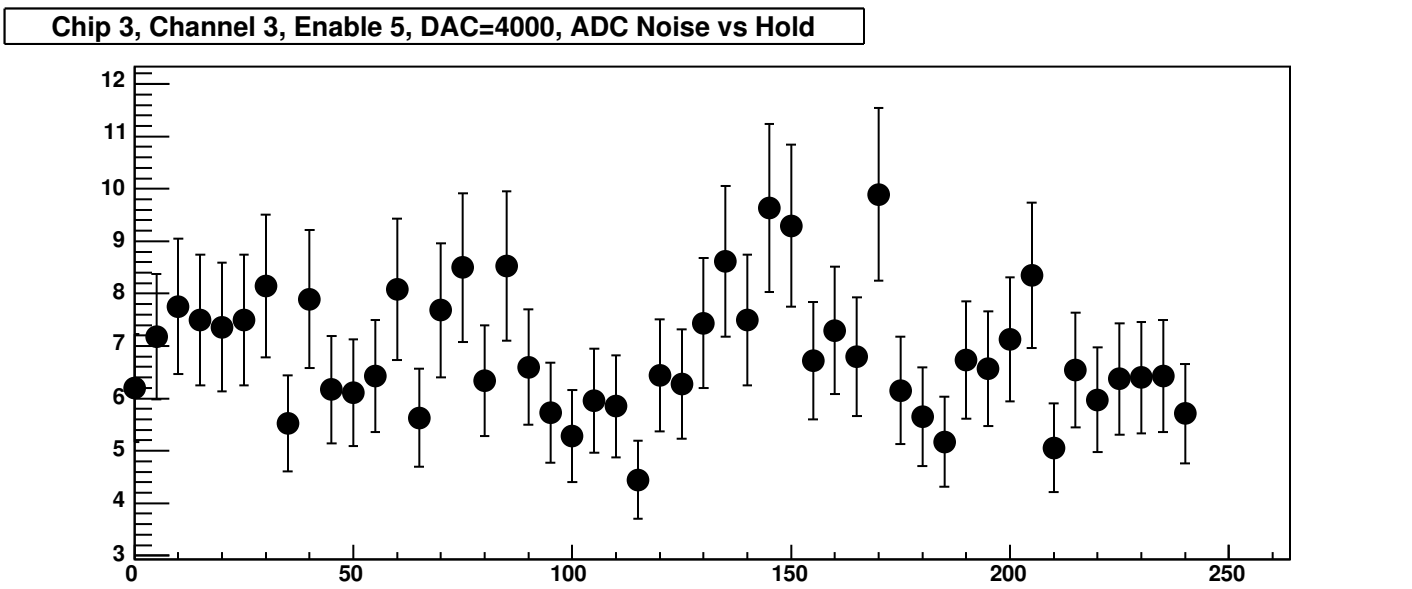
Chip 3, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold

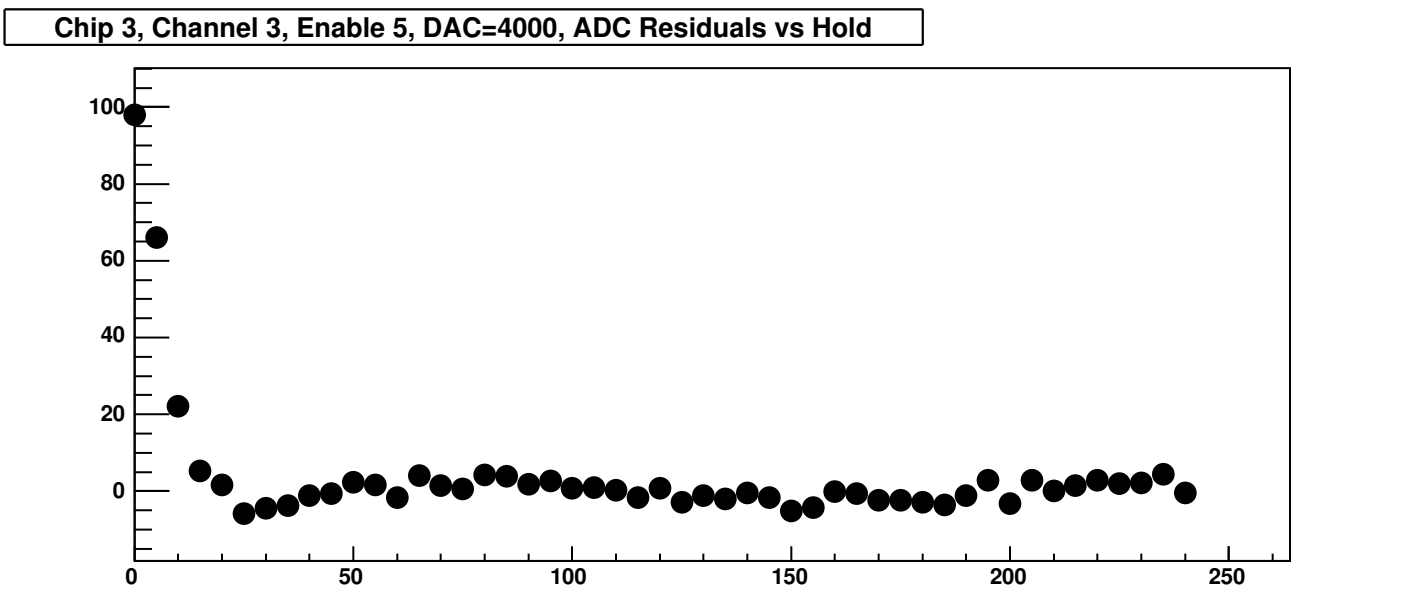


Chip 3, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold

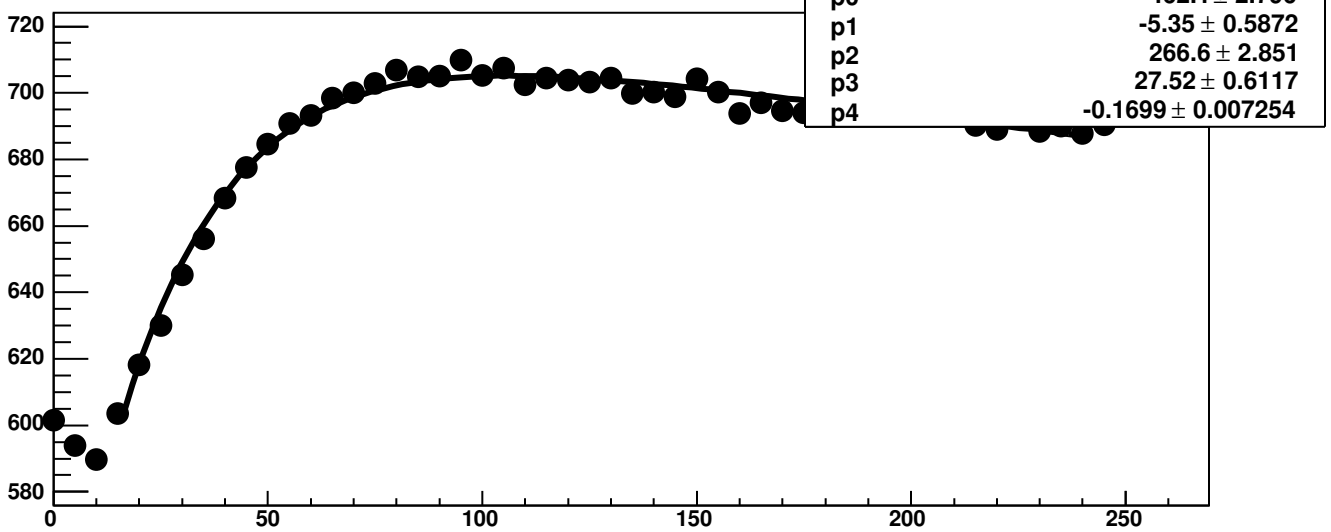



Chip 3, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 142.3 / 41
p0 422.3 ± 0.6142
p1 2.992 ± 0.3593
p2 225.3 ± 0.6662
p3 24.58 ± 0.5256
p4 -0.1569 ± 0.006811
640
620
600
580
560
540
520
500
0
50
100
150
200
250

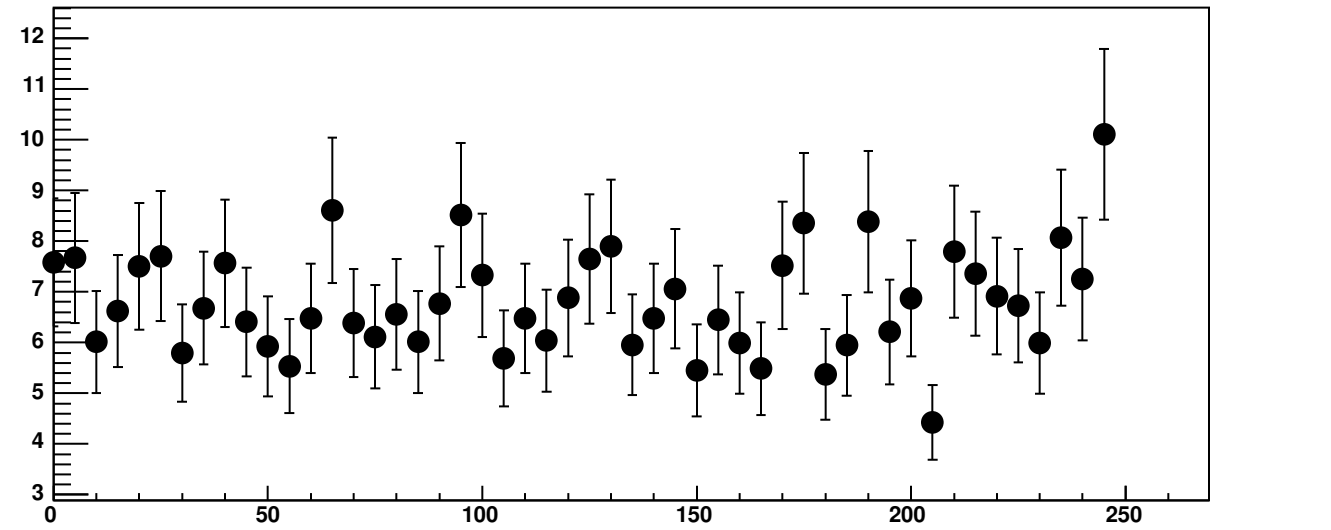

Chip 3, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 3, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold
100
80
60
40
20
0
0
50
100
150
200
250

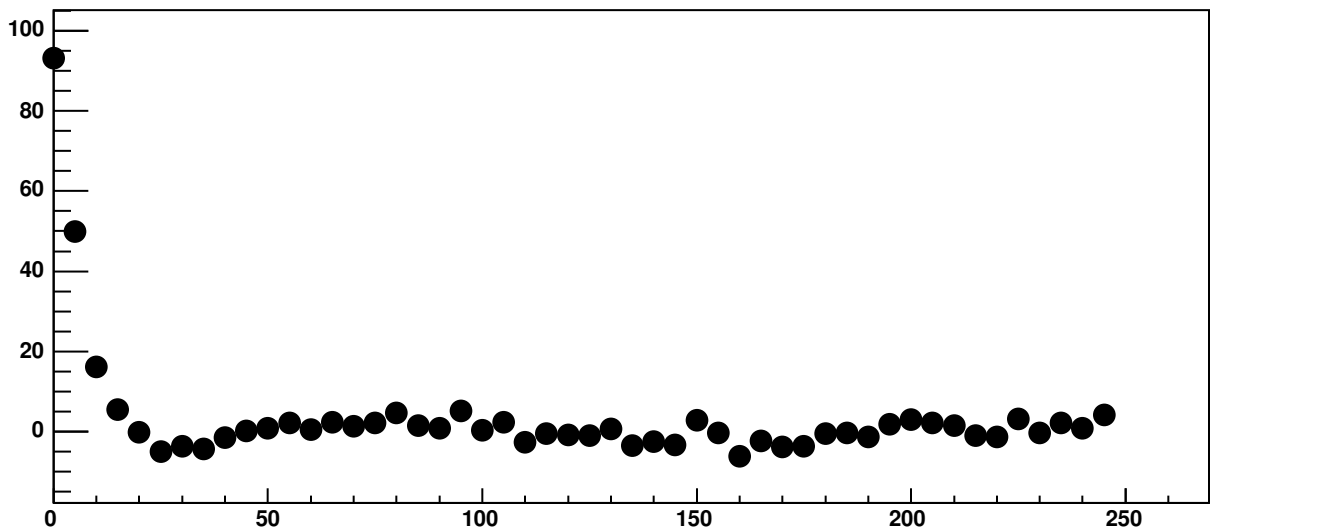
Chip 3, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold



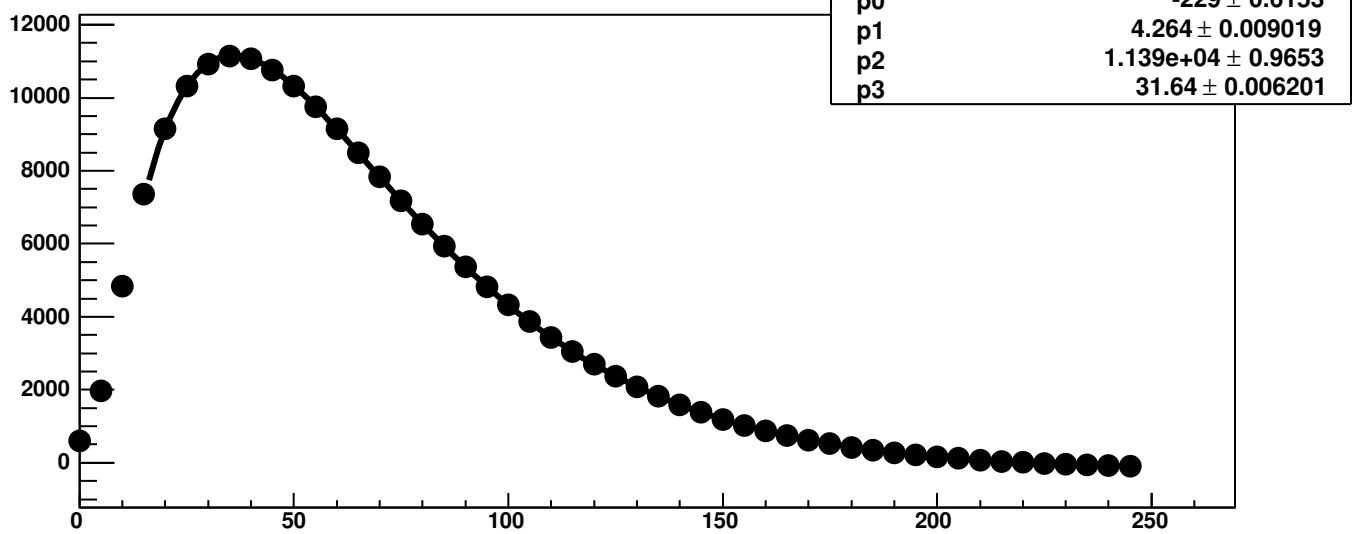
Chip 3, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold



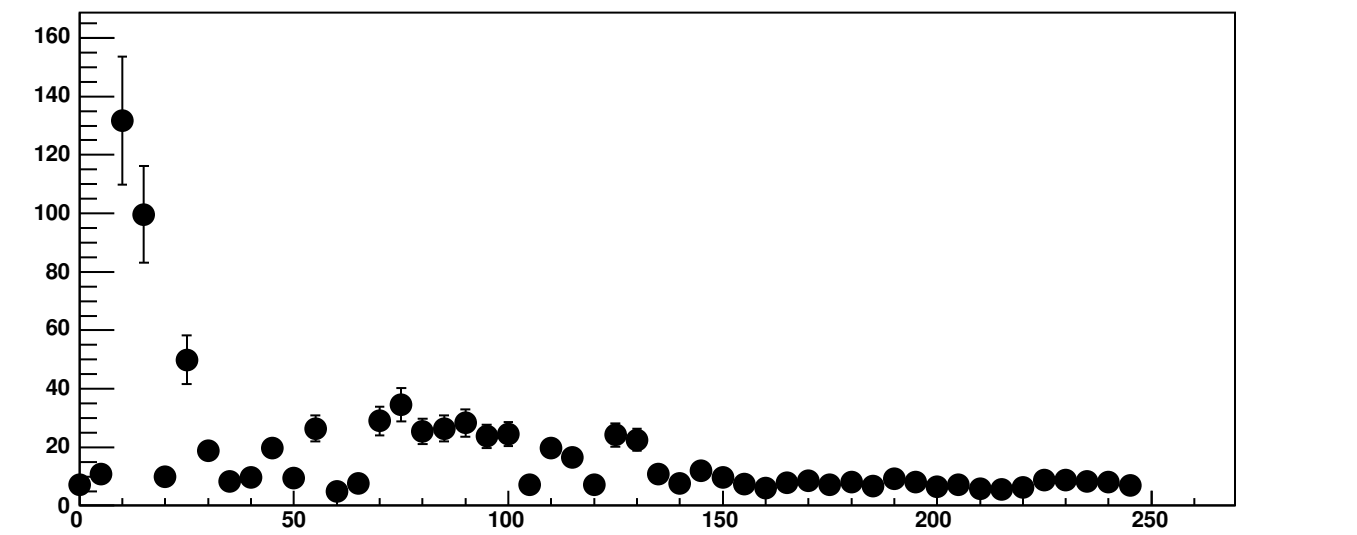
Chip 3, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold



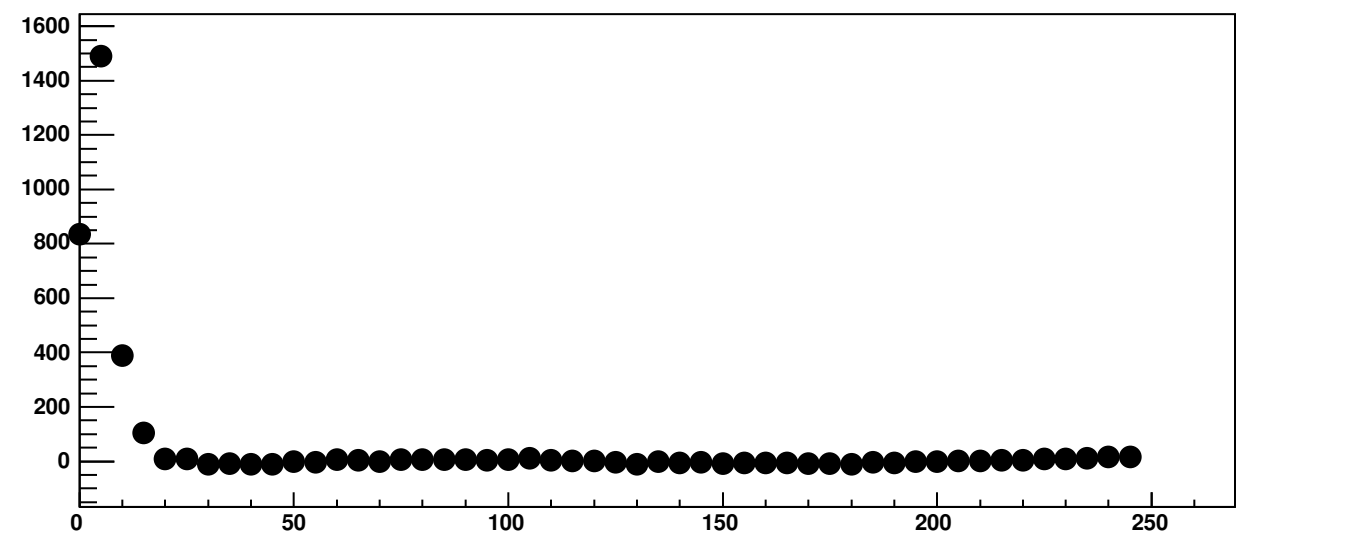
Chip 3, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold

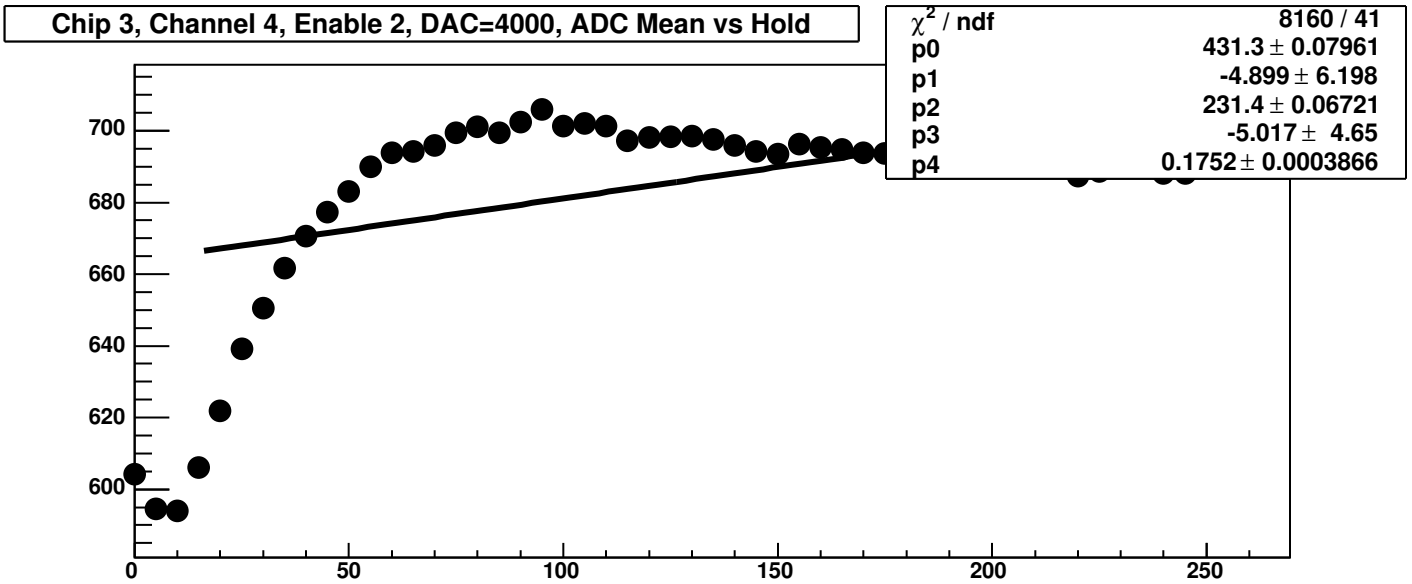


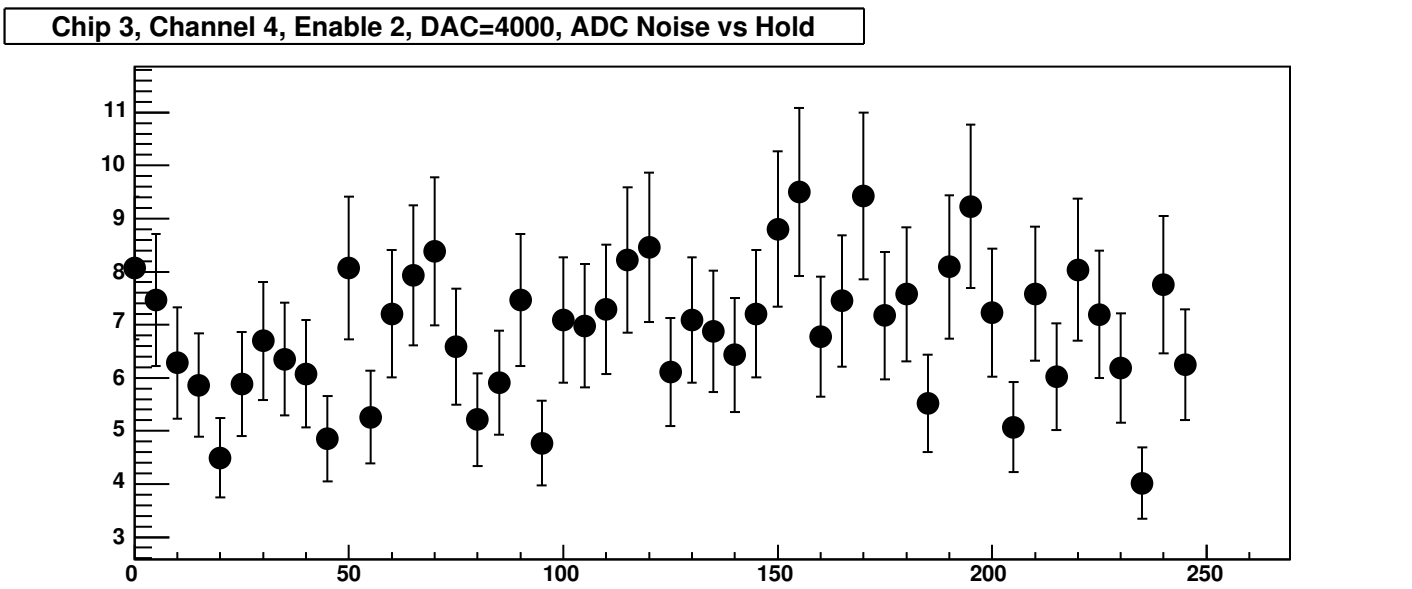
Chip 3, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold

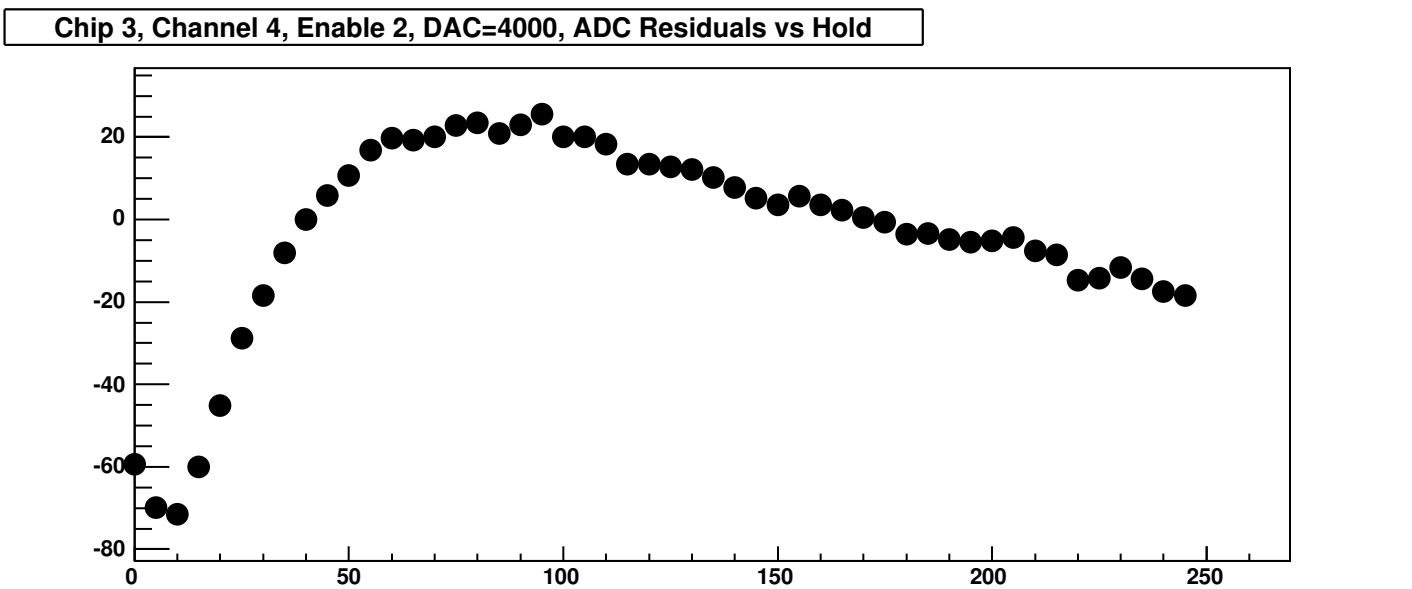


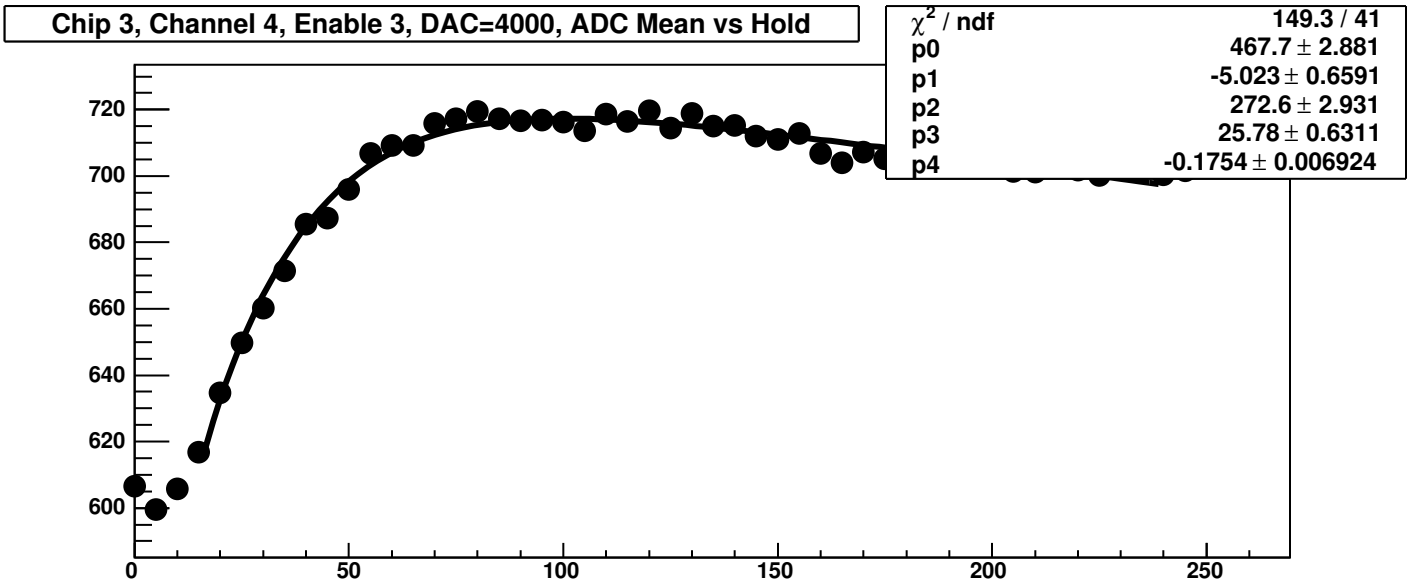
Chip 3, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold

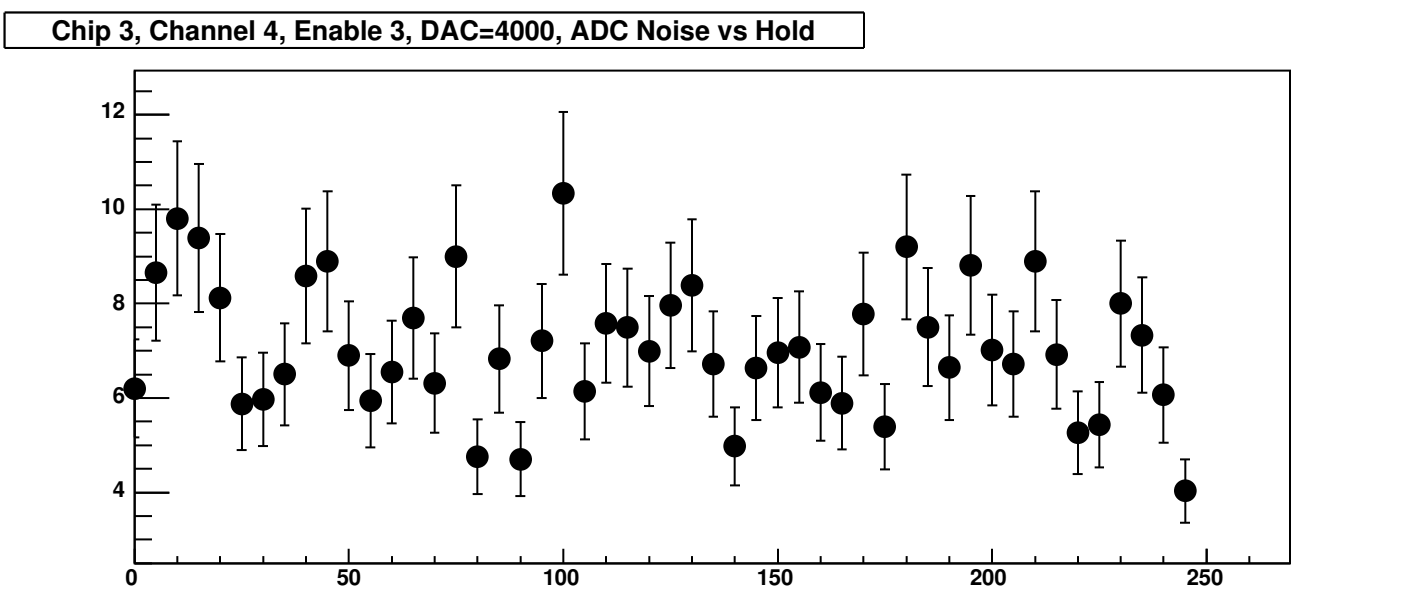


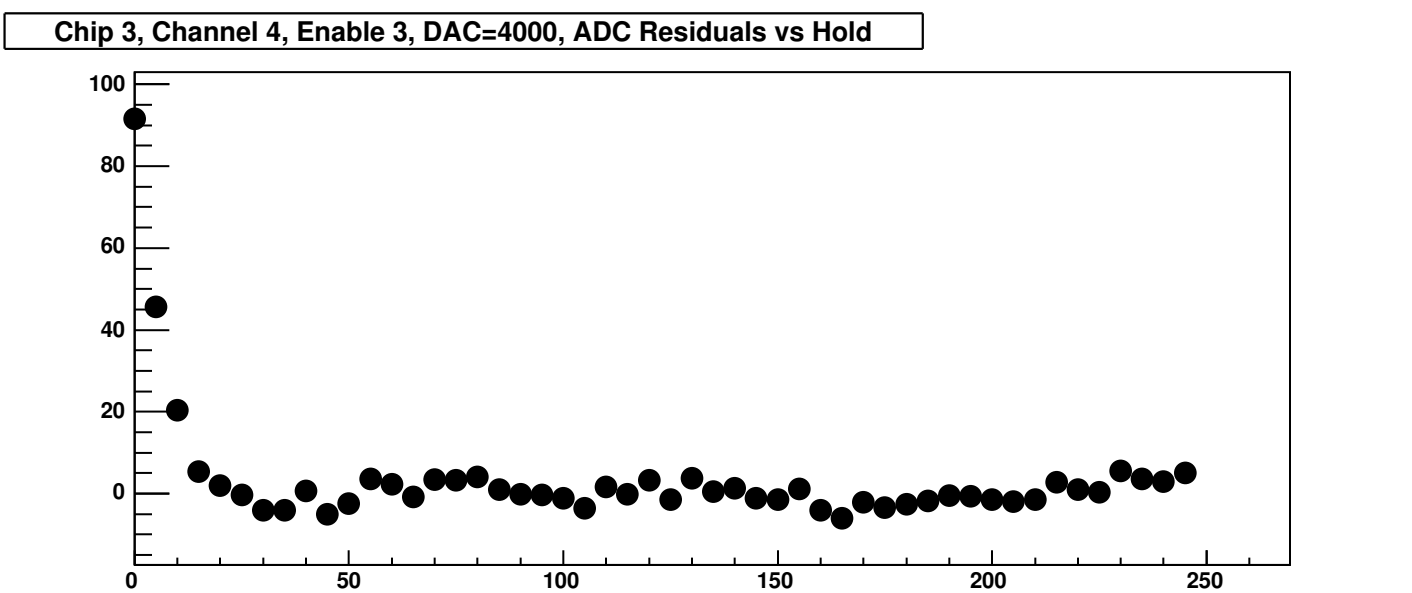

Chip 3, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 8160 / 41
p0 431.3 ± 0.07961
p1 -4.899 ± 6.198
p2 231.4 ± 0.06721
p3 -5.017 ± 4.65
p4 0.1752 ± 0.0003866
700
680
660
640
620
600
0
50
100
150
200
250

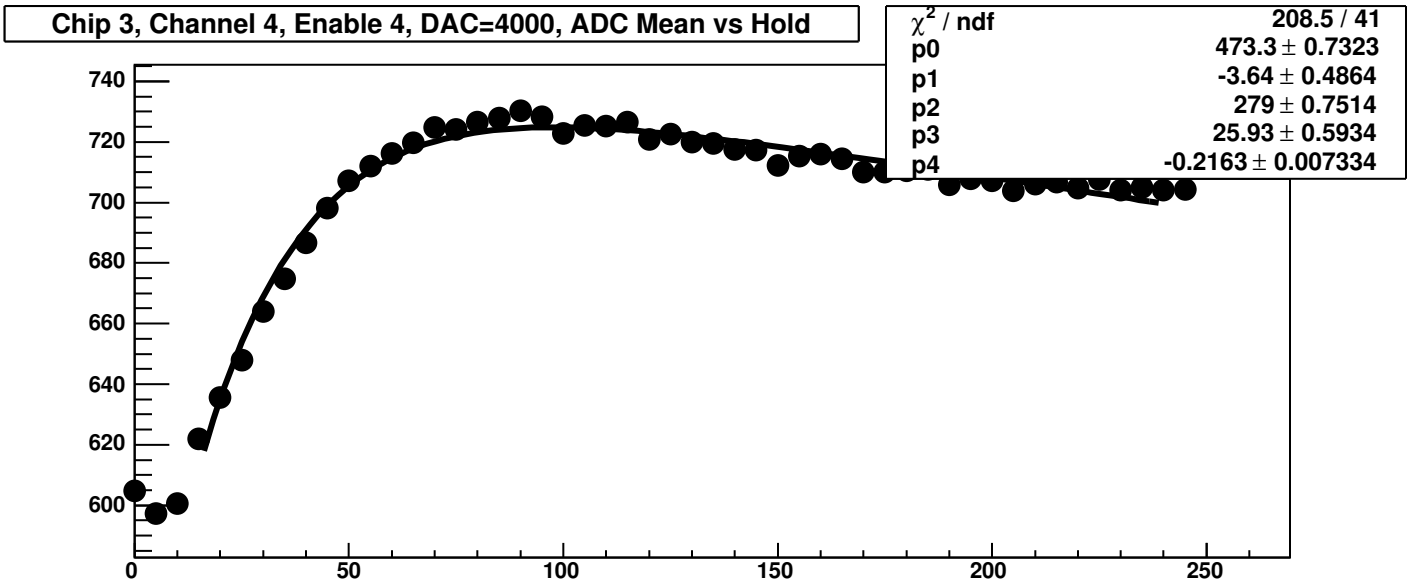

Chip 3, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

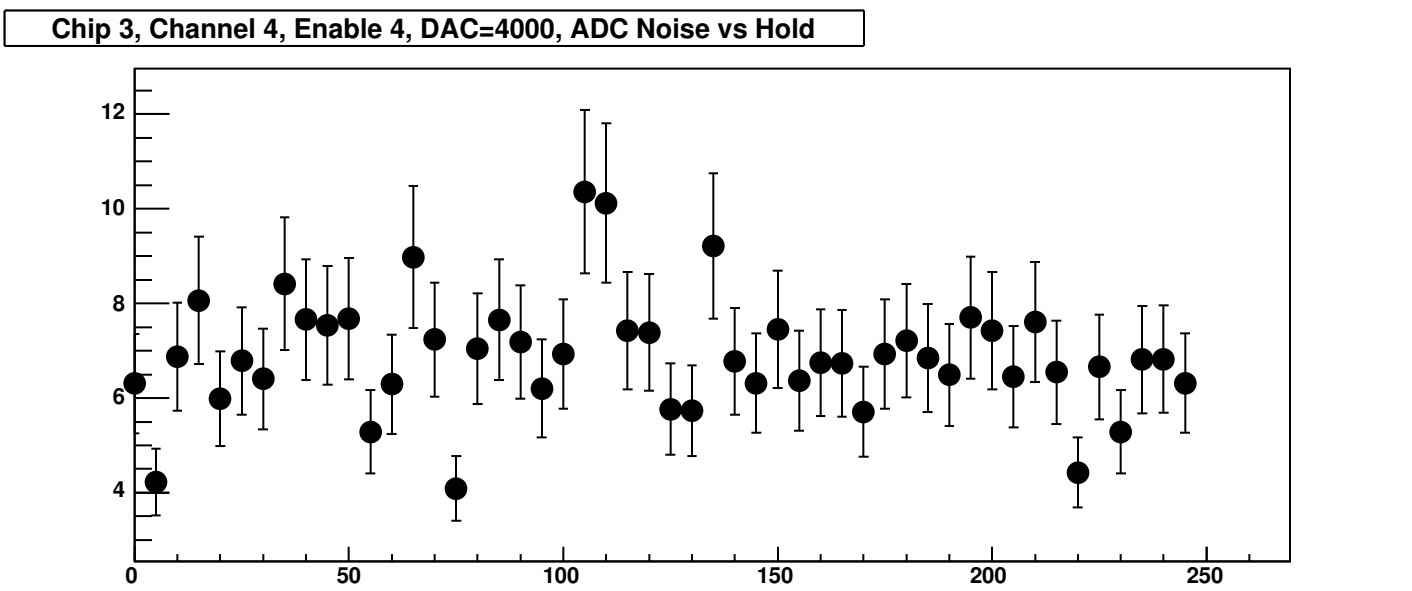

Chip 3, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
-80
0
50
100
150
200
250

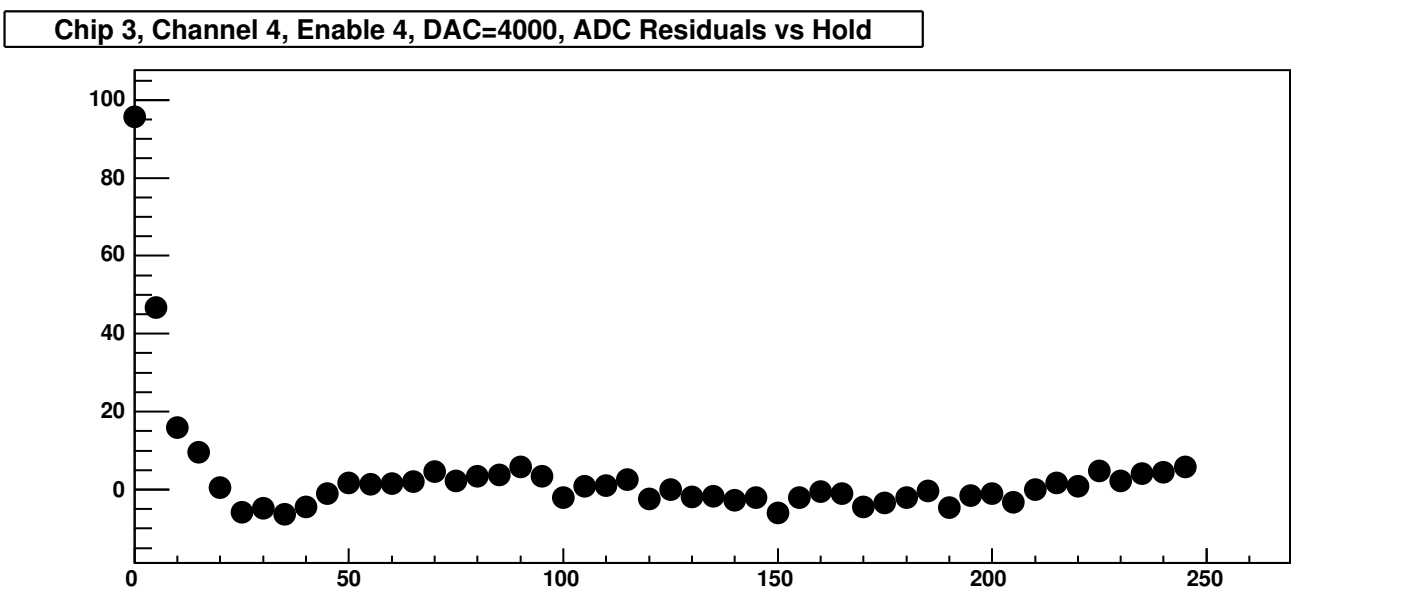

Chip 3, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 149.3 / 41
p0 467.7 ± 2.881
p1 -5.023 ± 0.6591
p2 272.6 ± 2.931
p3 25.78 ± 0.6311
p4 -0.1754 ± 0.006924
720
700
680
660
640
620
600
0
50
100
150
200
250

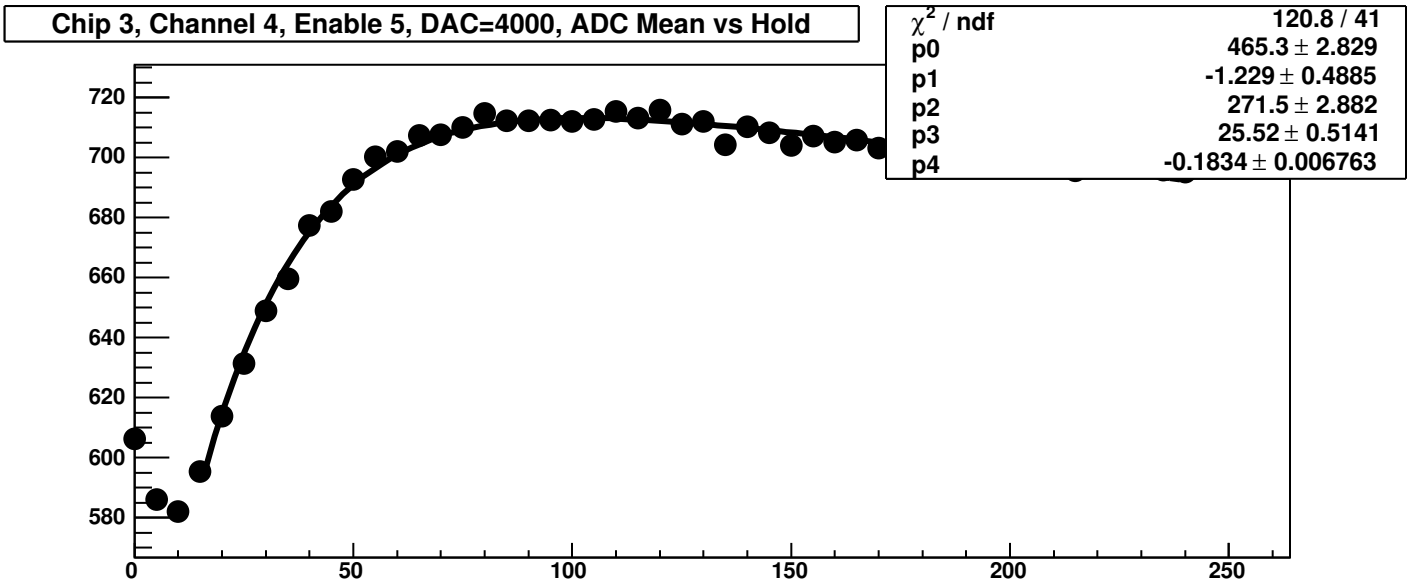

Chip 3, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

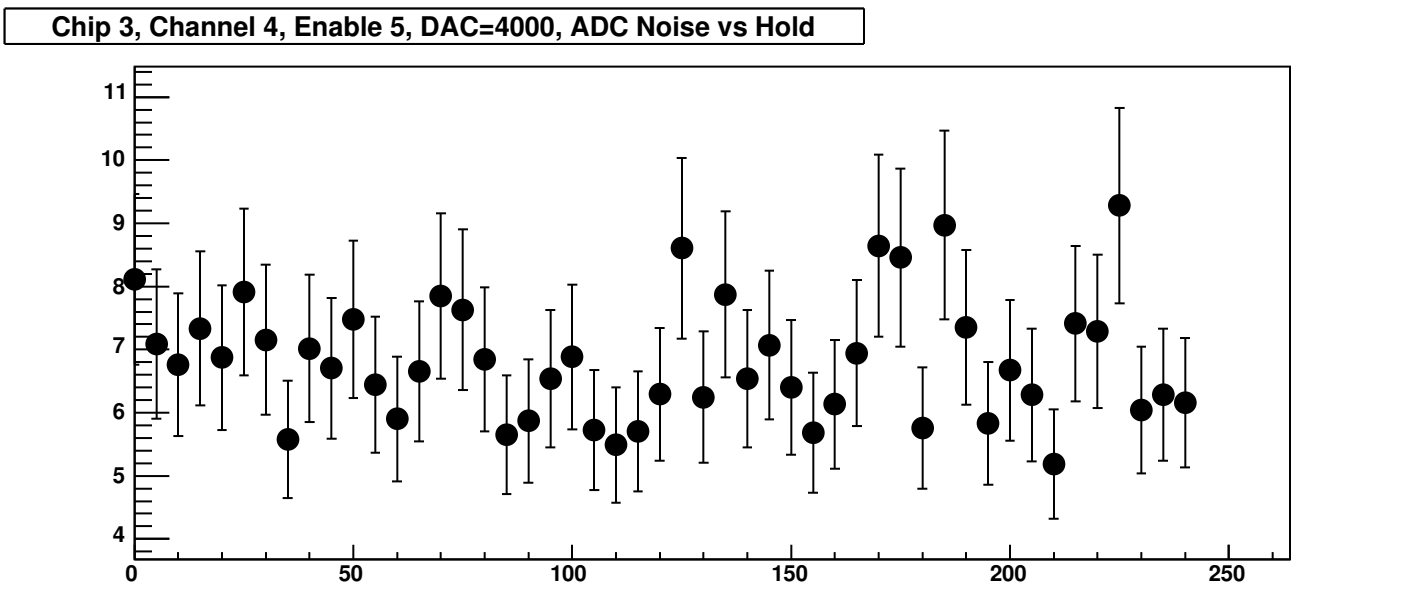

Chip 3, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold
100
80
60
40
20
0
0
50
100
150
200
250

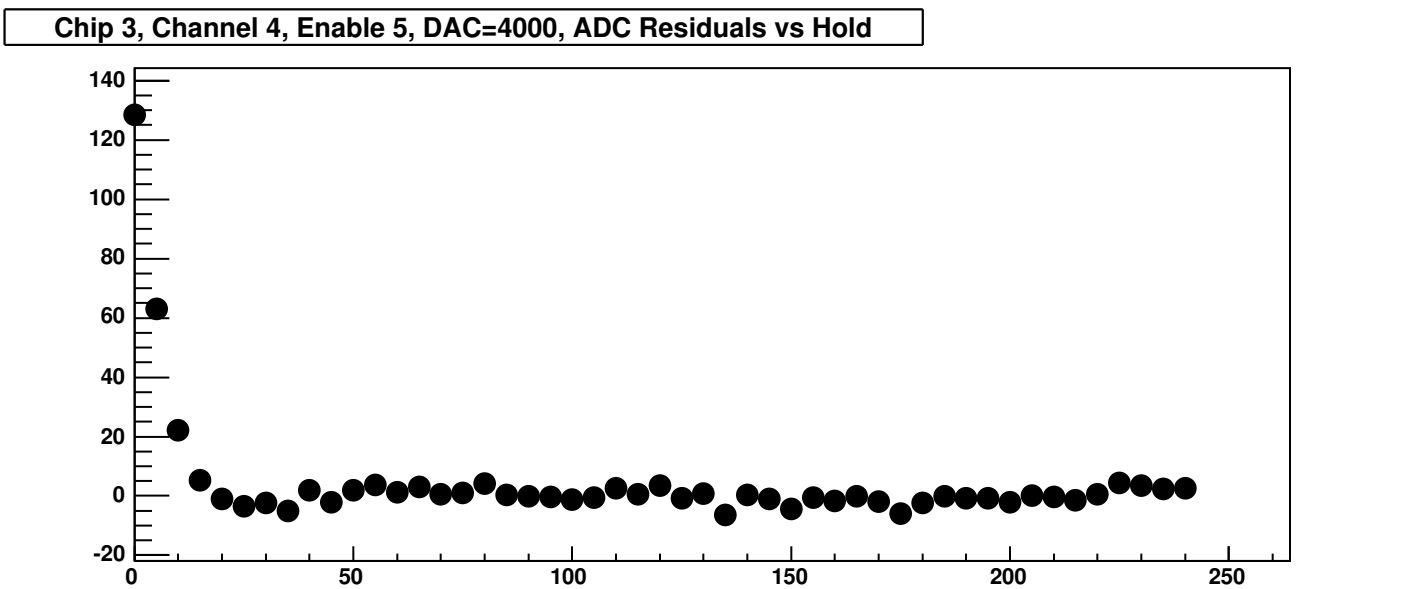

Chip 3, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 208.5 / 41
p0 473.3 ± 0.7323
p1 -3.64 ± 0.4864
p2 279 ± 0.7514
p3 25.93 ± 0.5934
p4 -0.2163 ± 0.007334
740
720
700
680
660
640
620
600
0
50
100
150
200
250


Chip 3, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

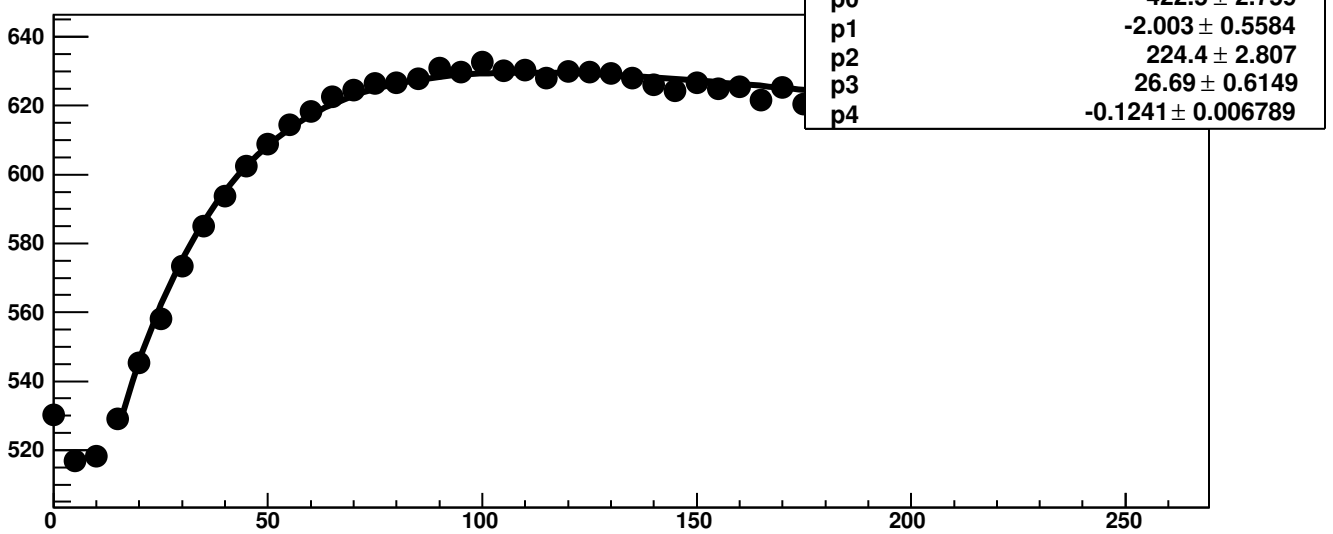

Chip 3, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold
100
80
60
40
20
0
0
50
100
150
200
250


Chip 3, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 120.8 / 41
p0 465.3 ± 2.829
p1 -1.229 ± 0.4885
p2 271.5 ± 2.882
p3 25.52 ± 0.5141
p4 -0.1834 ± 0.006763
720
700
680
660
640
620
600
580
0
50
100
150
200
250

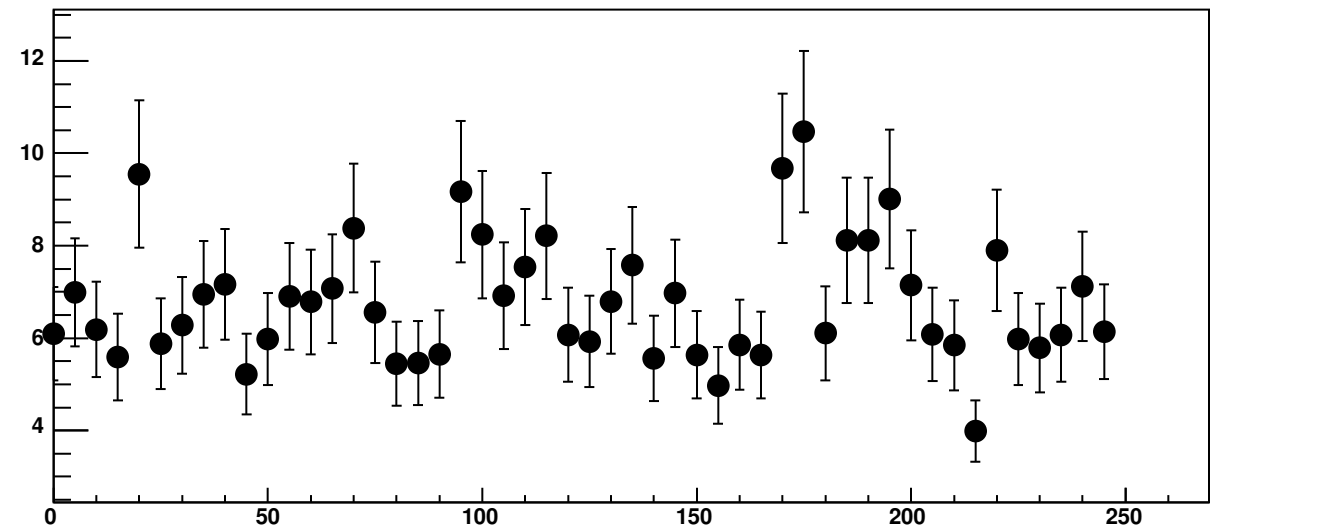

Chip 3, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 3, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold
140
120
100
80
60
40
20
0
-20
0
50
100
150
200
250

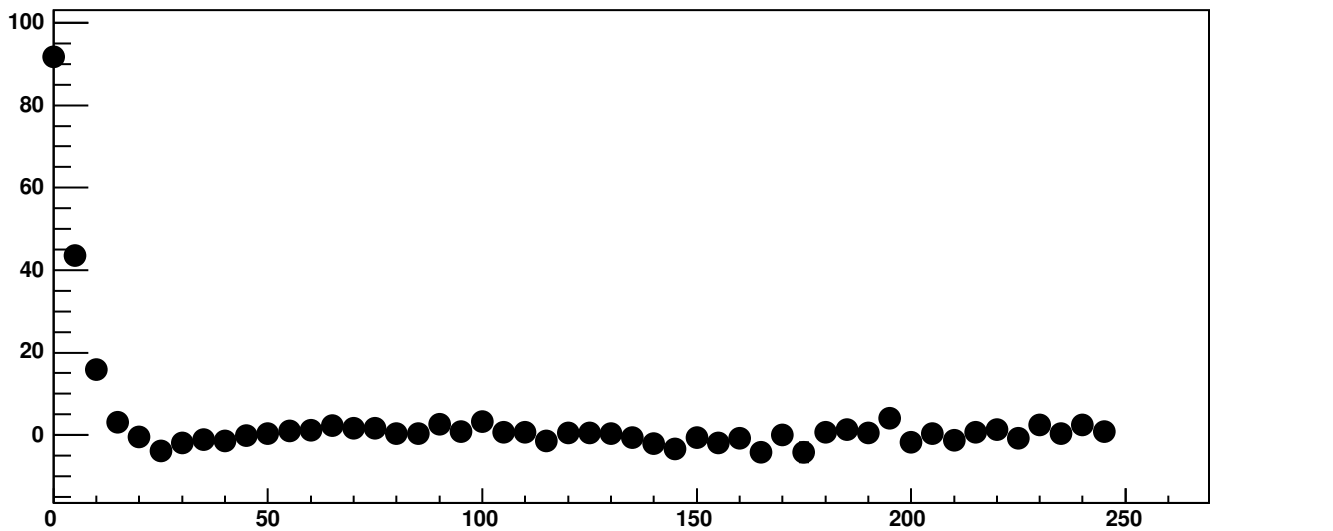
Chip 3, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold



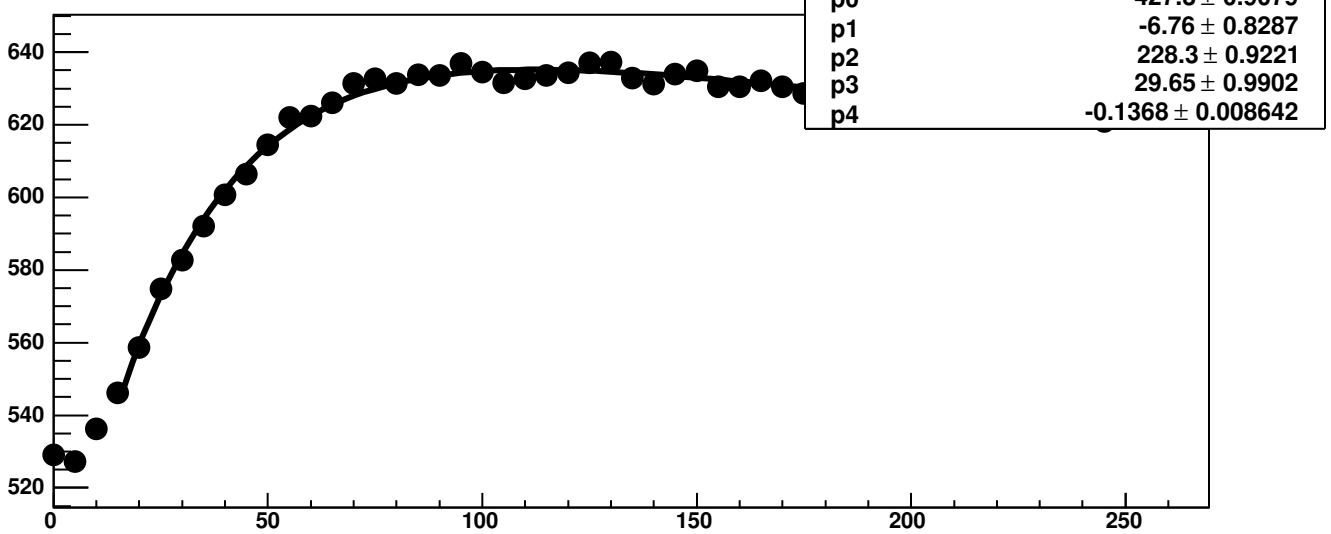
Chip 3, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



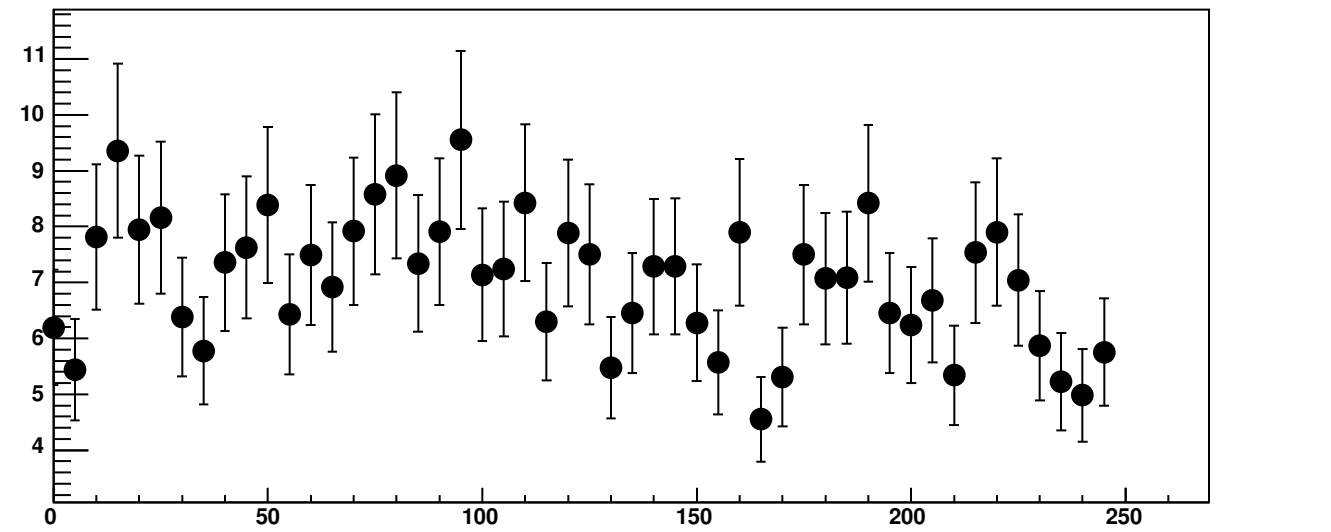
Chip 3, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold



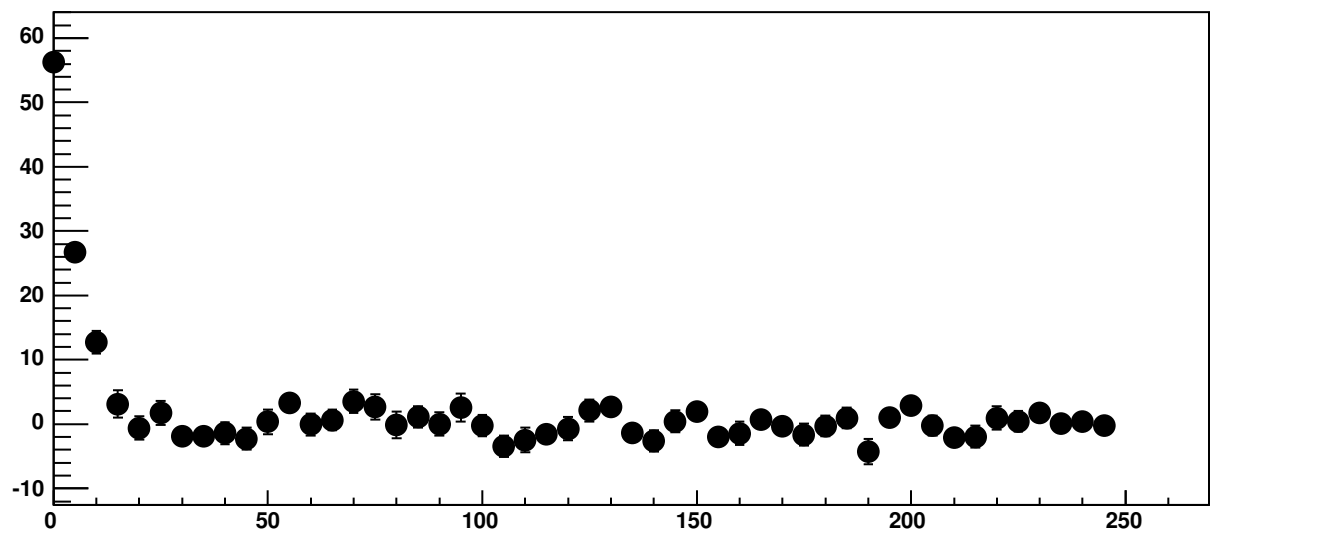
Chip 3, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold



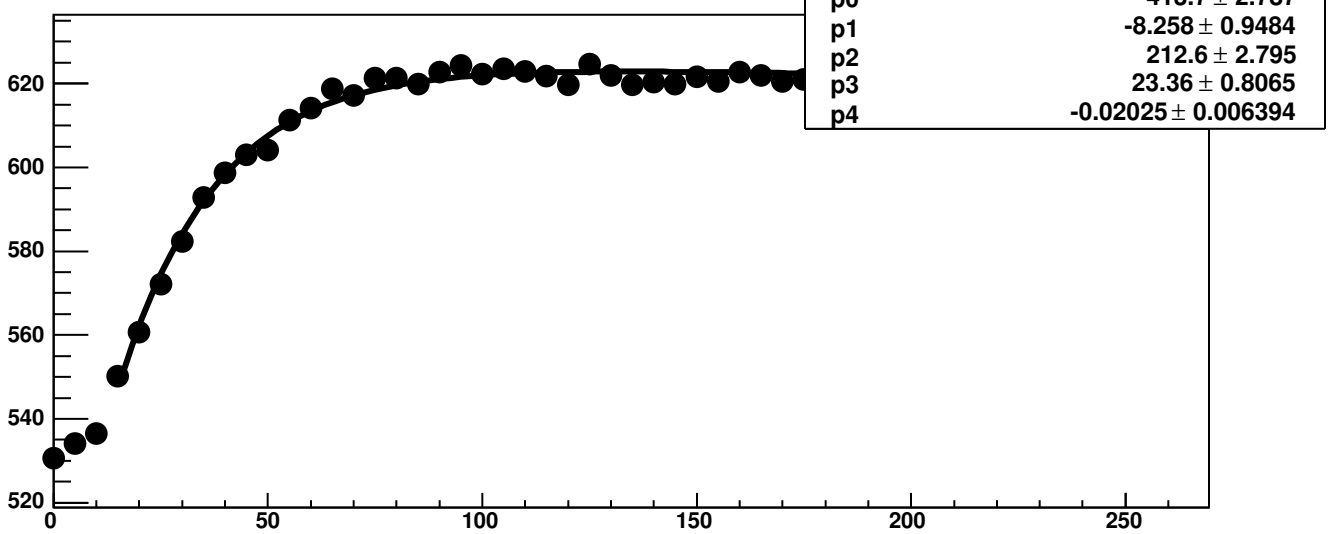
Chip 3, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold



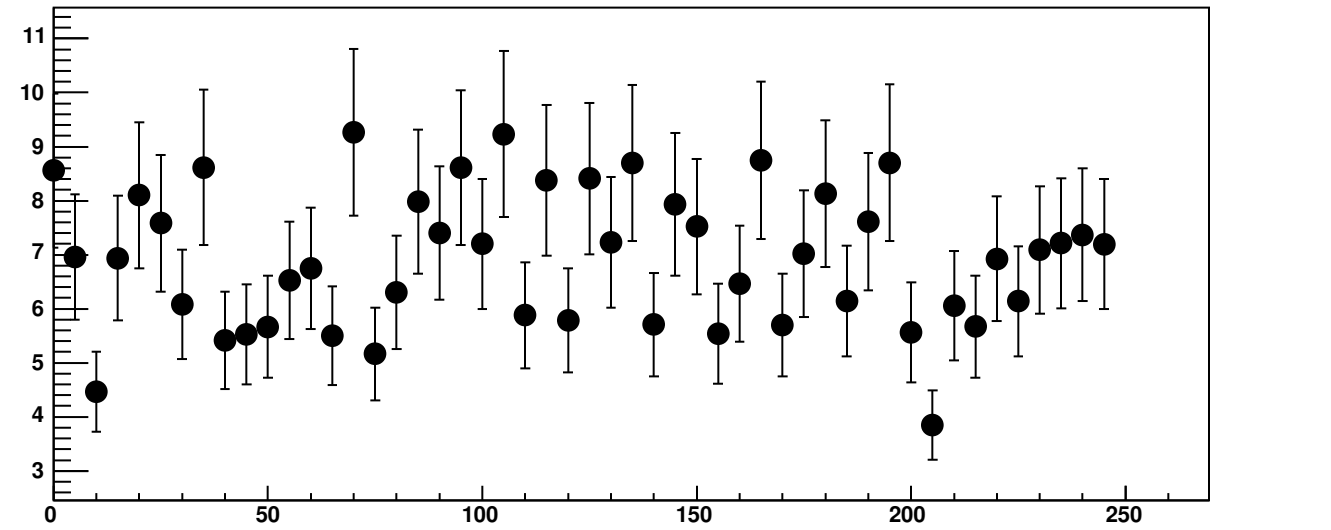
Chip 3, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold



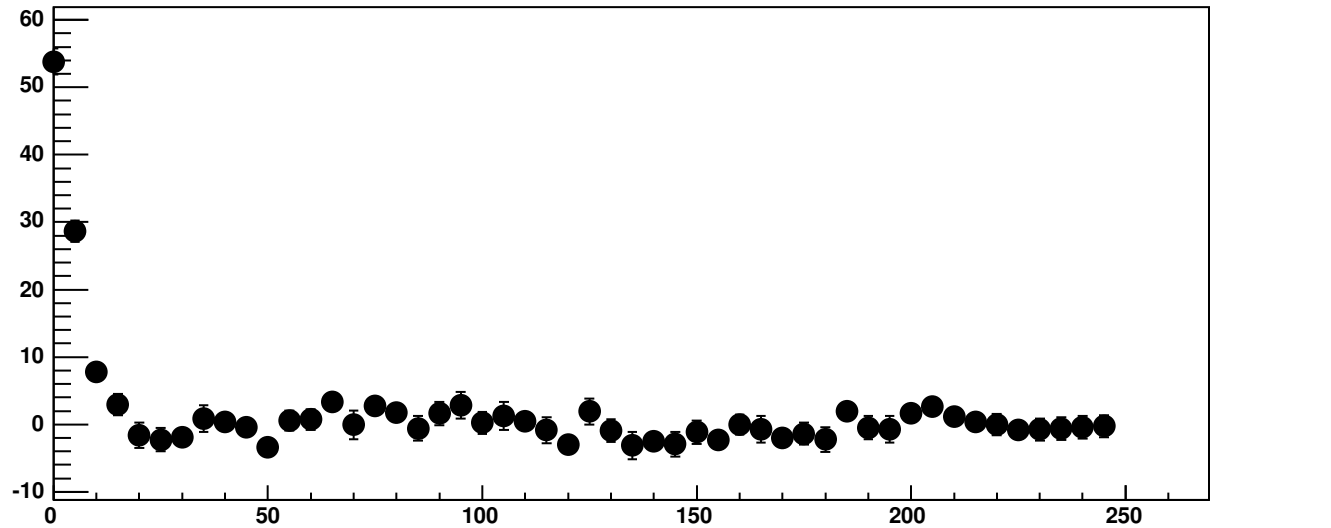
Chip 3, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold



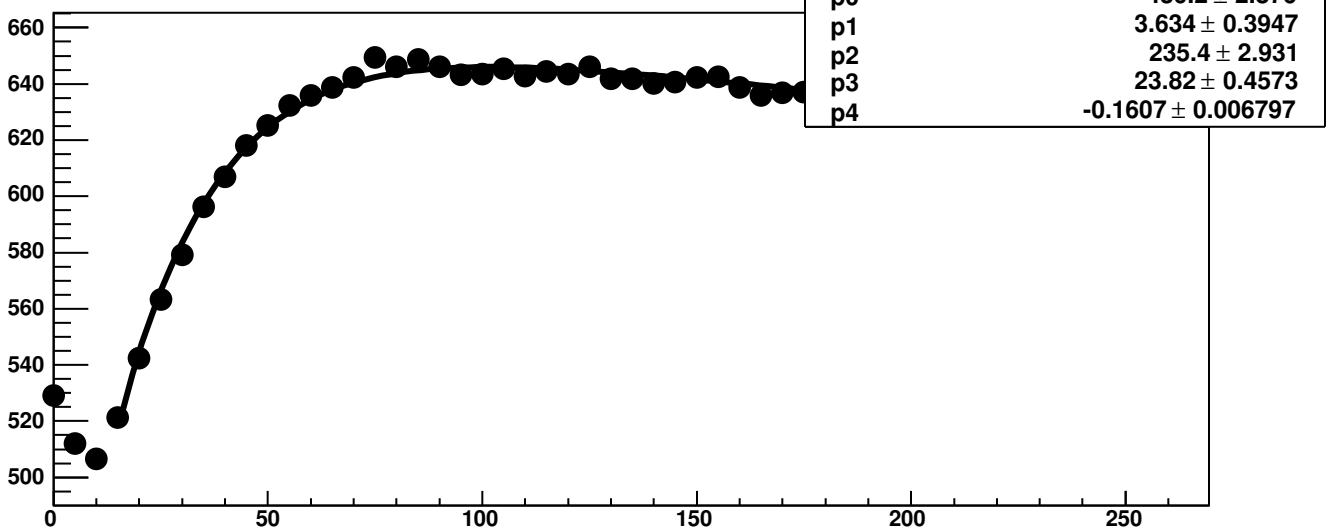
Chip 3, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



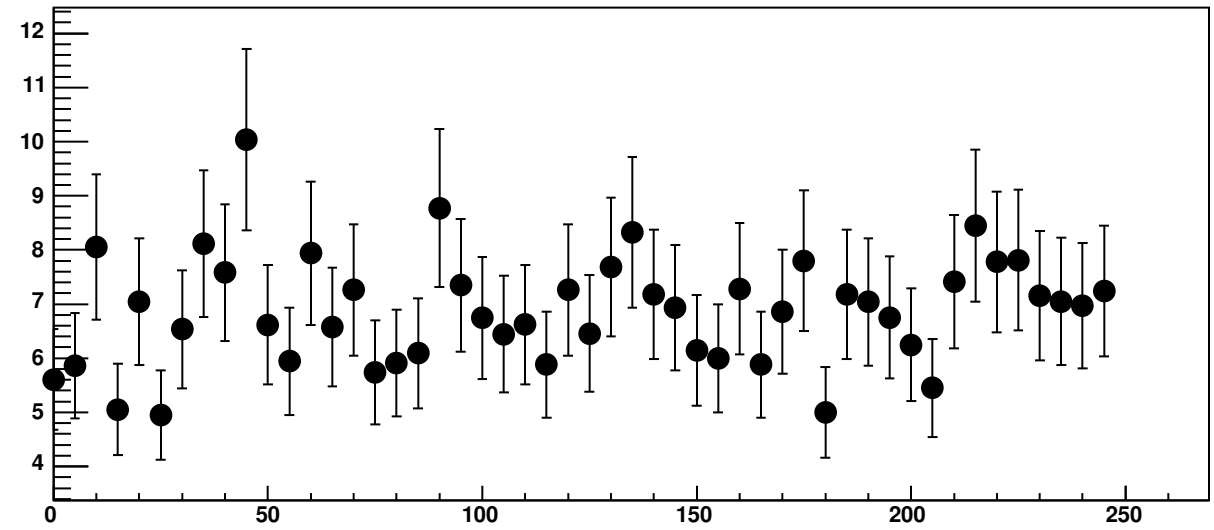
Chip 3, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold



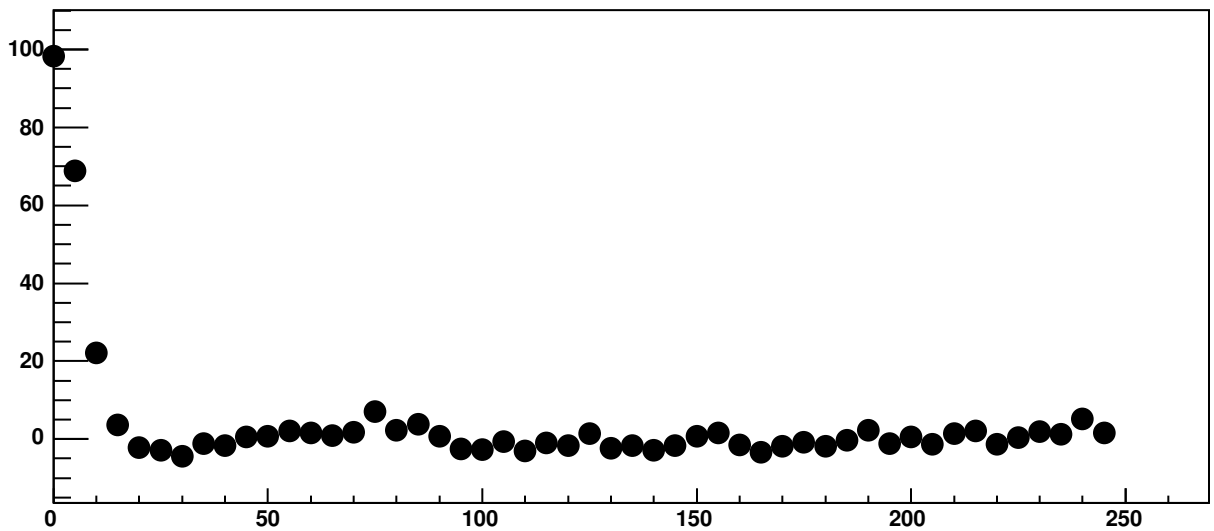
Chip 3, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

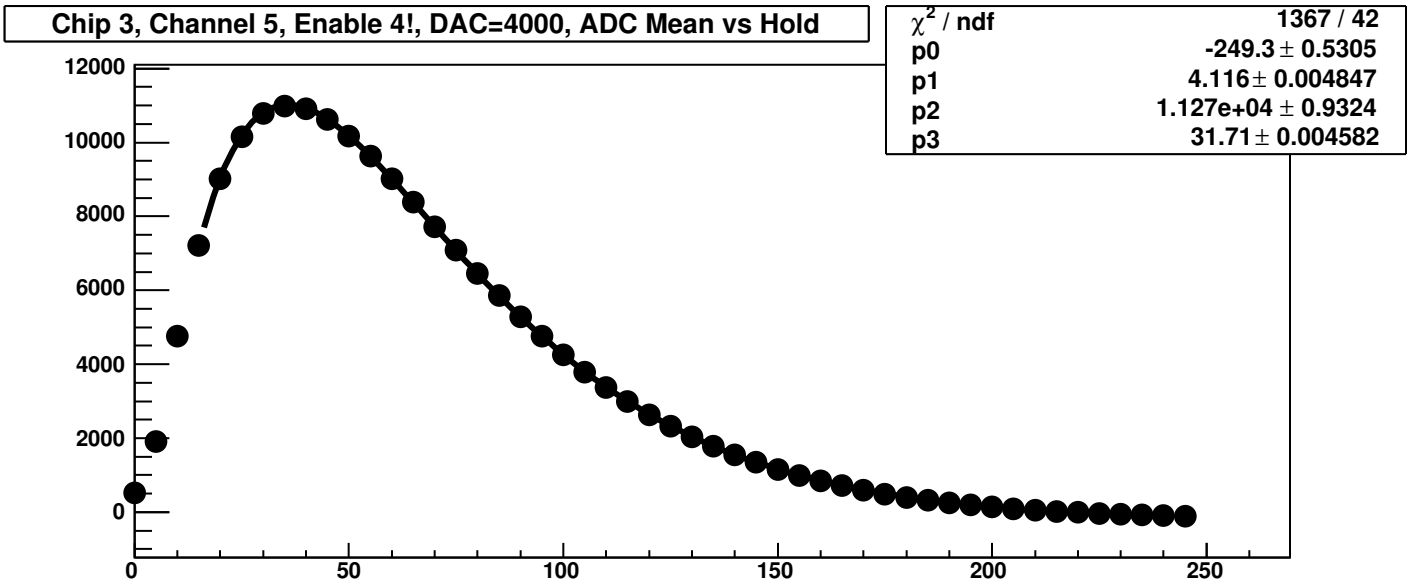


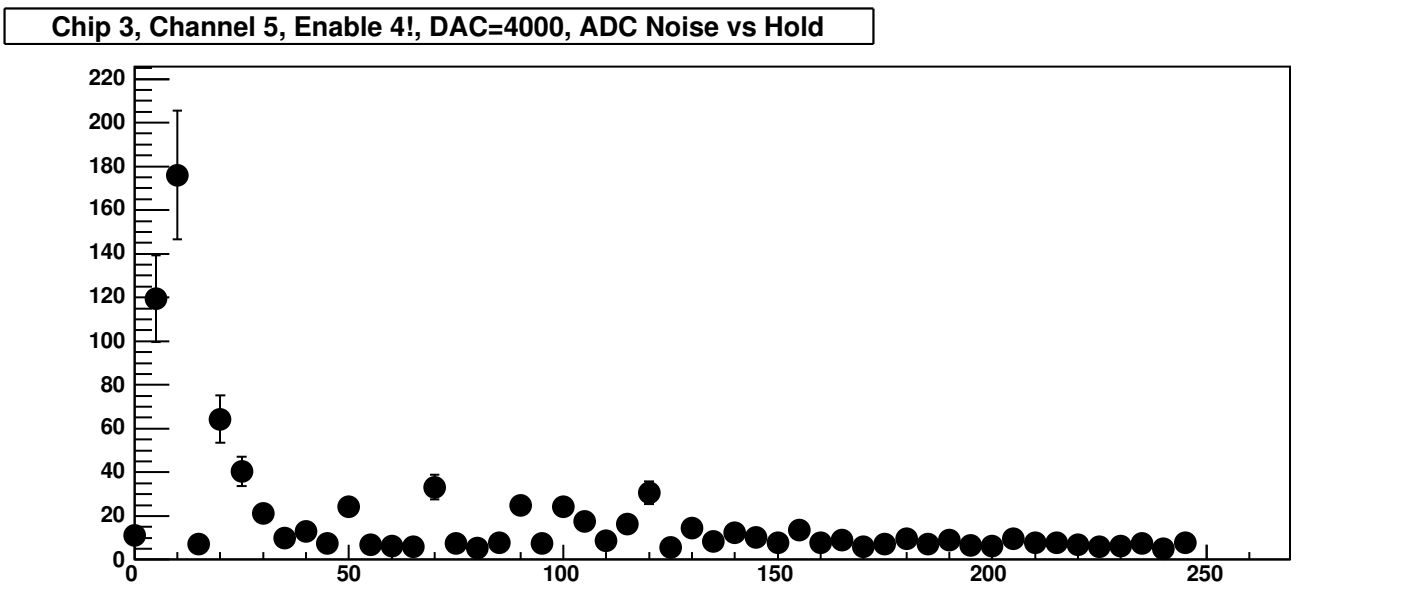
Chip 3, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold

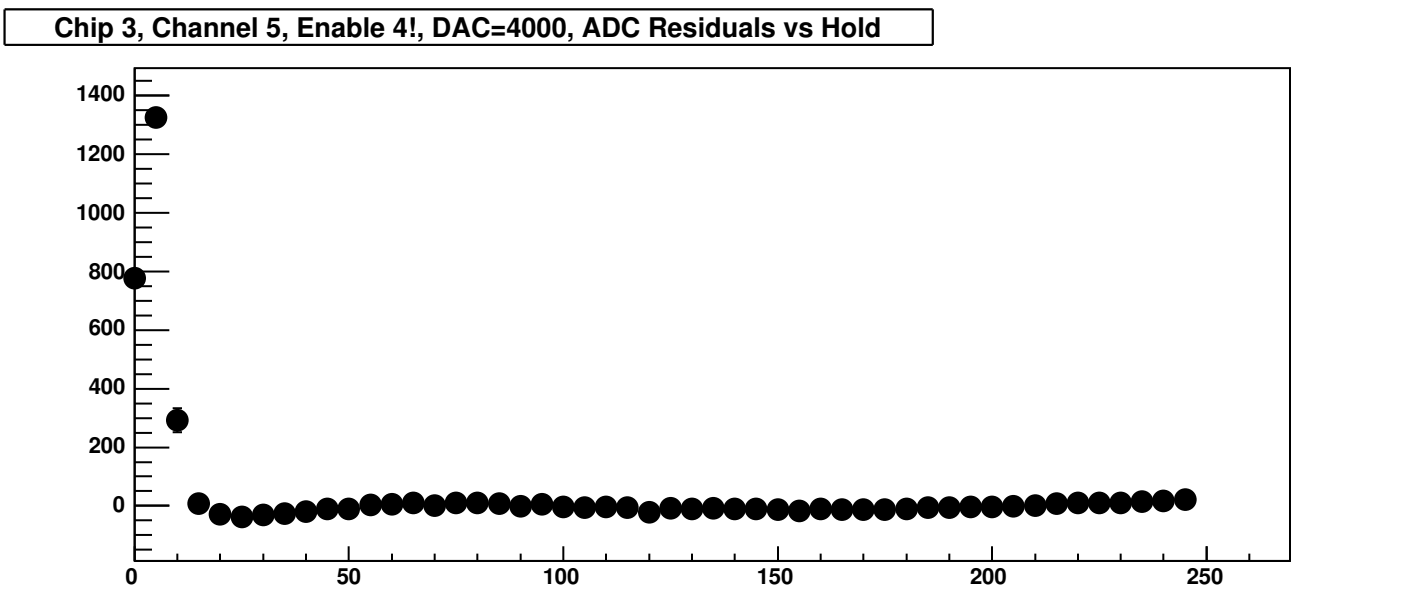


Chip 3, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

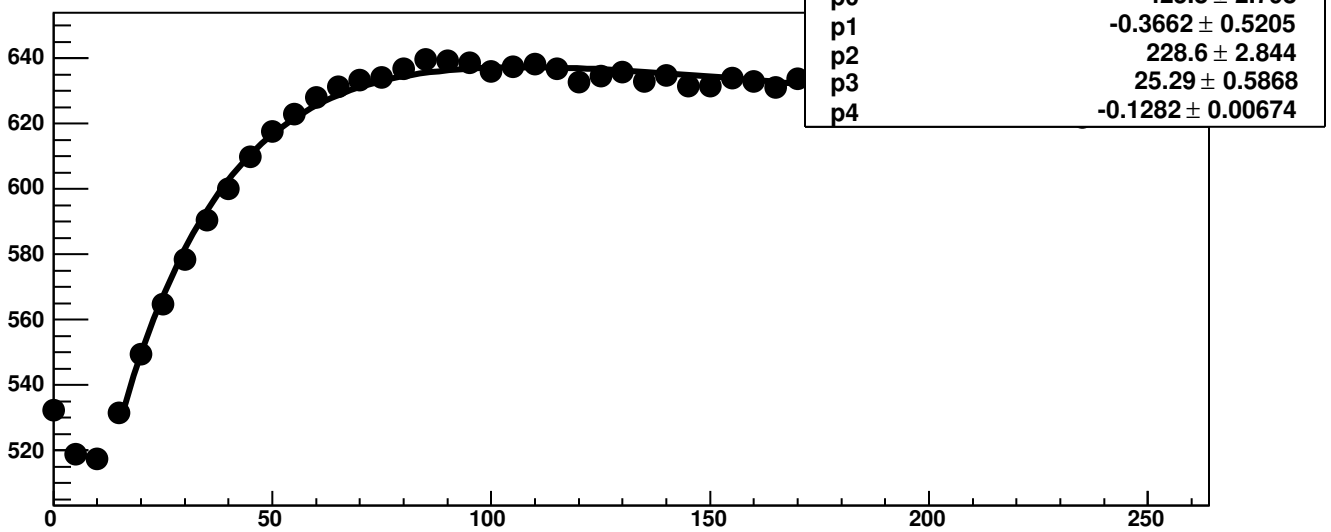



Chip 3, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1367 / 42
p0 -249.3 ± 0.5305
p1 4.116 ± 0.004847
p2 1.127e+04 ± 0.9324
p3 31.71 ± 0.004582
12000
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

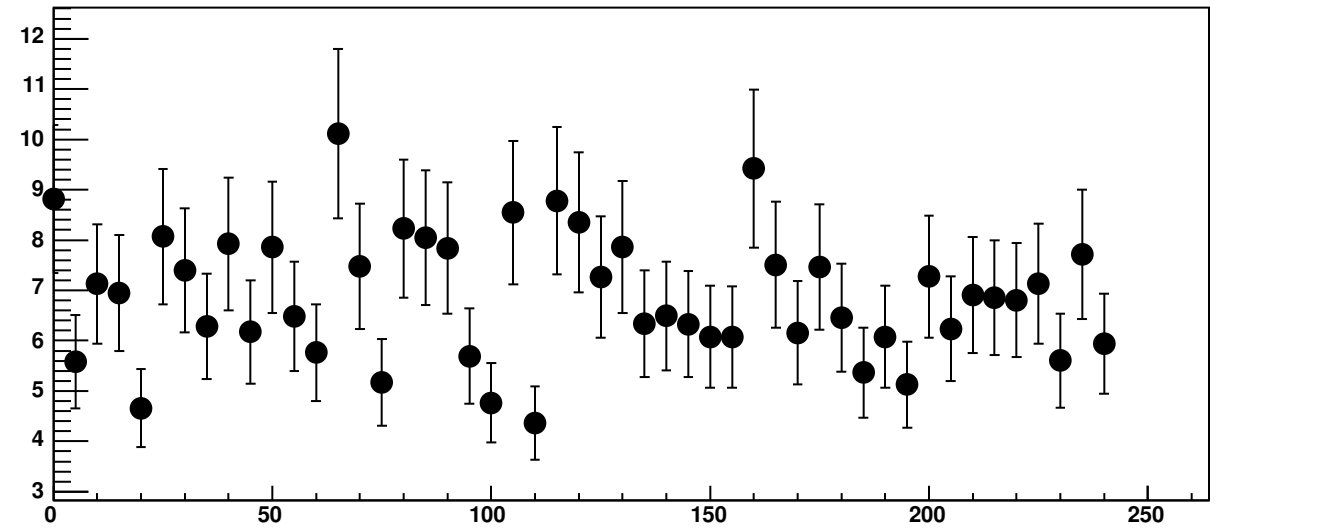

Chip 3, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 3, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

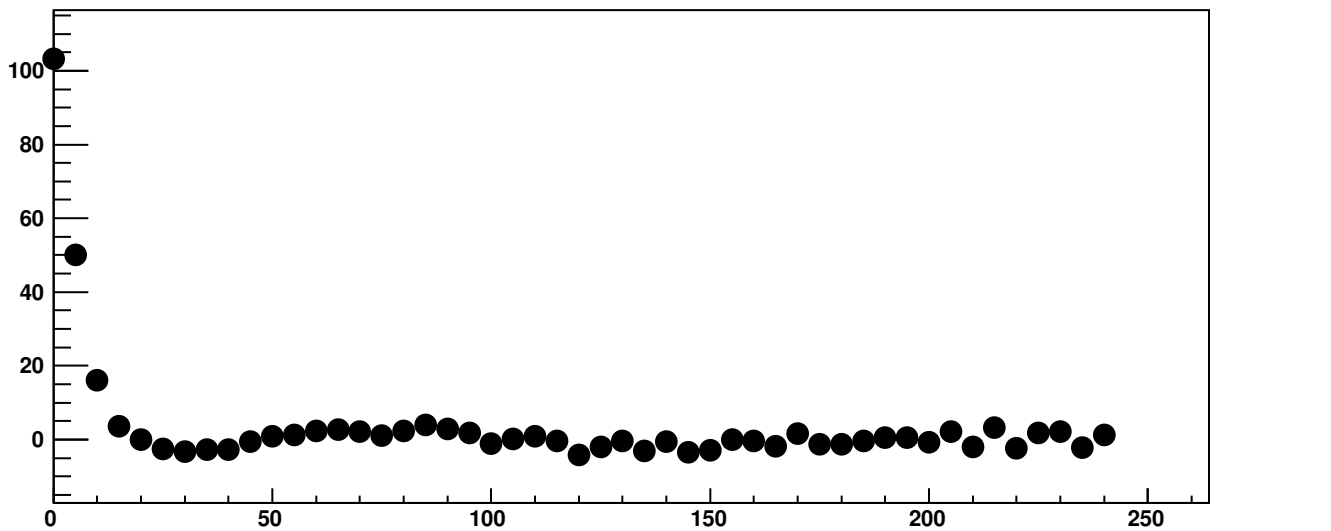
Chip 3, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold



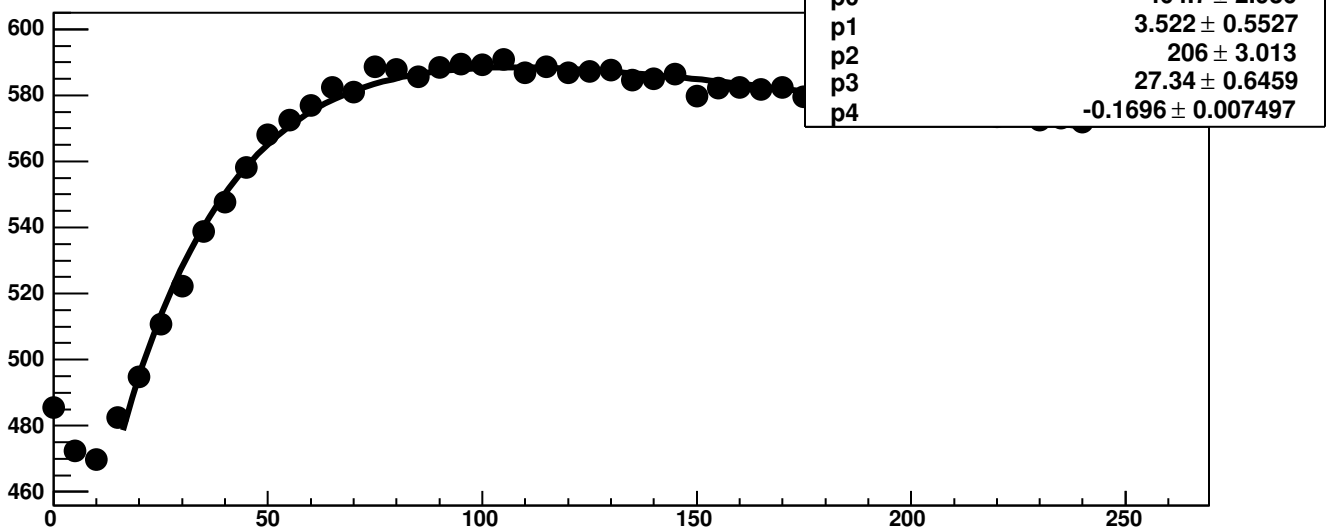
Chip 3, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



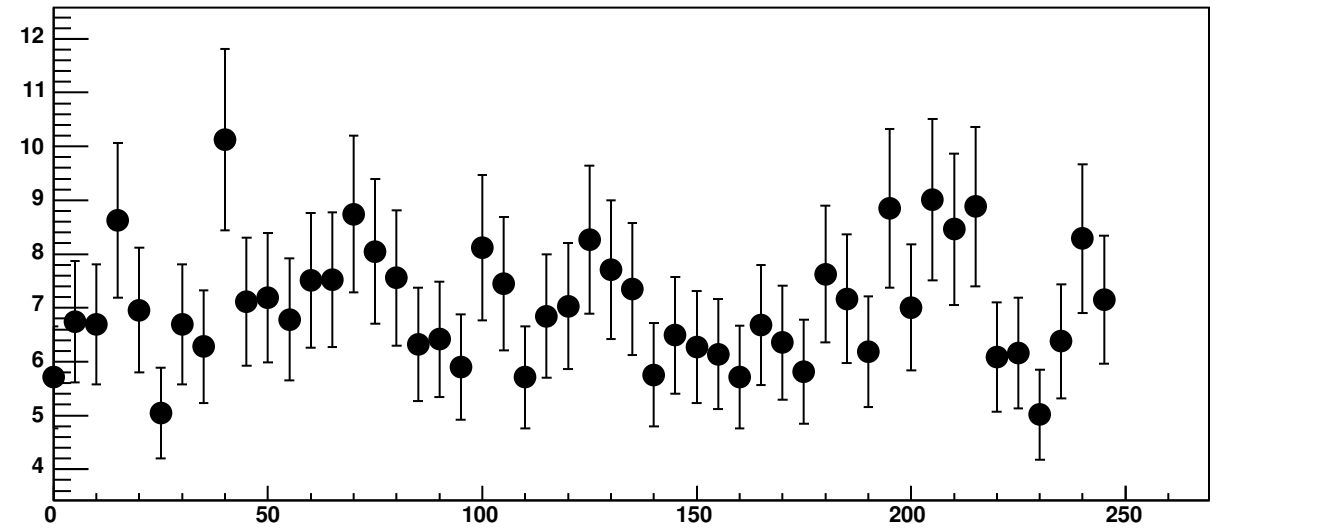
Chip 3, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold



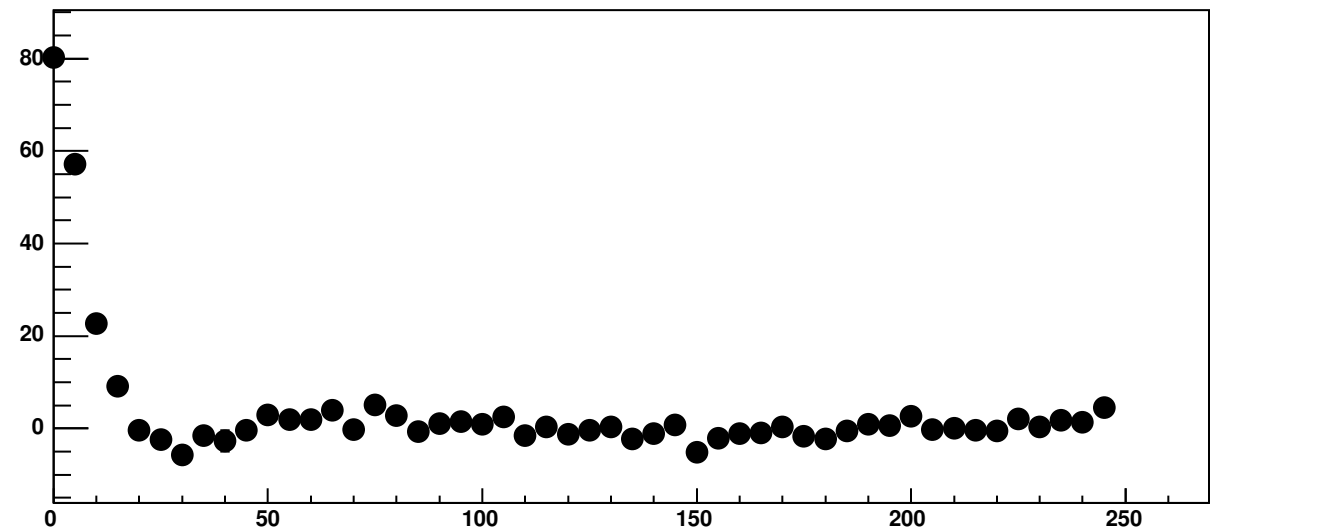
Chip 3, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold



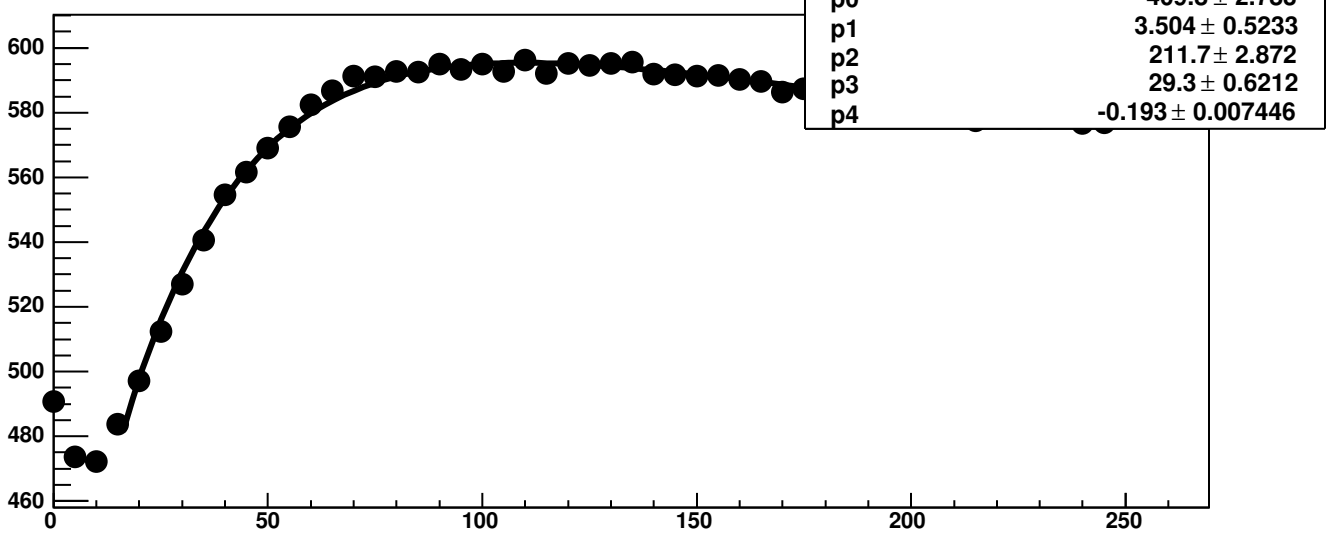
Chip 3, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



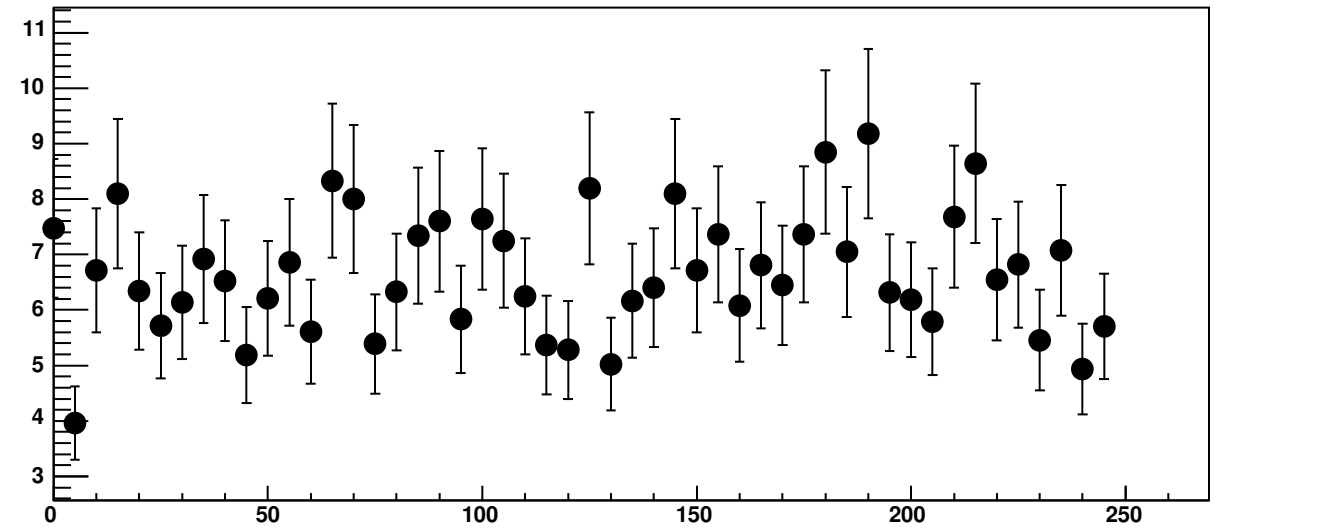
Chip 3, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold



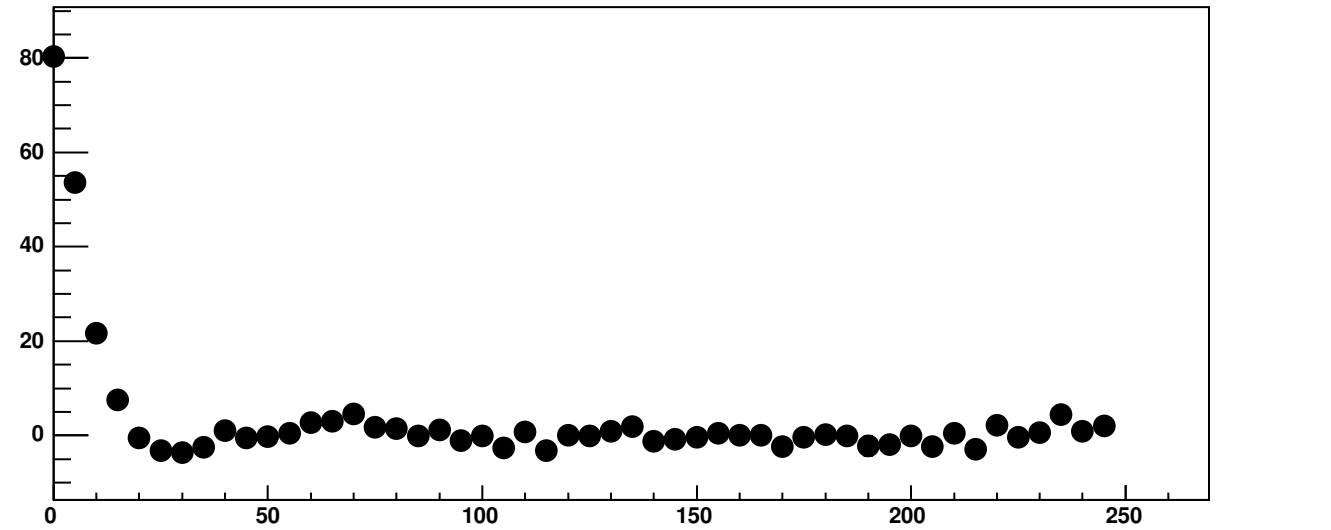
Chip 3, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold



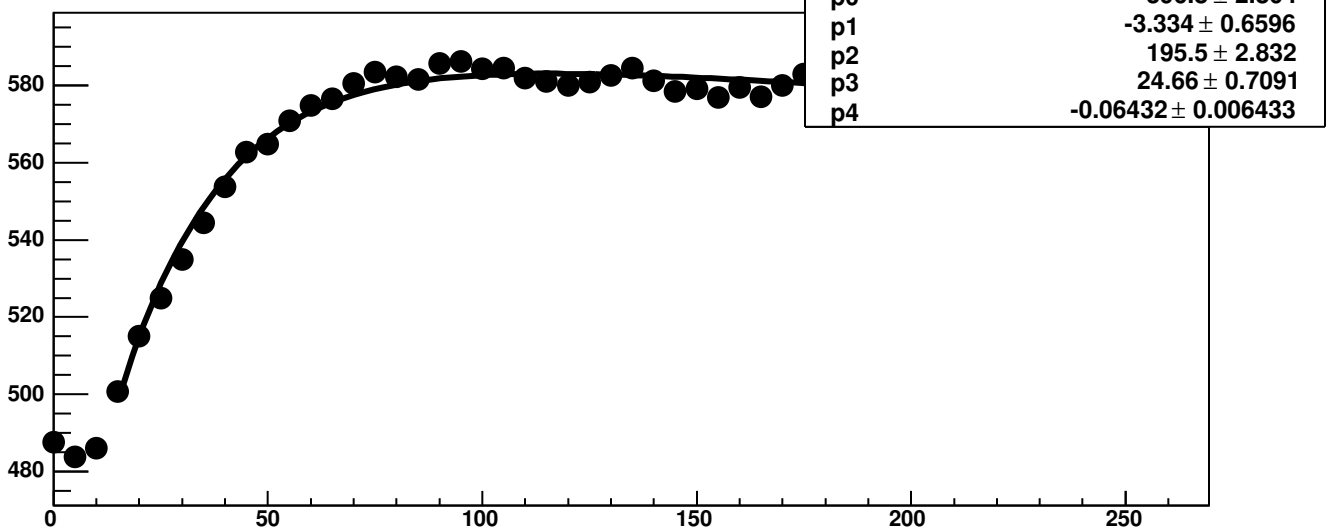
Chip 3, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold



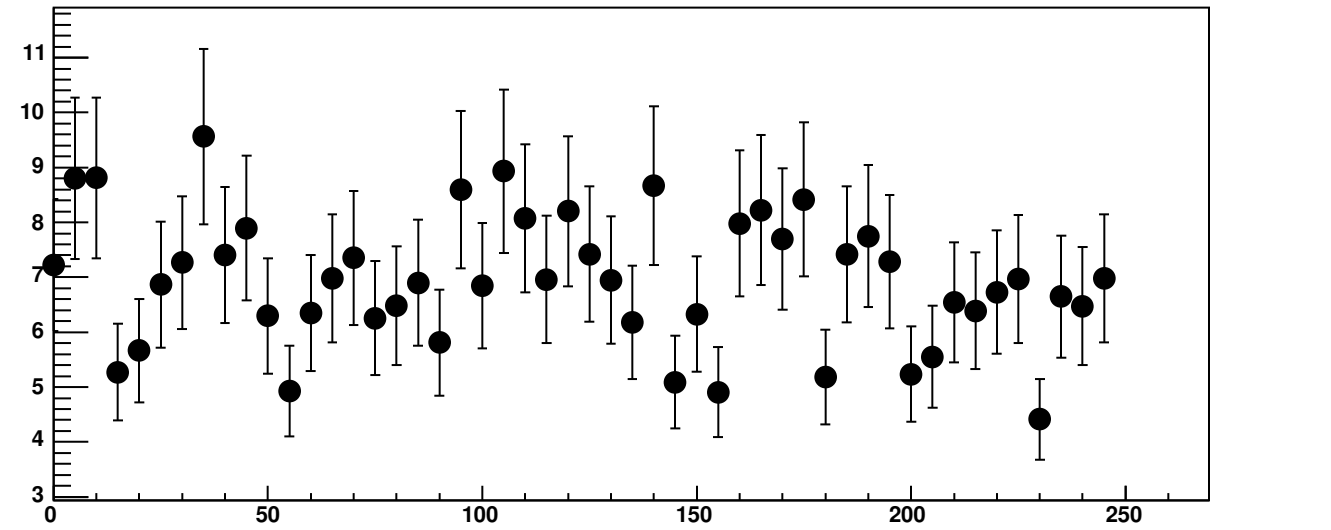
Chip 3, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold



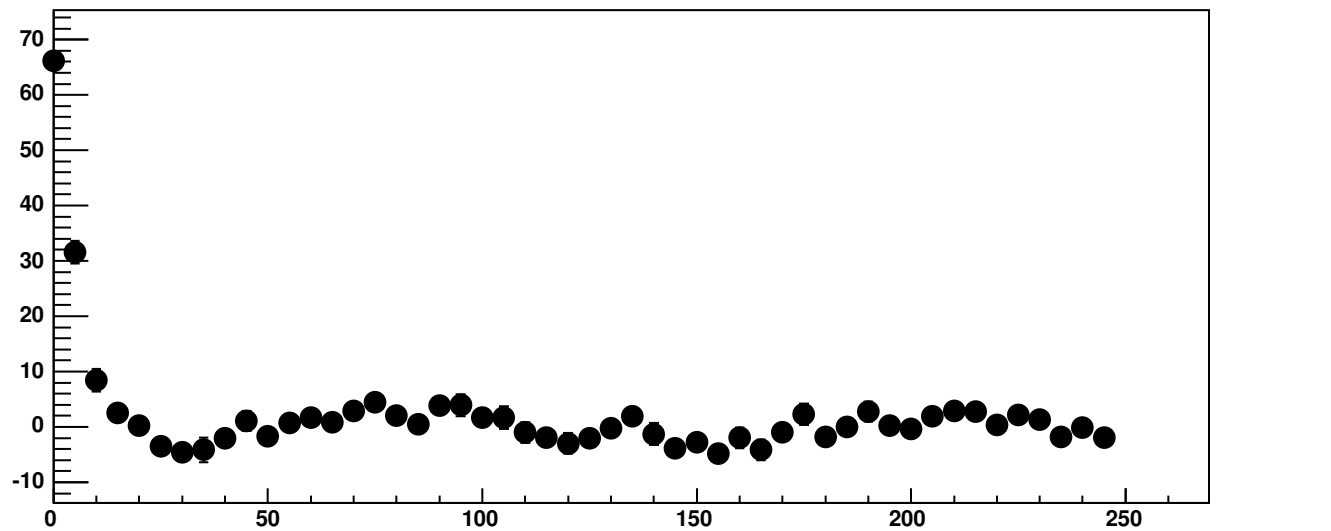
Chip 3, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold



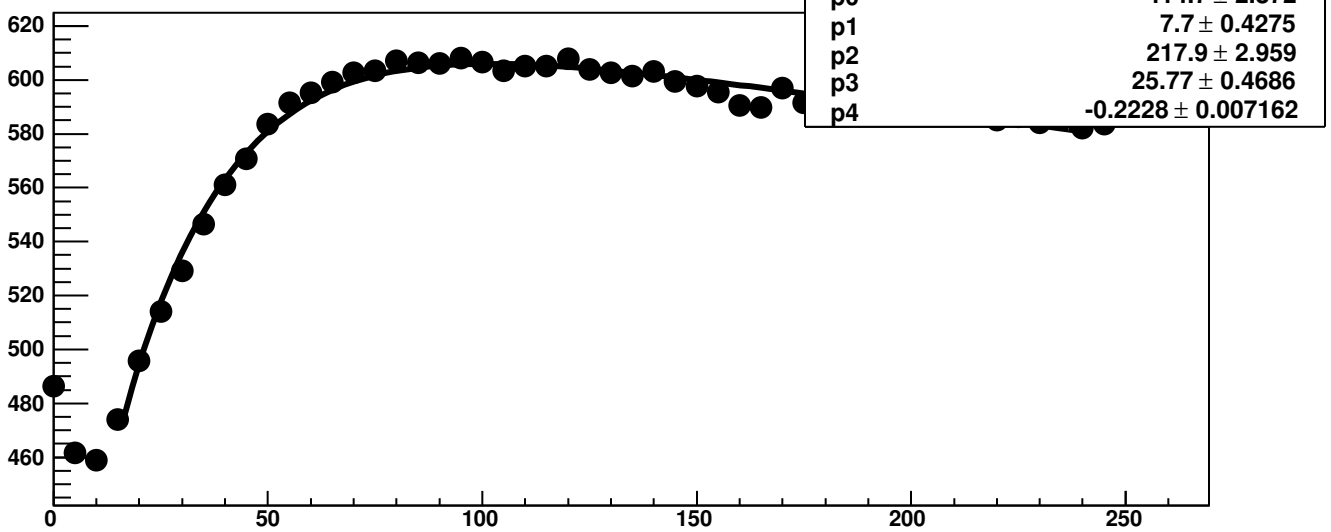
Chip 3, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold



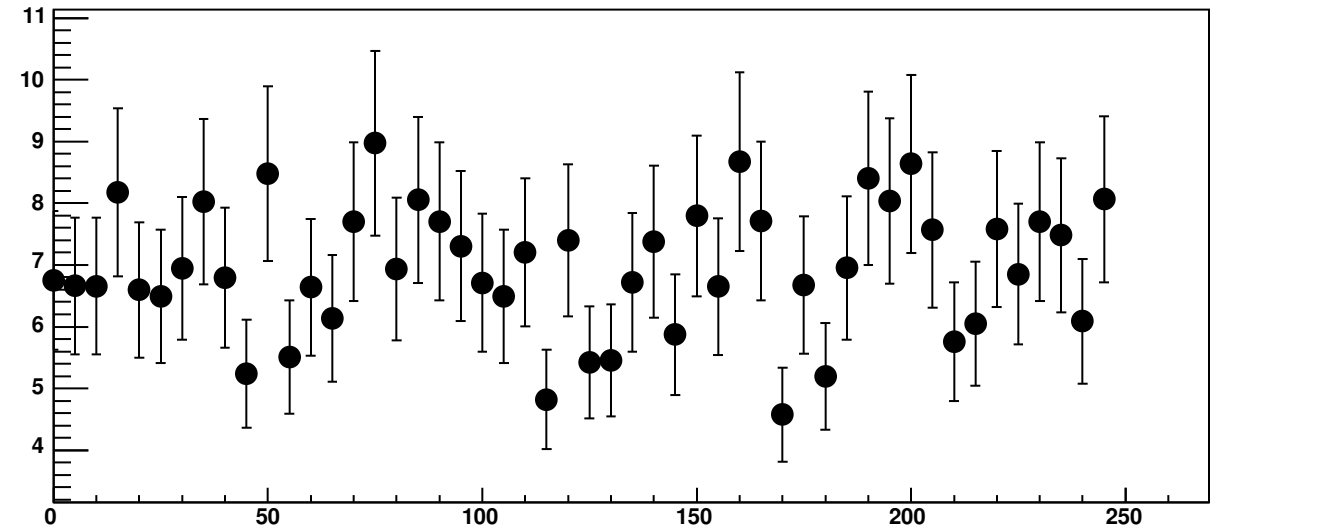
Chip 3, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold



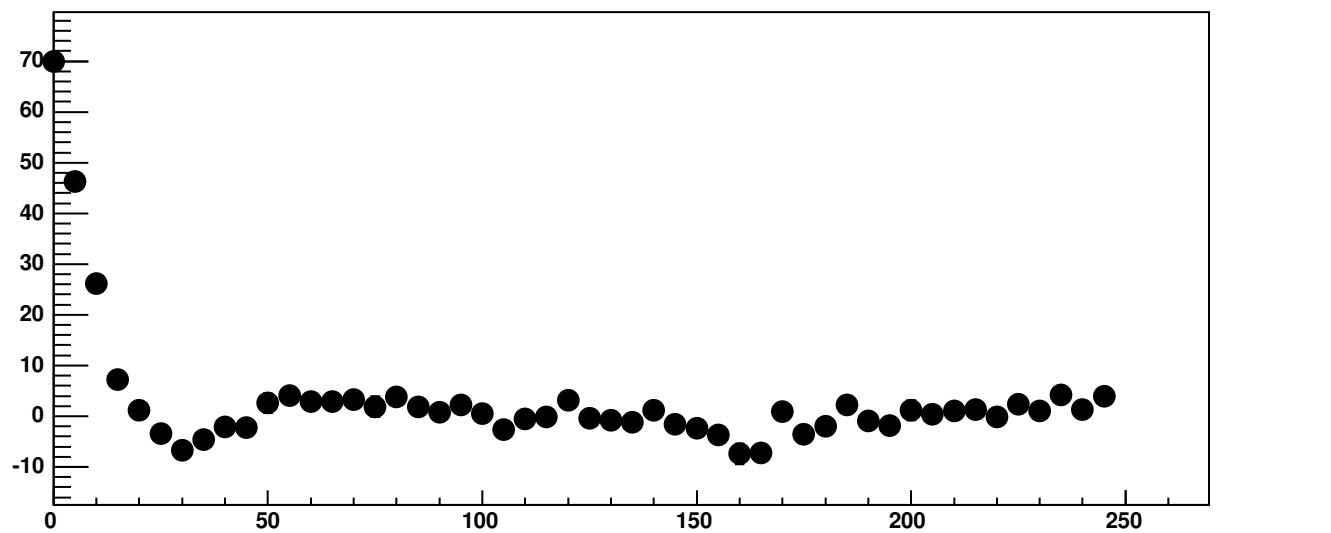
Chip 3, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

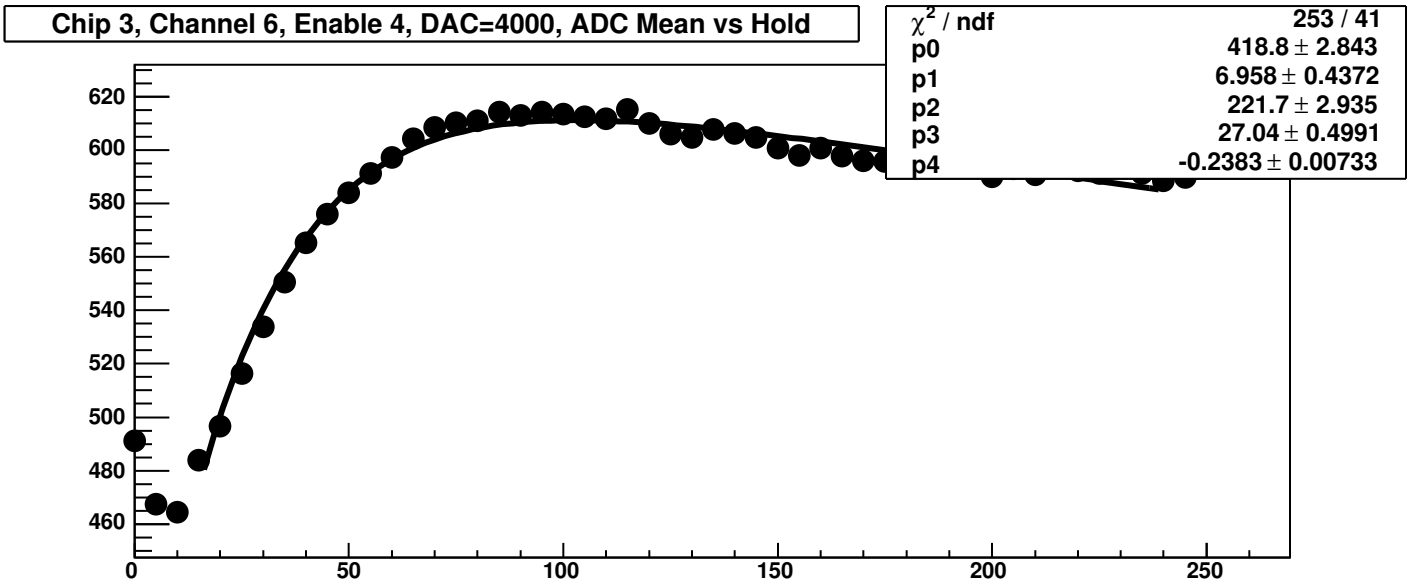


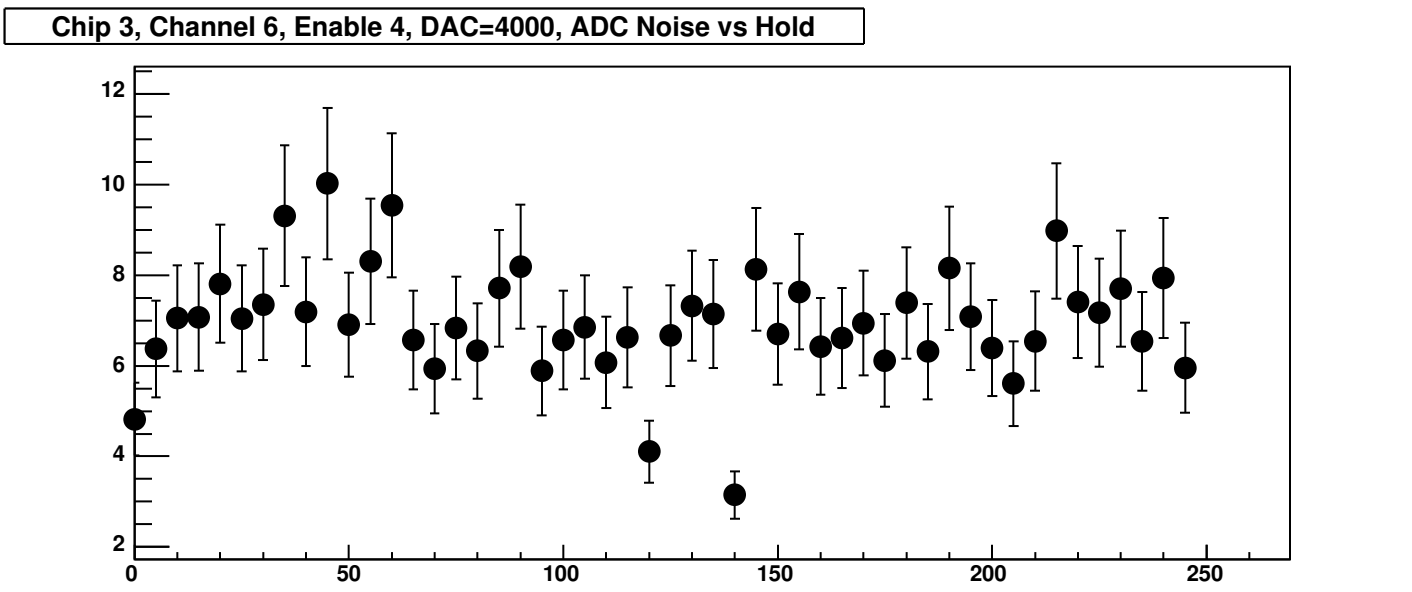
Chip 3, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold

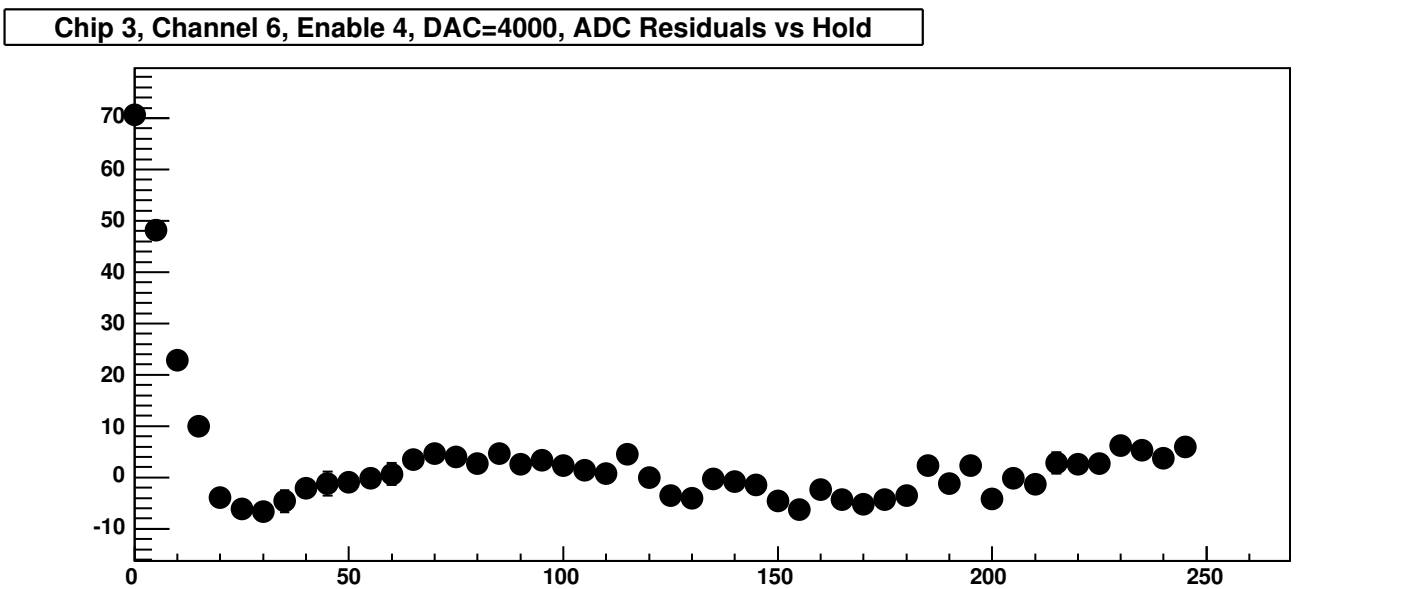


Chip 3, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold



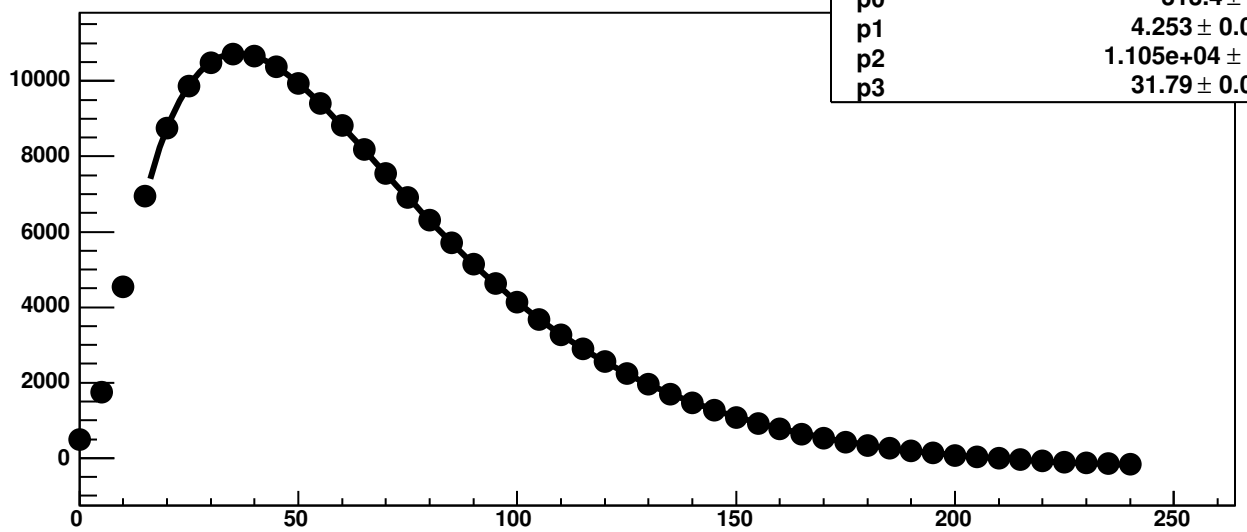

Chip 3, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 253 / 41
p0 418.8 ± 2.843
p1 6.958 ± 0.4372
p2 221.7 ± 2.935
p3 27.04 ± 0.4991
p4 -0.2383 ± 0.00733
620
600
580
560
540
520
500
480
460
0
50
100
150
200
250


Chip 3, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
2
0
50
100
150
200
250

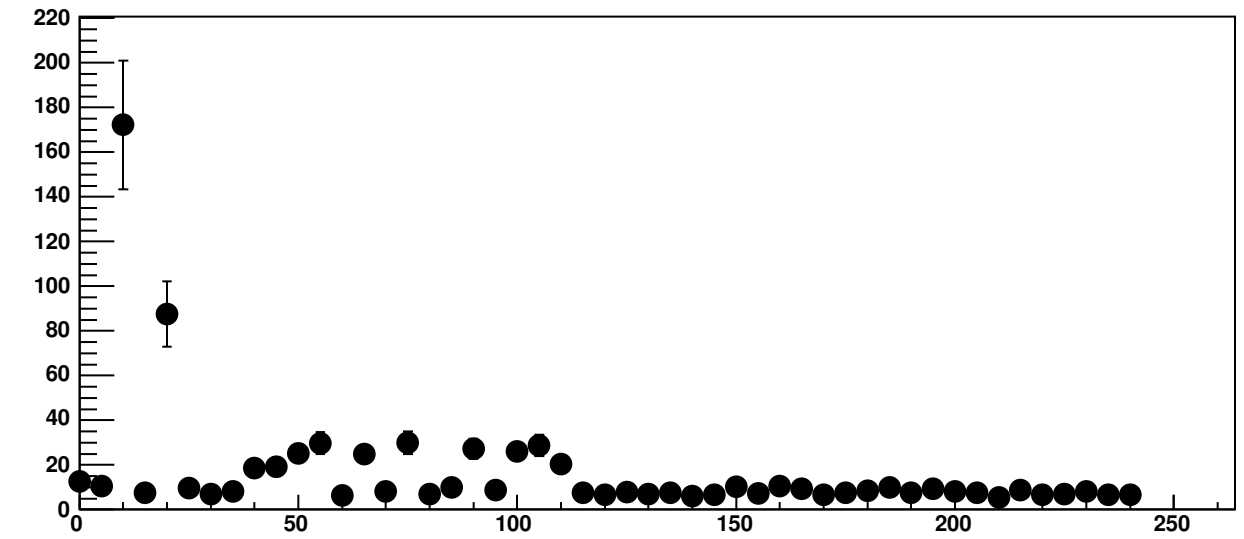

Chip 3, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

Chip 3, Channel 6, Enable 5!, DAC=4000, ADC Mean vs Hold

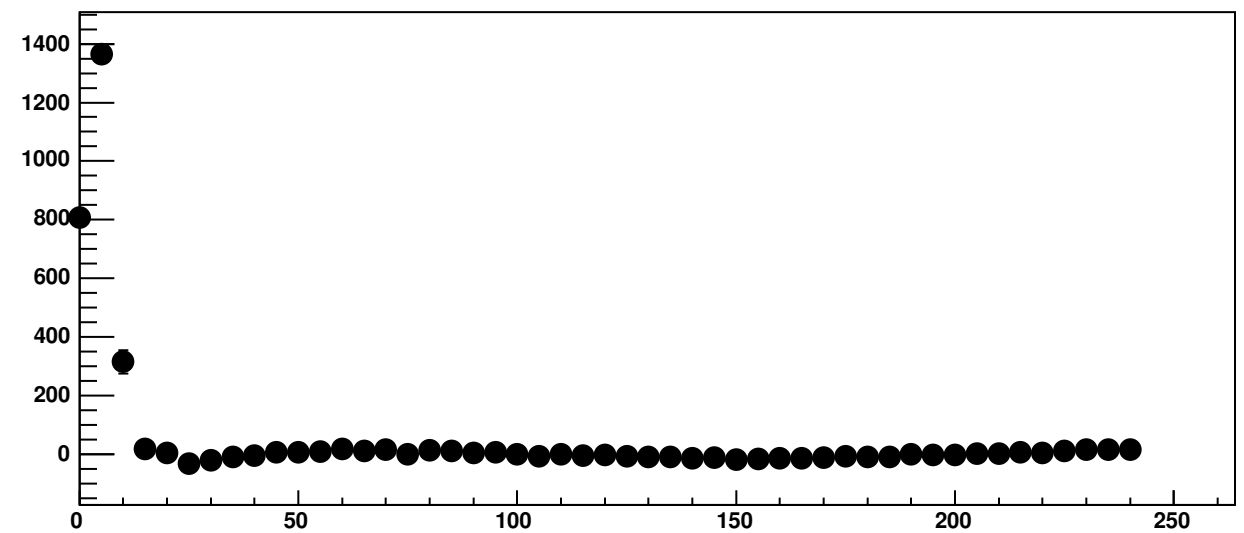
χ^2 / ndf	1725 / 42
p0	-313.4 ± 0.5628
p1	4.253 ± 0.005207
p2	1.105e+04 ± 0.9467
p3	31.79 ± 0.004524

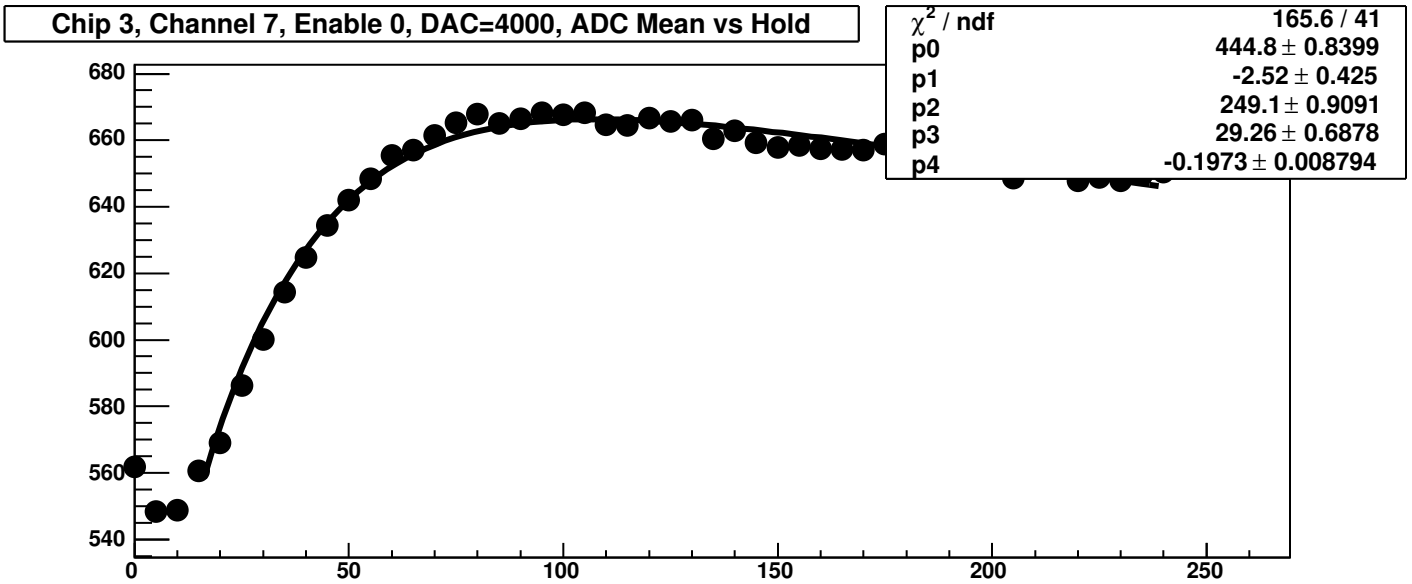


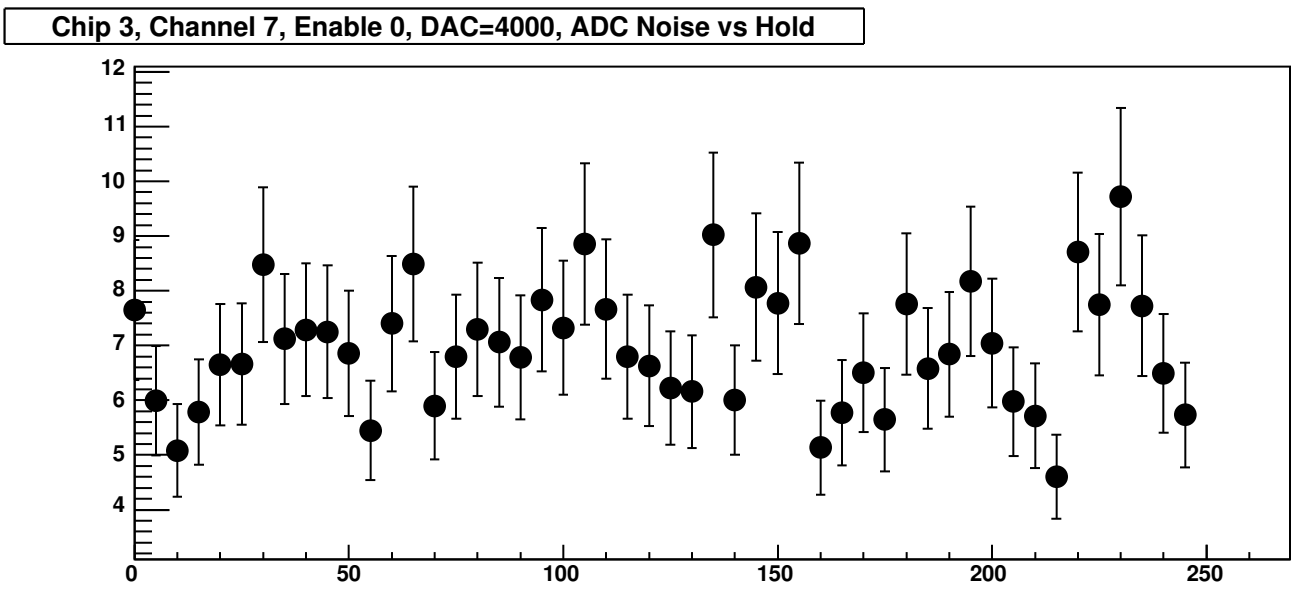
Chip 3, Channel 6, Enable 5!, DAC=4000, ADC Noise vs Hold

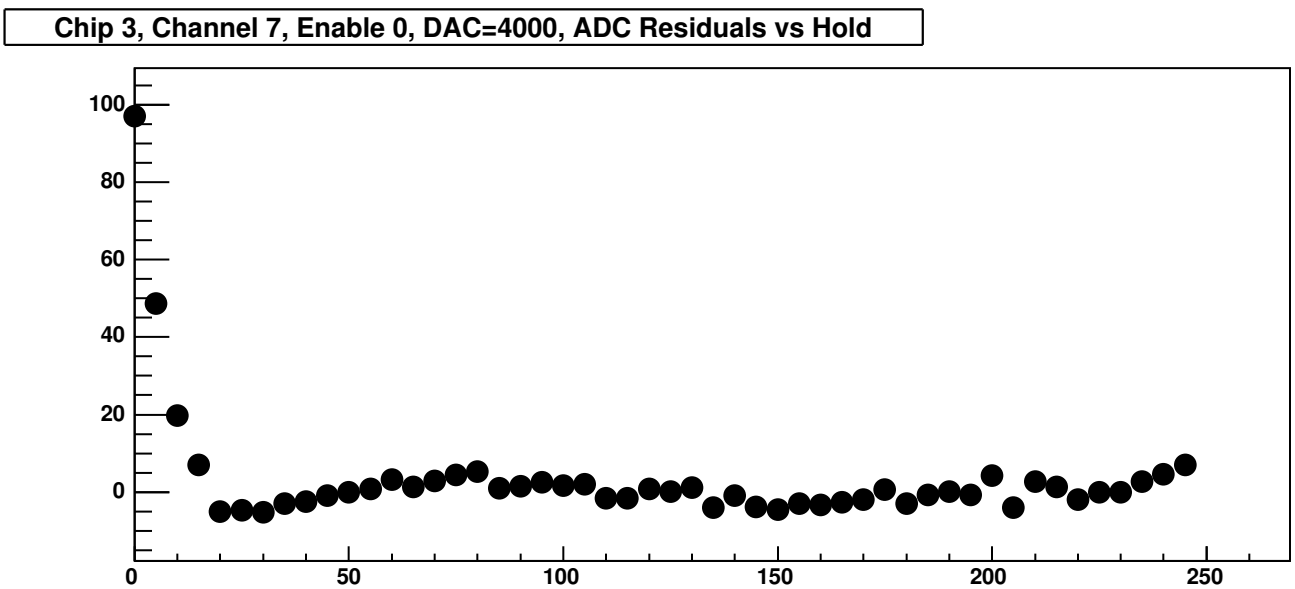


Chip 3, Channel 6, Enable 5!, DAC=4000, ADC Residuals vs Hold

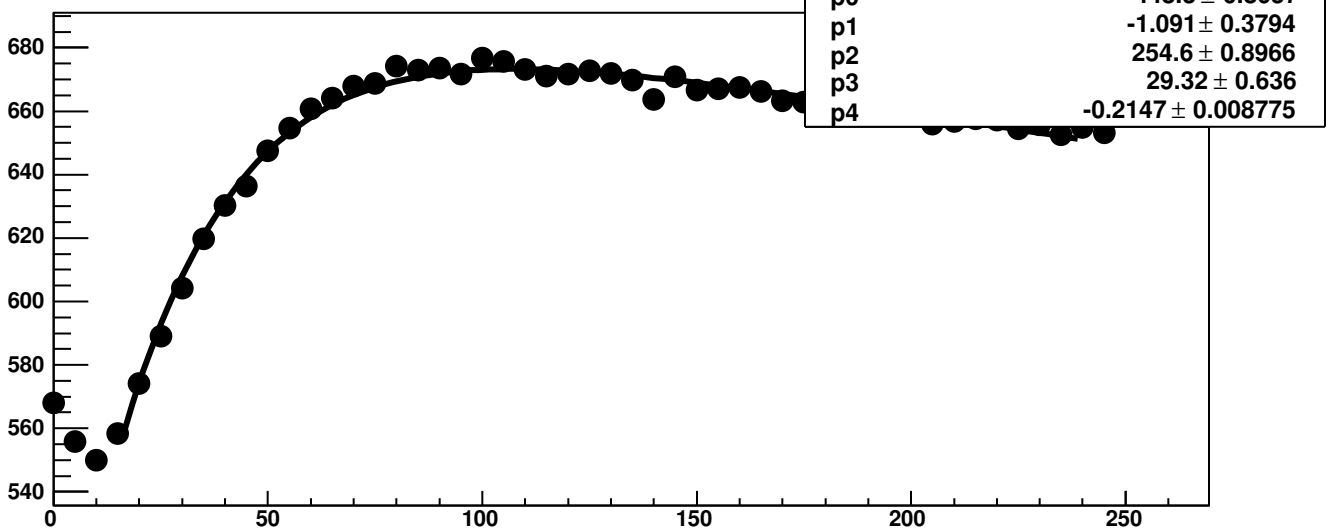



Chip 3, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 165.6 / 41
p0 444.8 ± 0.8399
p1 -2.52 ± 0.425
p2 249.1 ± 0.9091
p3 29.26 ± 0.6878
p4 -0.1973 ± 0.008794
680
660
640
620
600
580
560
540
0
50
100
150
200
250

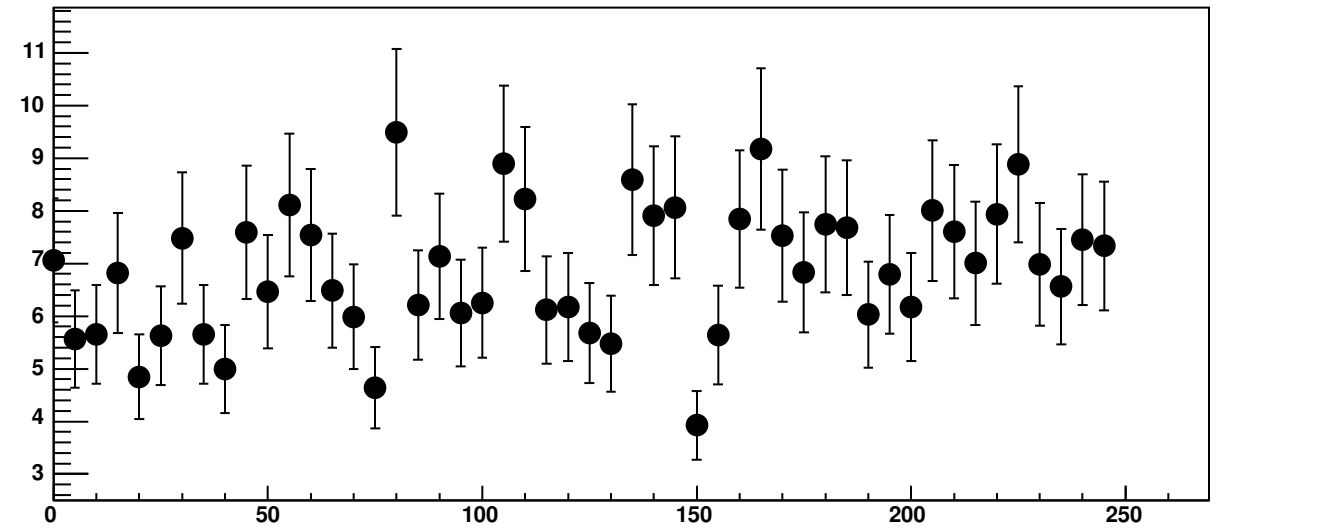

Chip 3, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 3, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold
100
80
60
40
20
0
0
50
100
150
200
250

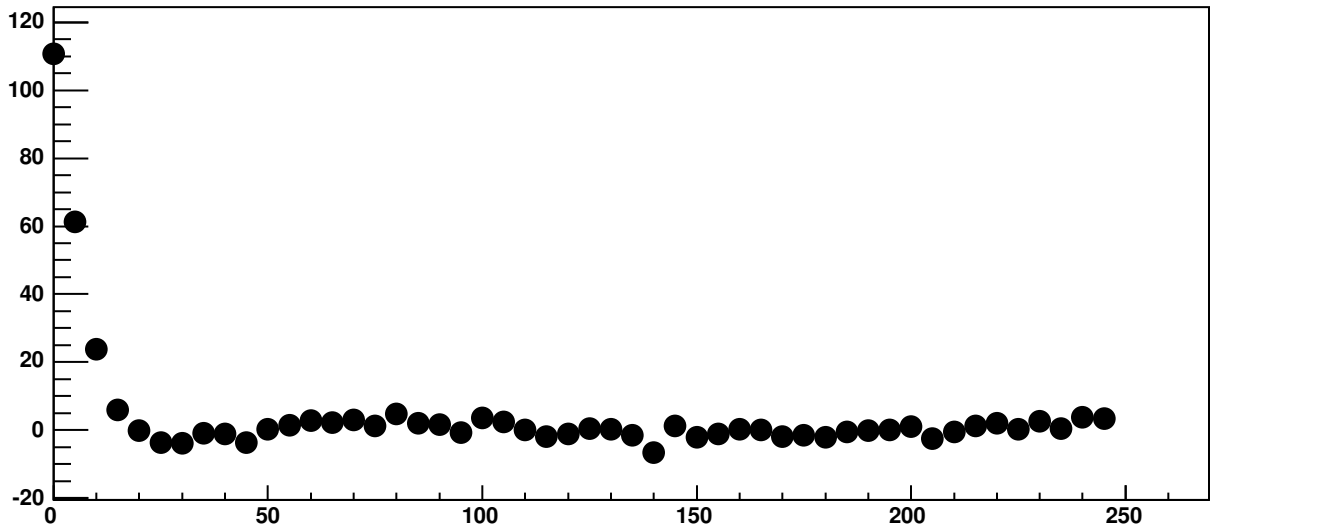
Chip 3, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold



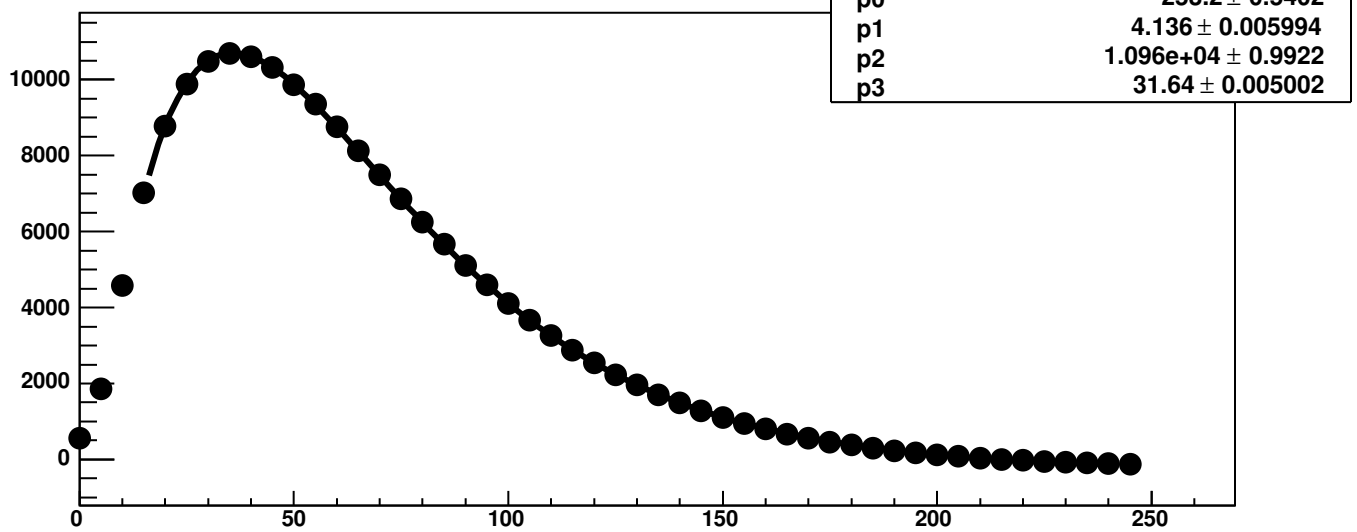
Chip 3, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold



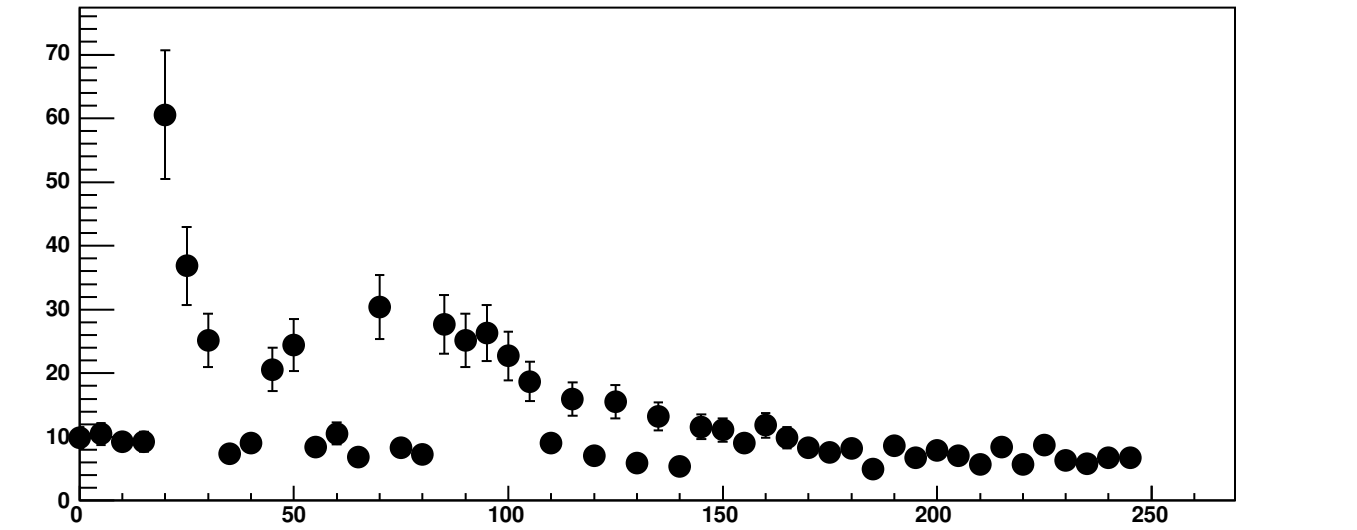
Chip 3, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold



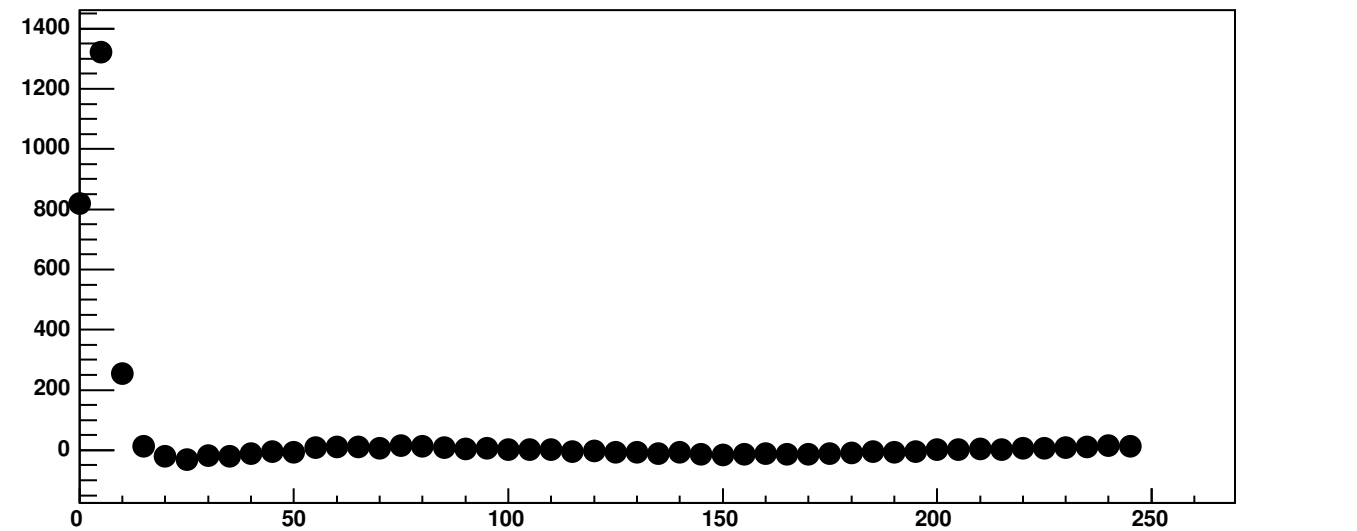
Chip 3, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold



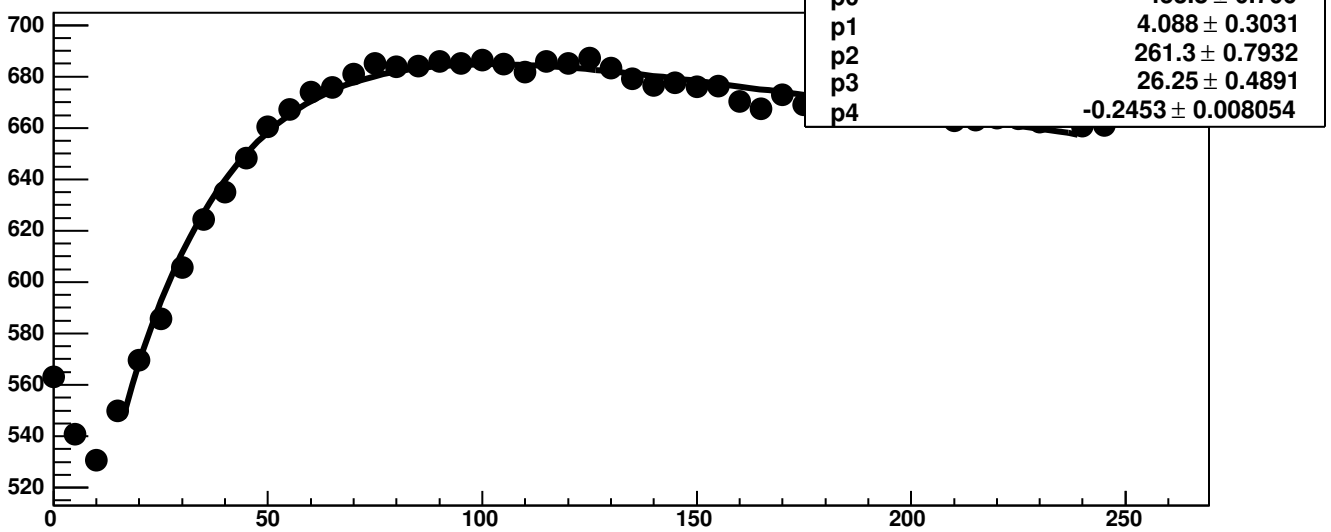
Chip 3, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold



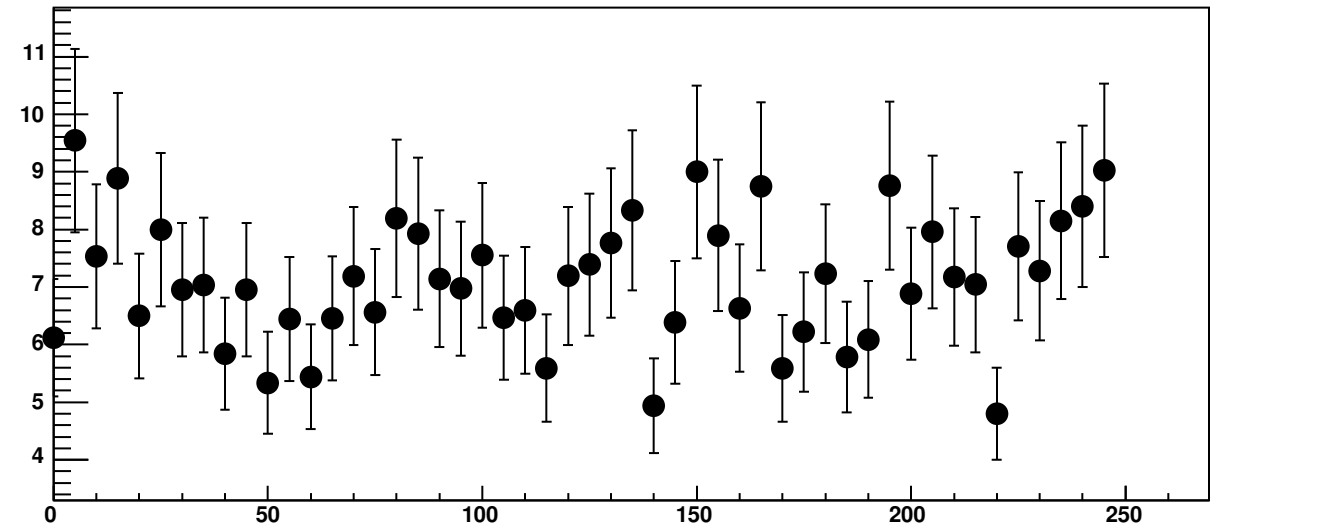
Chip 3, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold



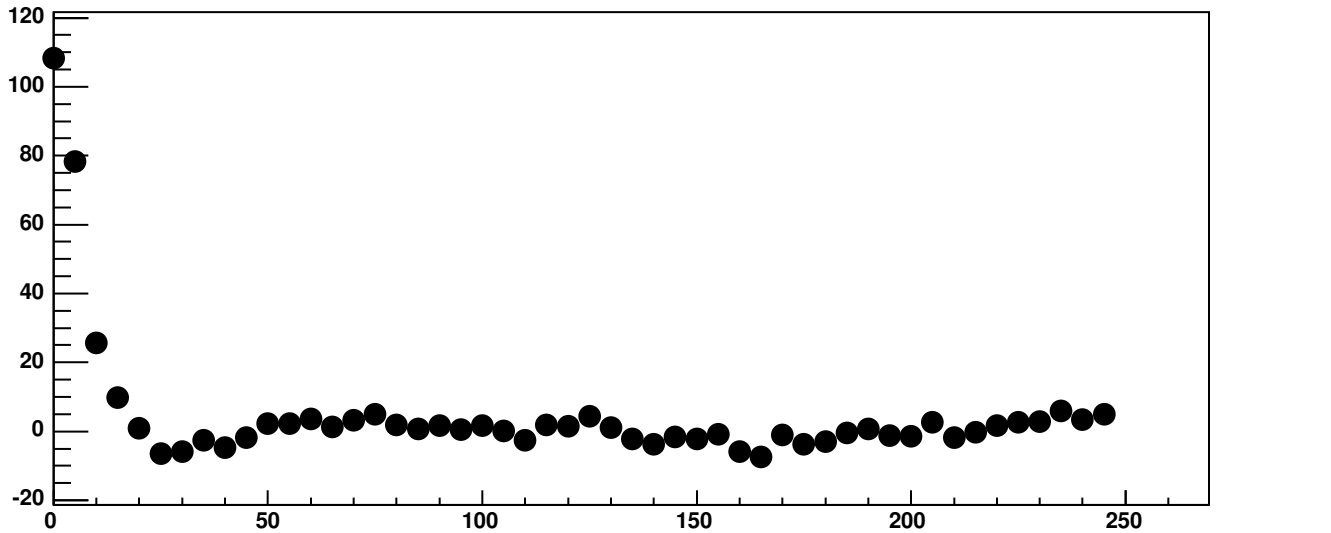
Chip 3, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold



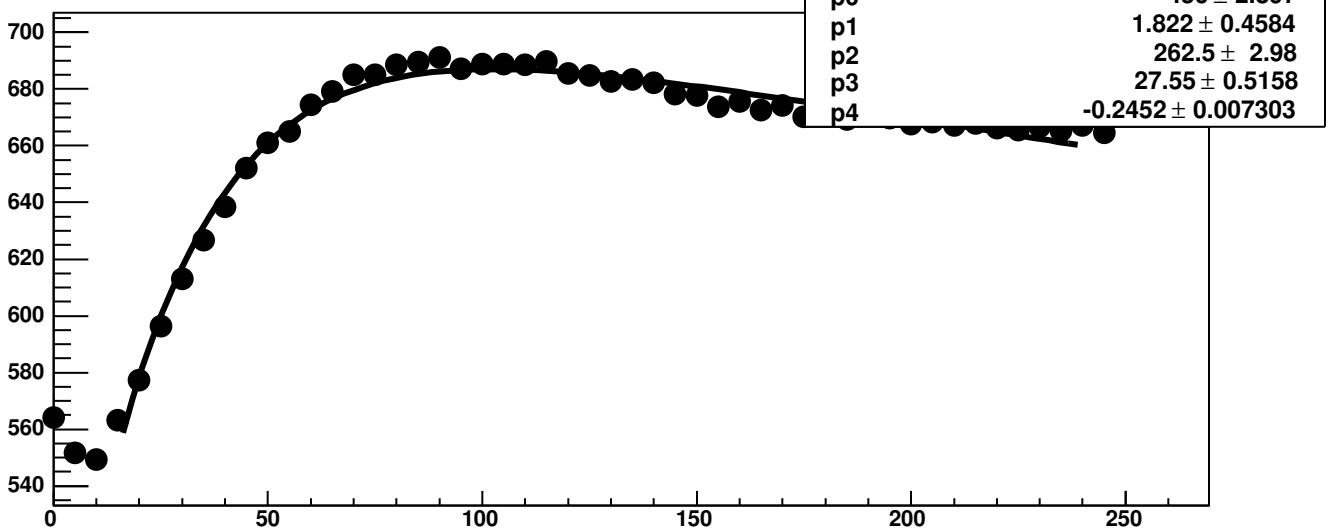
Chip 3, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



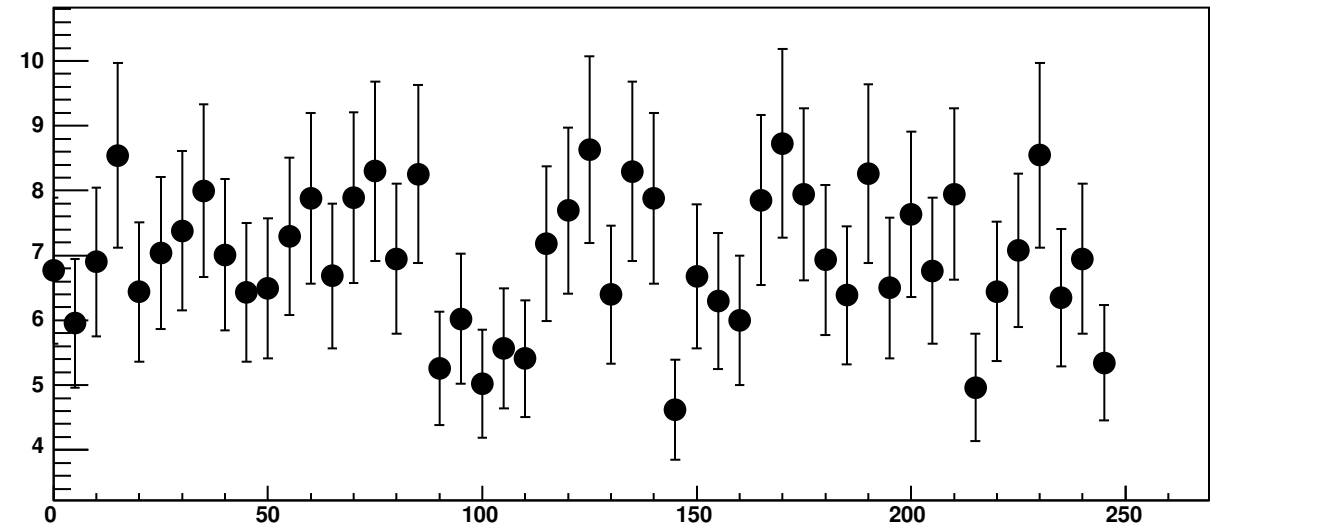
Chip 3, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold



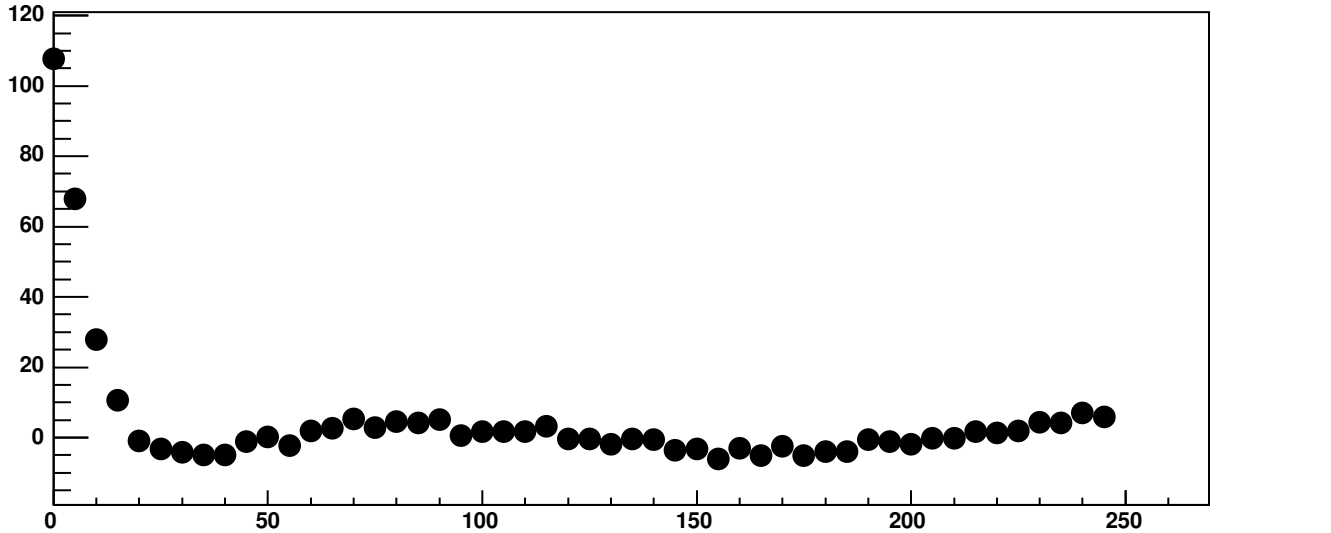
Chip 3, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold



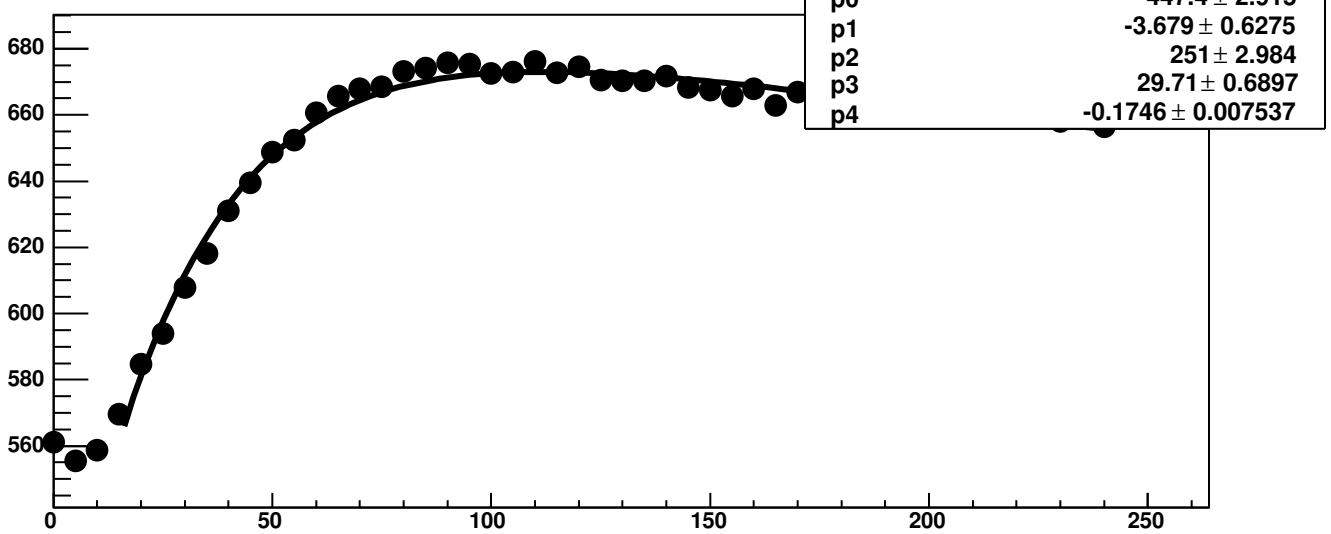
Chip 3, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold



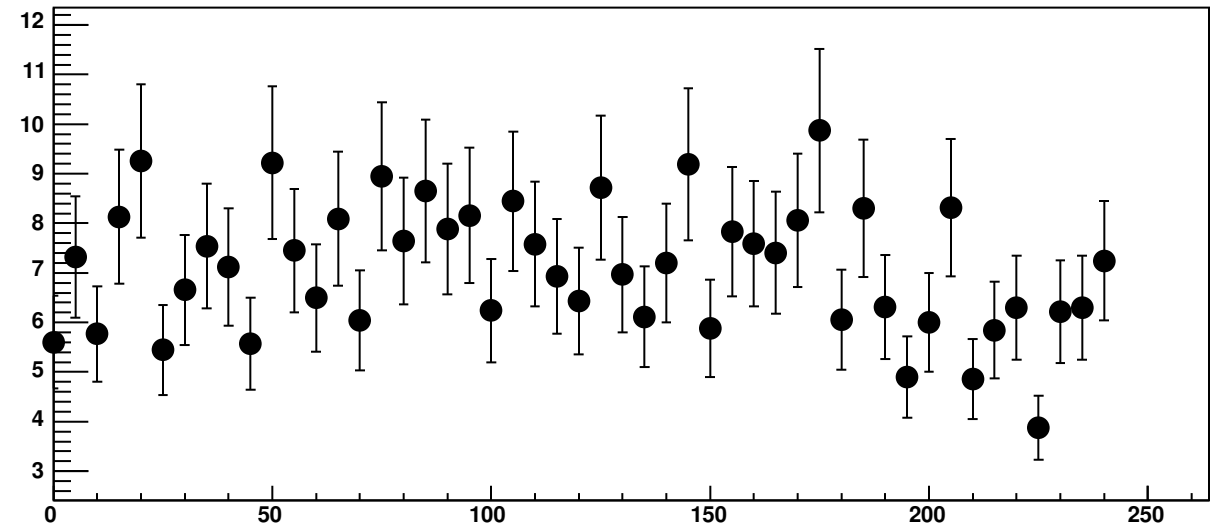
Chip 3, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold



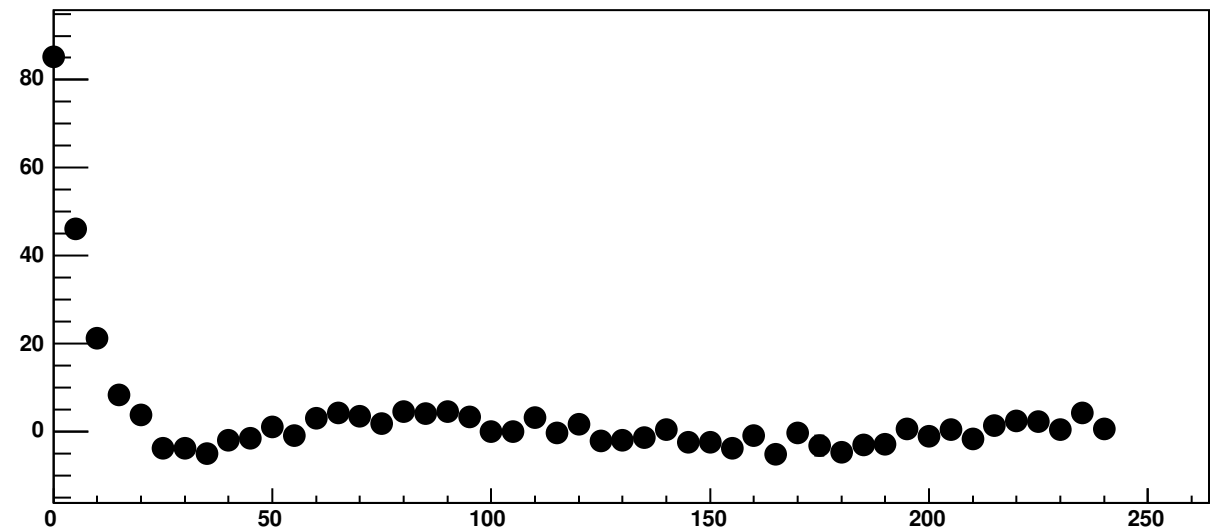
Chip 3, Channel 7, Enable 5, DAC=4000, ADC Mean vs Hold



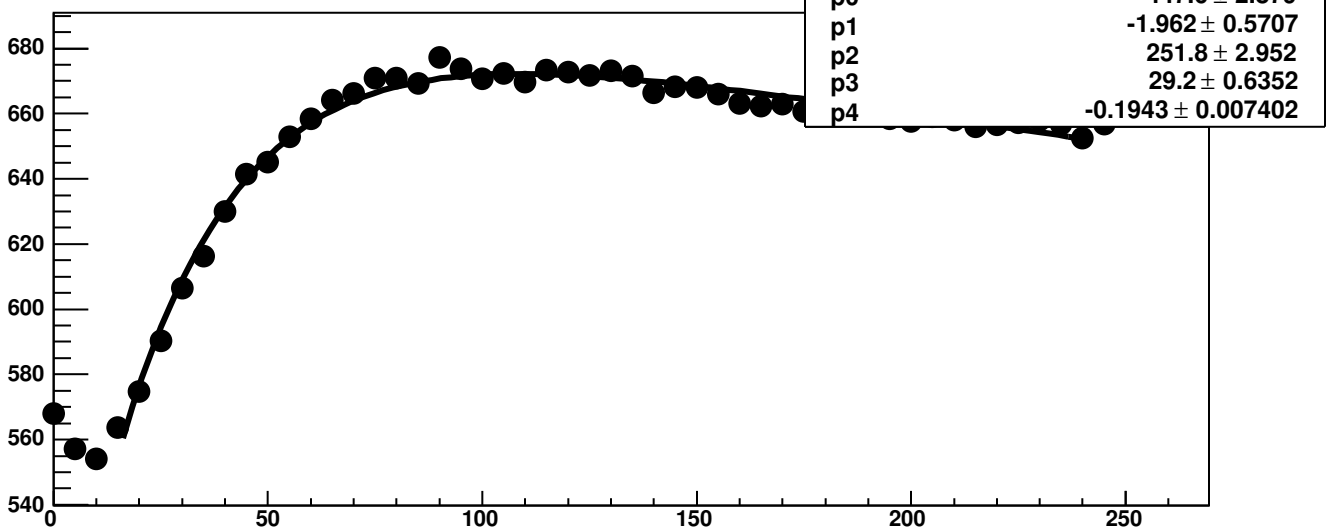
Chip 3, Channel 7, Enable 5, DAC=4000, ADC Noise vs Hold



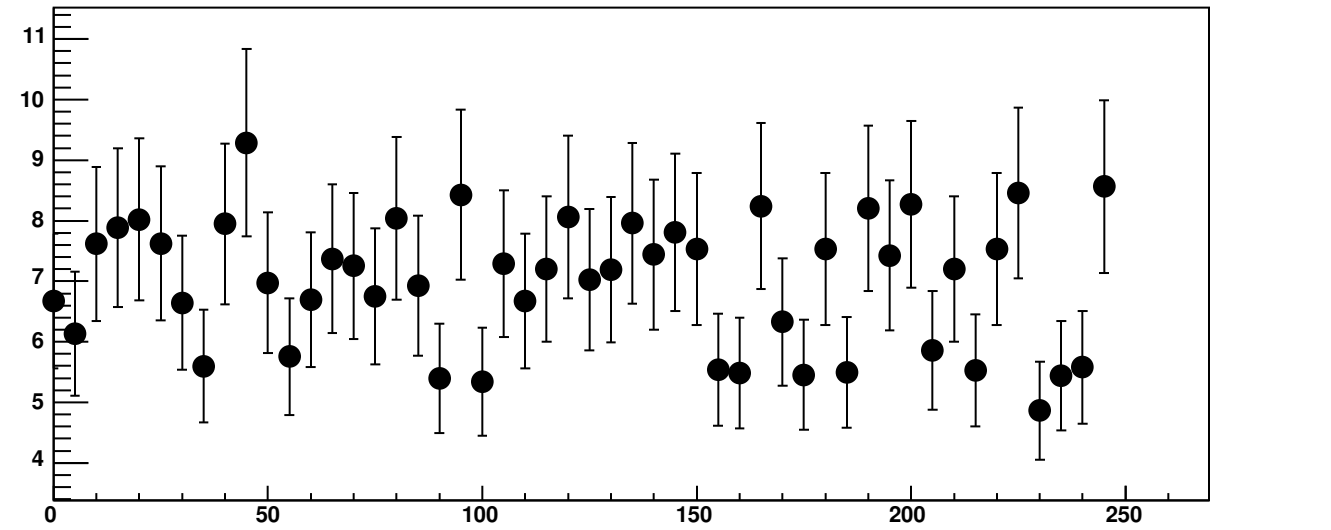
Chip 3, Channel 7, Enable 5, DAC=4000, ADC Residuals vs Hold



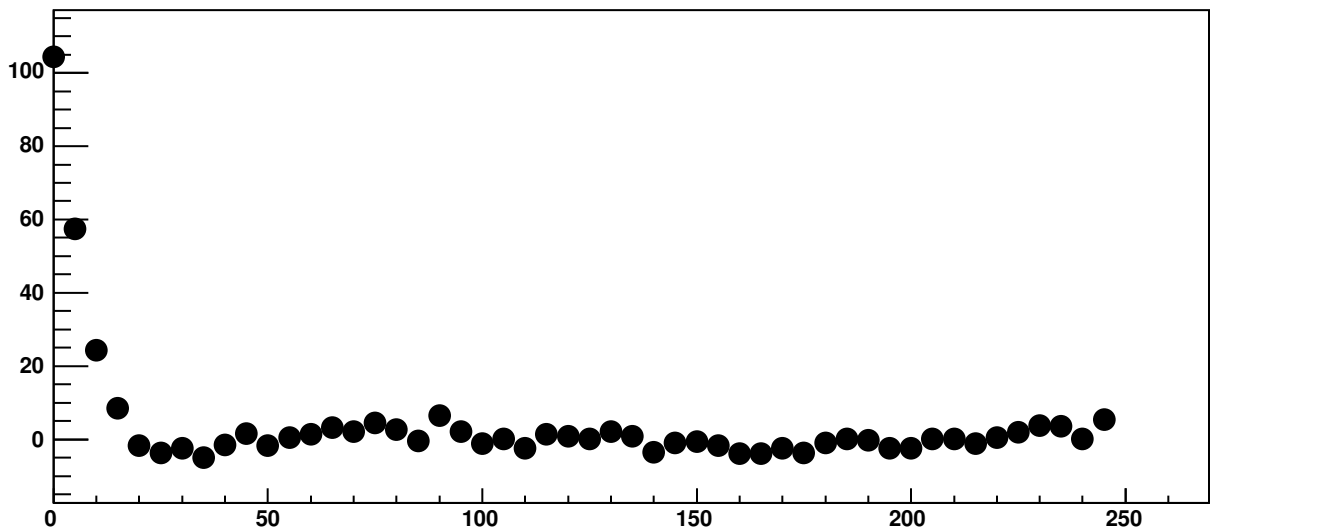
Chip 3, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold



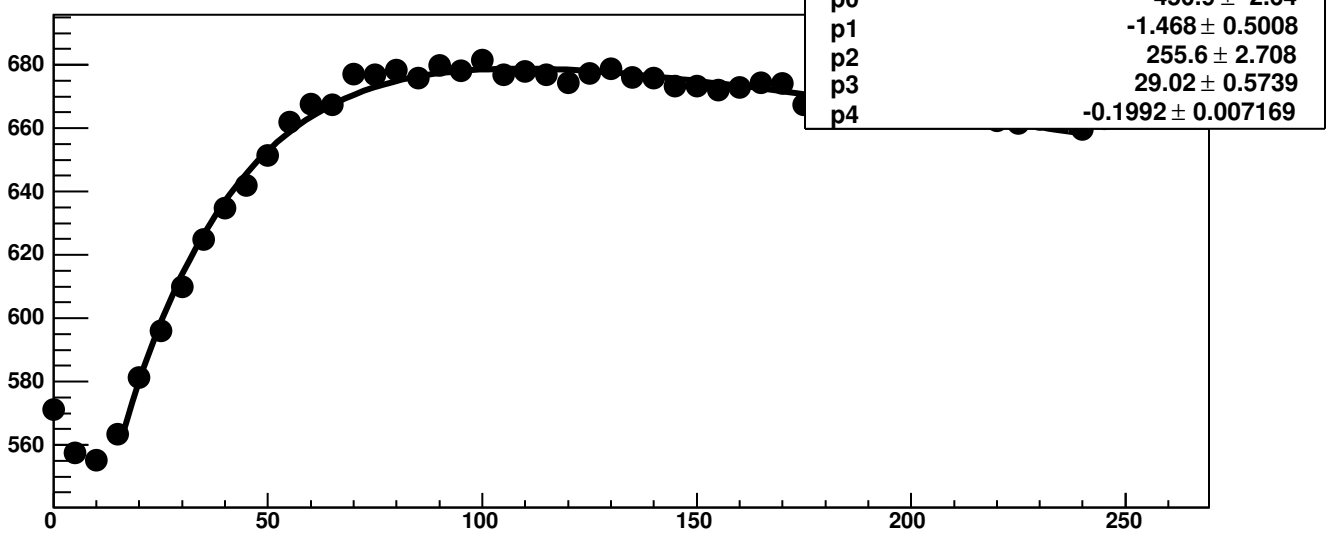
Chip 3, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



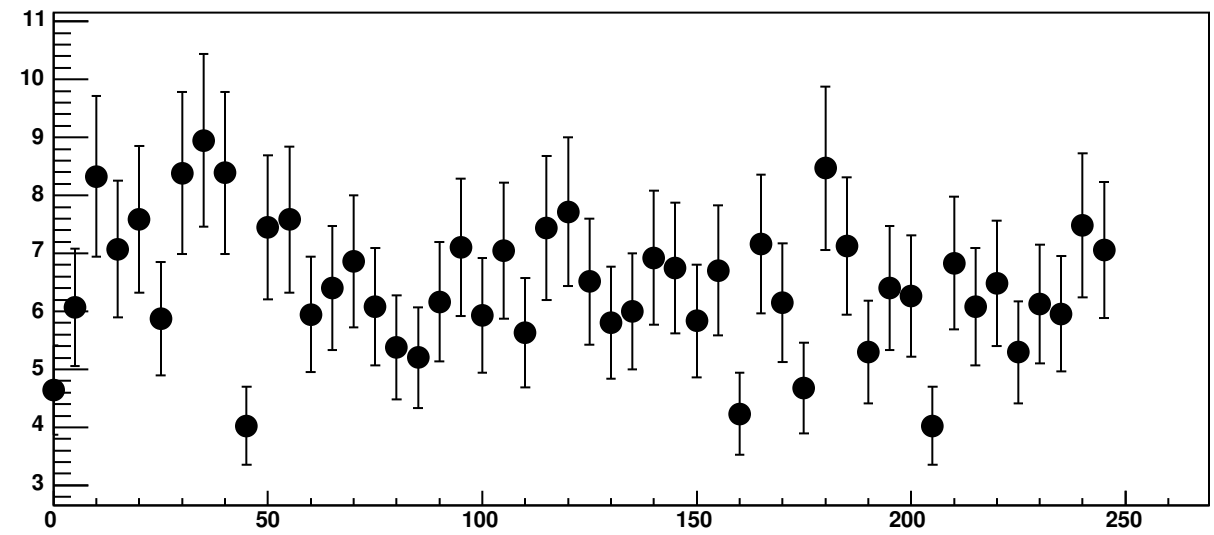
Chip 3, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold



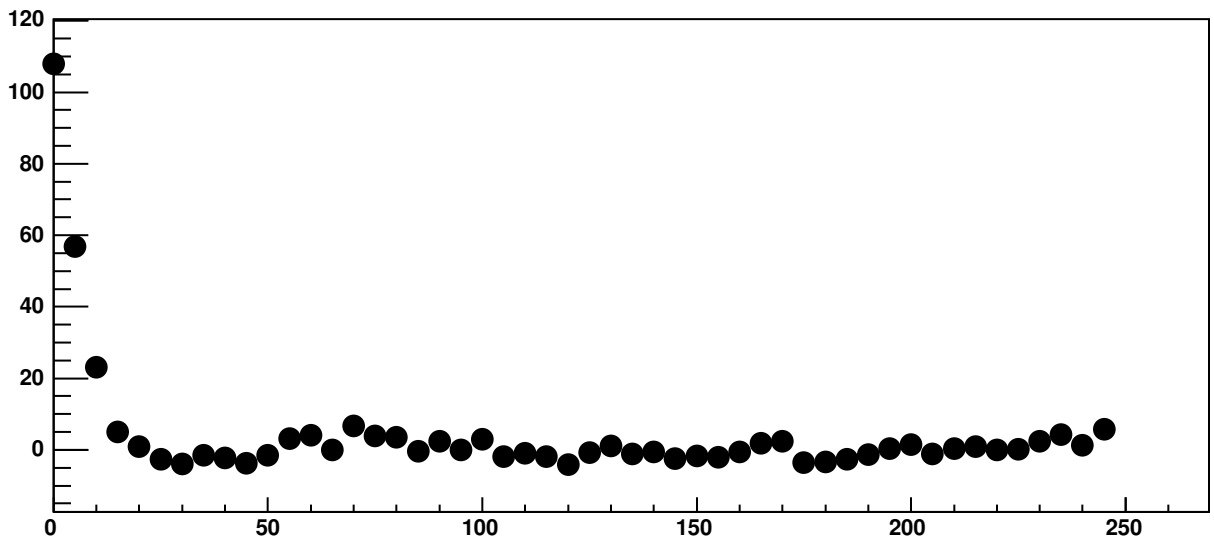
Chip 3, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold



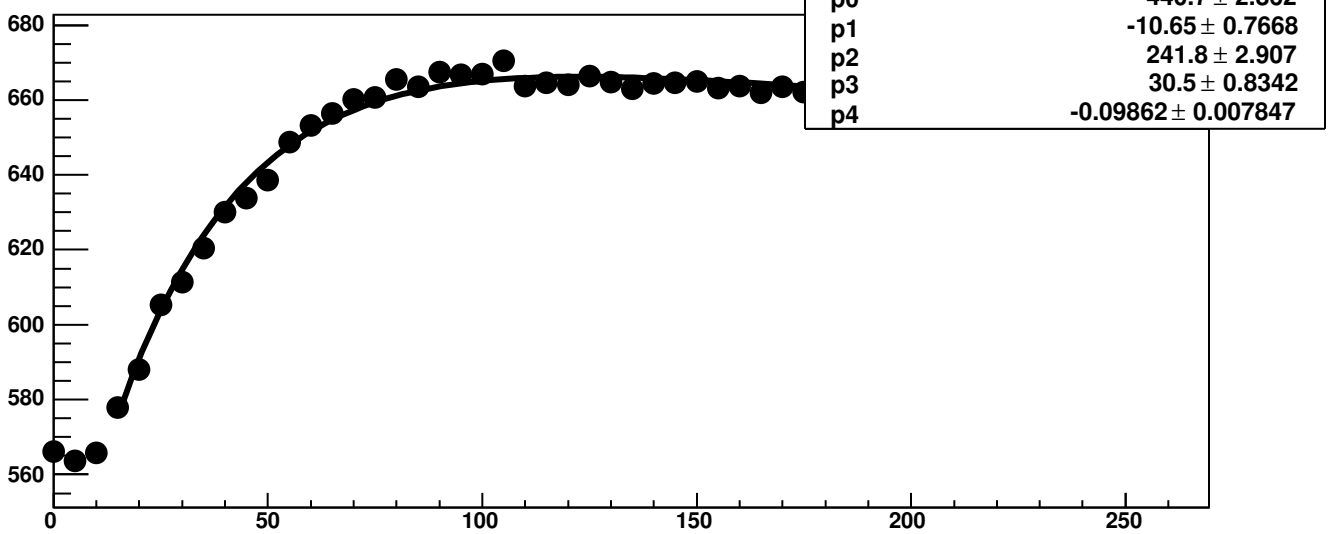
Chip 3, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold



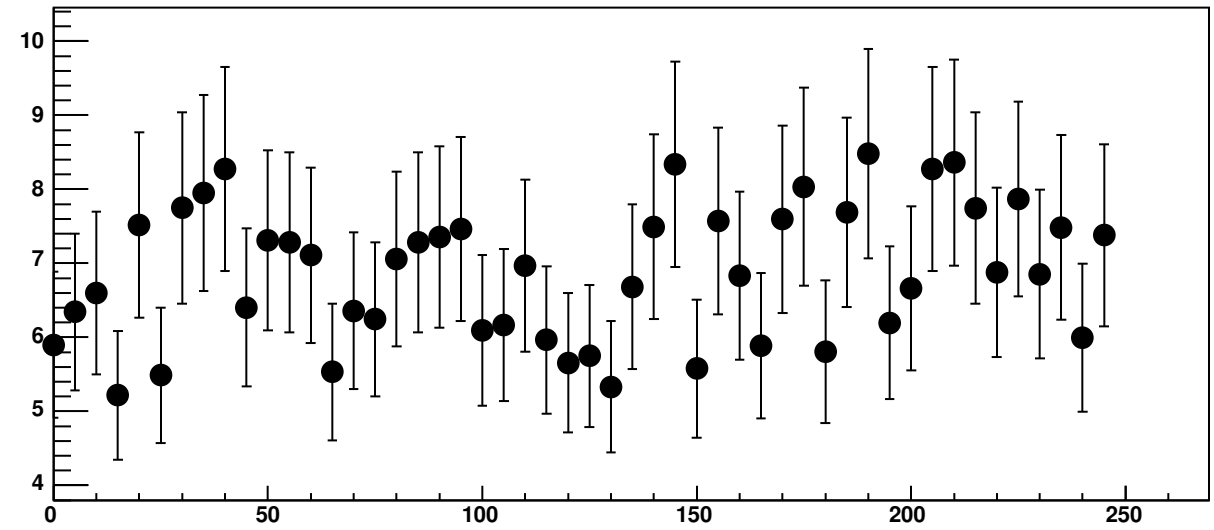
Chip 3, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold



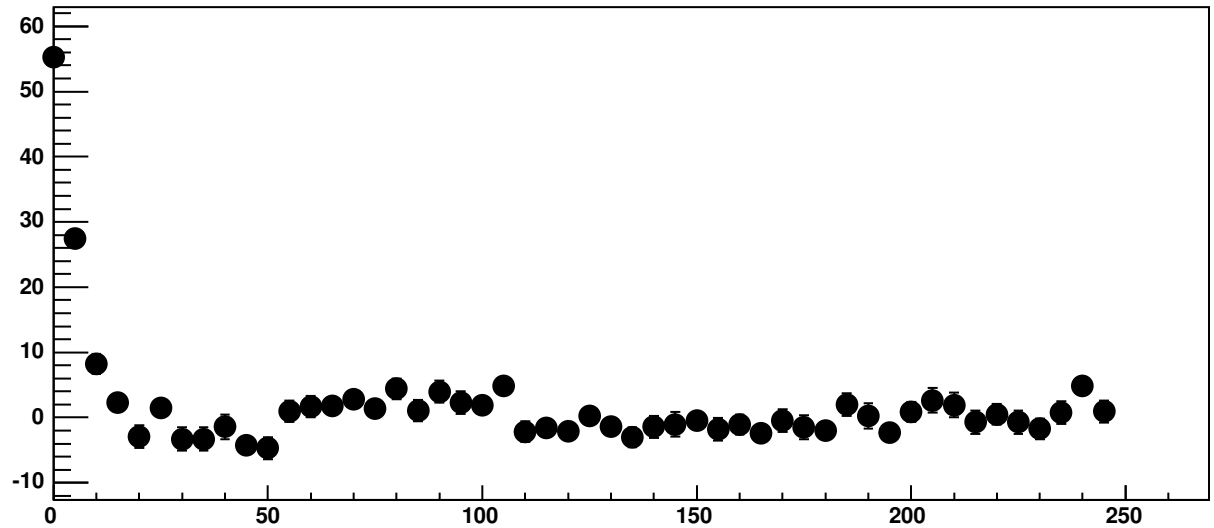
Chip 3, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold

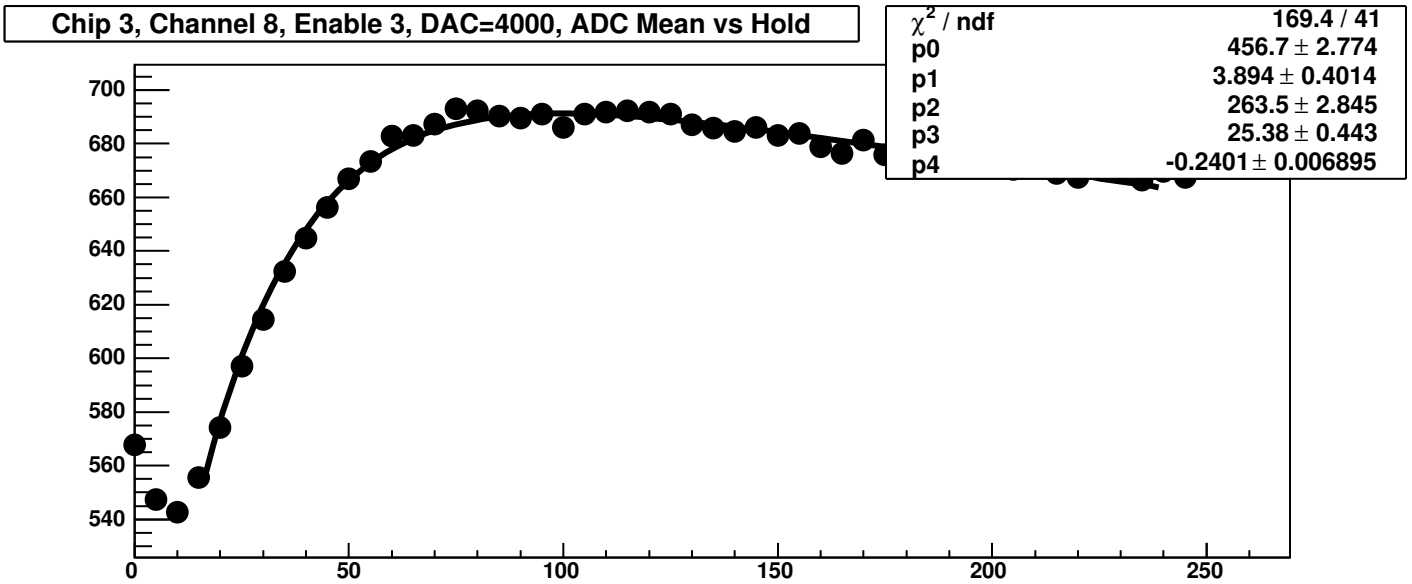


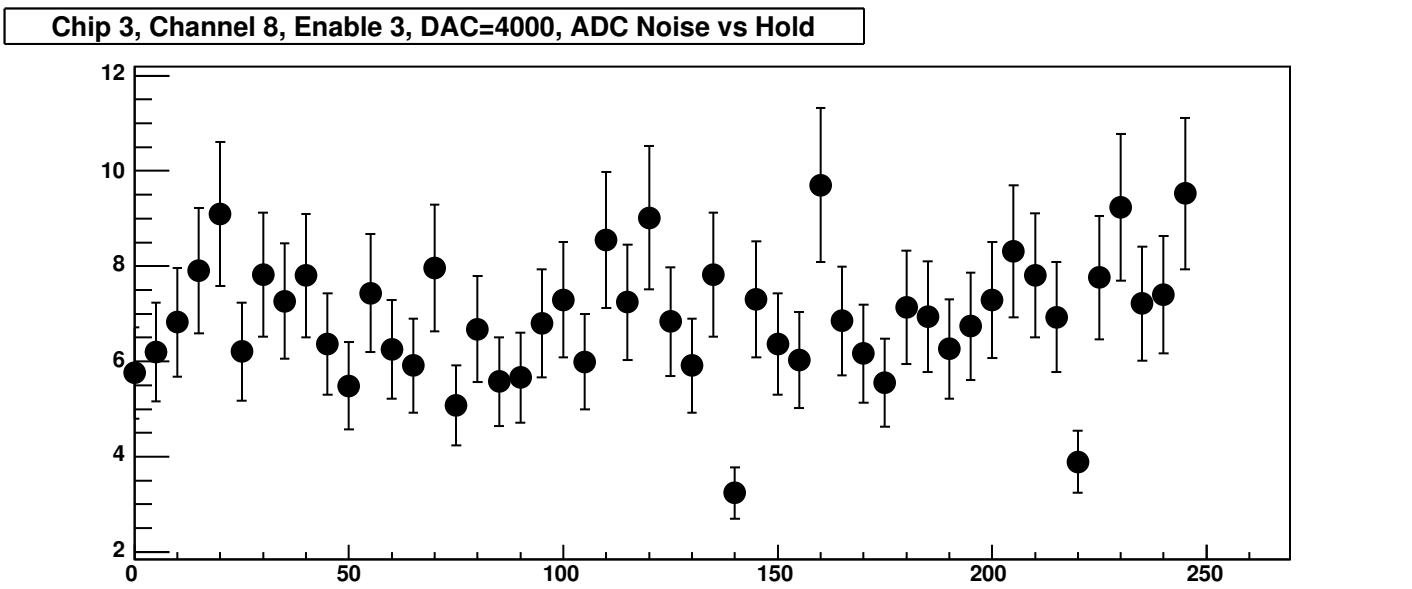
Chip 3, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold

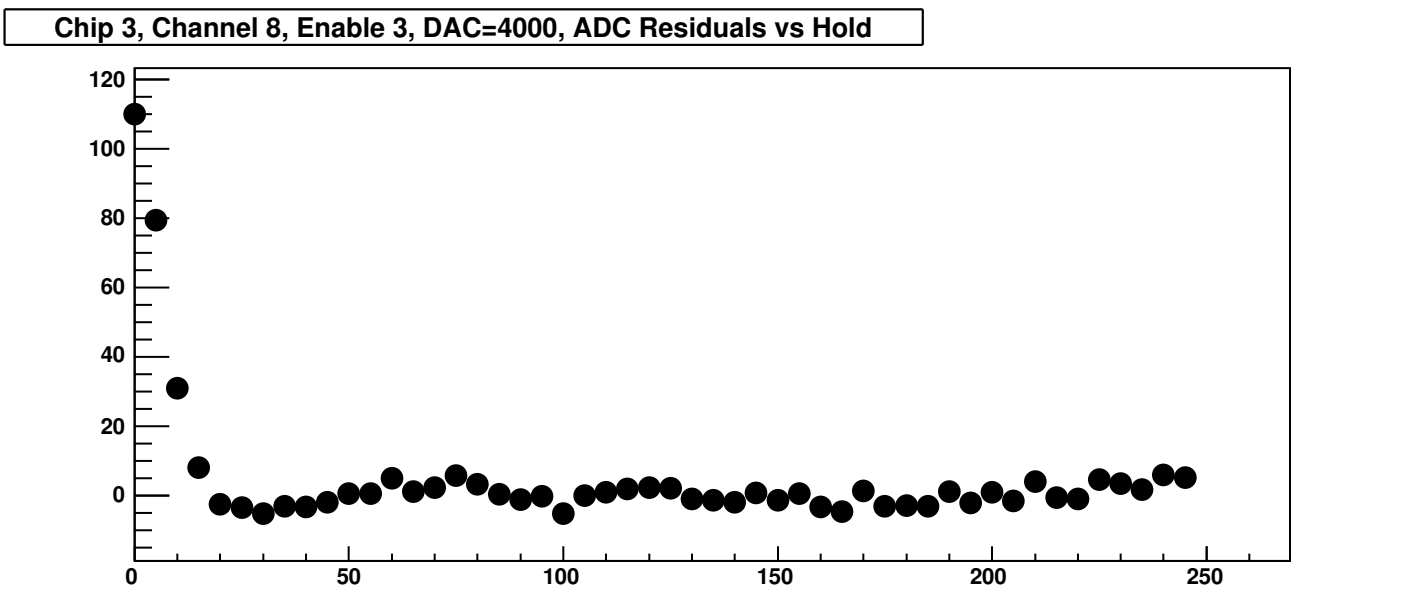


Chip 3, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold

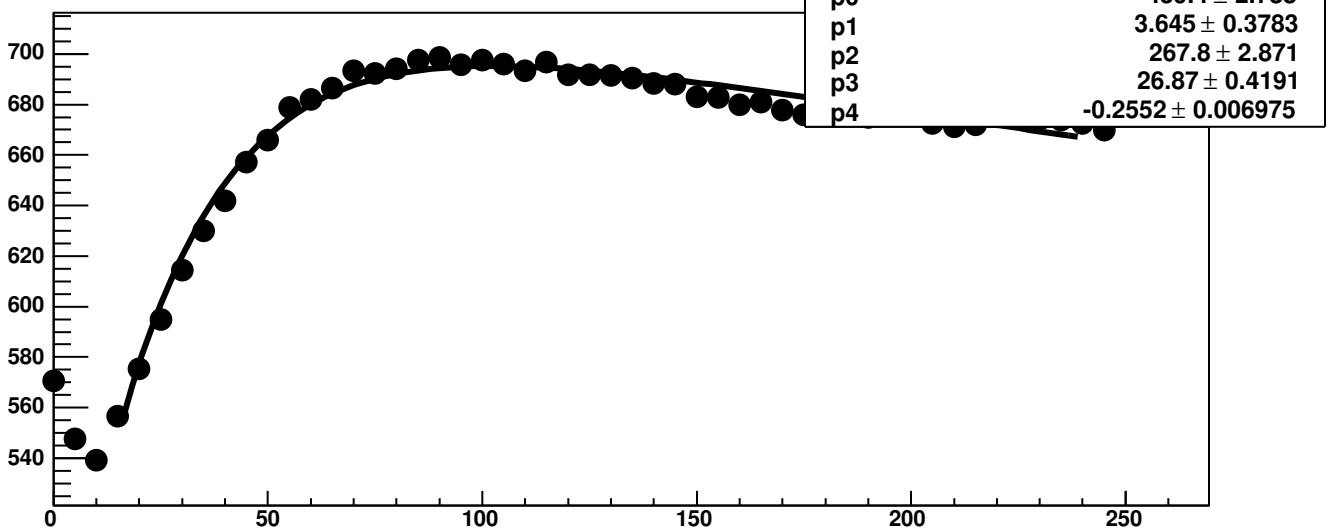



Chip 3, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 169.4 / 41
p0 456.7 ± 2.774
p1 3.894 ± 0.4014
p2 263.5 ± 2.845
p3 25.38 ± 0.443
p4 -0.2401 ± 0.006895
700
680
660
640
620
600
580
560
540
0
50
100
150
200
250

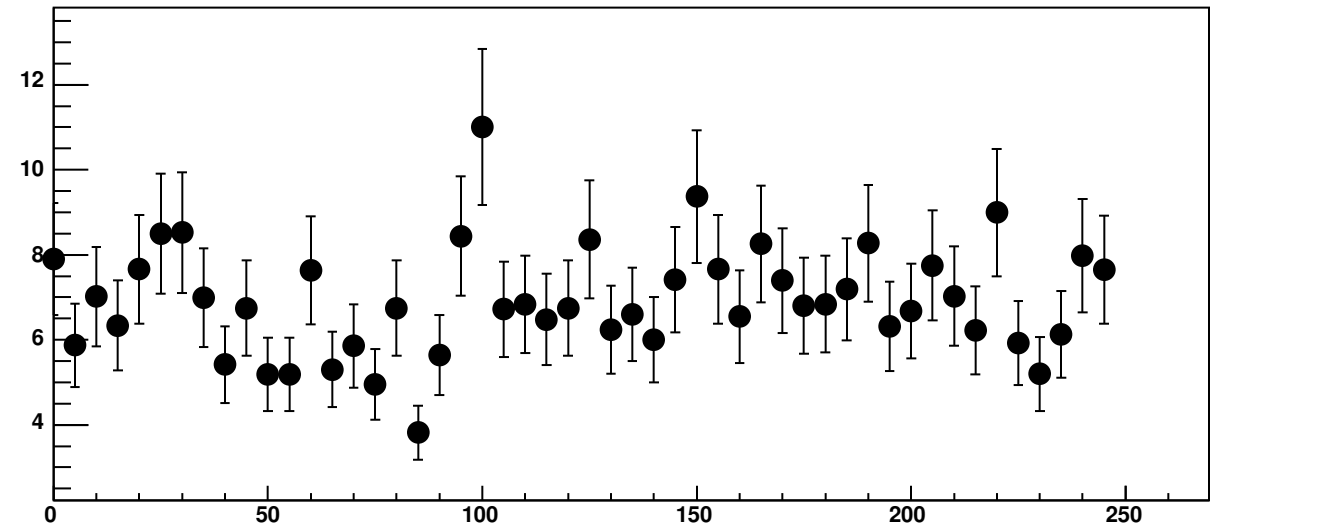

Chip 3, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
2
0
50
100
150
200
250


Chip 3, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold
120
100
80
60
40
20
0
0
50
100
150
200
250

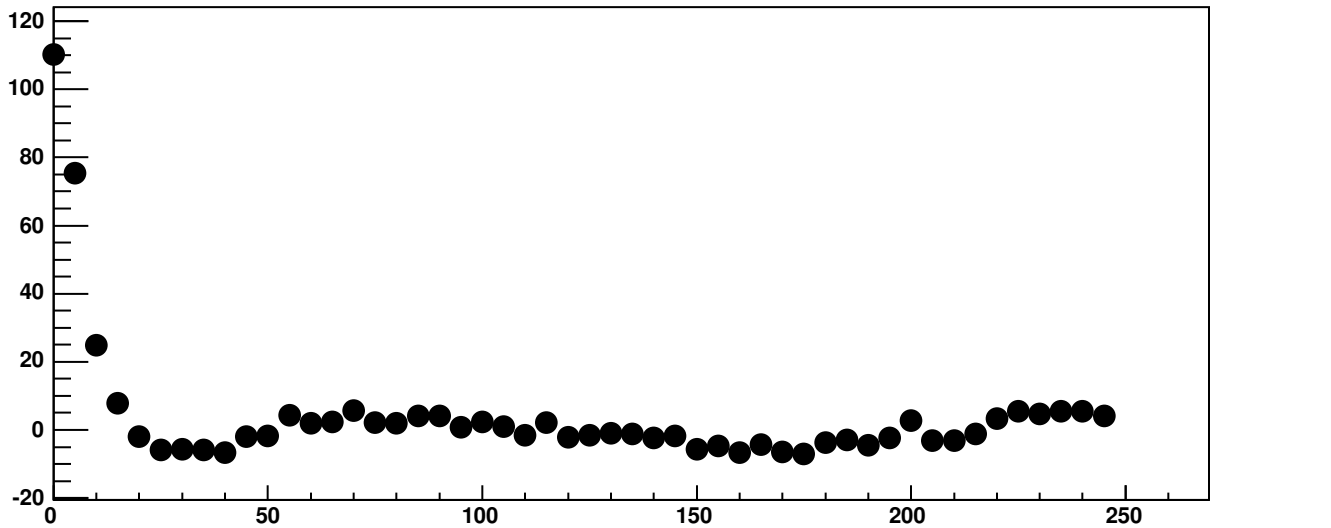
Chip 3, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold



Chip 3, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold

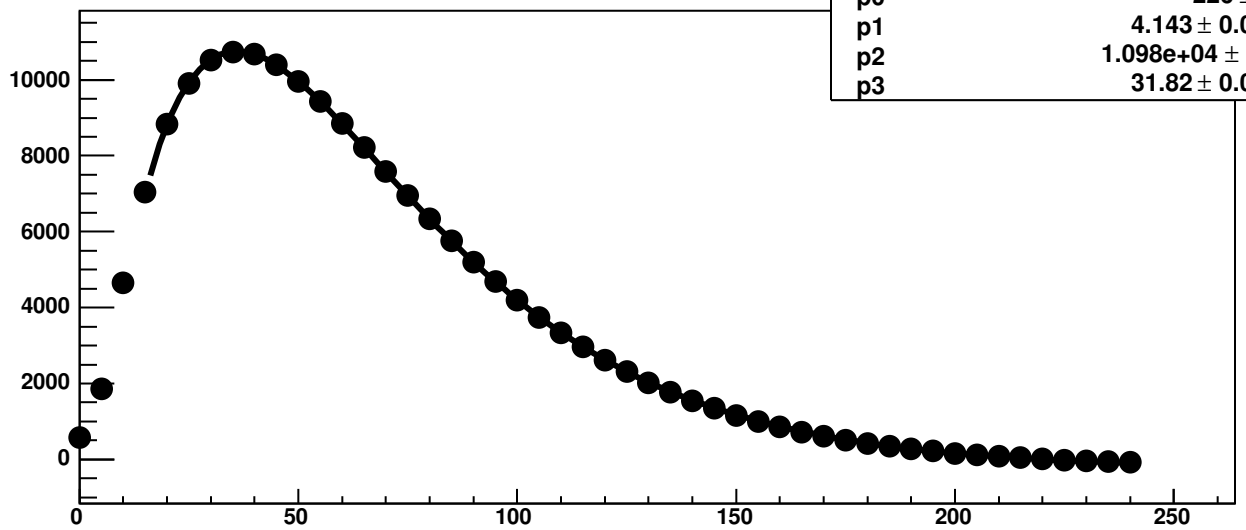


Chip 3, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

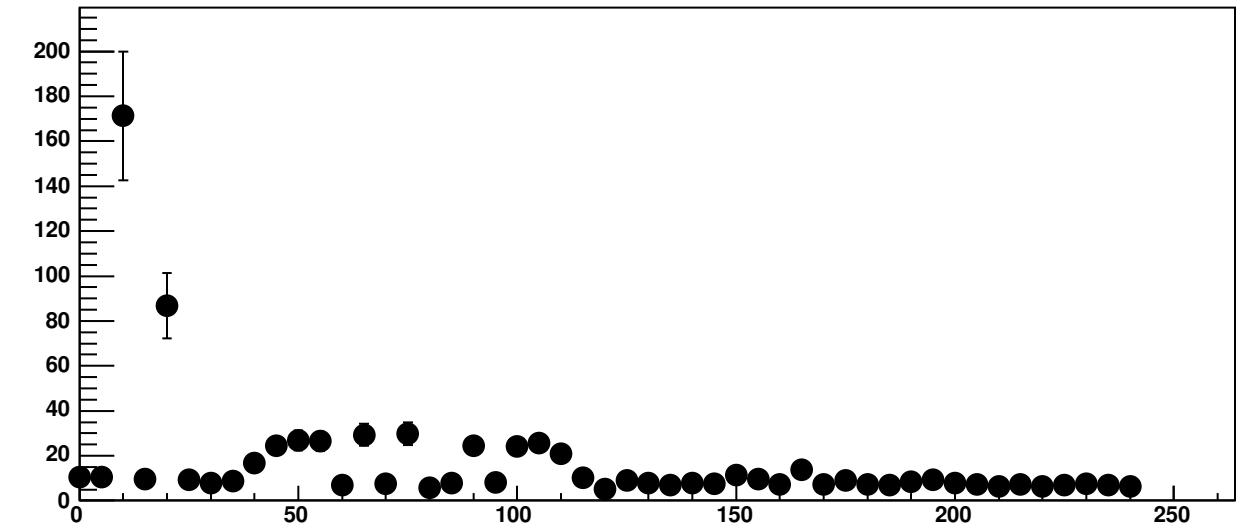


Chip 3, Channel 8, Enable 5!, DAC=4000, ADC Mean vs Hold

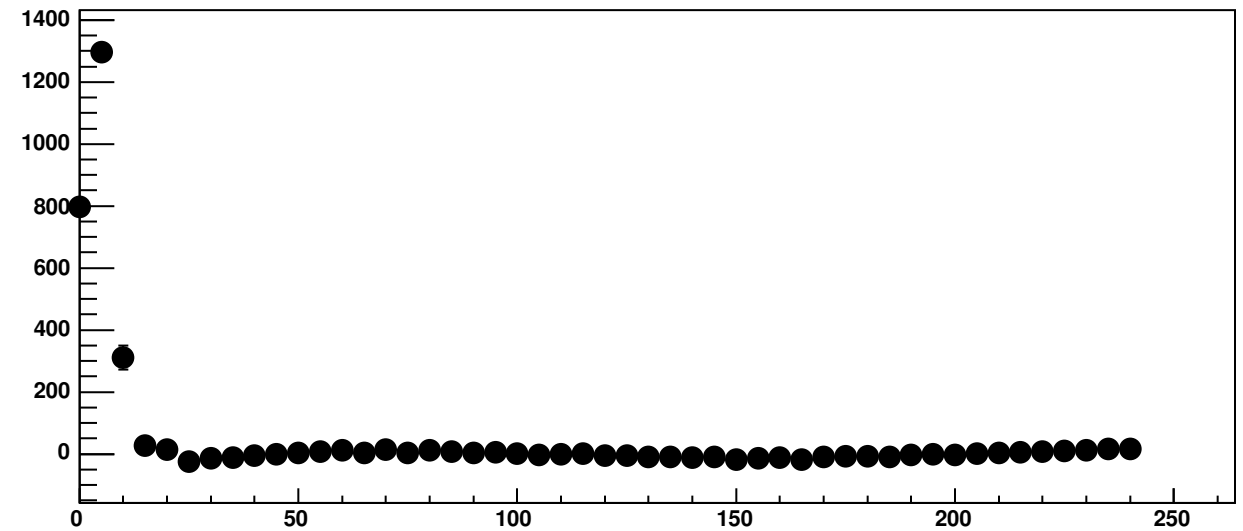
χ^2 / ndf	1386 / 42
p0	-226 ± 0.553
p1	4.143 ± 0.006347
p2	1.098e+04 ± 0.9704
p3	31.82 ± 0.004819

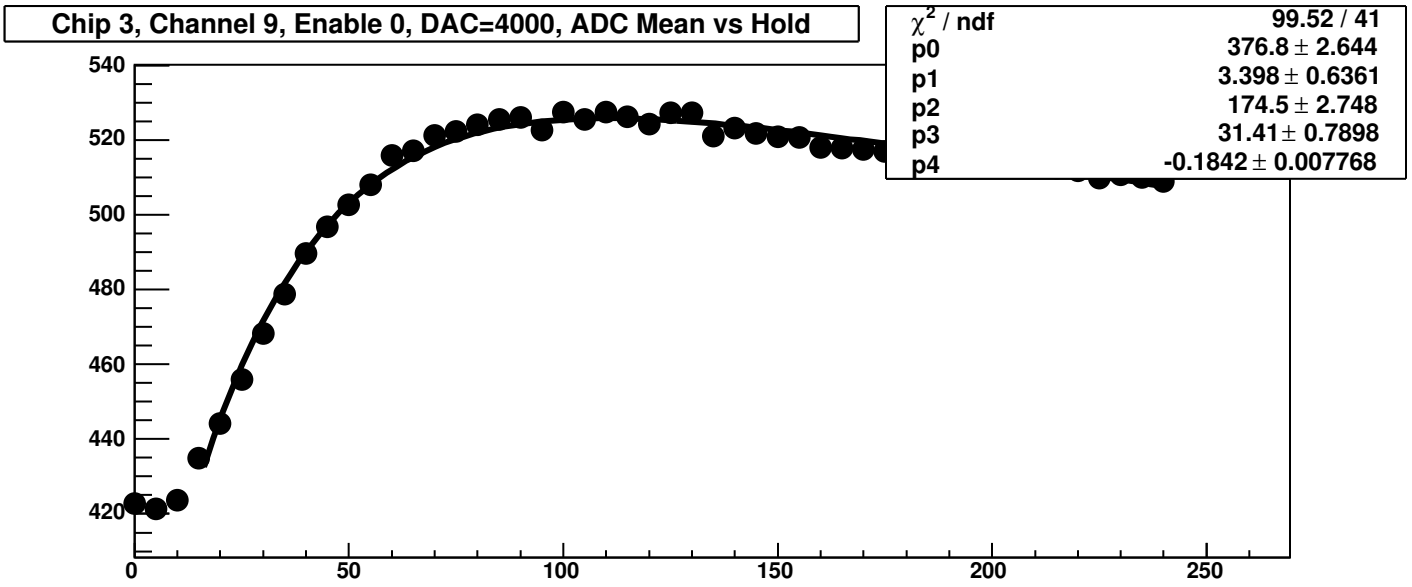


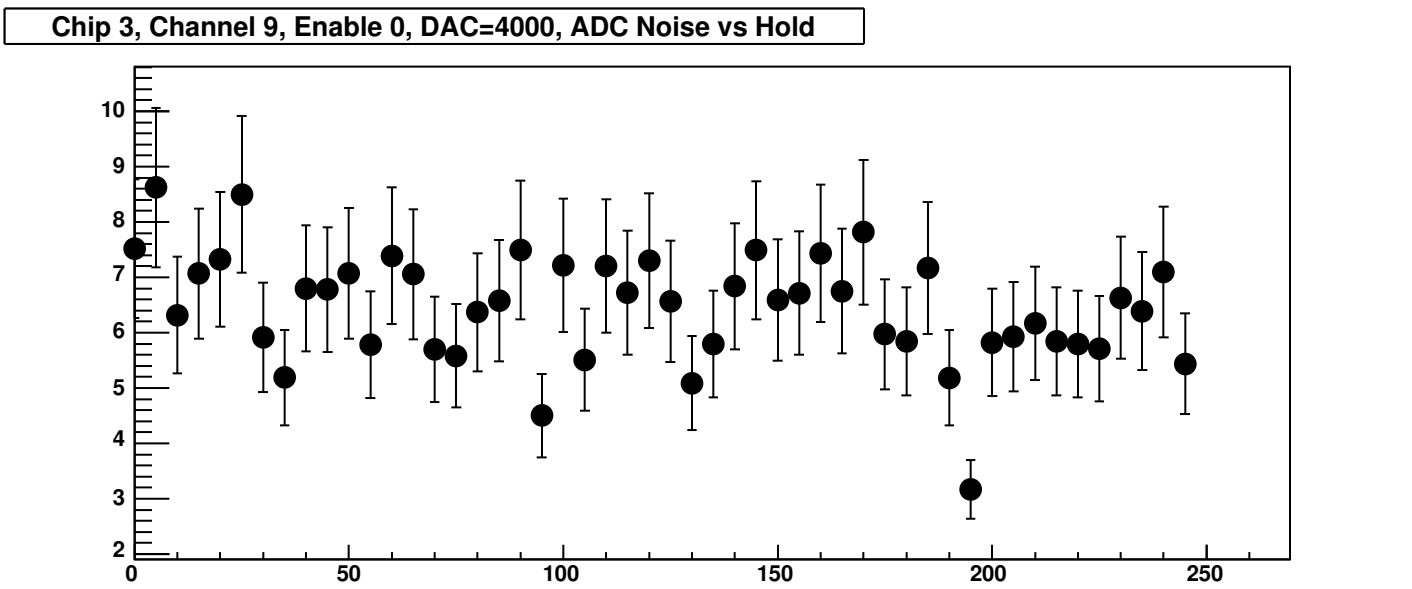
Chip 3, Channel 8, Enable 5!, DAC=4000, ADC Noise vs Hold

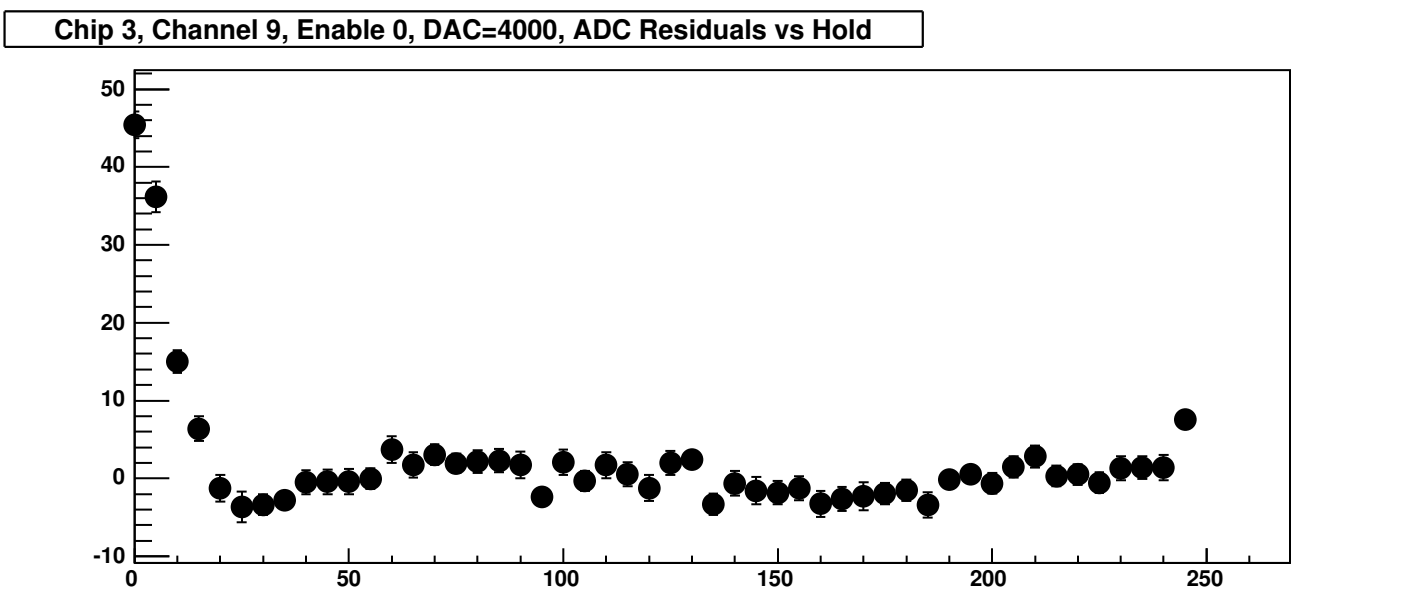


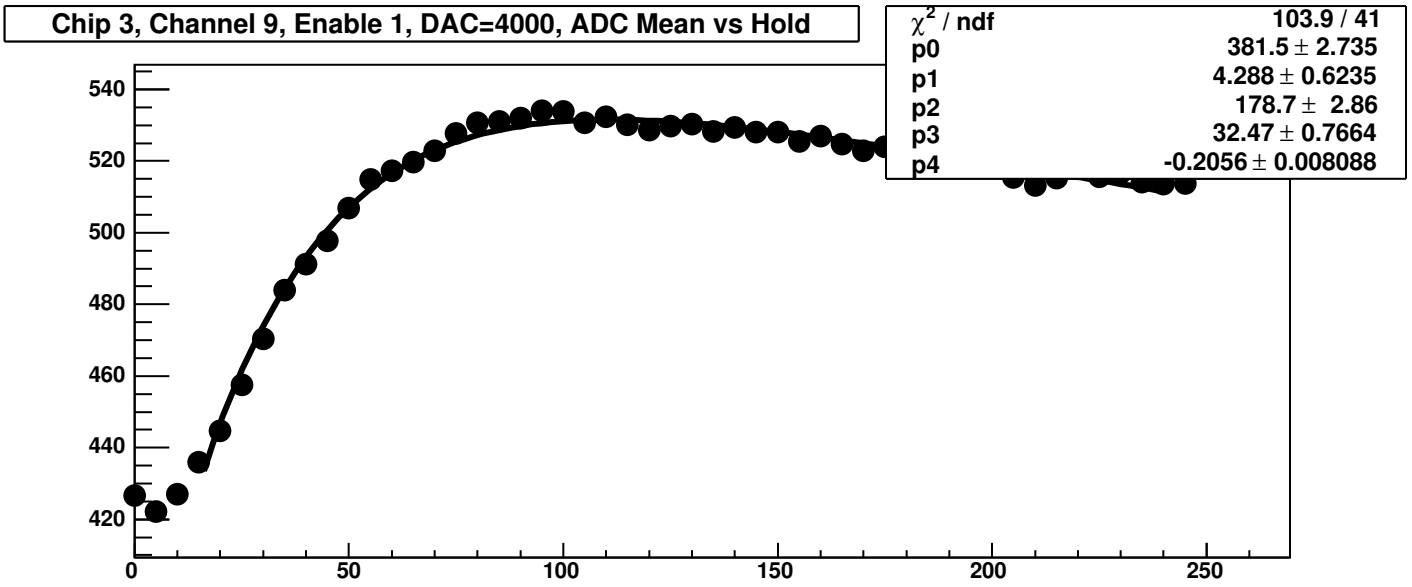
Chip 3, Channel 8, Enable 5!, DAC=4000, ADC Residuals vs Hold

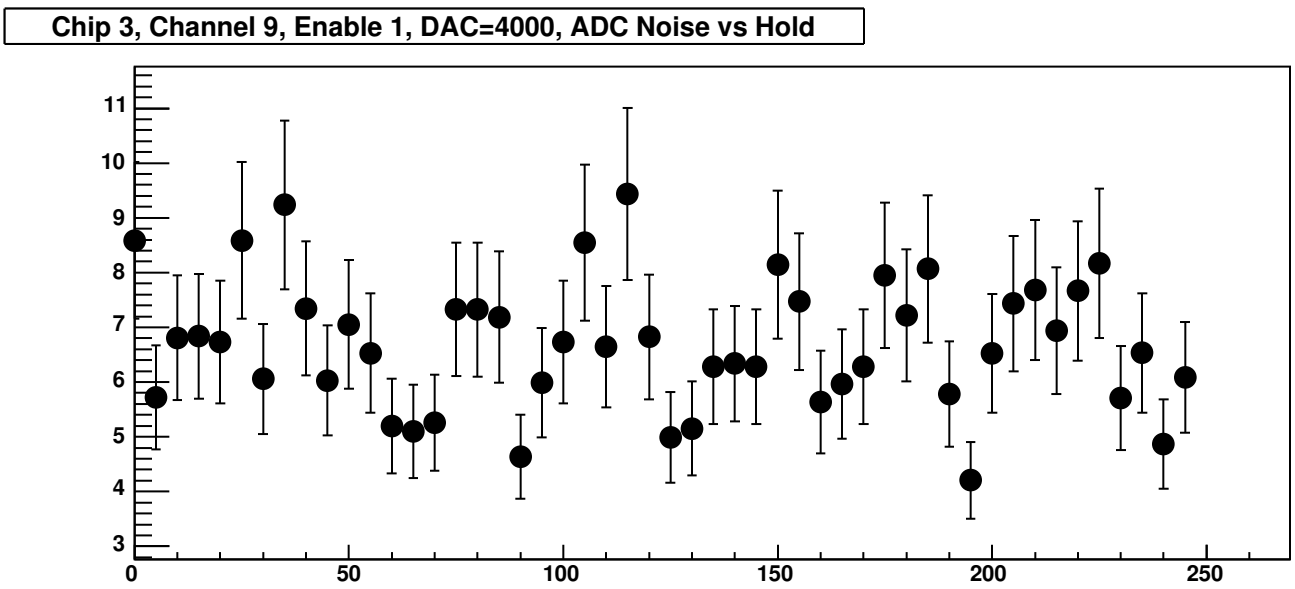


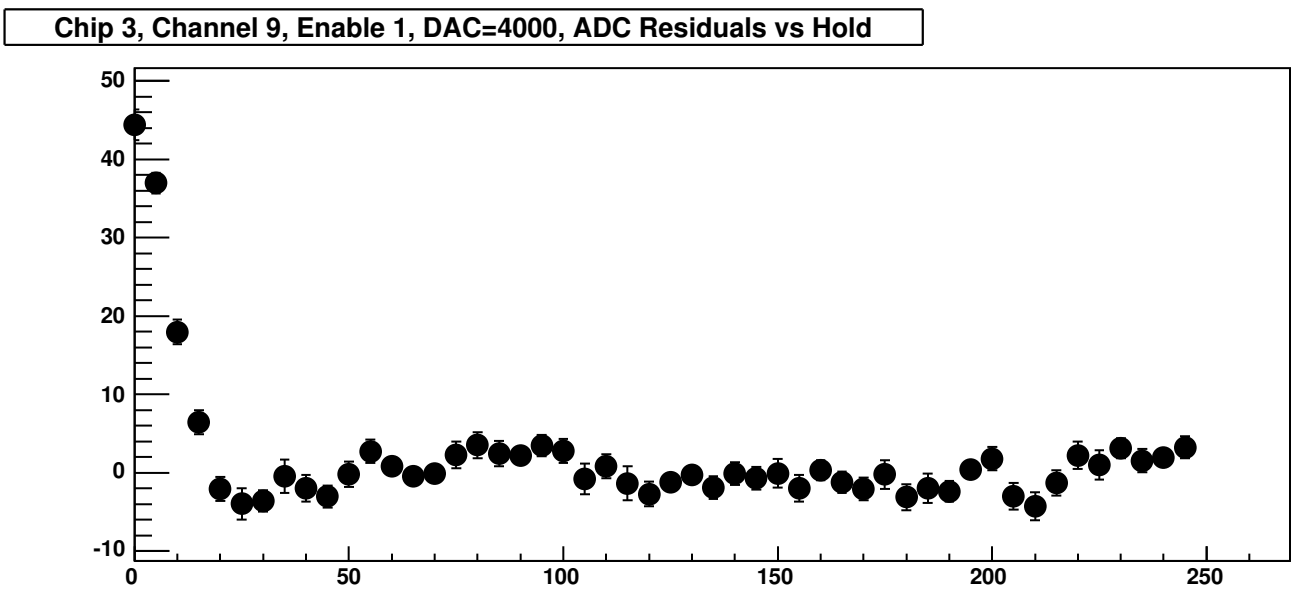

Chip 3, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 99.52 / 41
p0 376.8 ± 2.644
p1 3.398 ± 0.6361
p2 174.5 ± 2.748
p3 31.41 ± 0.7898
p4 -0.1842 ± 0.007768
540
520
500
480
460
440
420
0
50
100
150
200
250

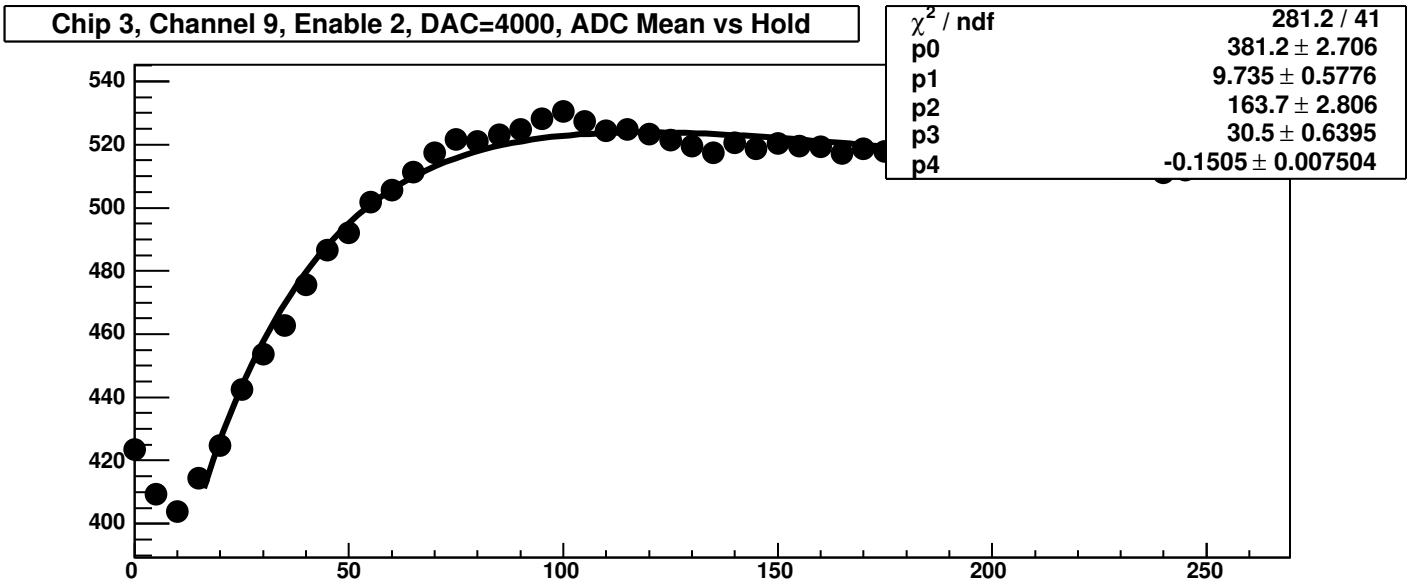

Chip 3, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold
10
9
8
7
6
5
4
3
2
0
50
100
150
200
250

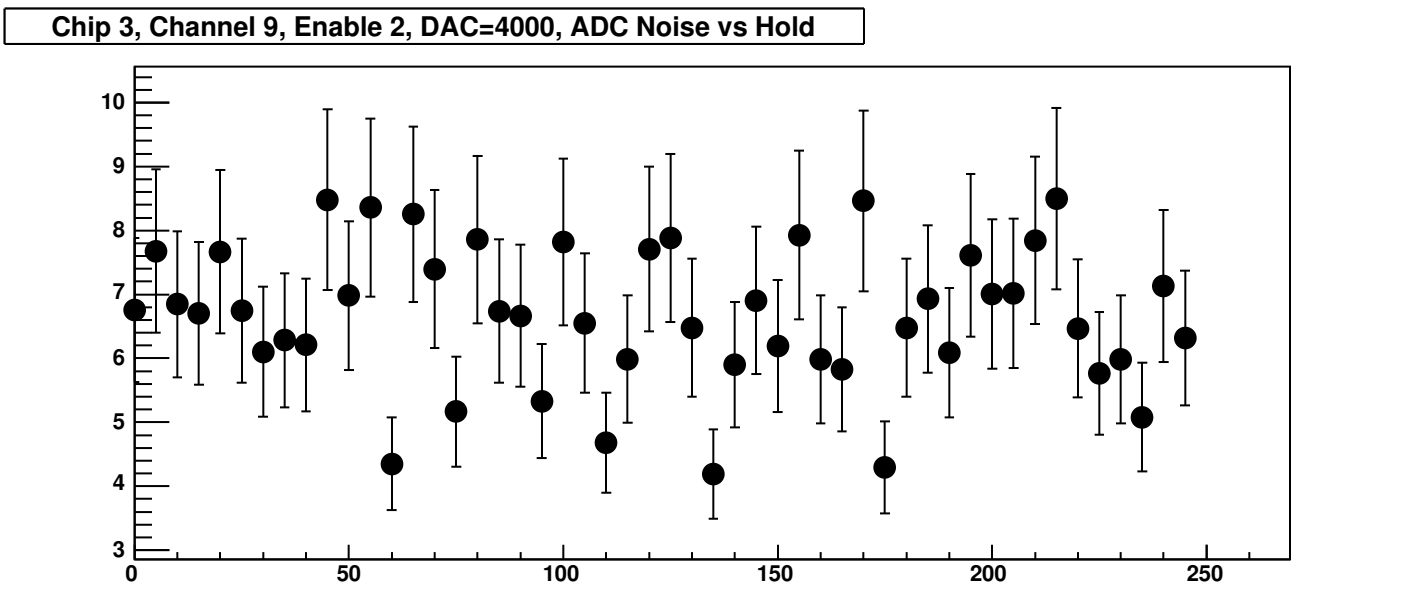

Chip 3, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250

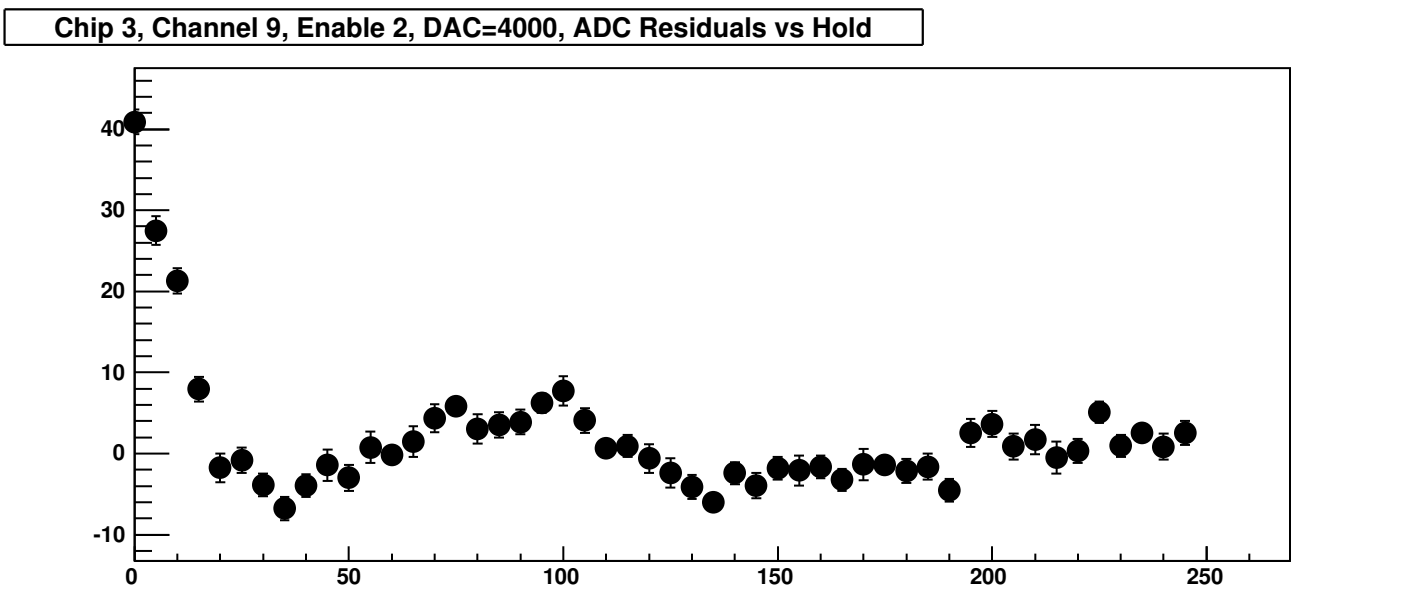

Chip 3, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 103.9 / 41
p0 381.5 ± 2.735
p1 4.288 ± 0.6235
p2 178.7 ± 2.86
p3 32.47 ± 0.7664
p4 -0.2056 ± 0.008088
540
520
500
480
460
440
420
0
50
100
150
200
250


Chip 3, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

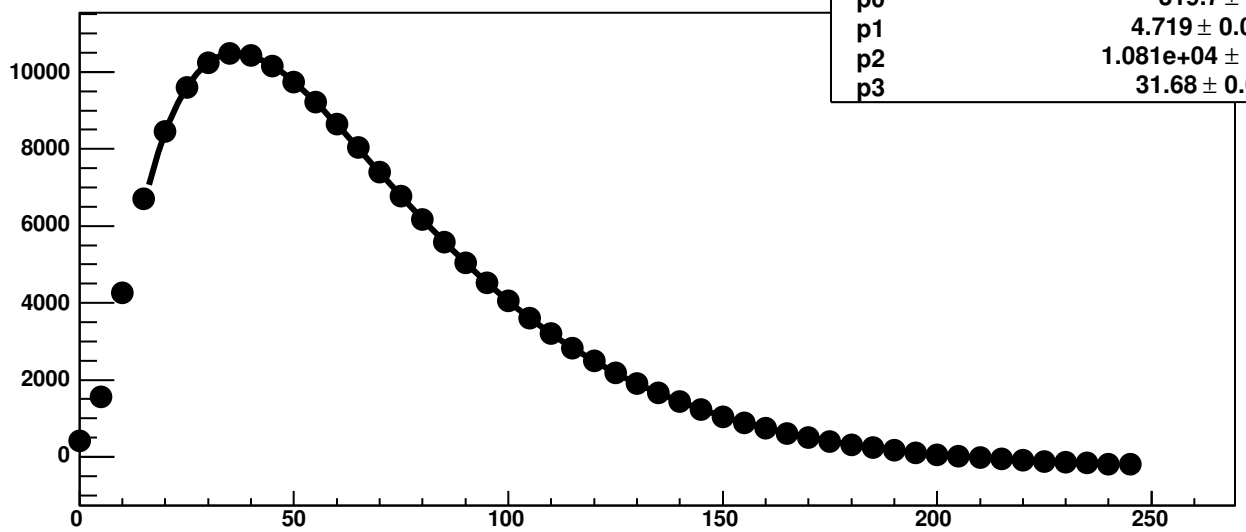

Chip 3, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250


Chip 3, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 281.2 / 41
p0 381.2 ± 2.706
p1 9.735 ± 0.5776
p2 163.7 ± 2.806
p3 30.5 ± 0.6395
p4 -0.1505 ± 0.007504
540
520
500
480
460
440
420
400
0
50
100
150
200
250

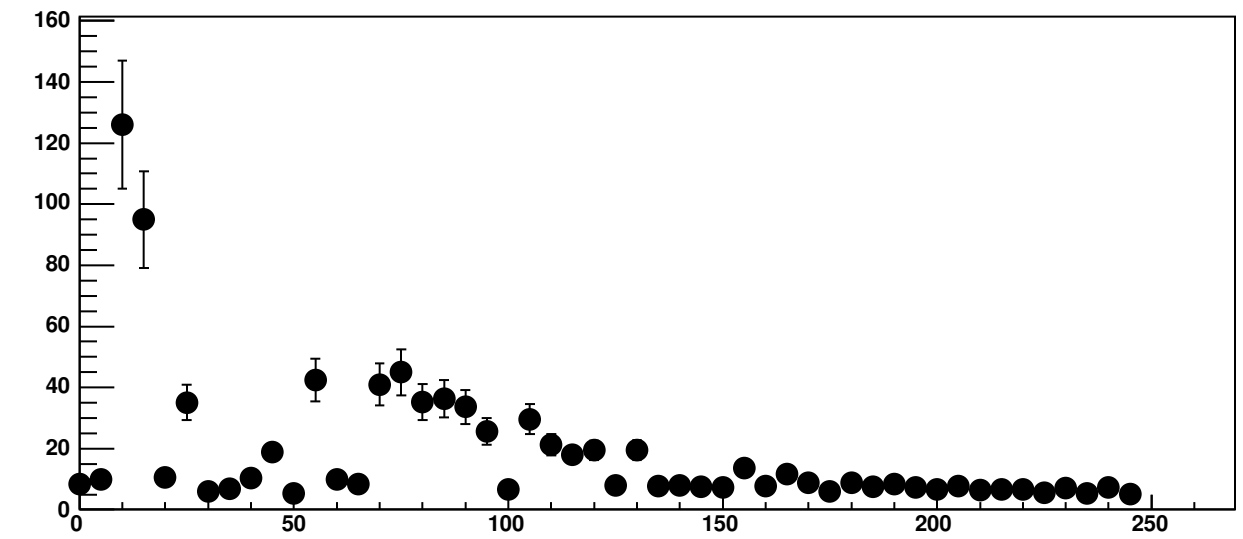

Chip 3, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 3, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

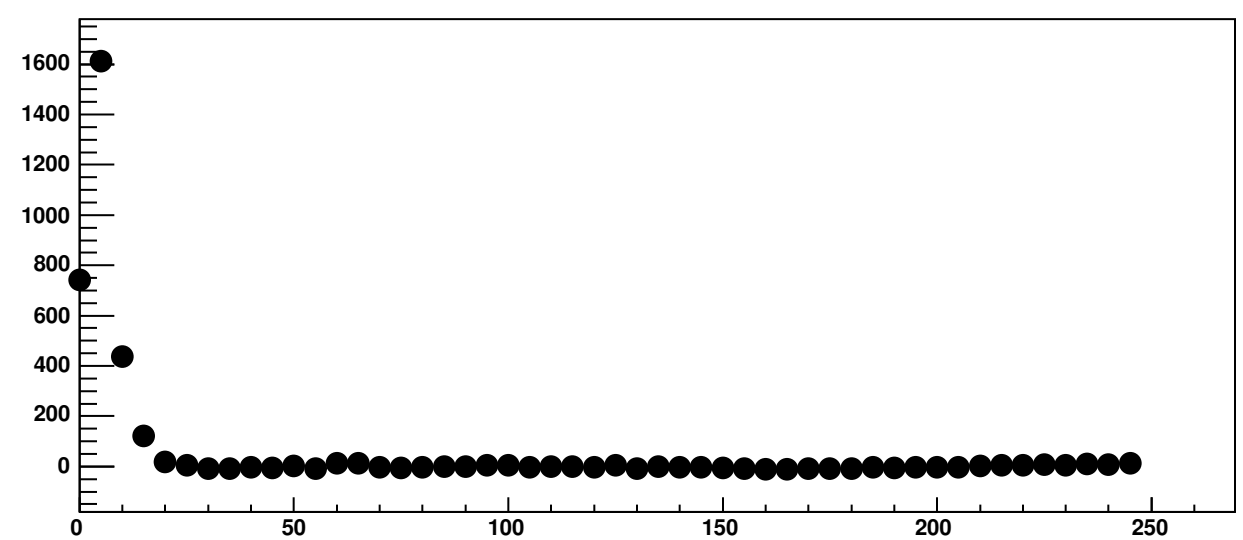
Chip 3, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold

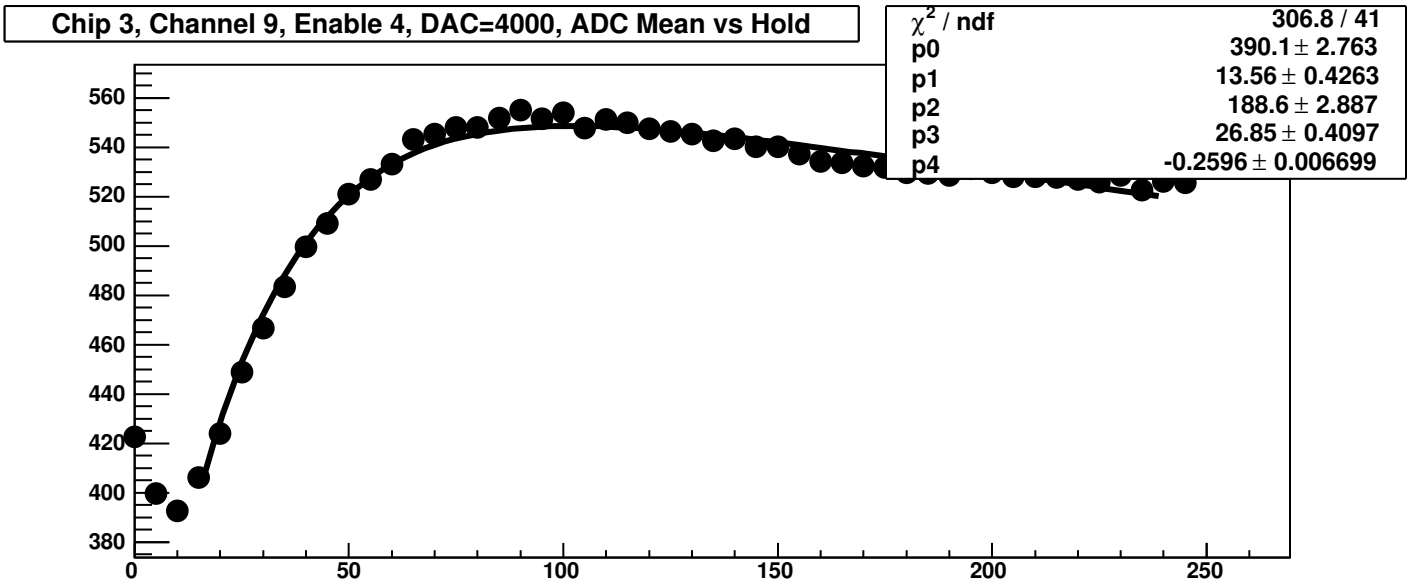


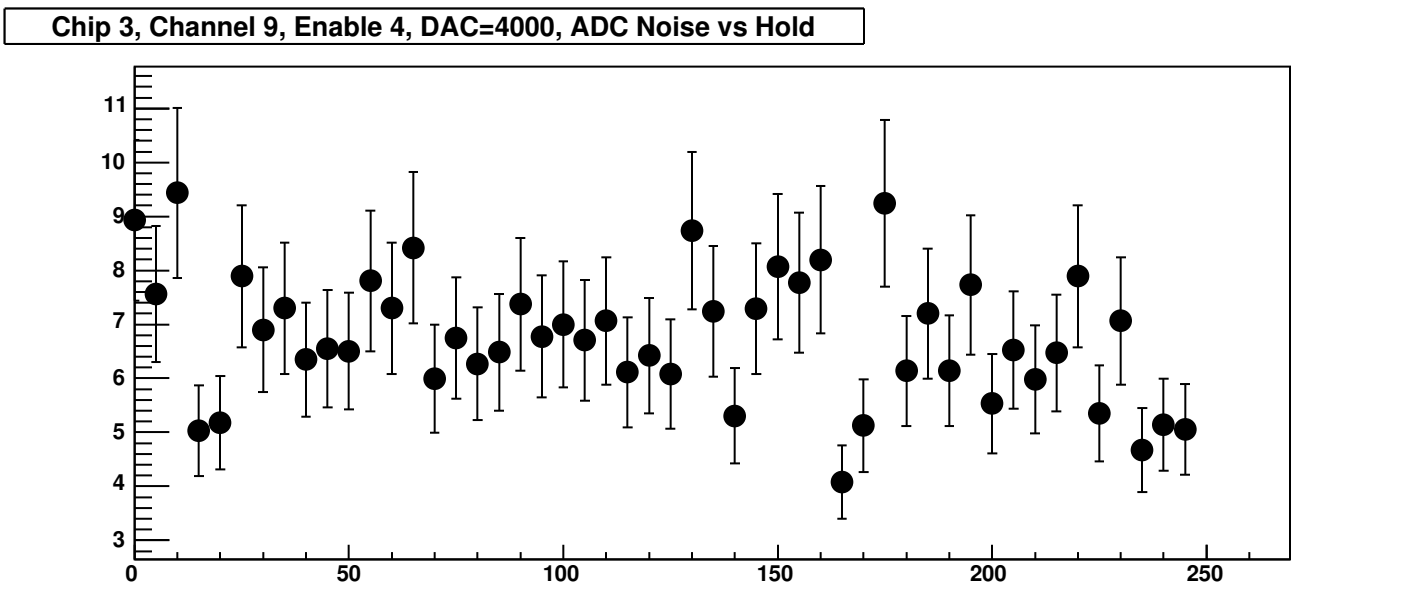
Chip 3, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold

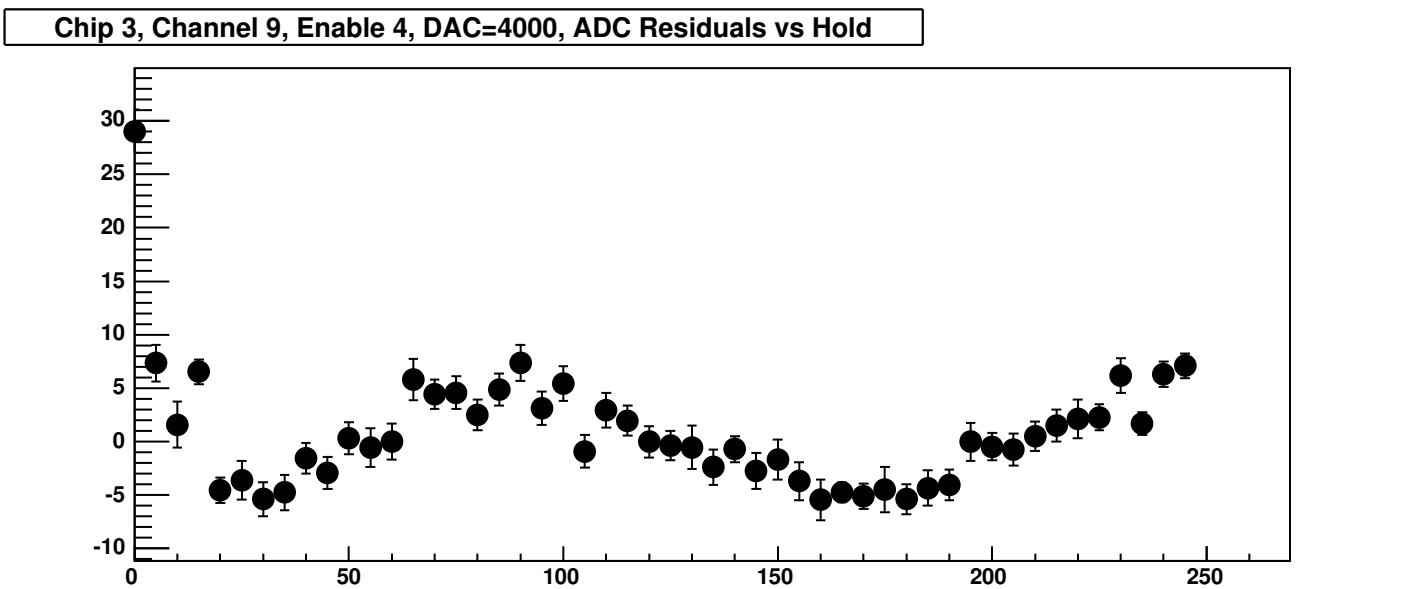


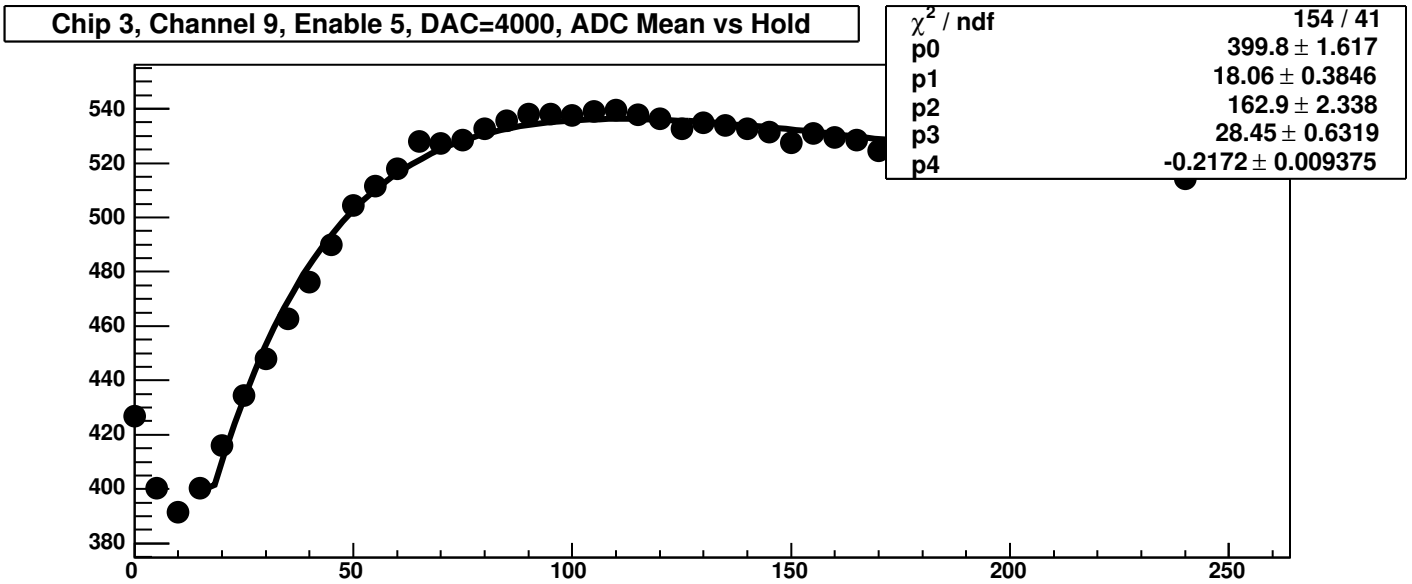
Chip 3, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold

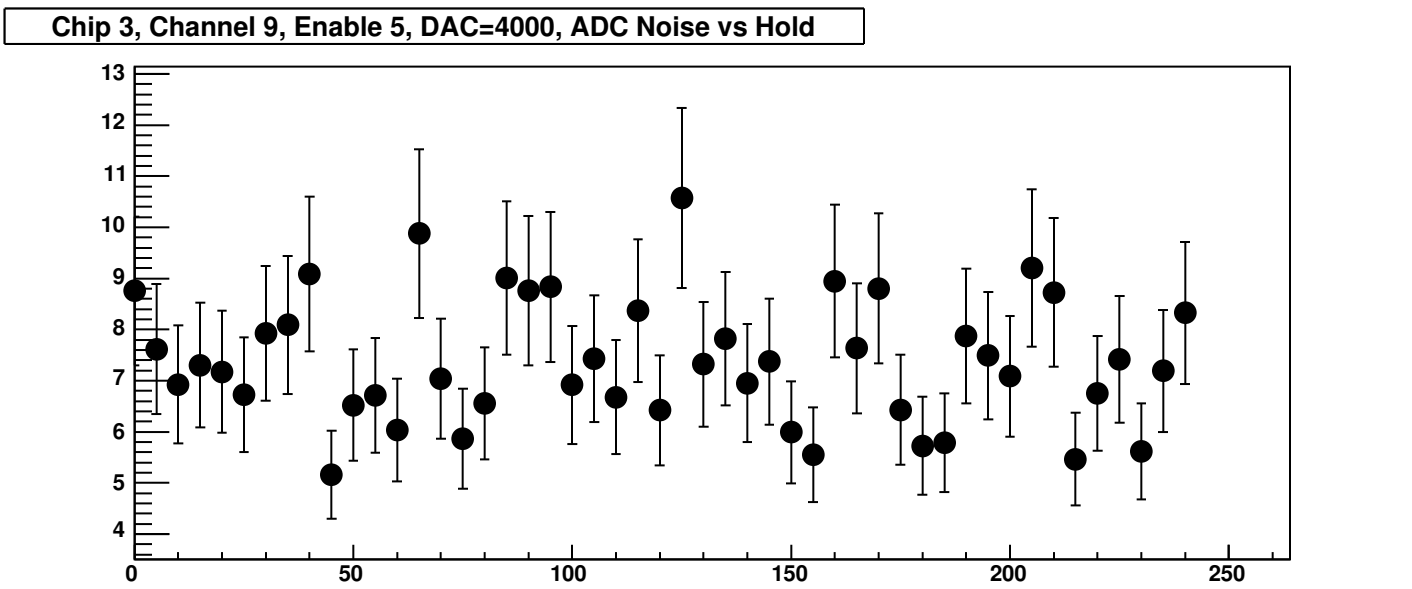


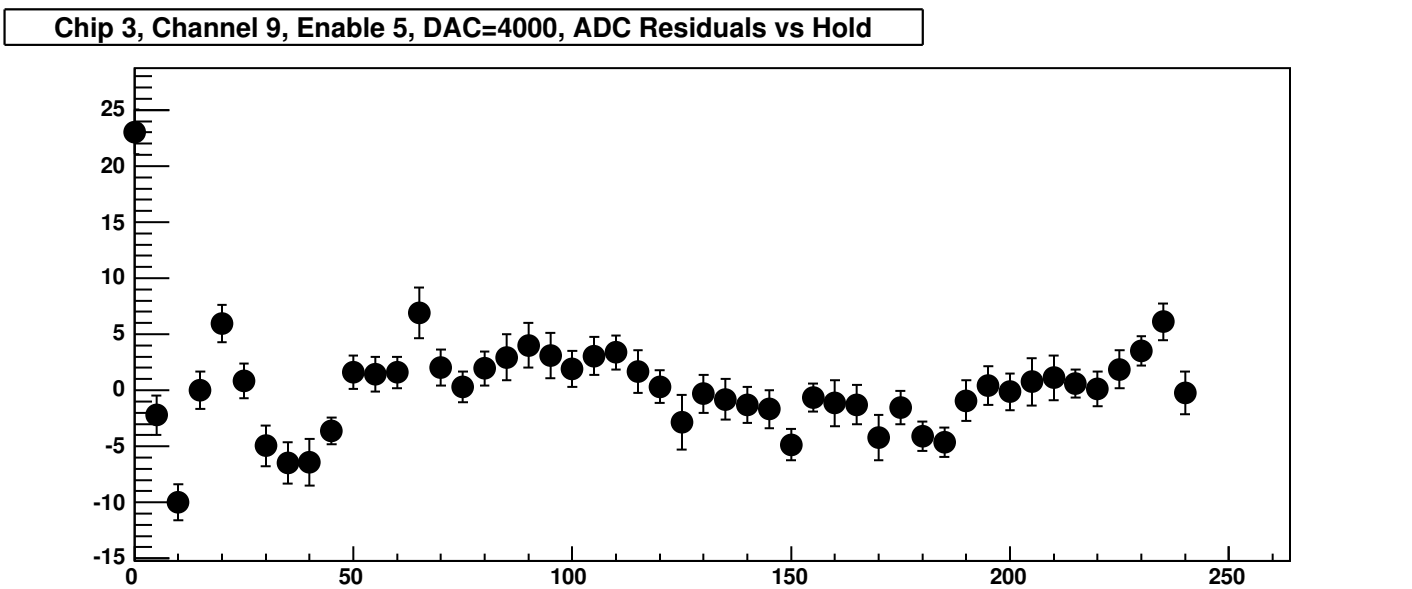

Chip 3, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 306.8 / 41
p0 390.1 ± 2.763
p1 13.56 ± 0.4263
p2 188.6 ± 2.887
p3 26.85 ± 0.4097
p4 -0.2596 ± 0.006699
560
540
520
500
480
460
440
420
400
380
0
50
100
150
200
250

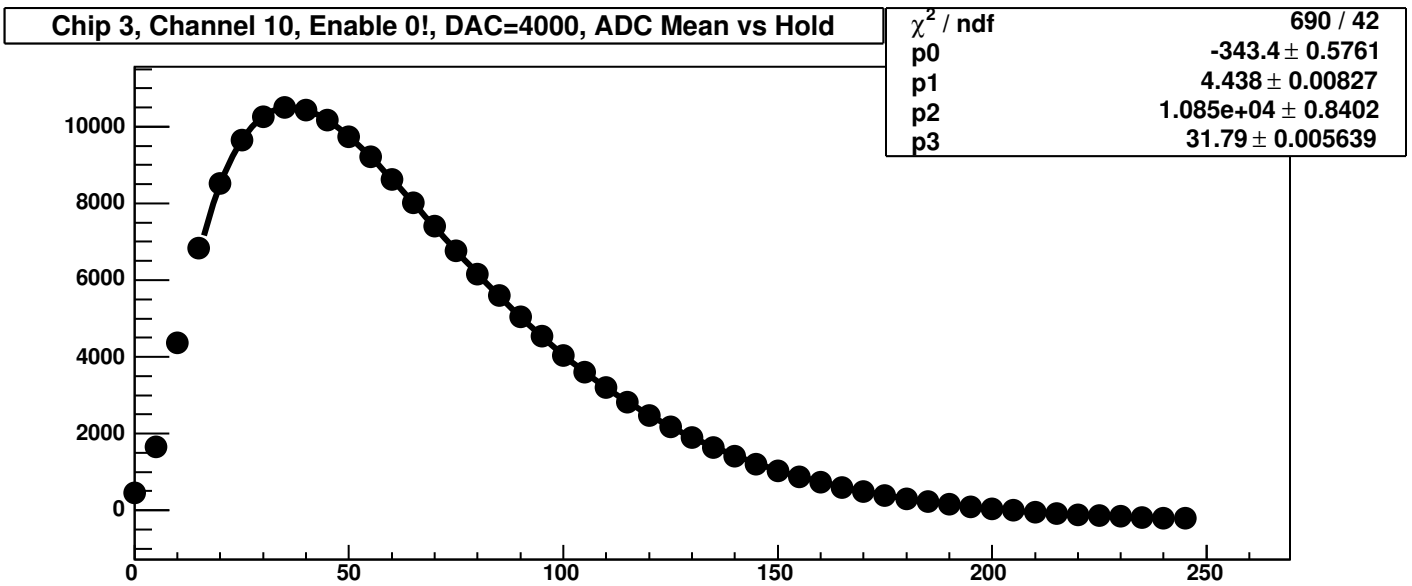

Chip 3, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

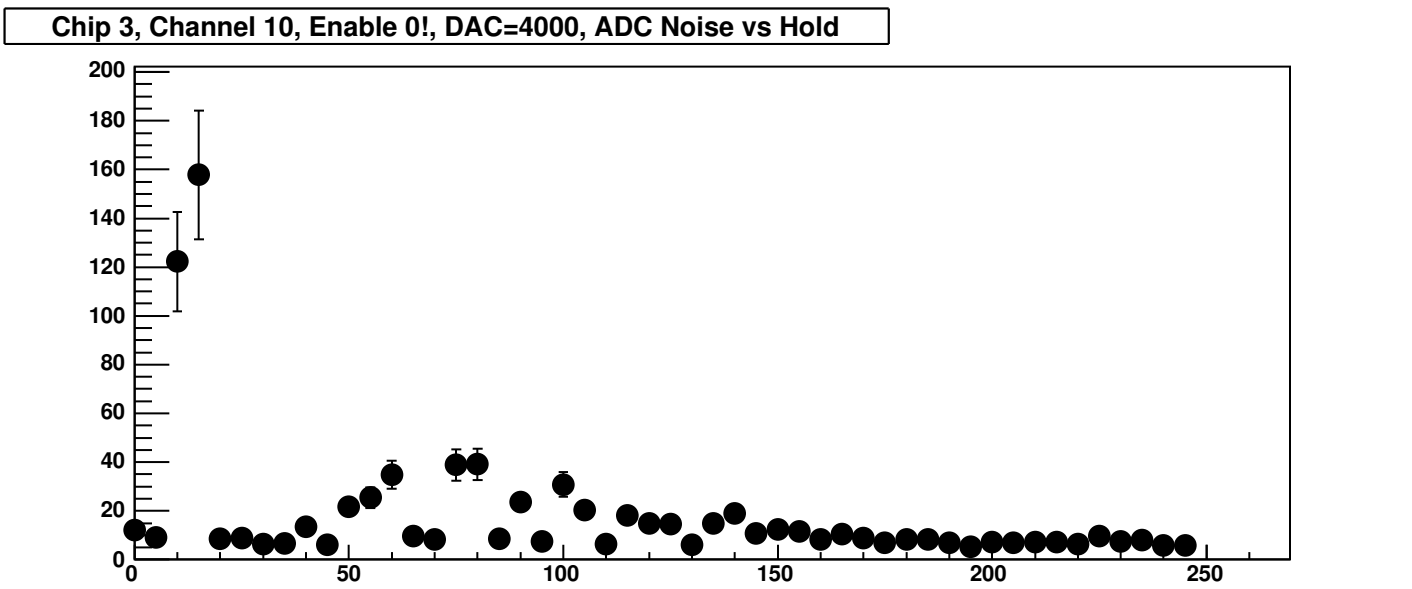

Chip 3, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
0
50
100
150
200
250

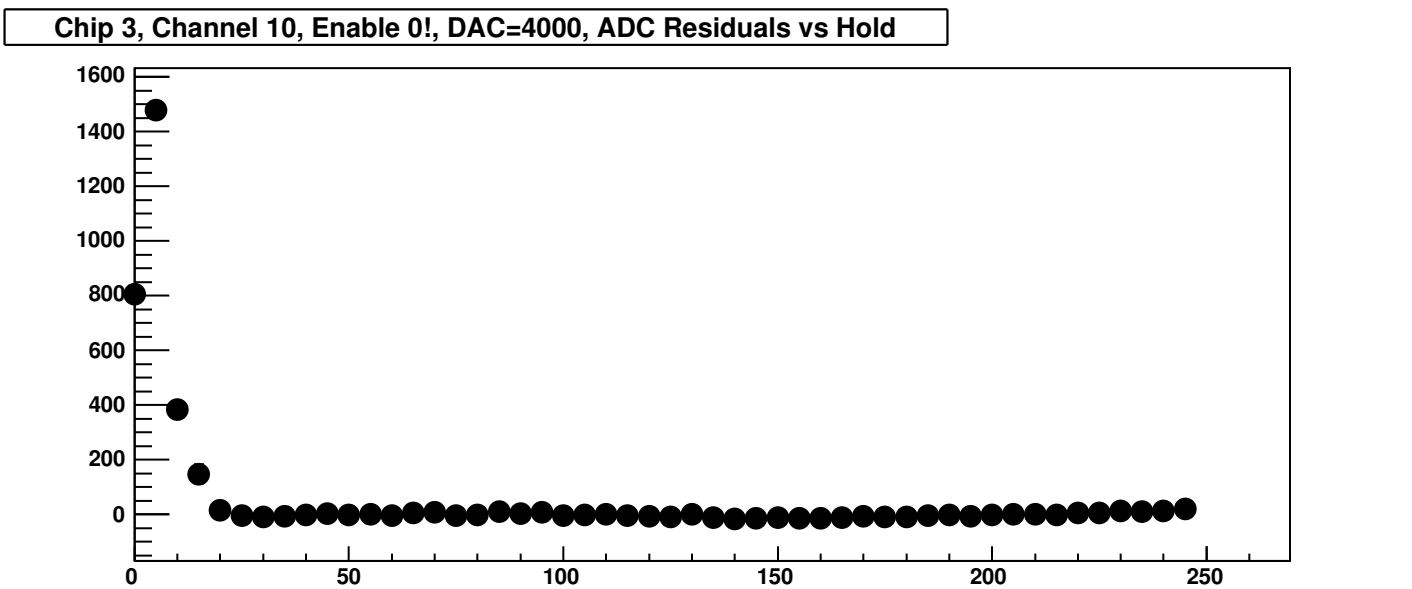

Chip 3, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 154 / 41
p0 399.8 ± 1.617
p1 18.06 ± 0.3846
p2 162.9 ± 2.338
p3 28.45 ± 0.6319
p4 -0.2172 ± 0.009375
540
520
500
480
460
440
420
400
380
0
50
100
150
200
250


Chip 3, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

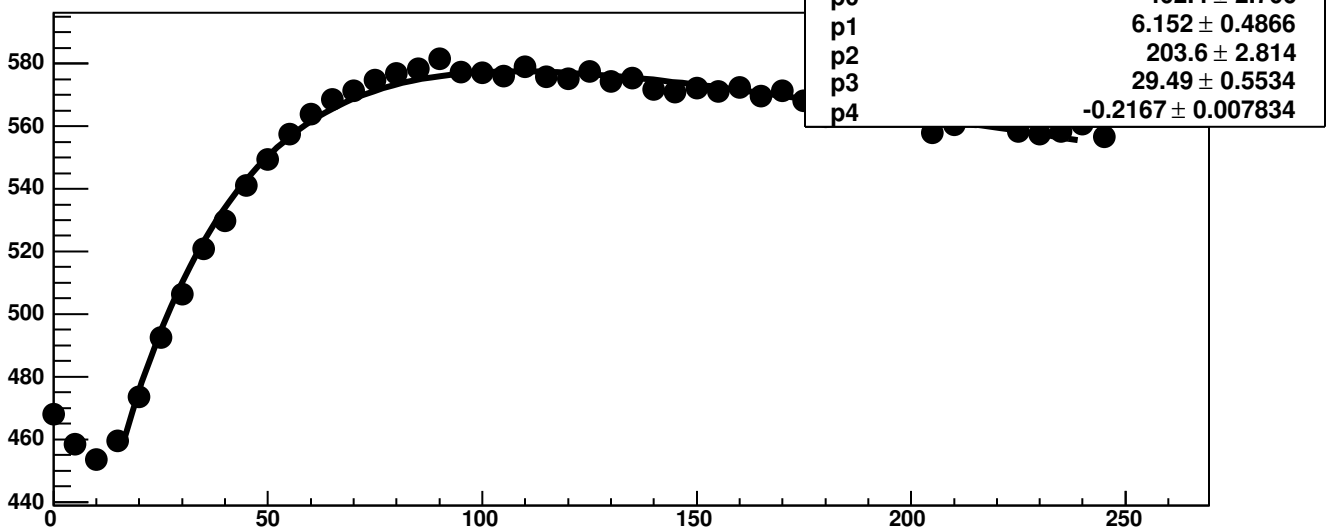

Chip 3, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold
25
20
15
10
5
0
-5
-10
-15
0
50
100
150
200
250


Chip 3, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 690 / 42
p0 -343.4 ± 0.5761
p1 4.438 ± 0.00827
p2 1.085e+04 ± 0.8402
p3 31.79 ± 0.005639
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

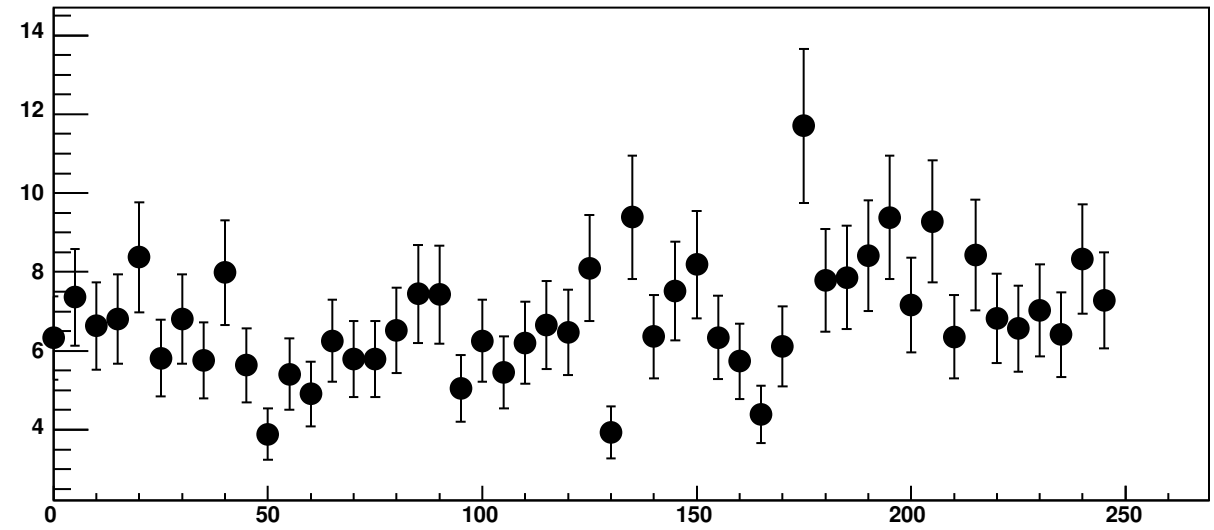

Chip 3, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 3, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

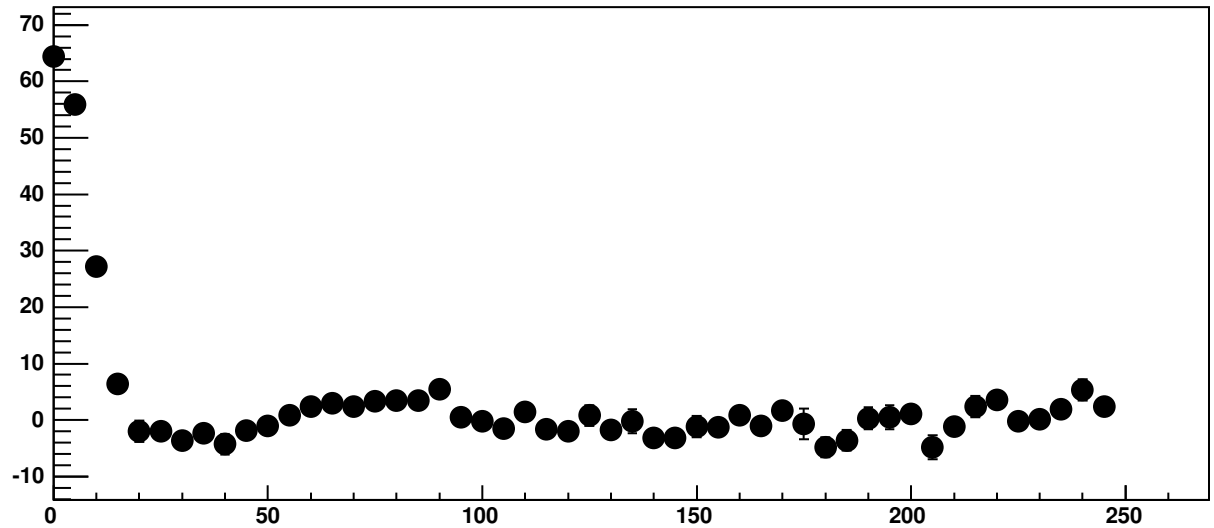
Chip 3, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold



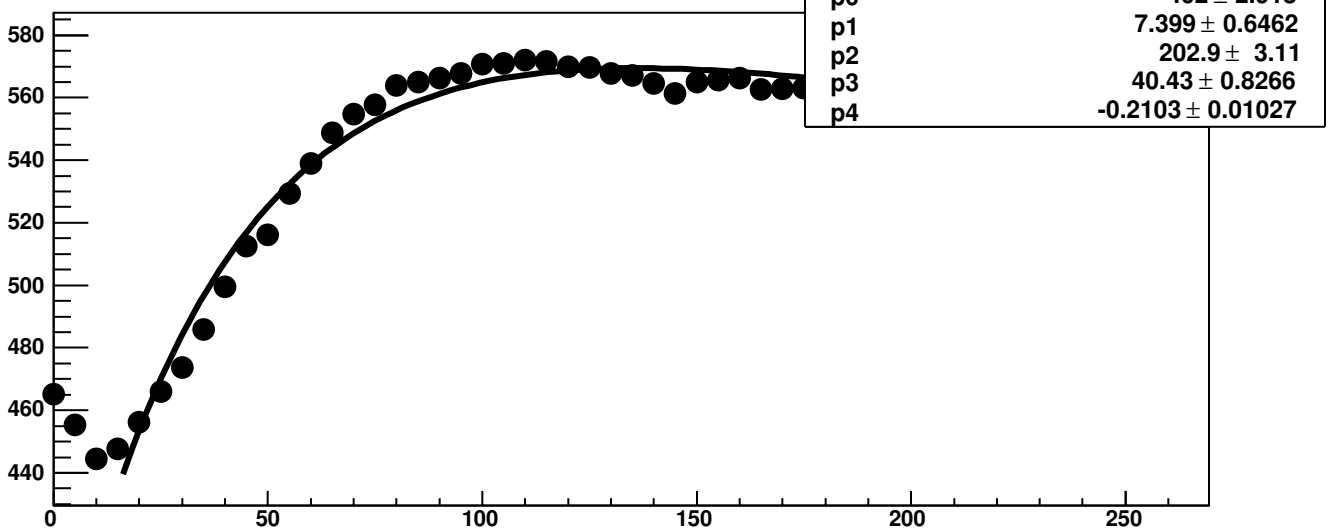
Chip 3, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



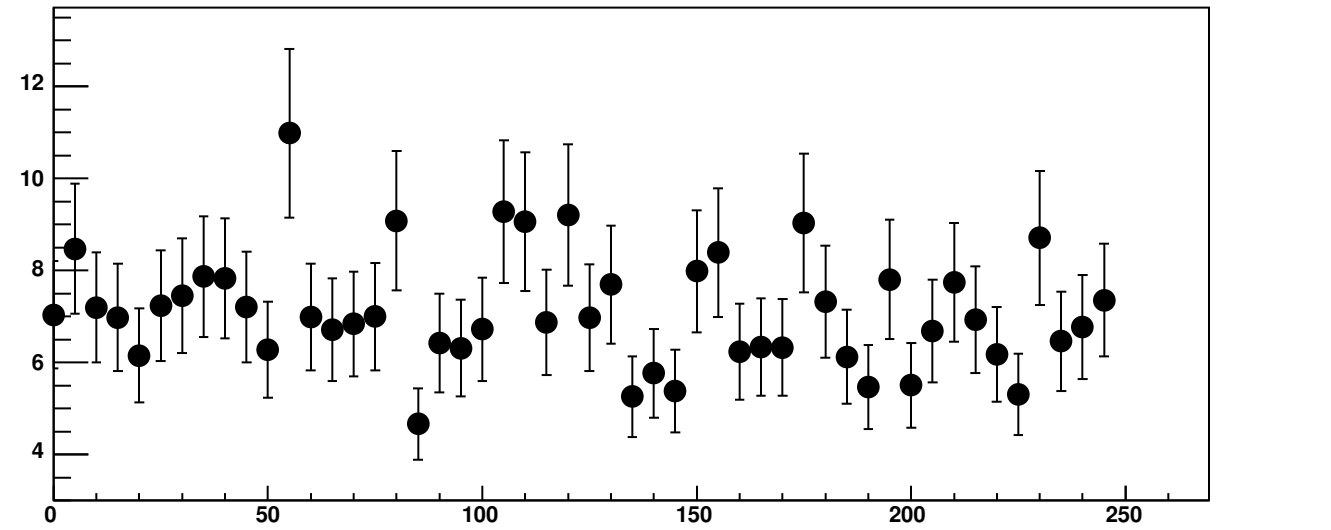
Chip 3, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold



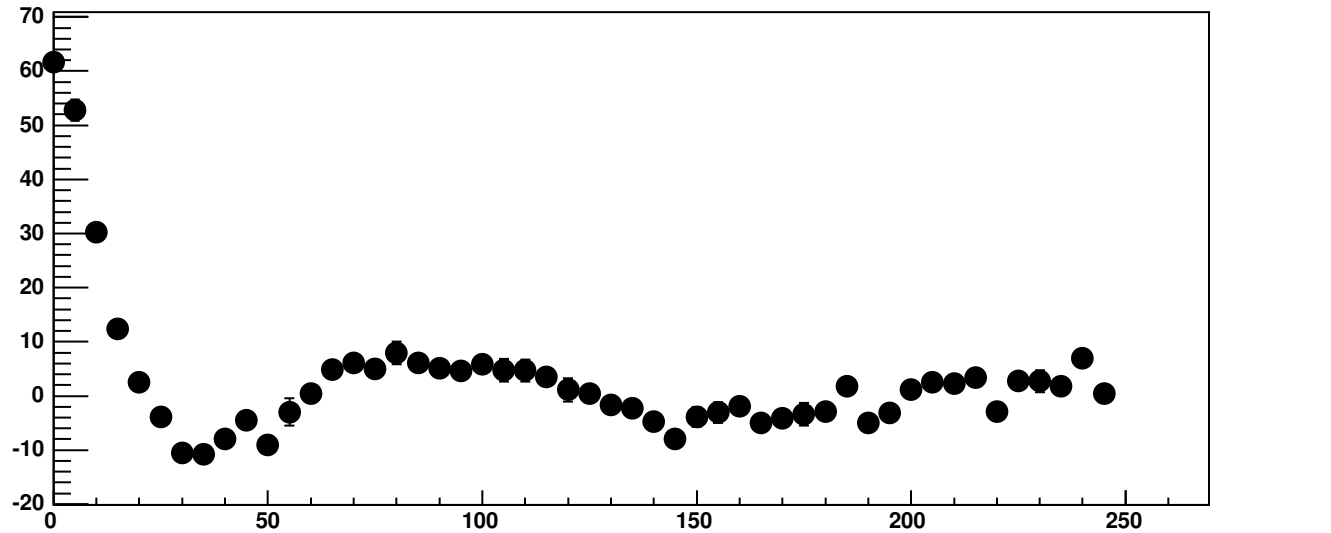
Chip 3, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold



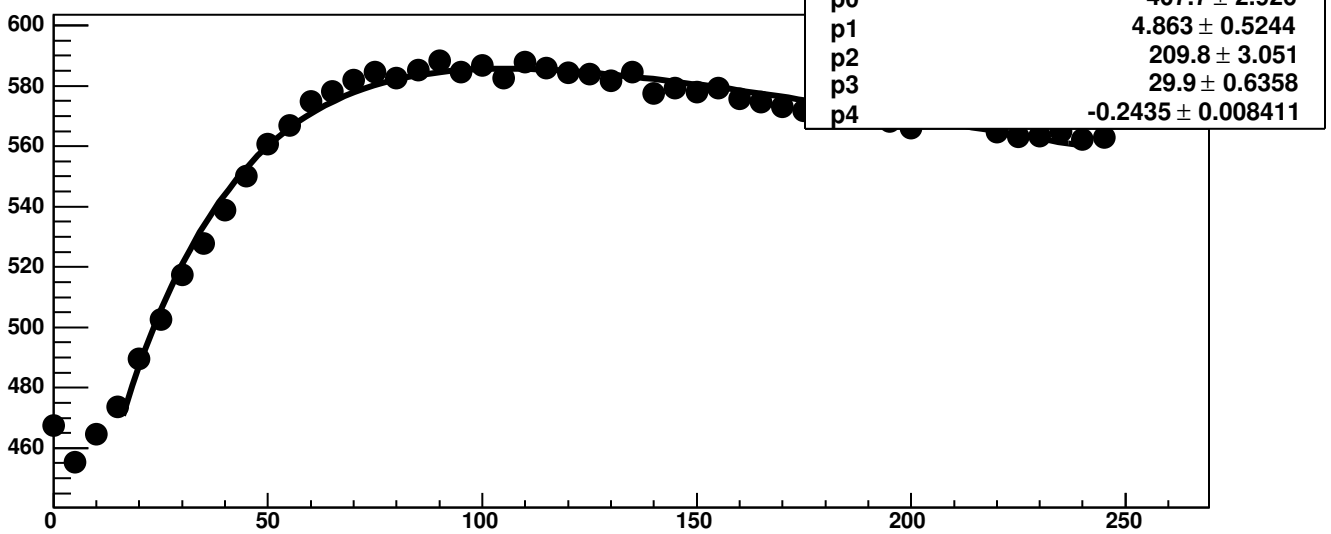
Chip 3, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



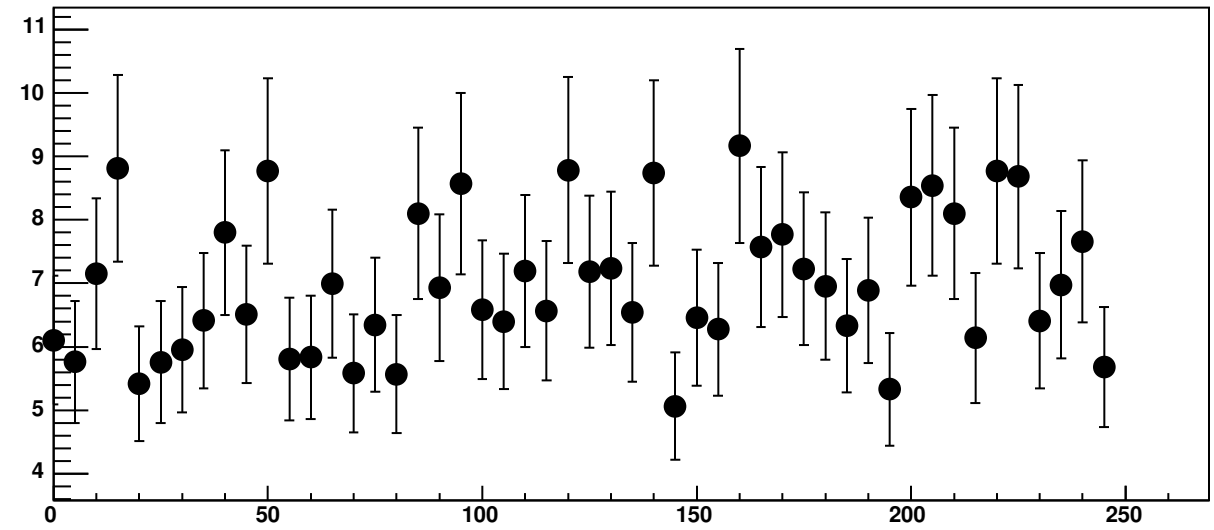
Chip 3, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold



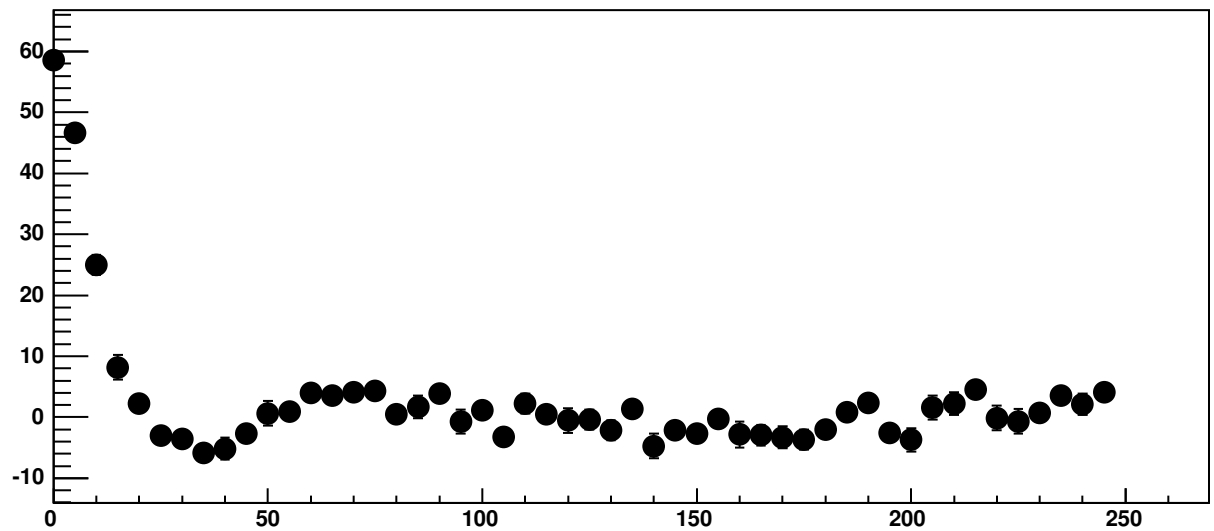
Chip 3, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold



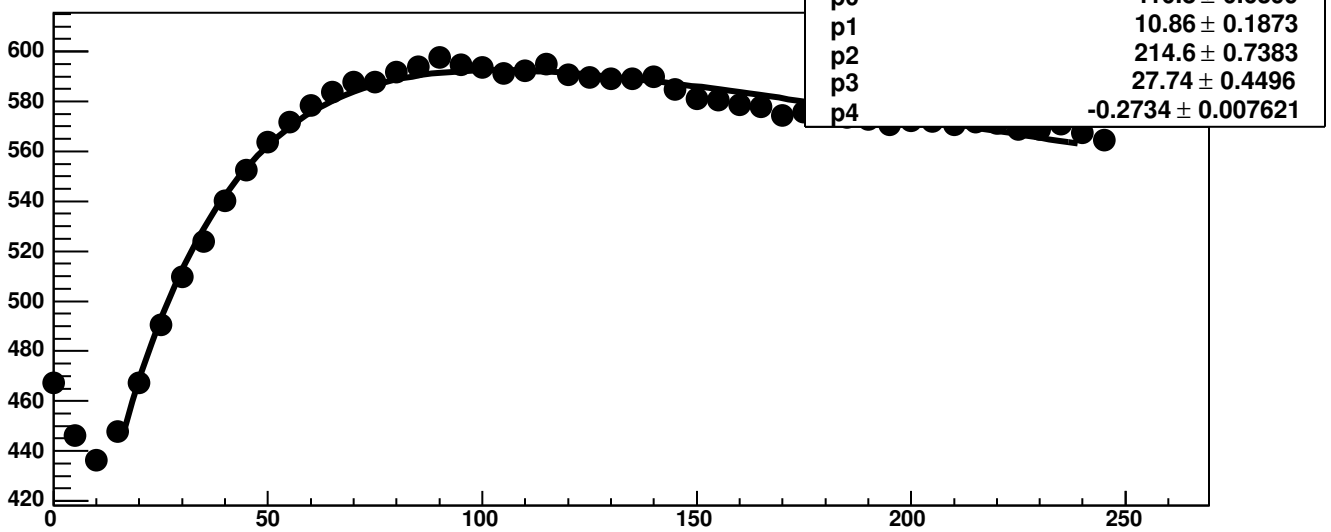
Chip 3, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold



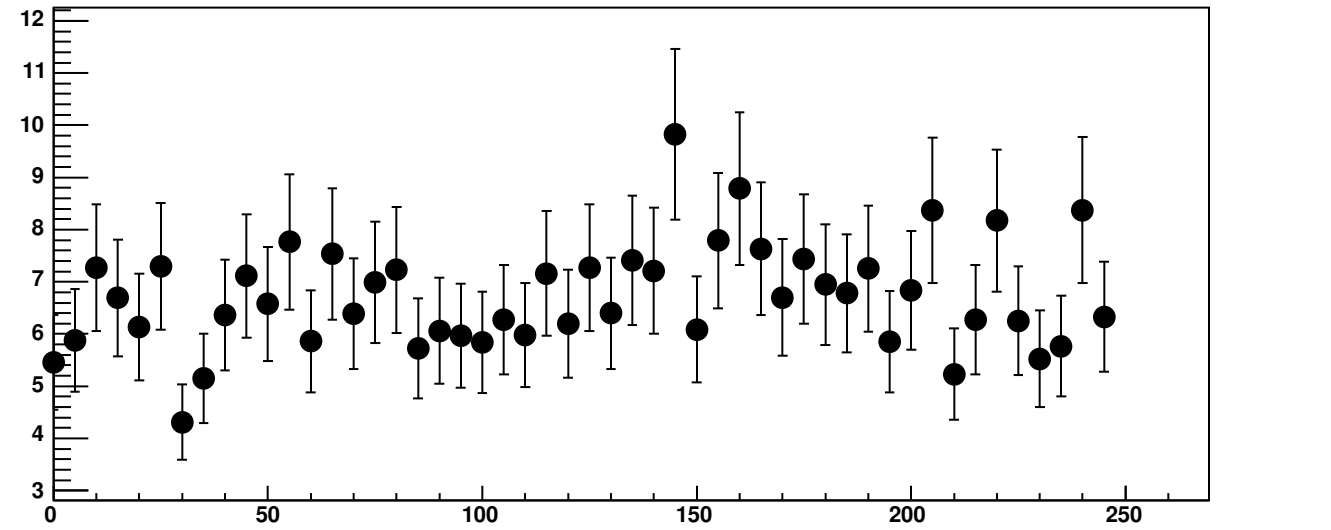
Chip 3, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold



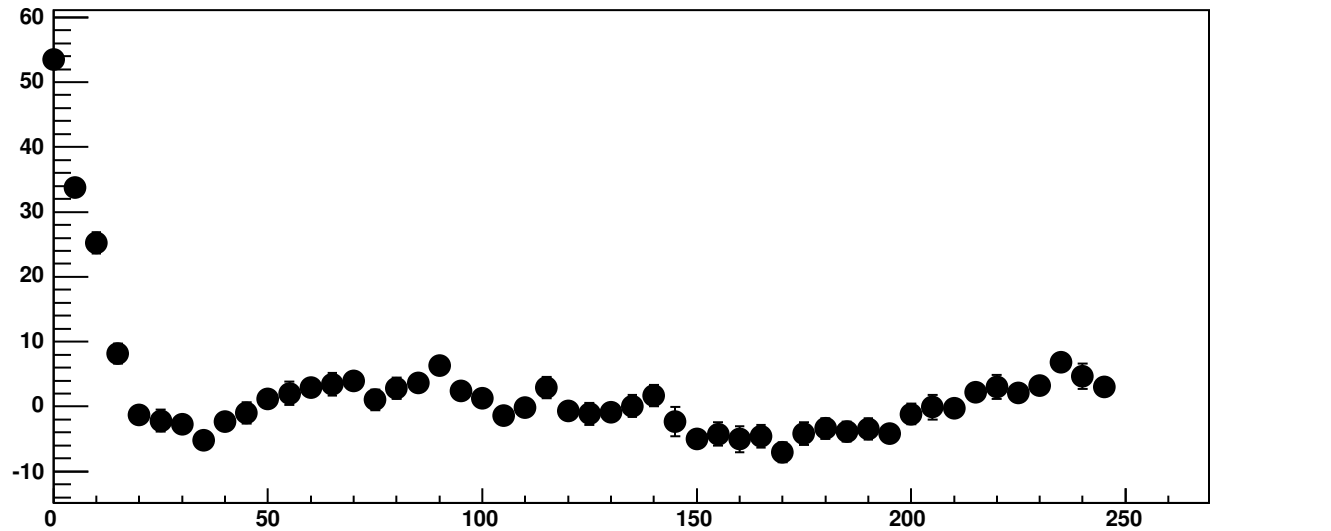
Chip 3, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

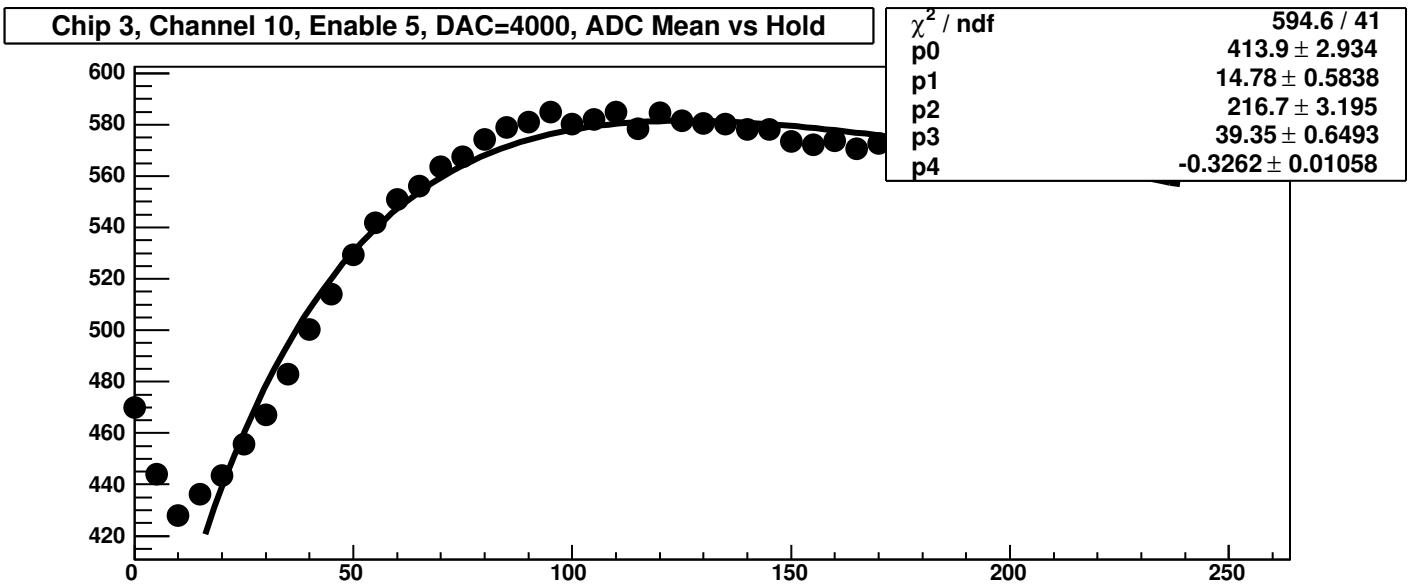


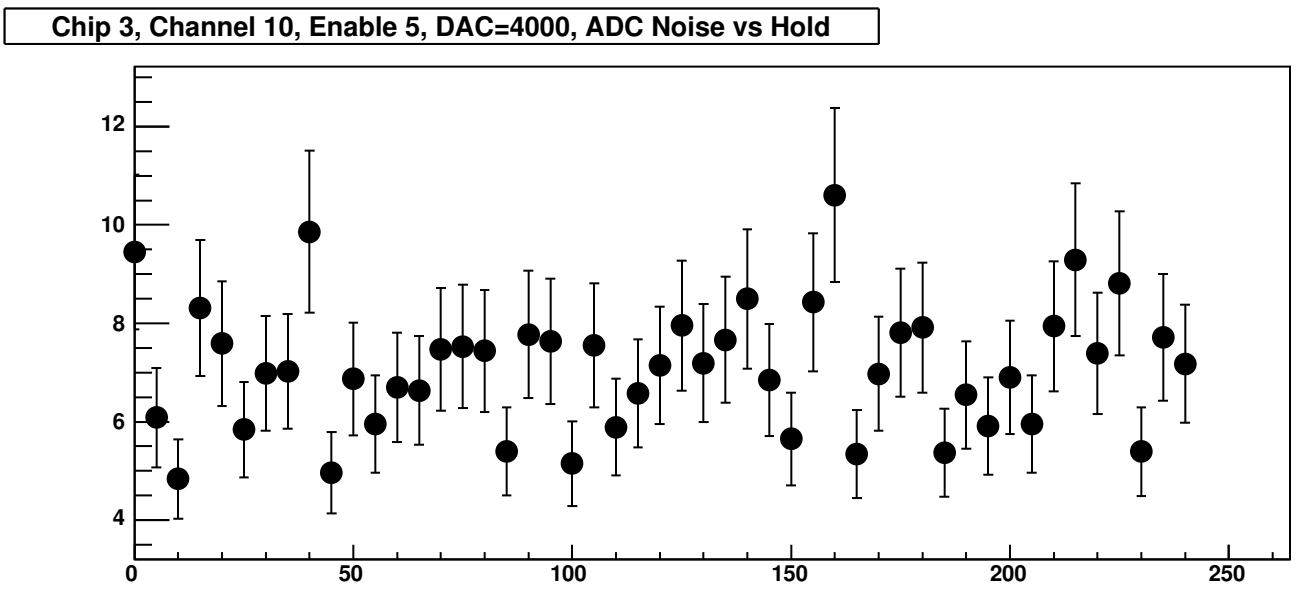
Chip 3, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold

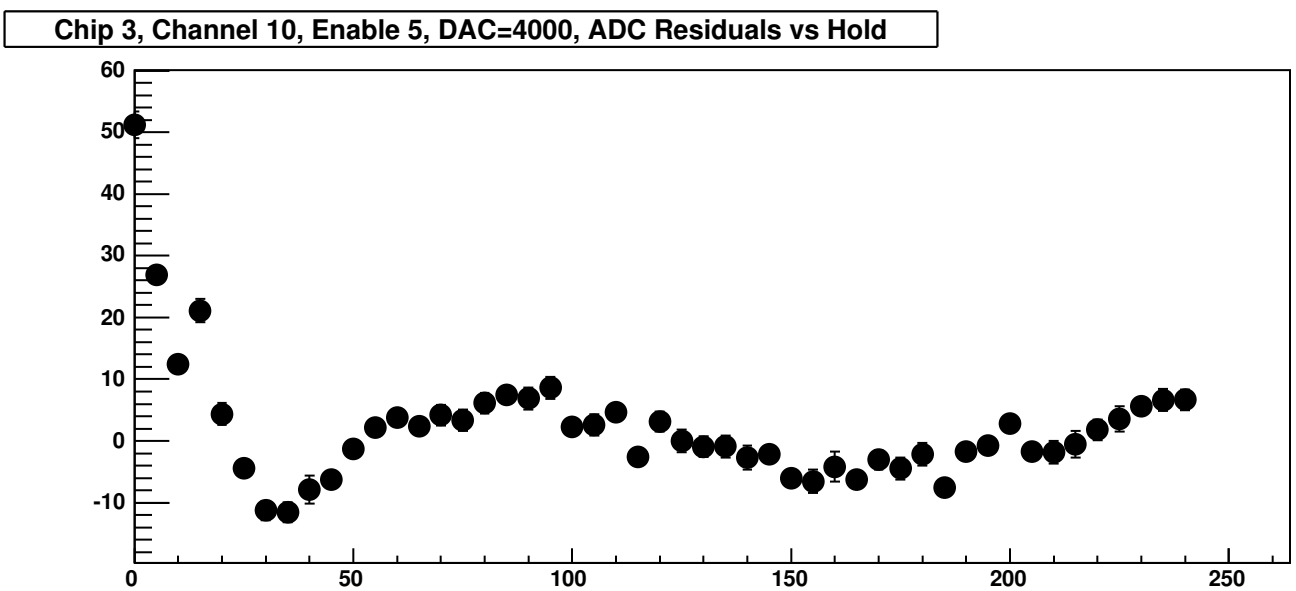


Chip 3, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

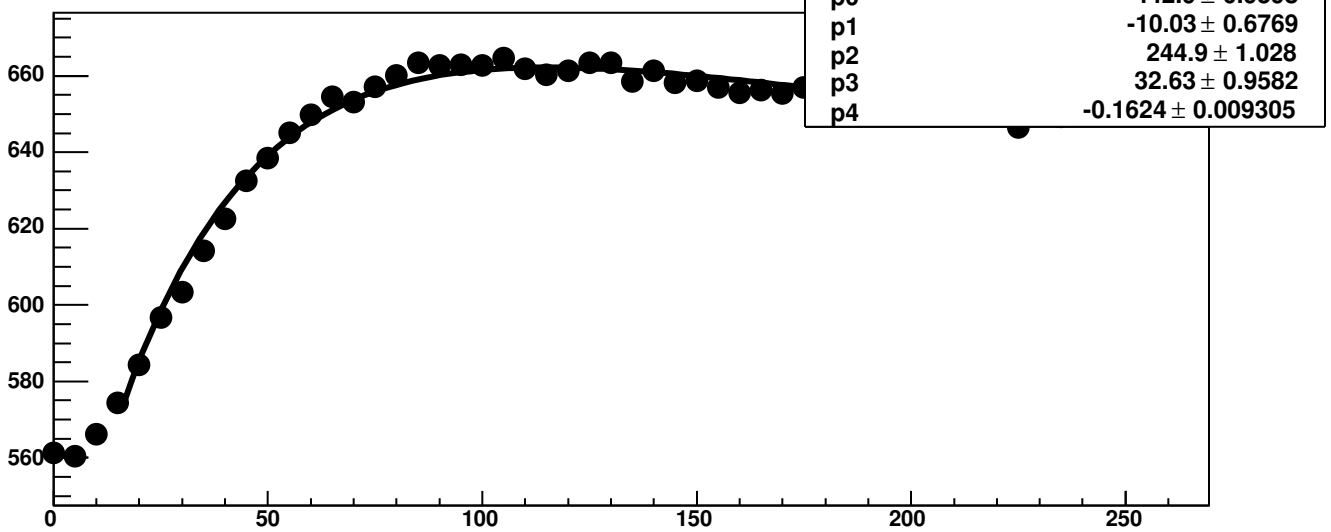



Chip 3, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 594.6 / 41
p0 413.9 ± 2.934
p1 14.78 ± 0.5838
p2 216.7 ± 3.195
p3 39.35 ± 0.6493
p4 -0.3262 ± 0.01058
600
580
560
540
520
500
480
460
440
420
0
50
100
150
200
250

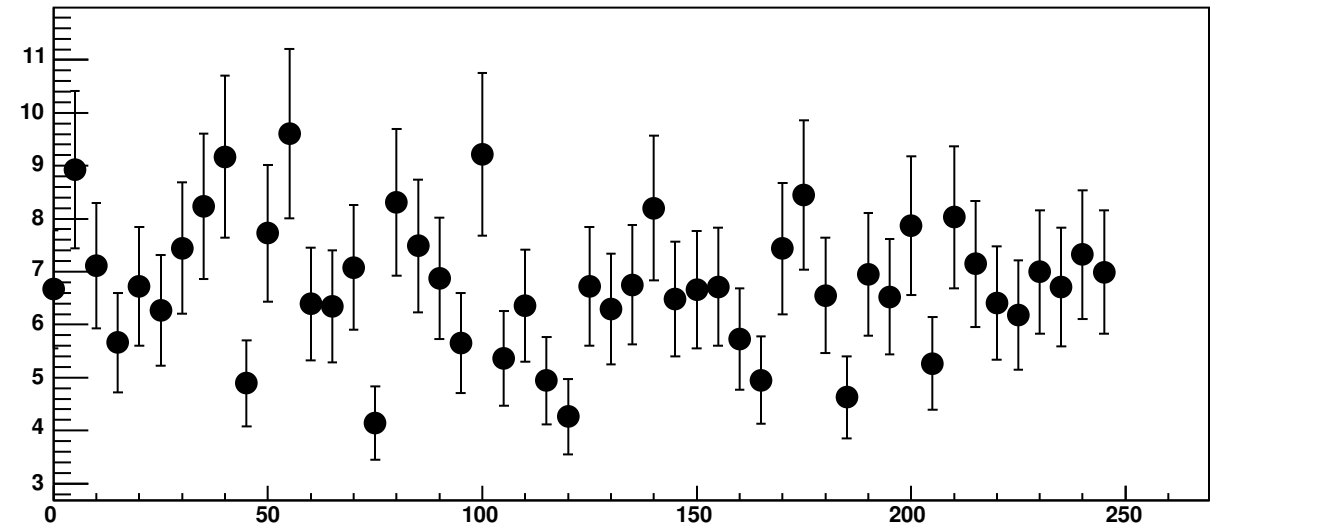

Chip 3, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 3, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

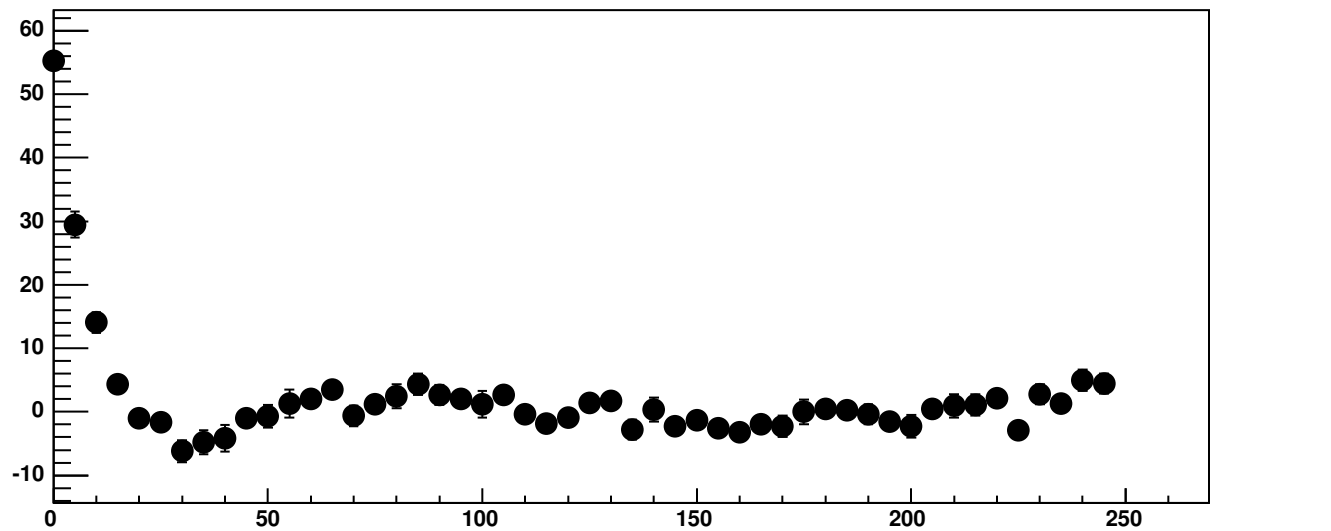
Chip 3, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold



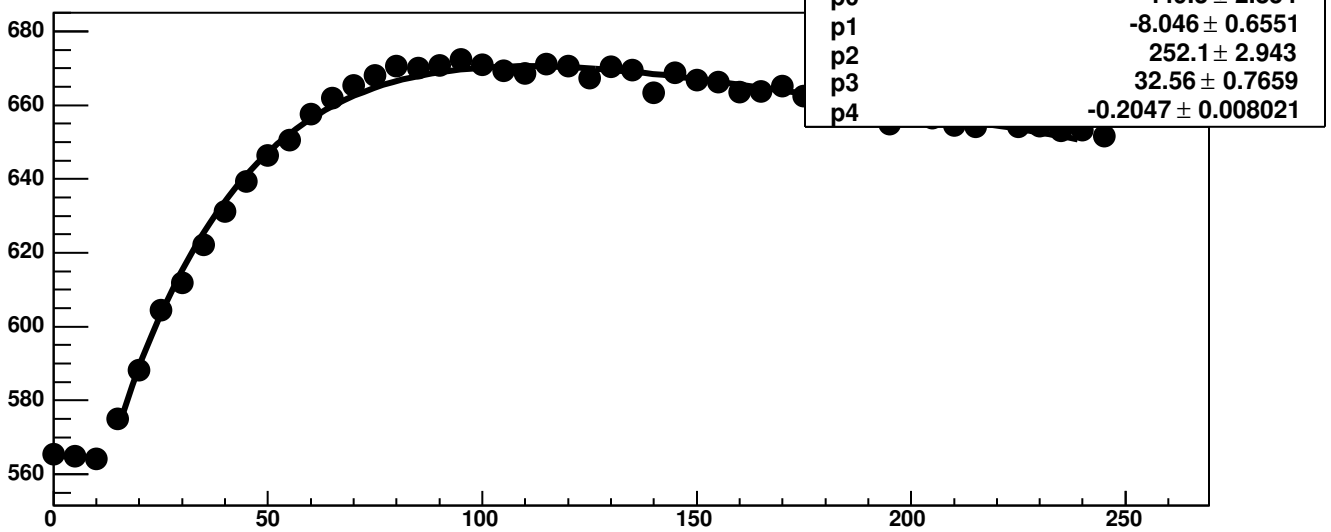
Chip 3, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold



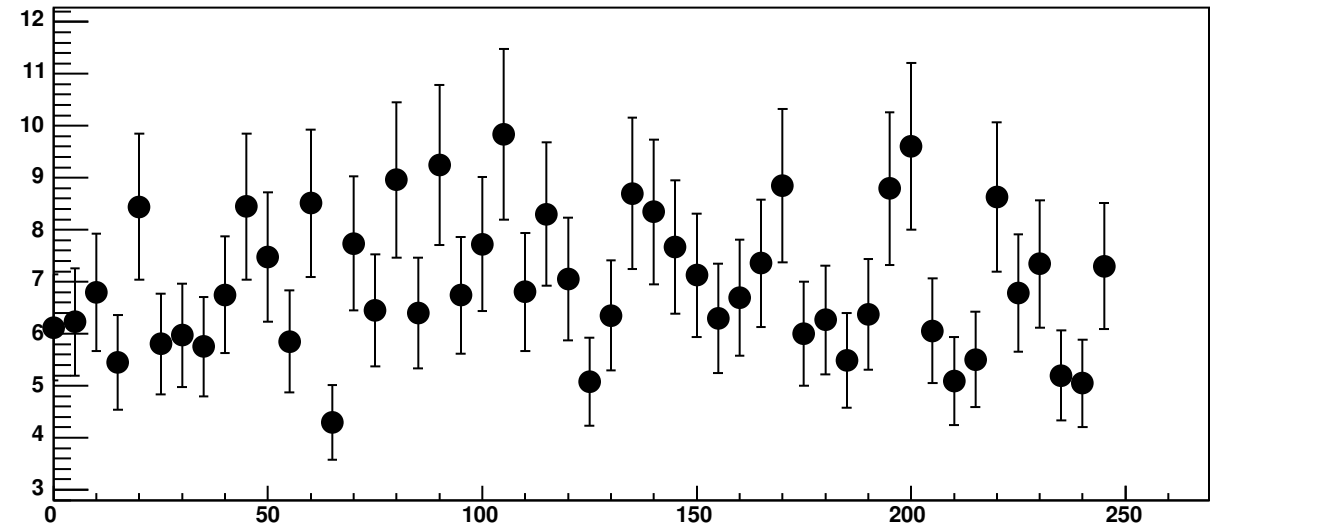
Chip 3, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold



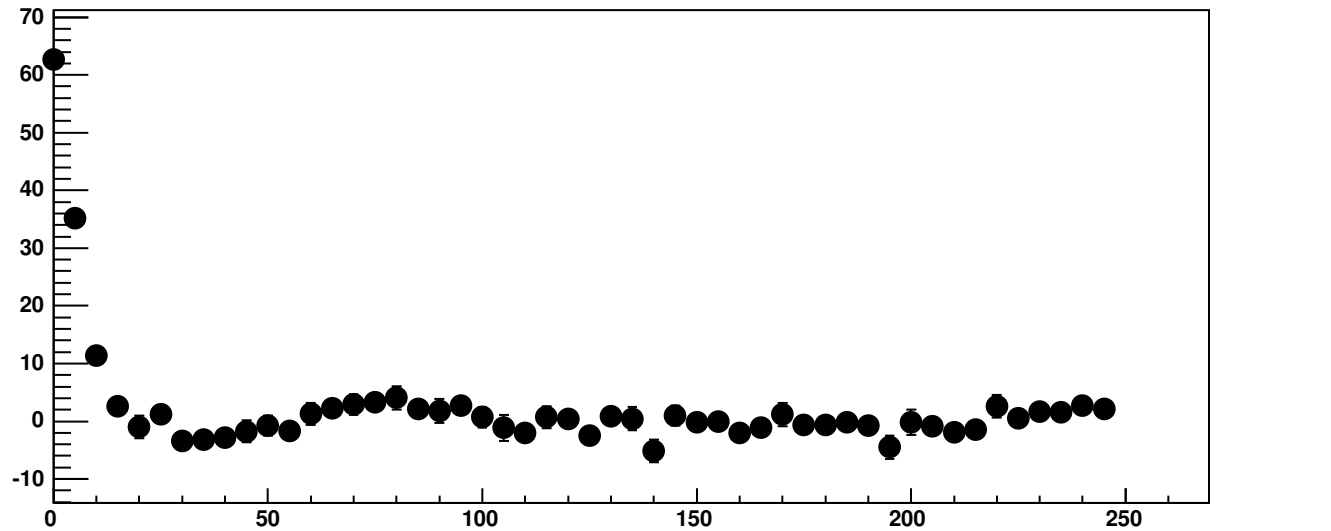
Chip 3, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold



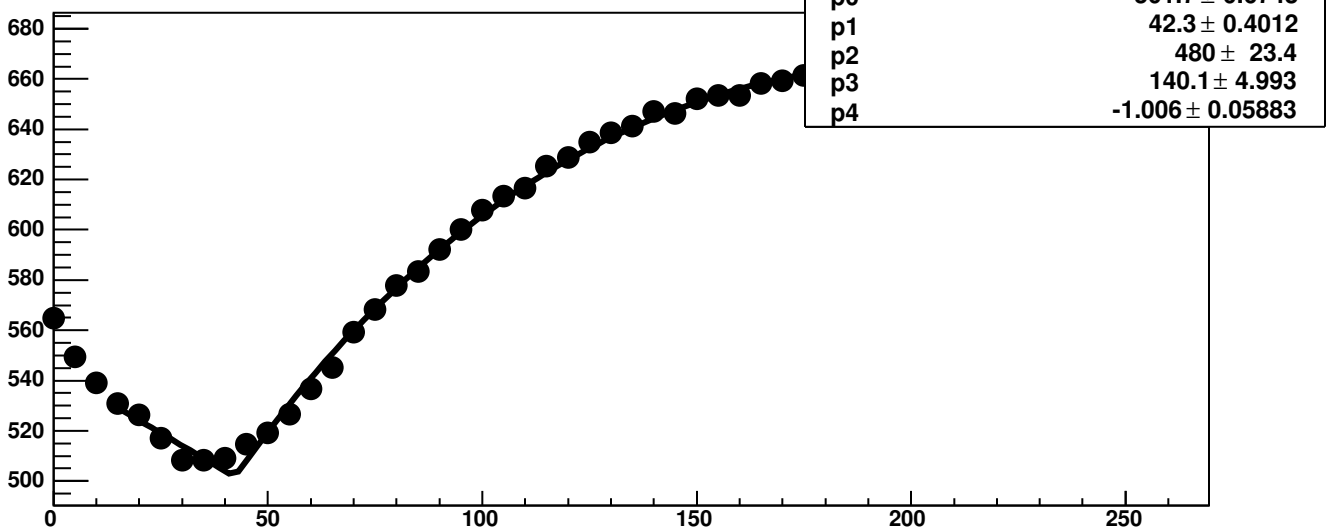
Chip 3, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



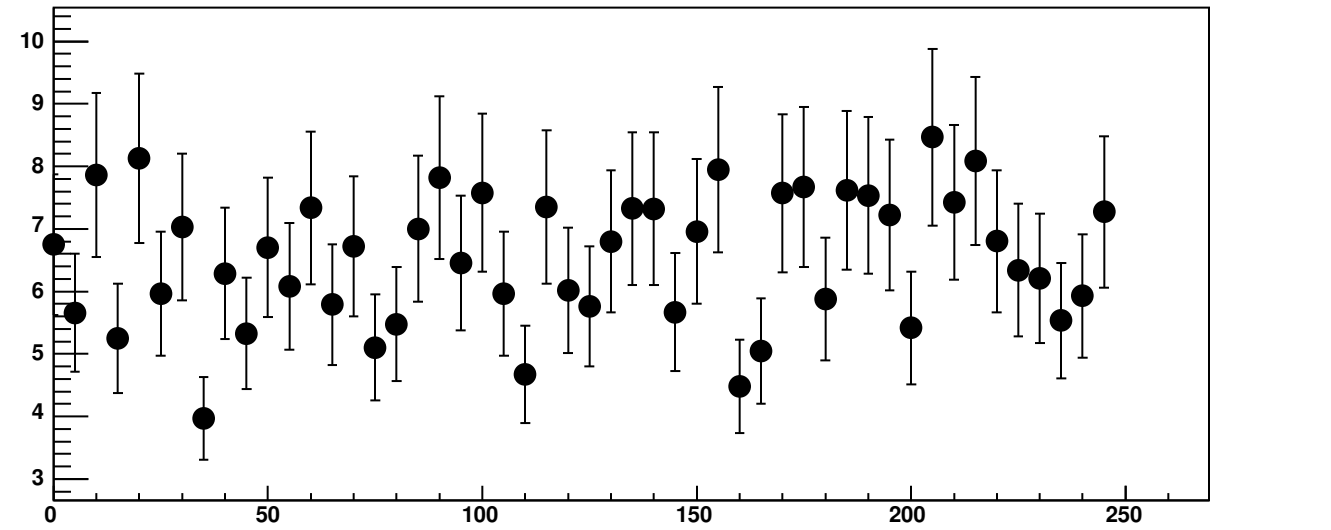
Chip 3, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold



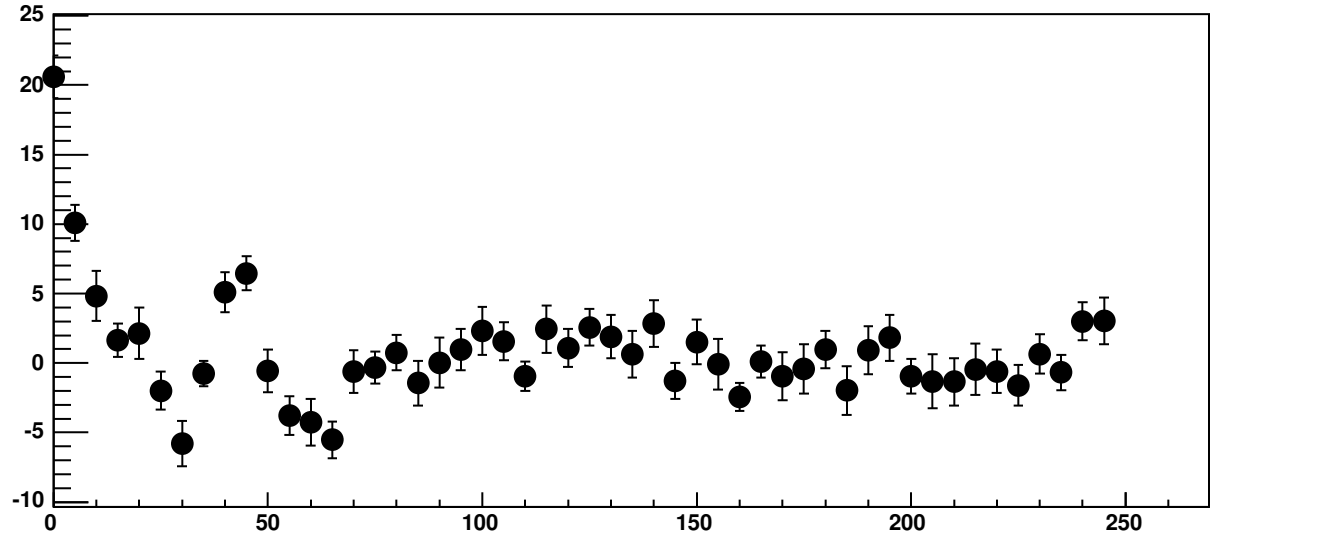
Chip 3, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold



Chip 3, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold

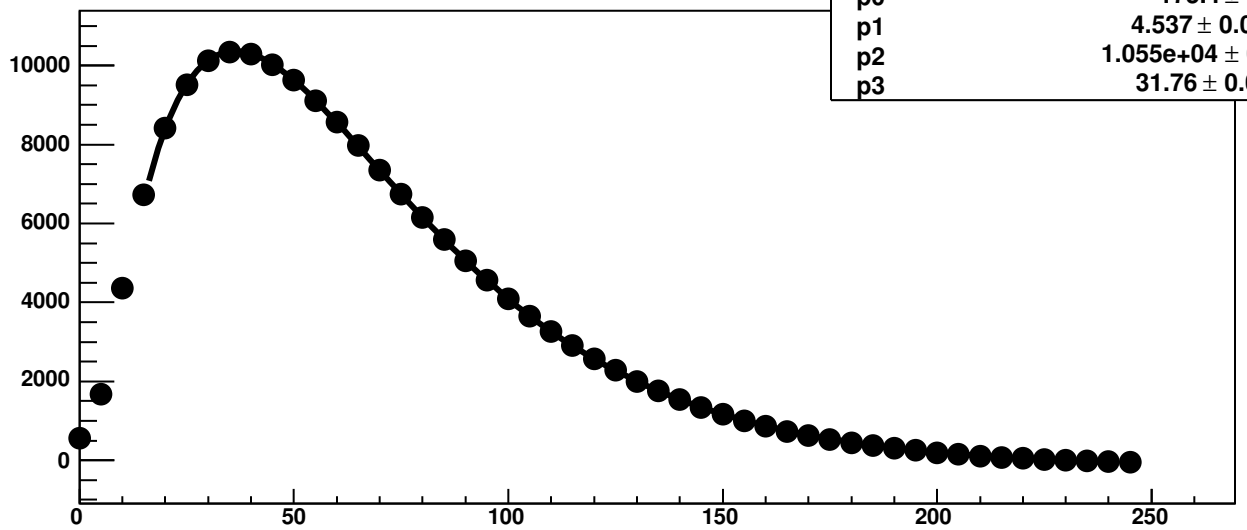


Chip 3, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

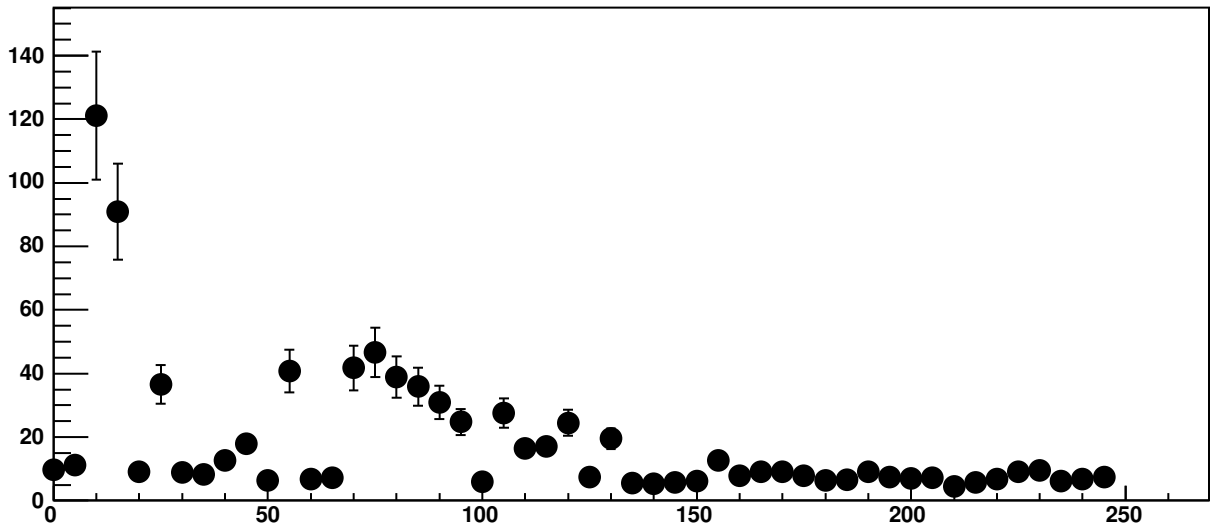


Chip 3, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold

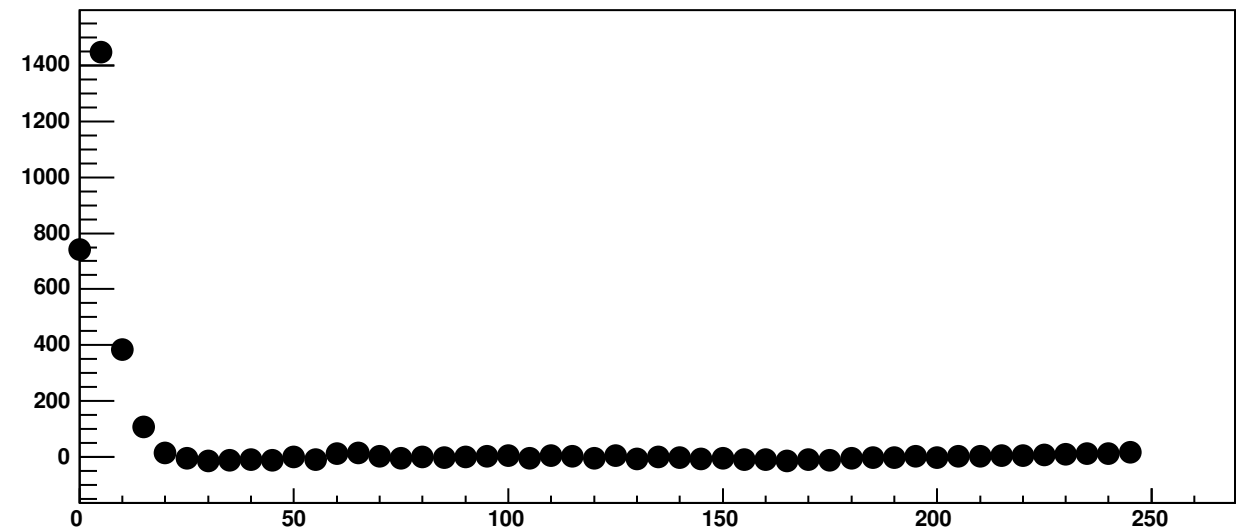
χ^2 / ndf	740.3 / 42
p0	-175.4 ± 0.5735
p1	4.537 ± 0.008624
p2	1.055e+04 ± 0.8966
p3	31.76 ± 0.005851



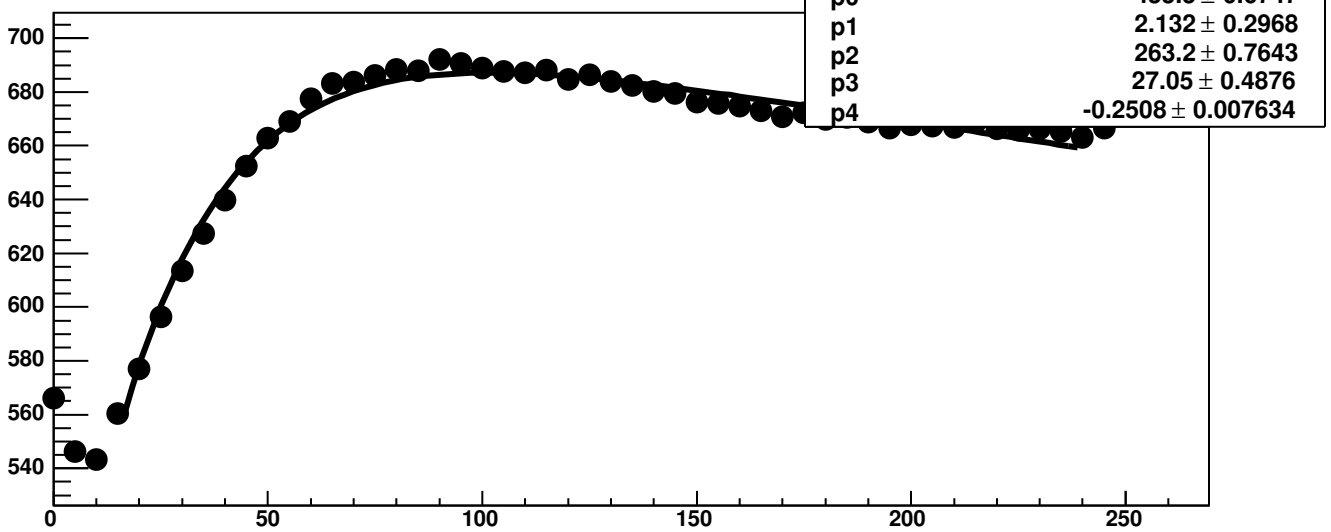
Chip 3, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold



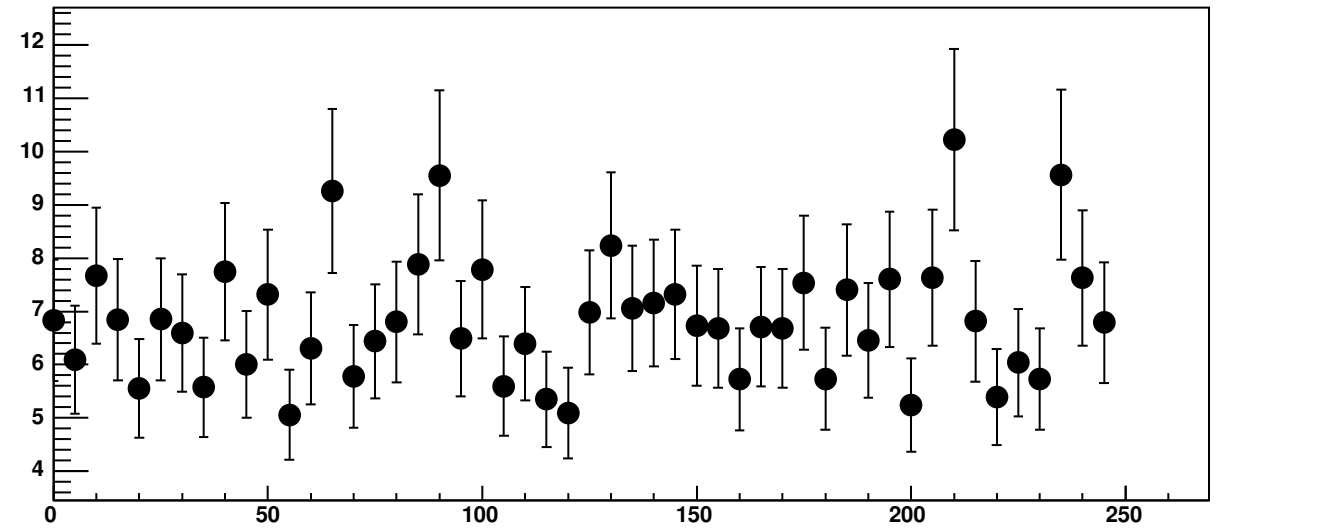
Chip 3, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold



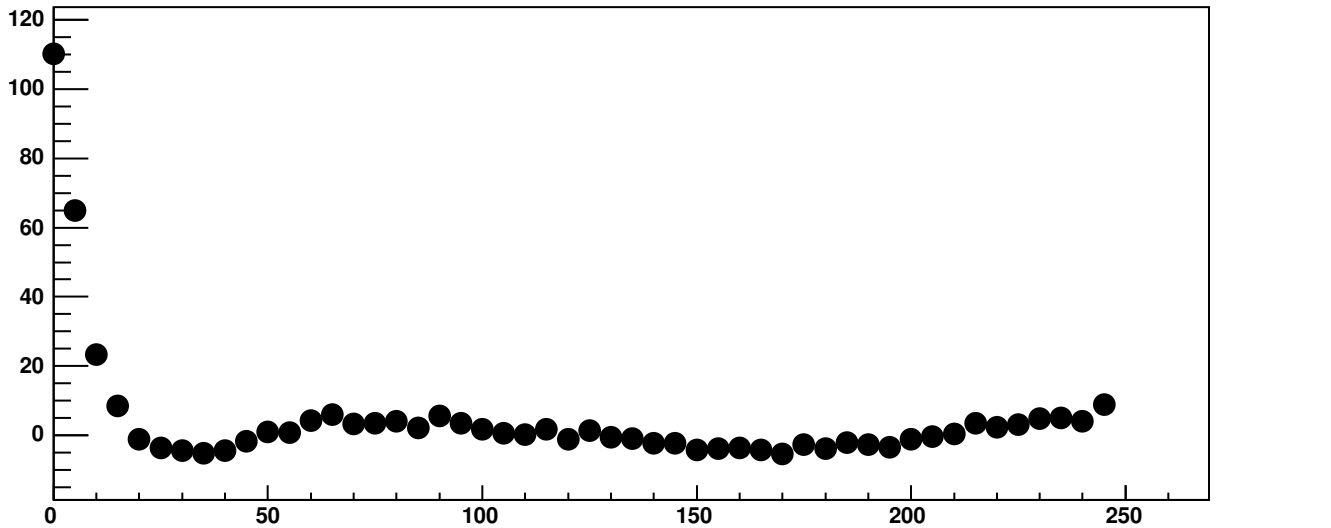
Chip 3, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold



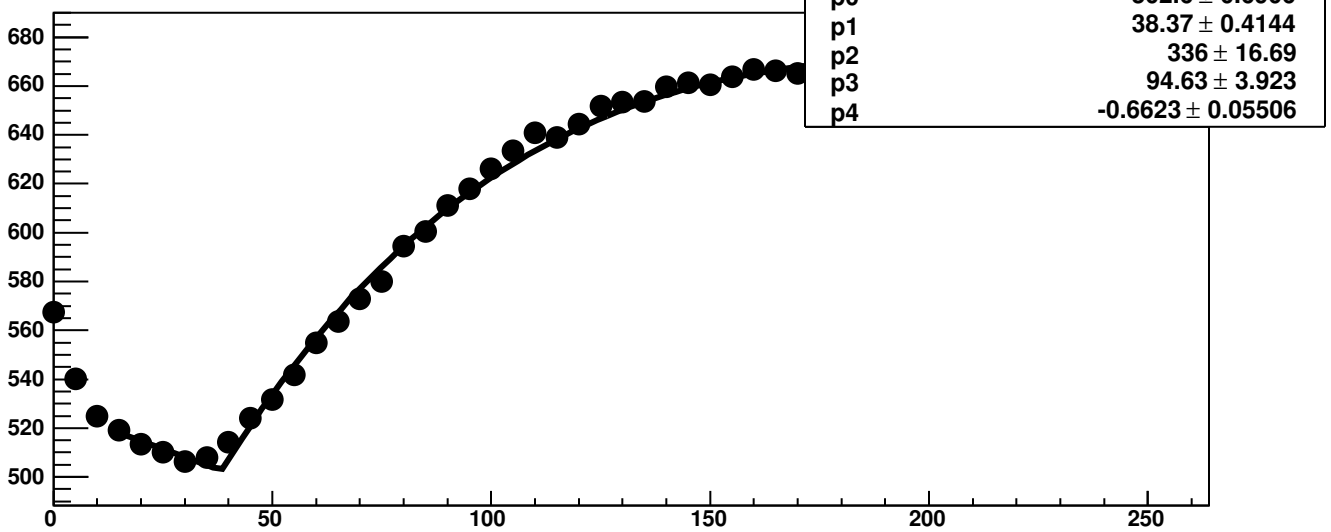
Chip 3, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold



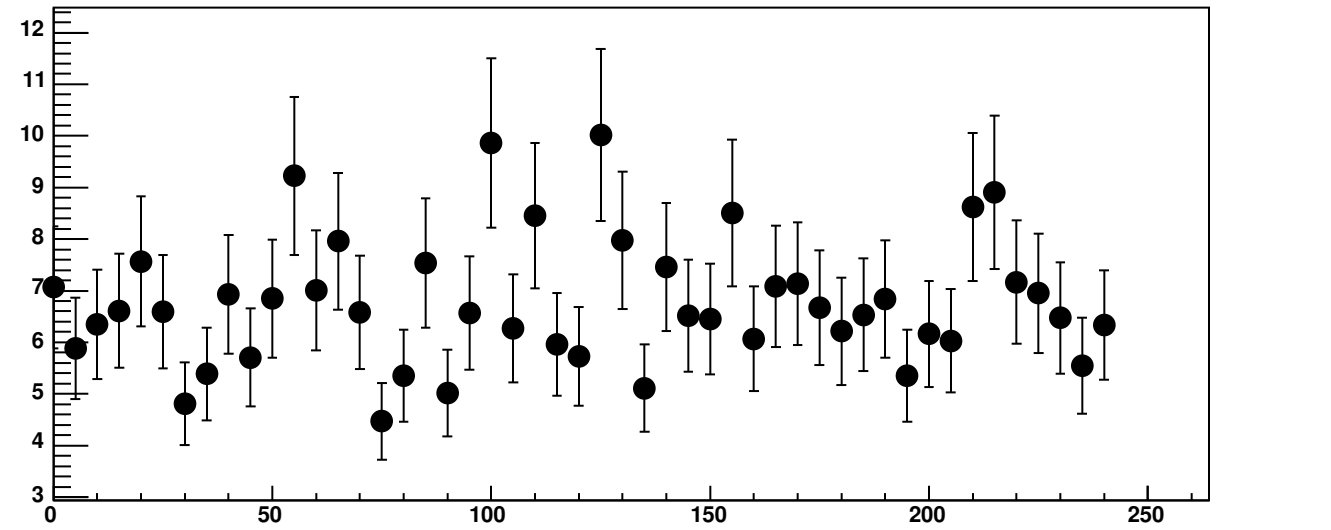
Chip 3, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold



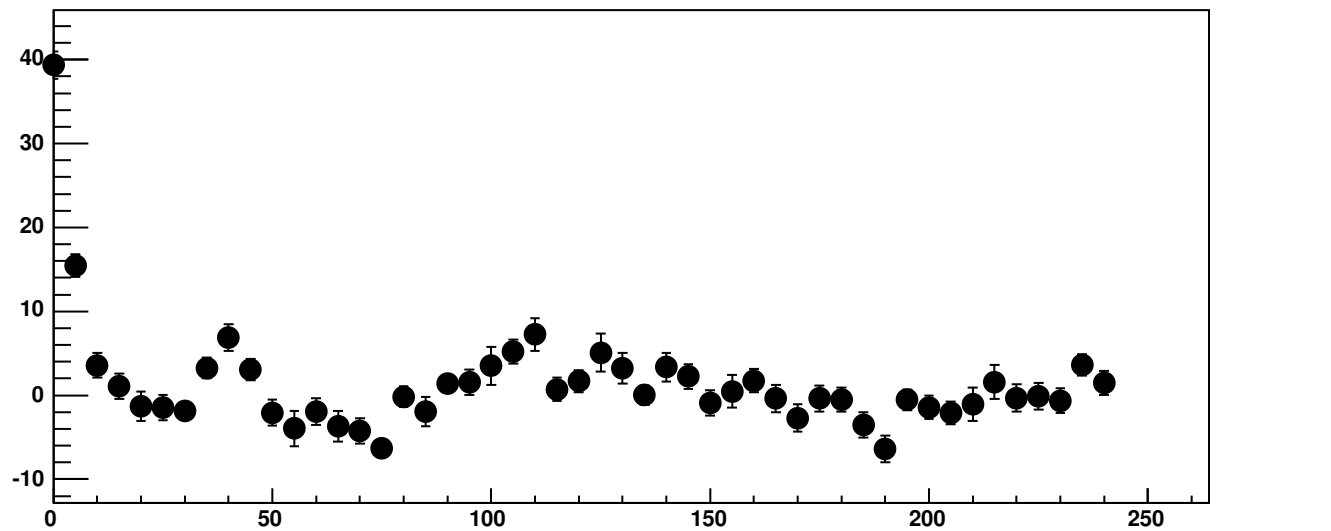
Chip 3, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold

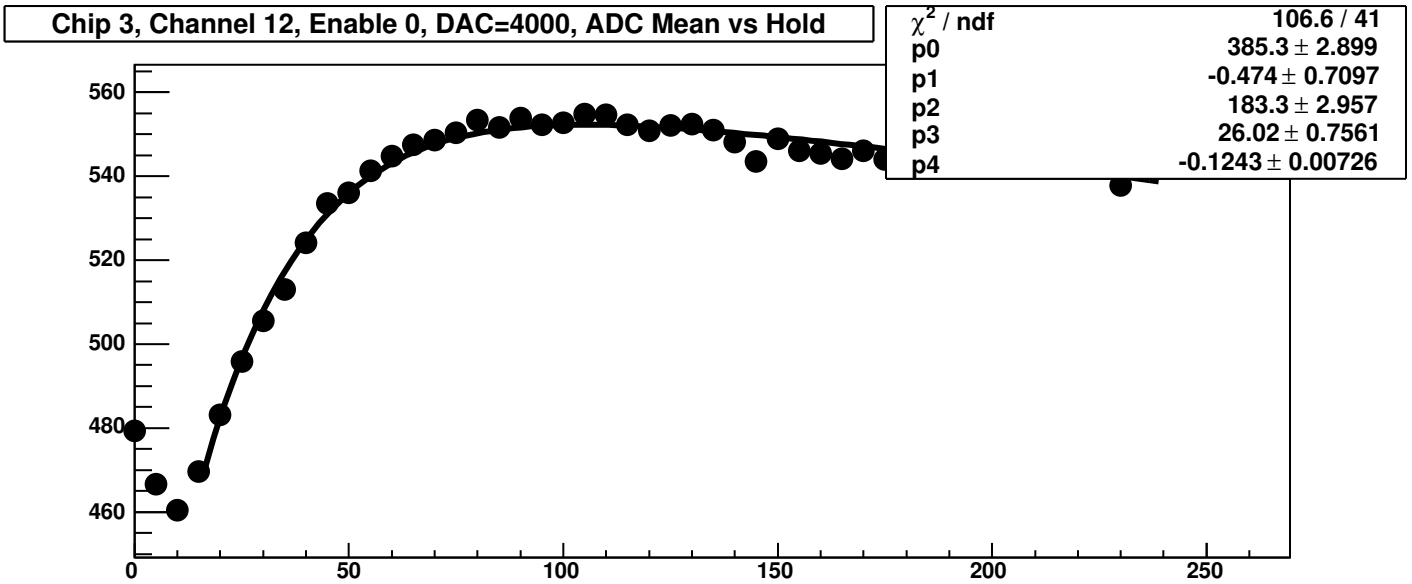


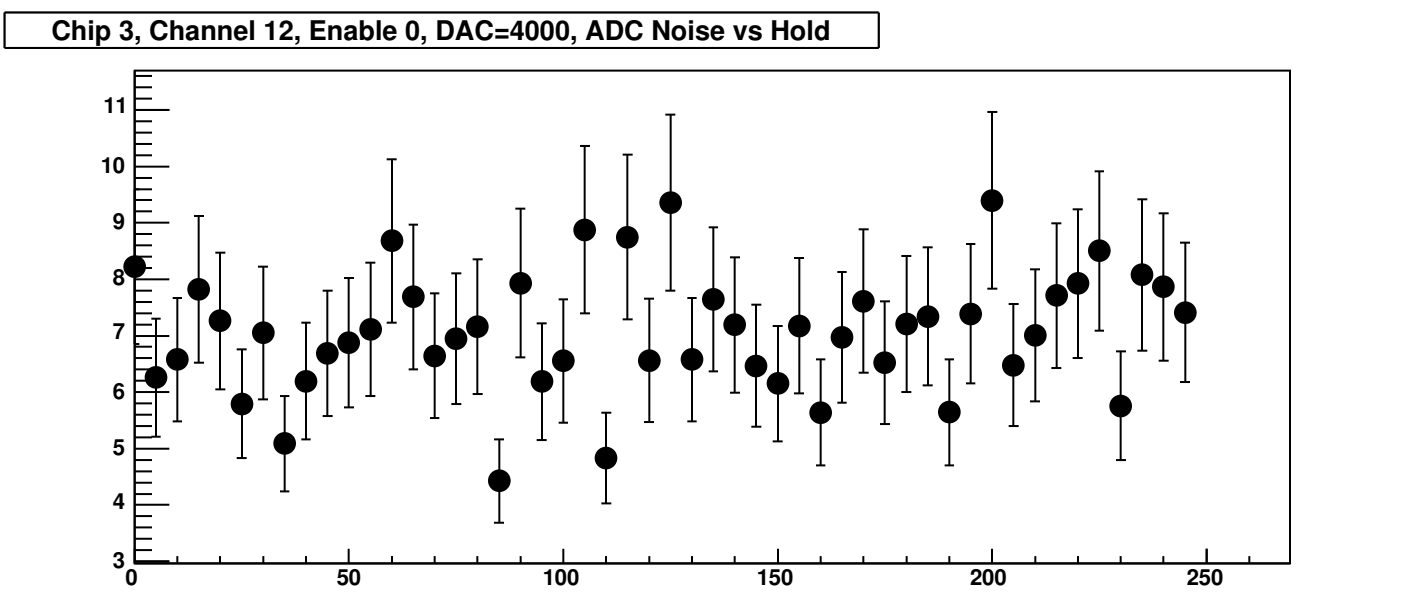
Chip 3, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold

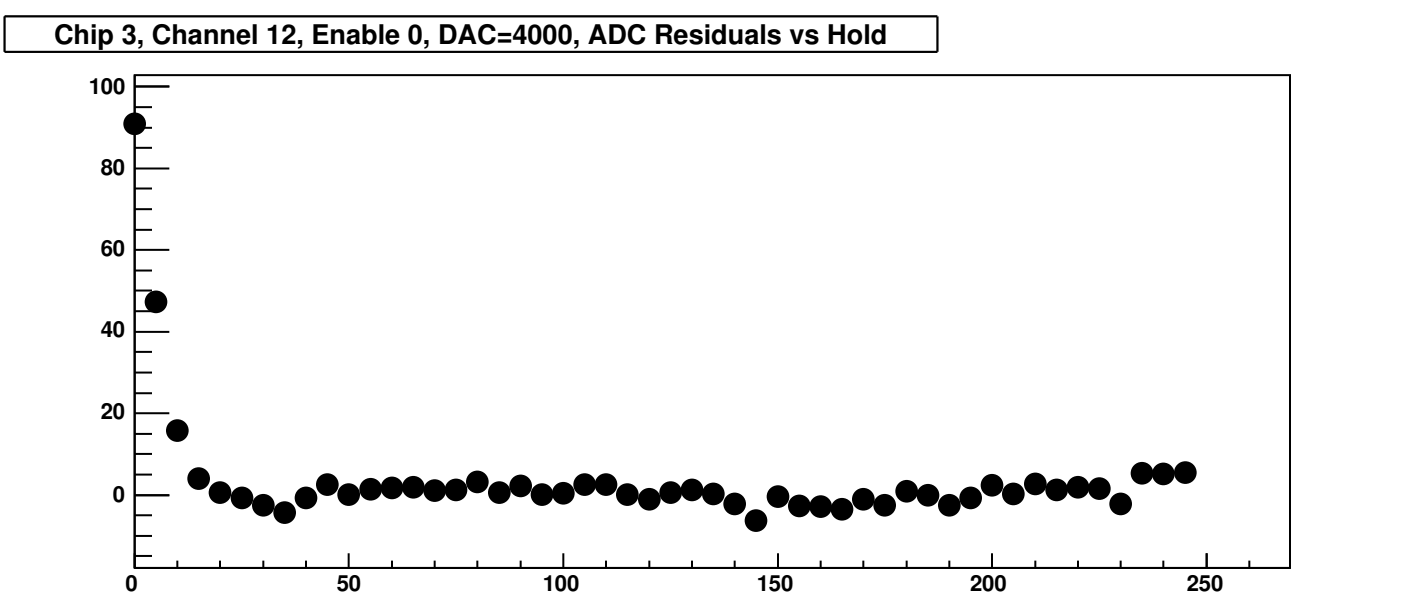


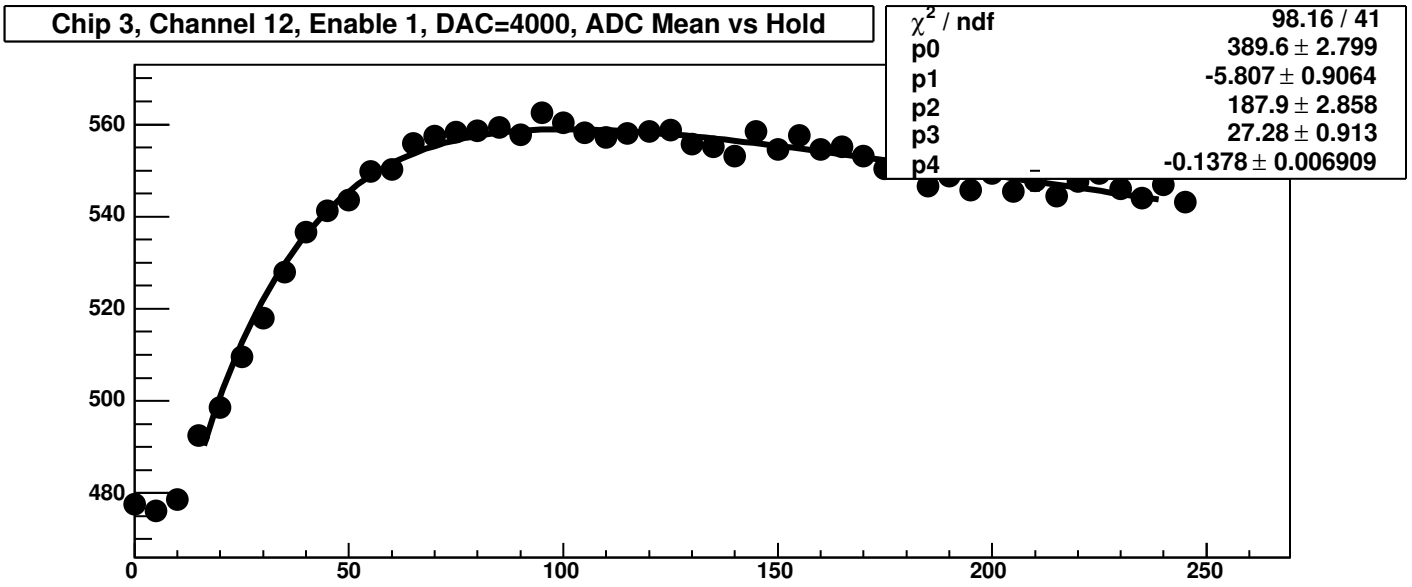
Chip 3, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold

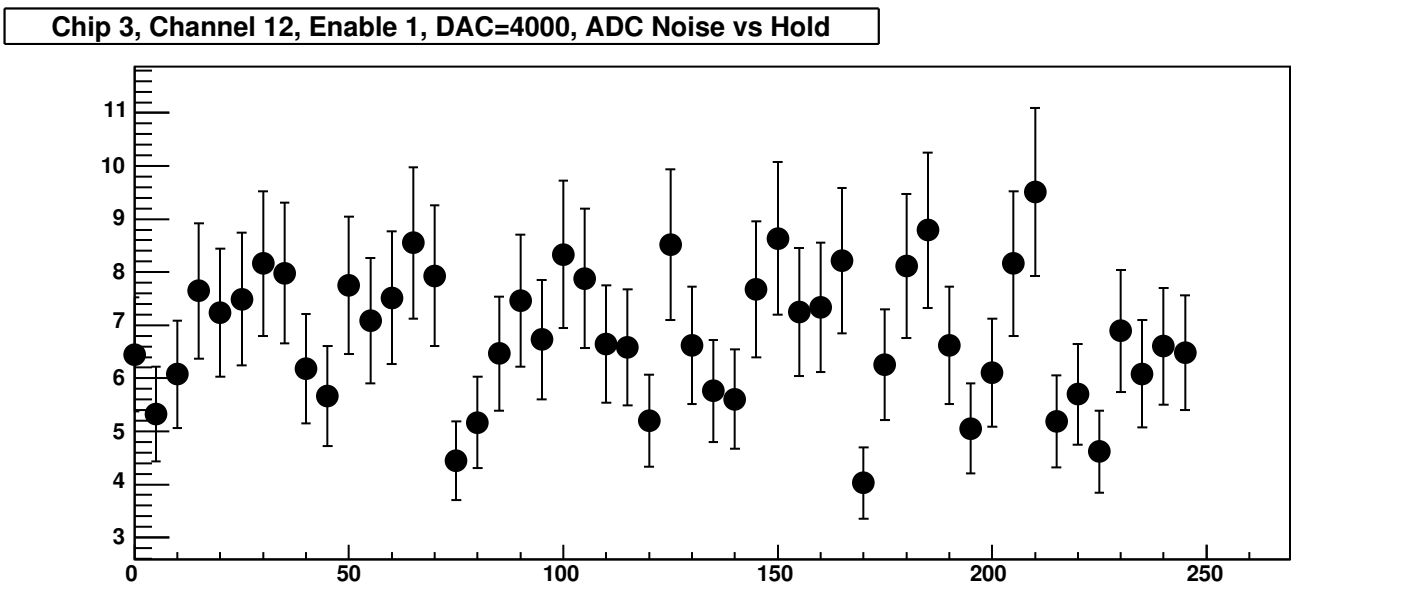


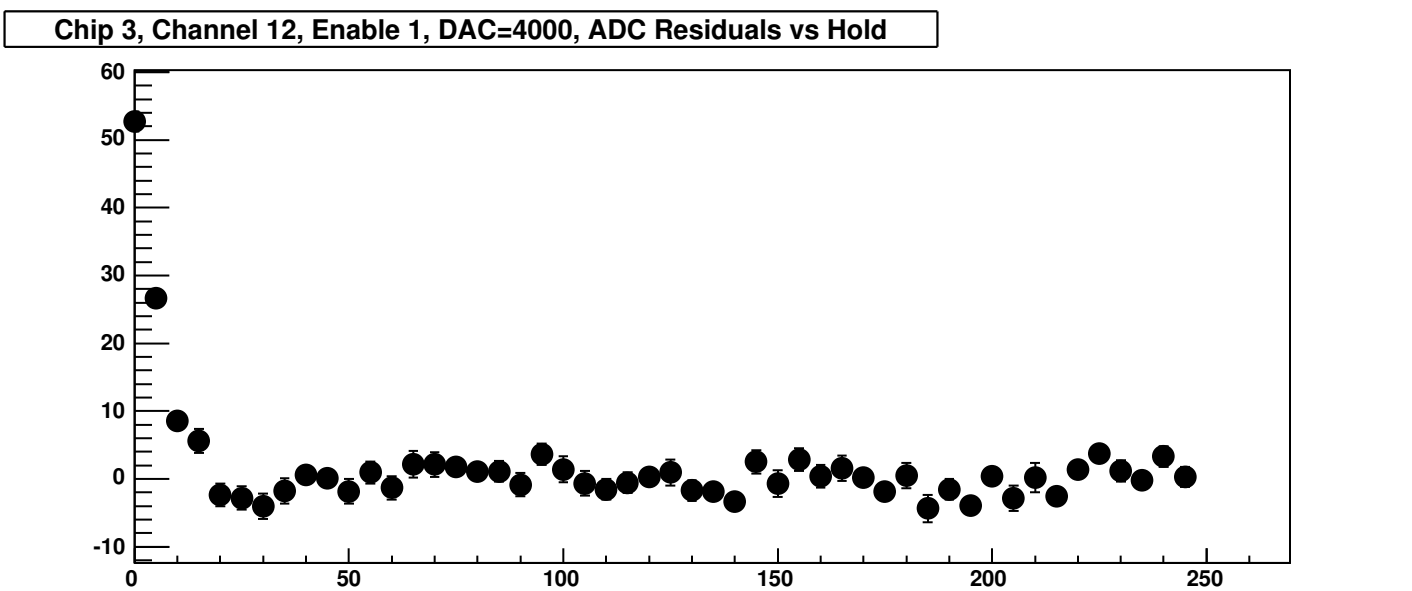

Chip 3, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 106.6 / 41
p0 385.3 ± 2.899
p1 -0.474 ± 0.7097
p2 183.3 ± 2.957
p3 26.02 ± 0.7561
p4 -0.1243 ± 0.00726
560
540
520
500
480
460
0
50
100
150
200
250

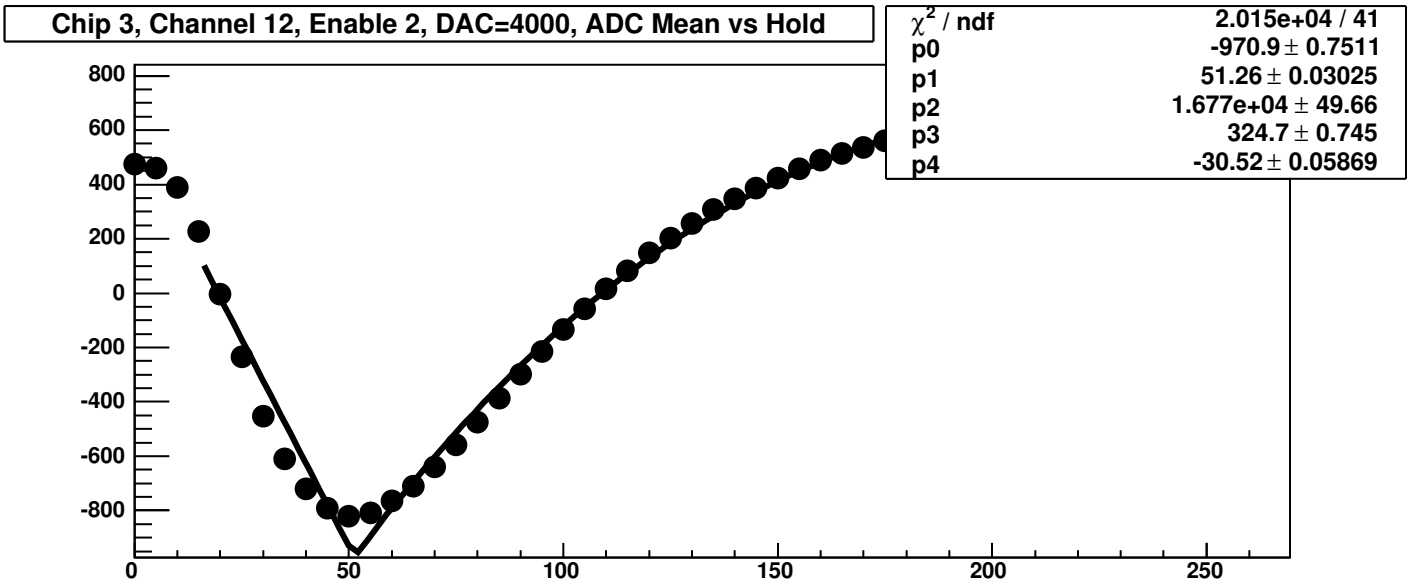

Chip 3, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

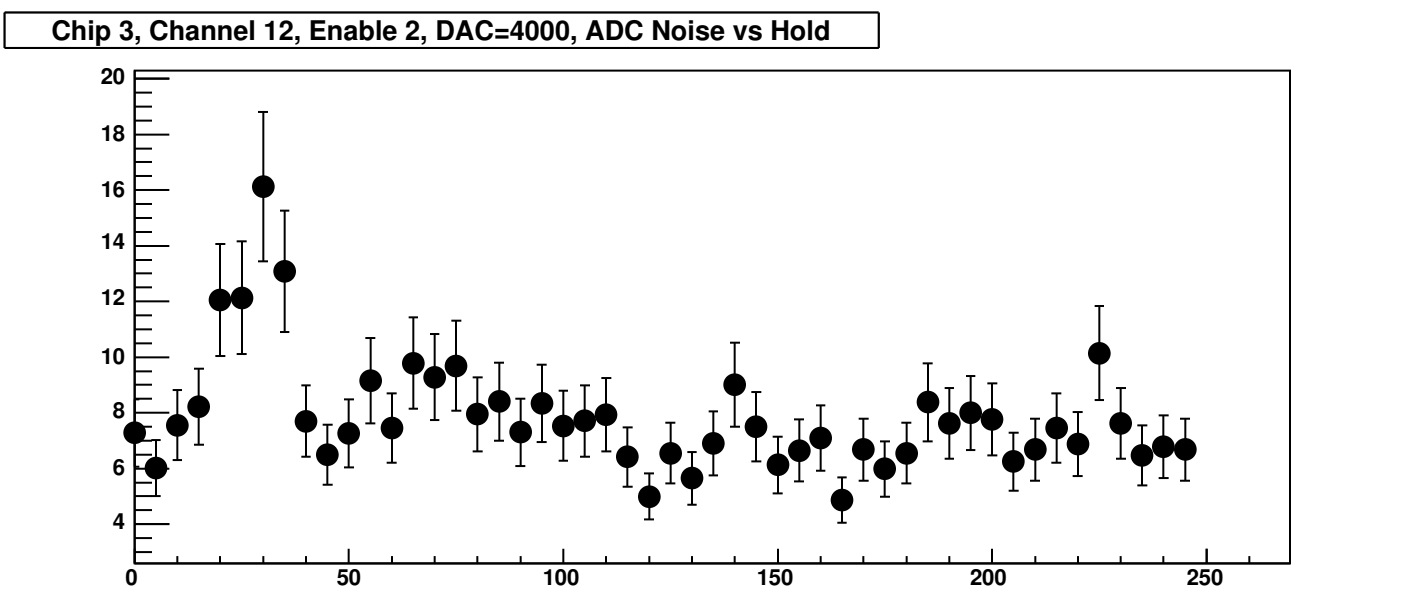

Chip 3, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold
100
80
60
40
20
0
0
50
100
150
200
250

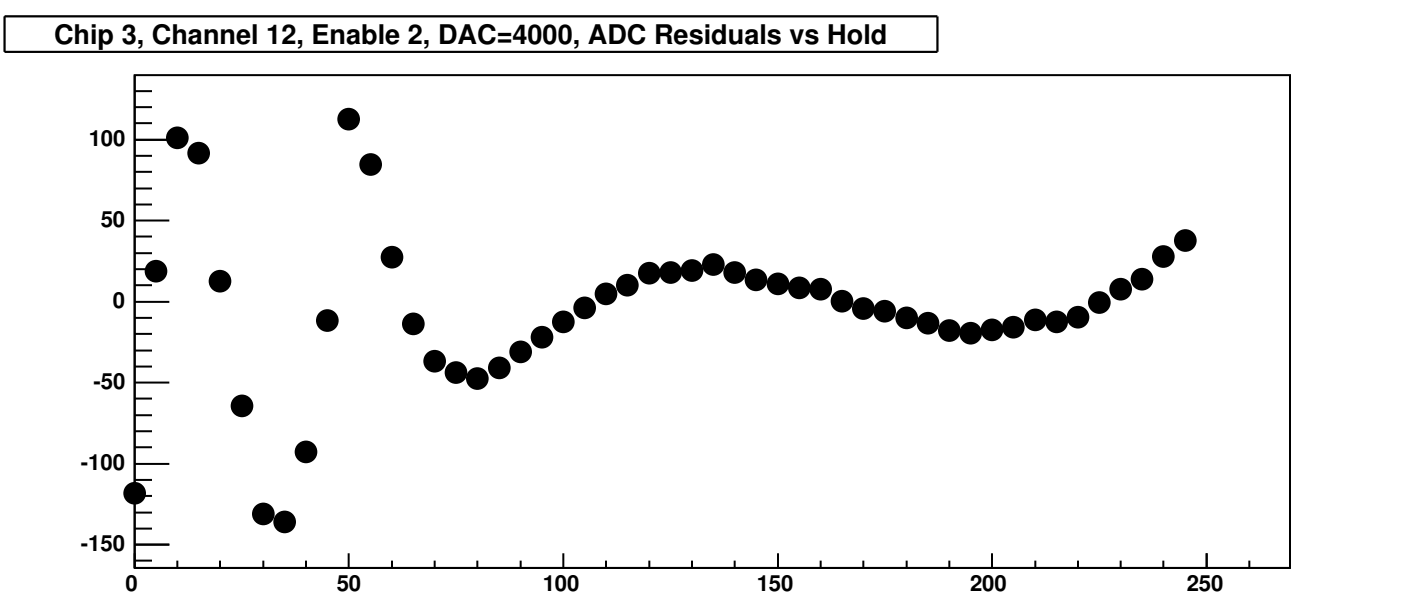

Chip 3, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 98.16 / 41
p0 389.6 ± 2.799
p1 -5.807 ± 0.9064
p2 187.9 ± 2.858
p3 27.28 ± 0.913
p4 -0.1378 ± 0.006909
560
540
520
500
480
0
50
100
150
200
250


Chip 3, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

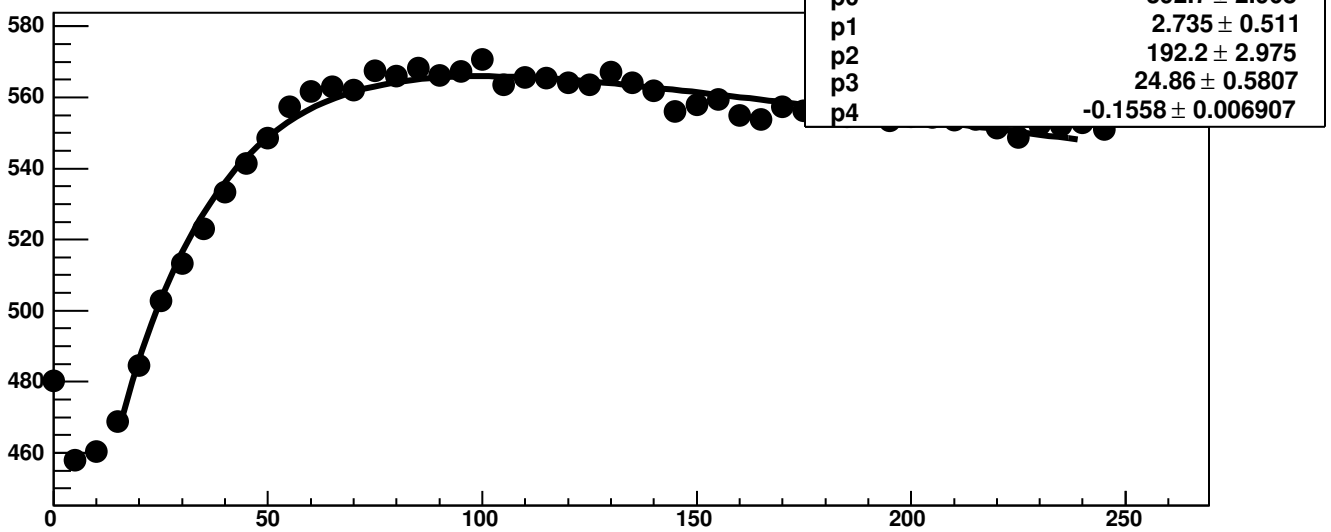

Chip 3, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold
60
50
40
30
20
10
0
-10
0
50
100
150
200
250


Chip 3, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 2.015e+04 / 41
p0 -970.9 ± 0.7511
p1 51.26 ± 0.03025
p2 1.677e+04 ± 49.66
p3 324.7 ± 0.745
p4 -30.52 ± 0.05869
800
600
400
200
0
-200
-400
-600
-800
0
50
100
150
200
250

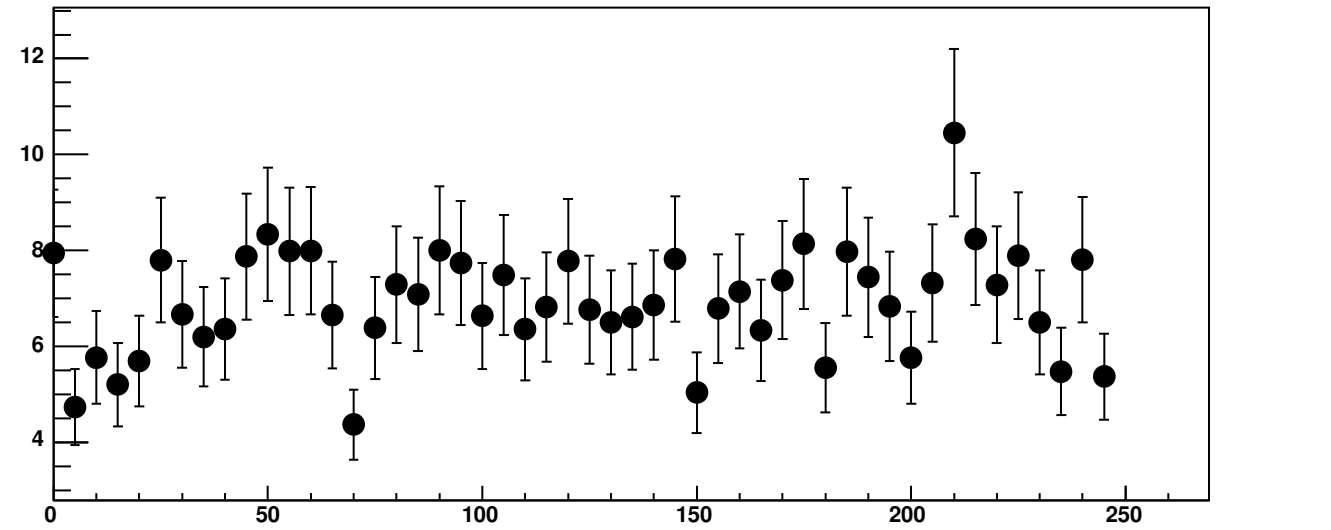

Chip 3, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold
20
18
16
14
12
10
8
6
4
0
50
100
150
200
250


Chip 3, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold
100
50
0
-50
-100
-150
0
50
100
150
200
250

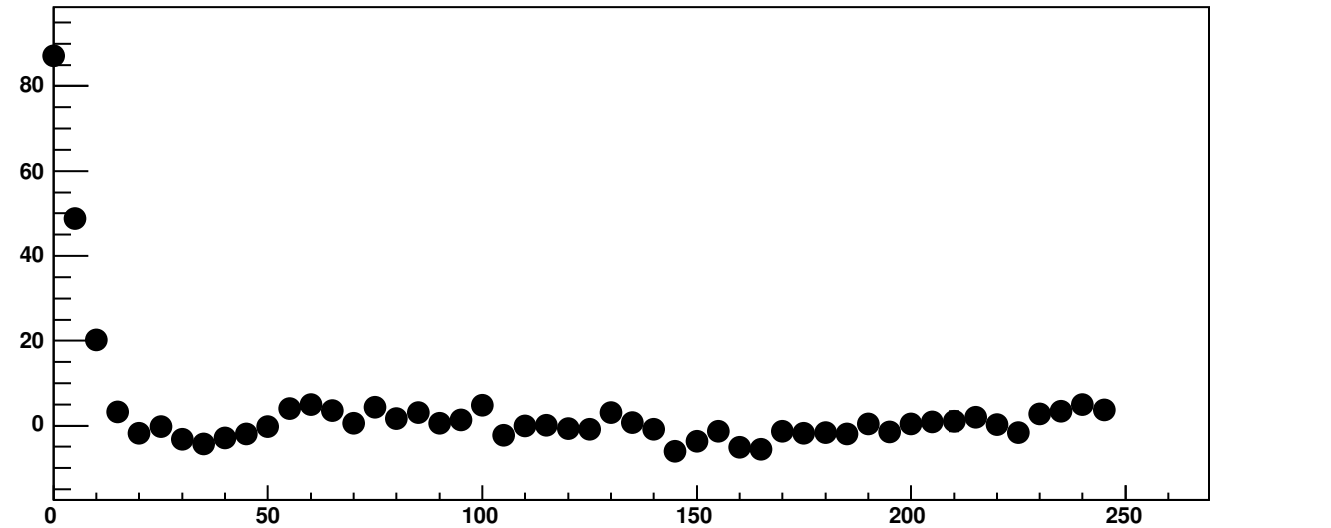
Chip 3, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold



Chip 3, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold

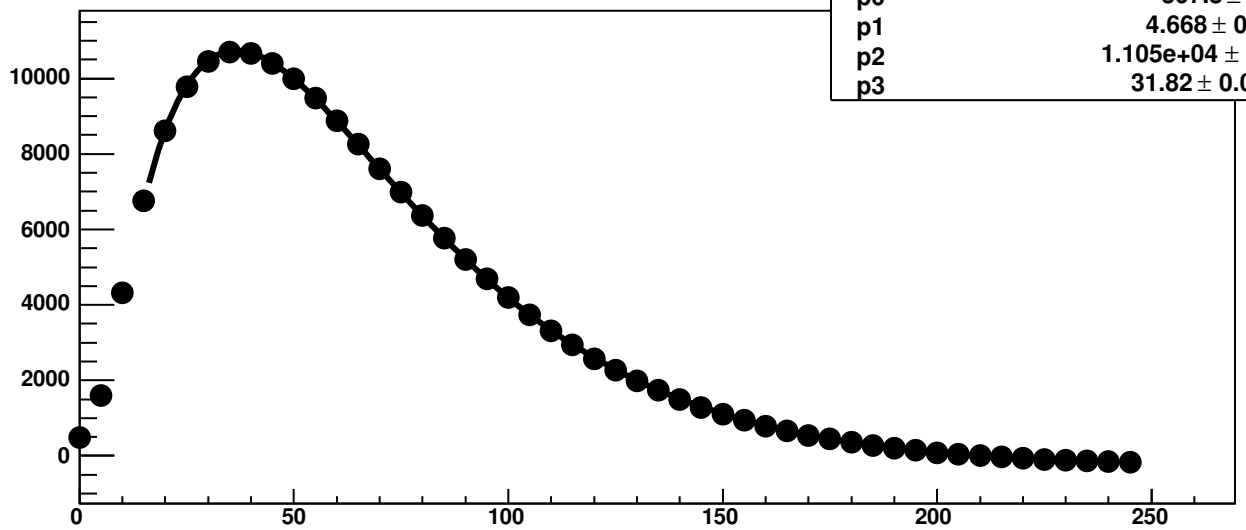


Chip 3, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

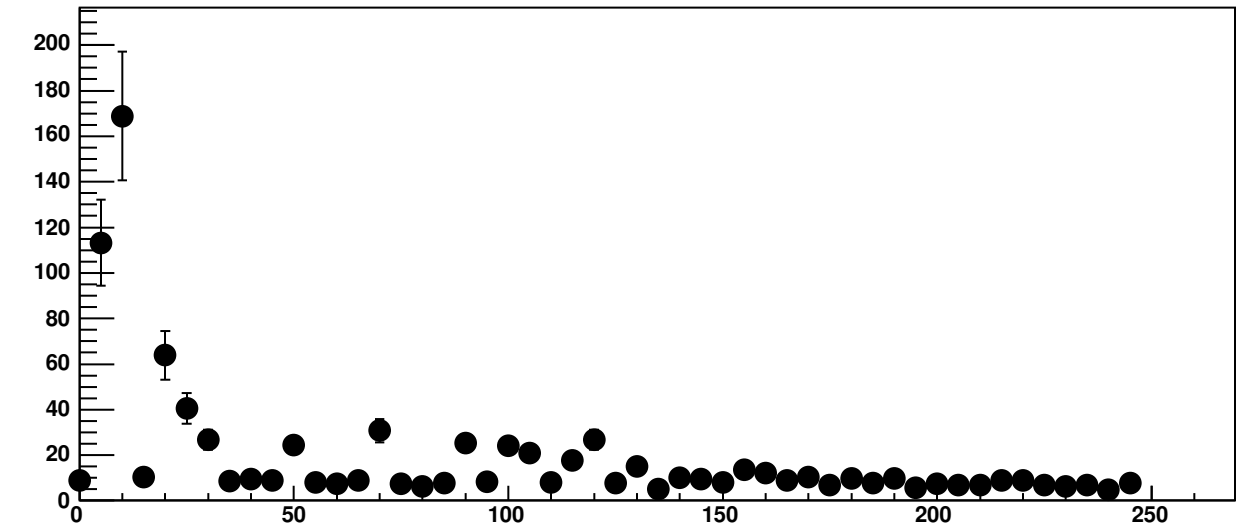


Chip 3, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold

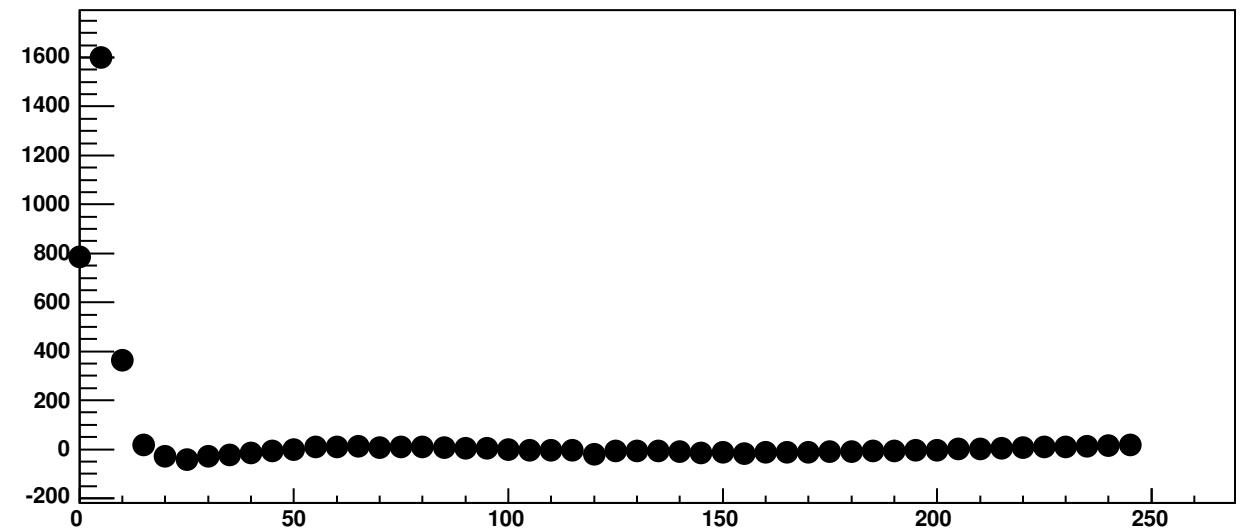
χ^2 / ndf	1268 / 42
p0	-307.8 ± 0.5408
p1	4.668 ± 0.00607
p2	1.105e+04 ± 0.9664
p3	31.82 ± 0.004738

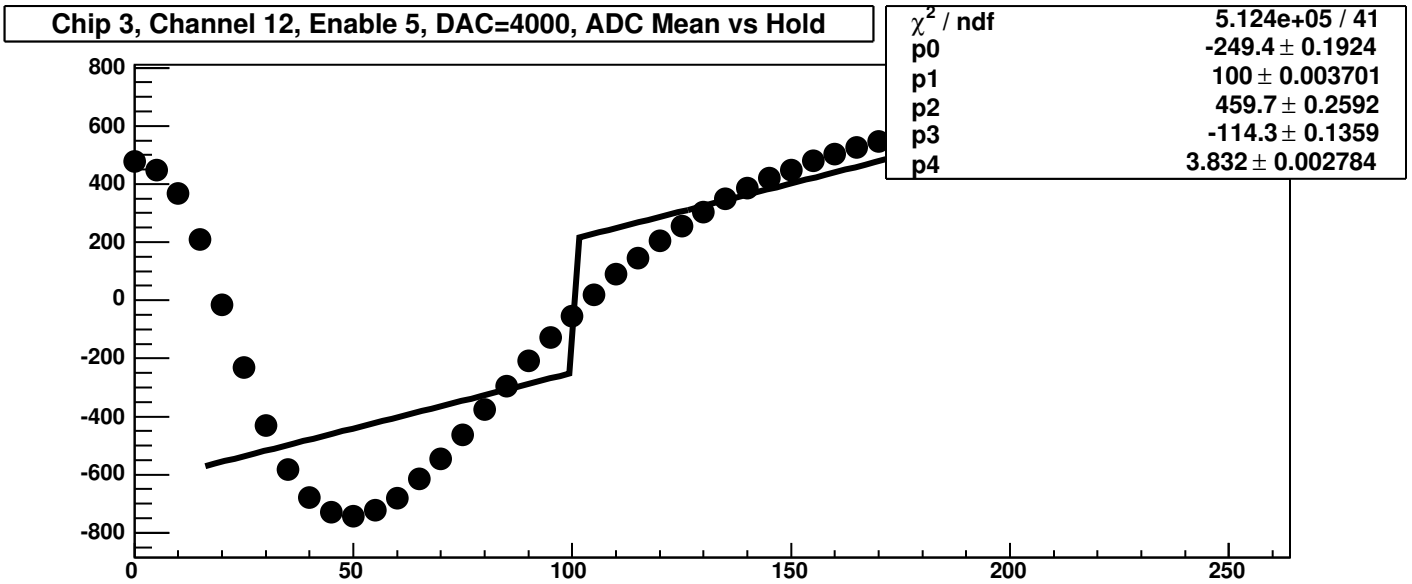


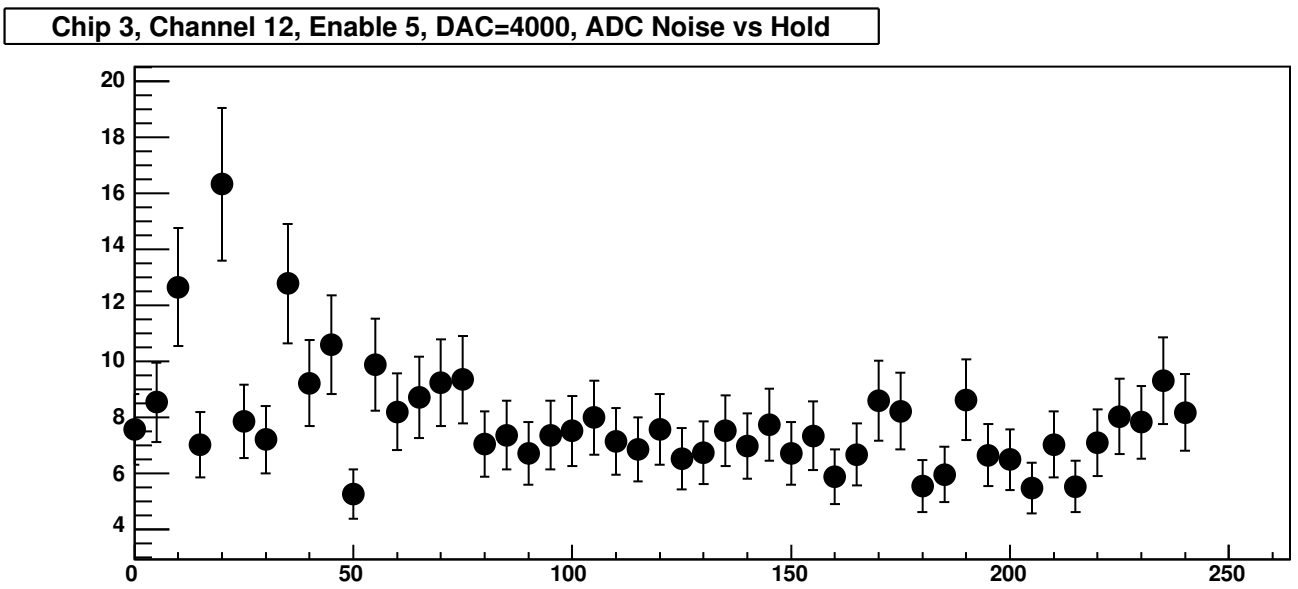
Chip 3, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold

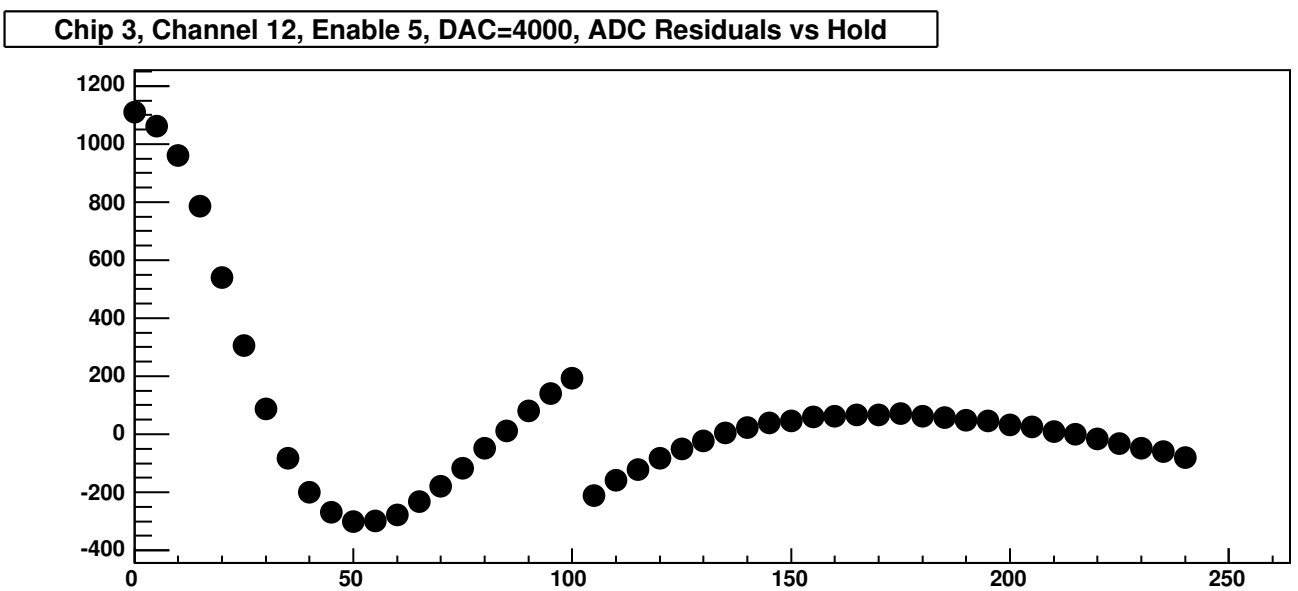


Chip 3, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold

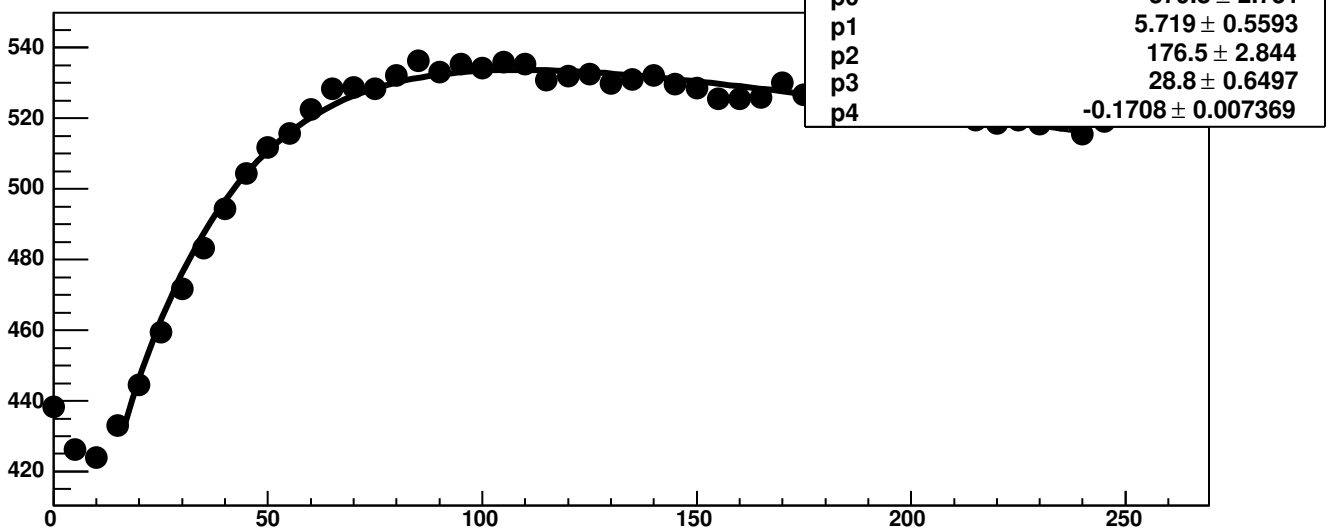



Chip 3, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 5.124e+05 / 41
p0 -249.4 ± 0.1924
p1 100 ± 0.003701
p2 459.7 ± 0.2592
p3 -114.3 ± 0.1359
p4 3.832 ± 0.002784
800
600
400
200
0
-200
-400
-600
-800
0
50
100
150
200
250

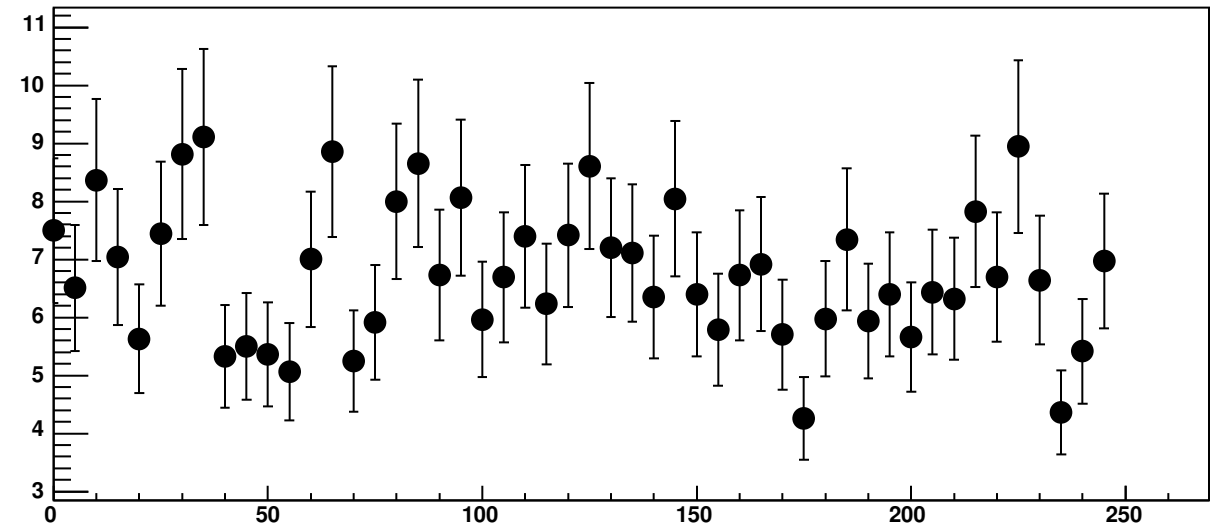

Chip 3, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold
20
18
16
14
12
10
8
6
4
0
50
100
150
200
250


Chip 3, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold
1200
1000
800
600
400
200
0
-200
-400
0
50
100
150
200
250

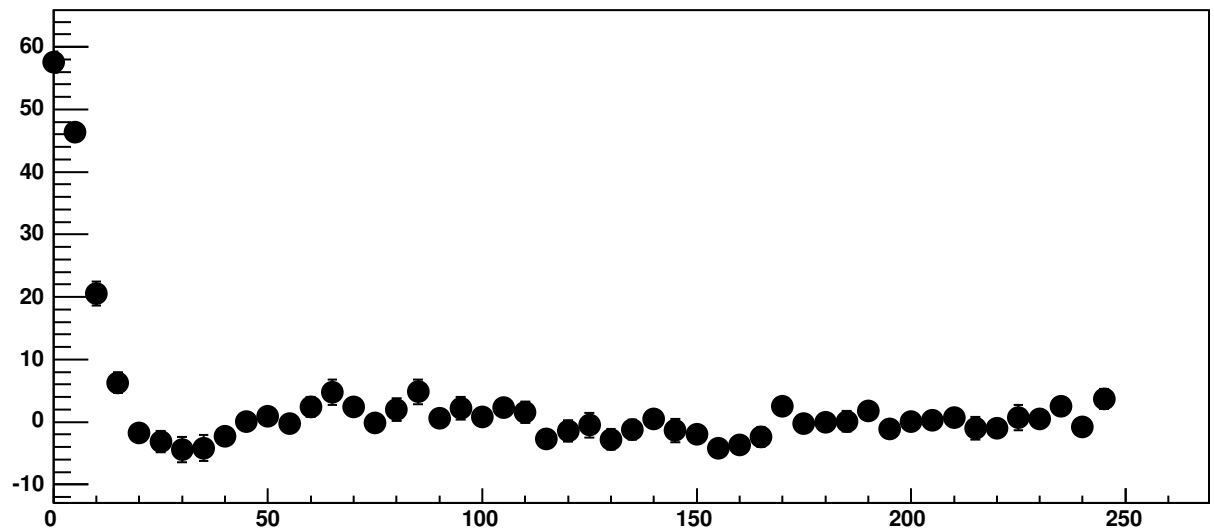
Chip 3, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold



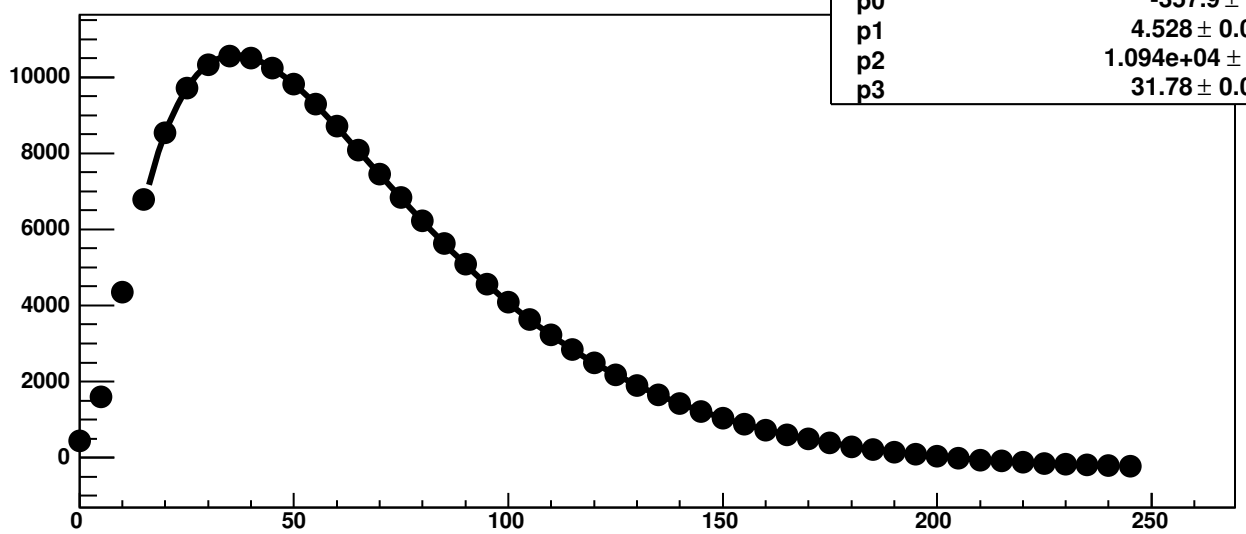
Chip 3, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold



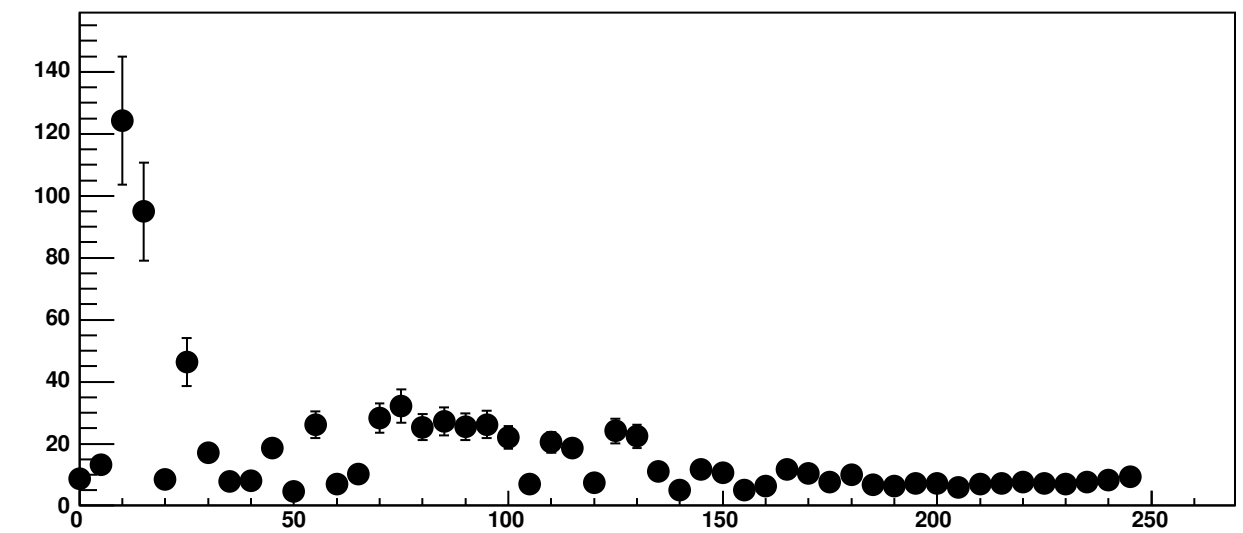
Chip 3, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold



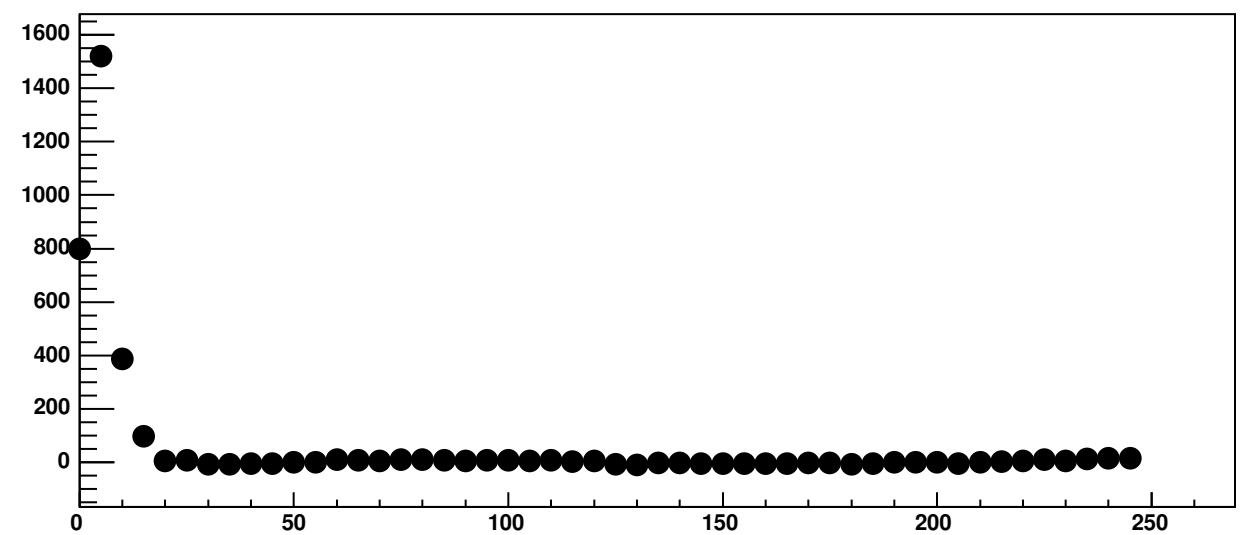
Chip 3, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold



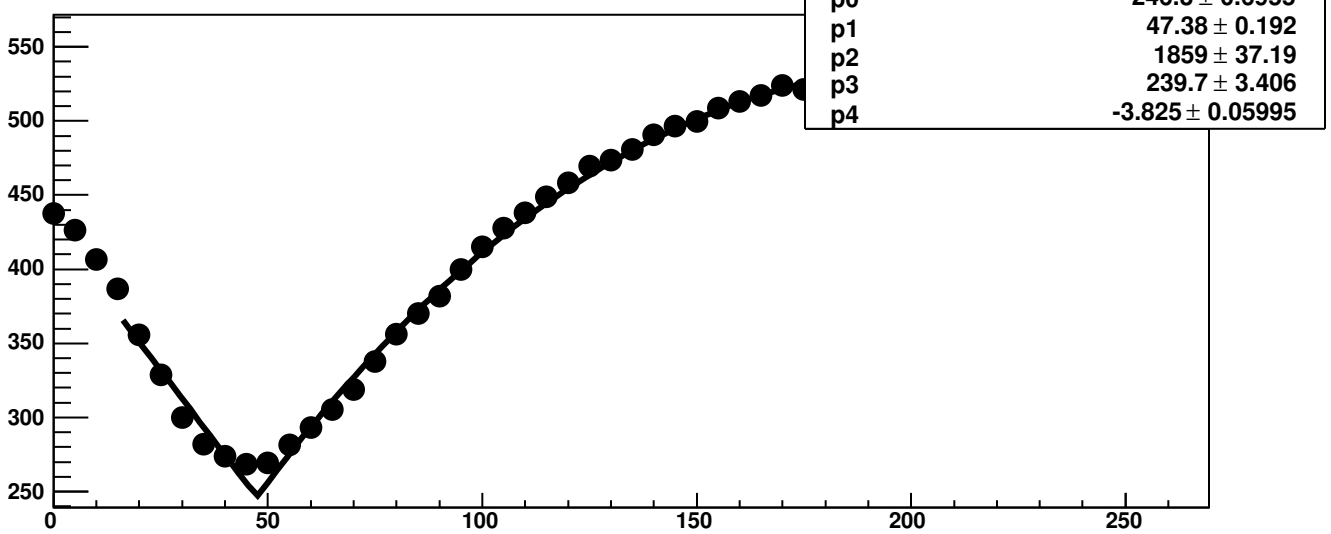
Chip 3, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold



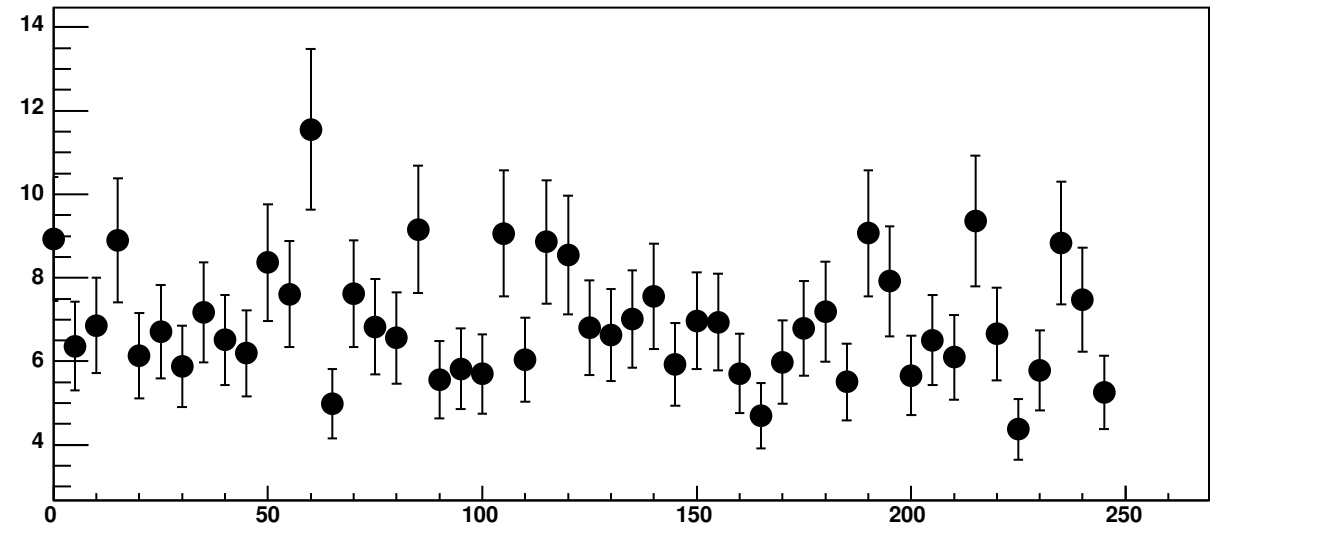
Chip 3, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold



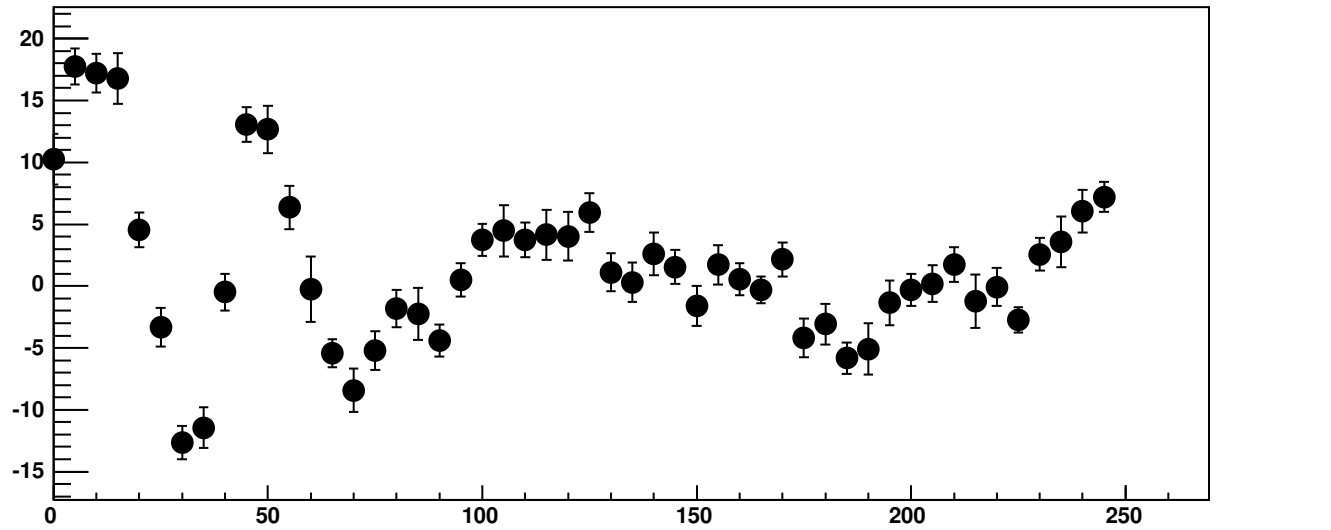
Chip 3, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold

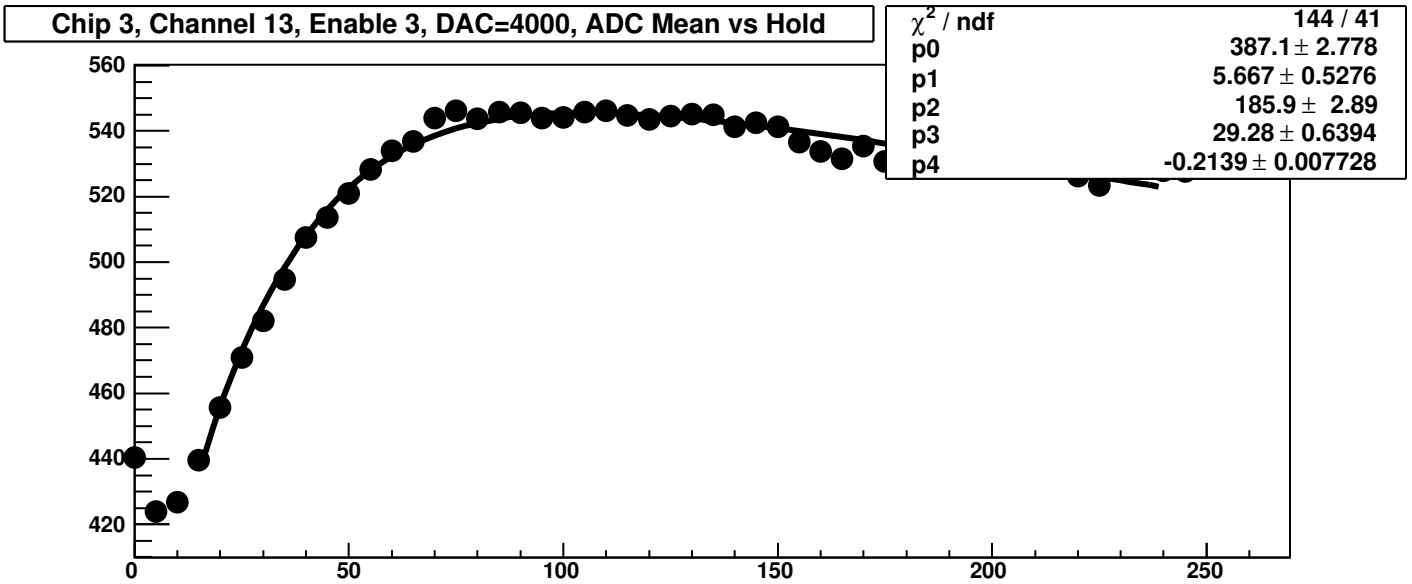


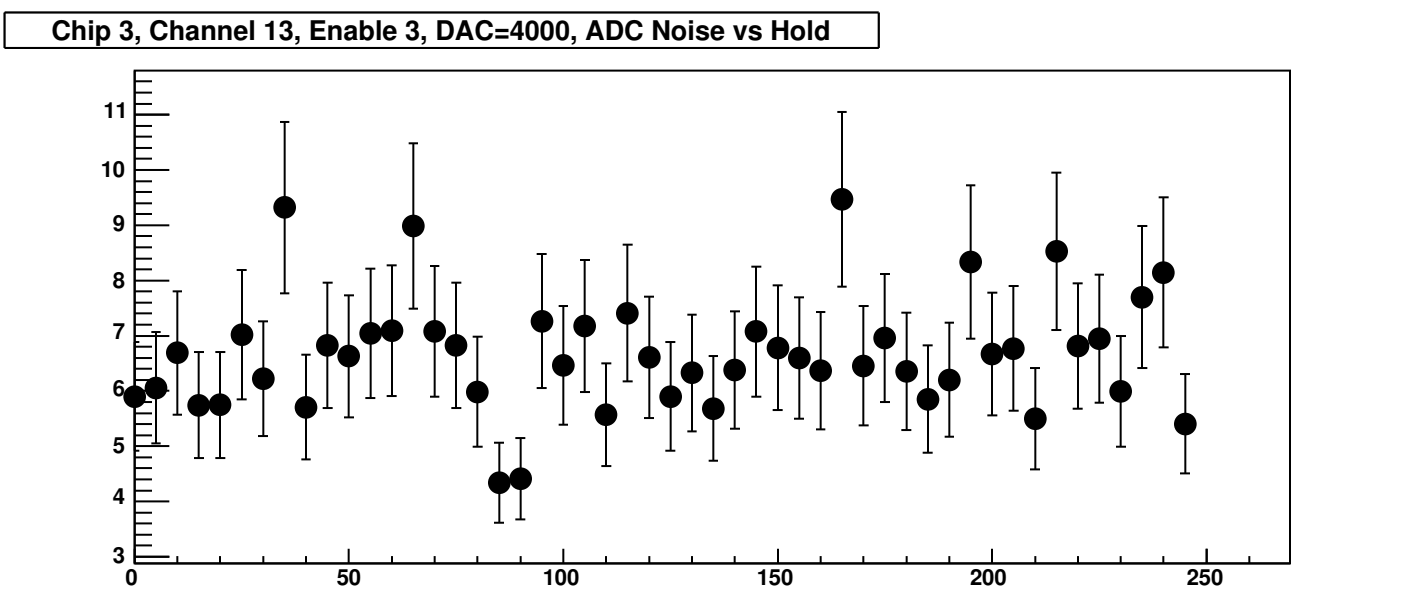
Chip 3, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold

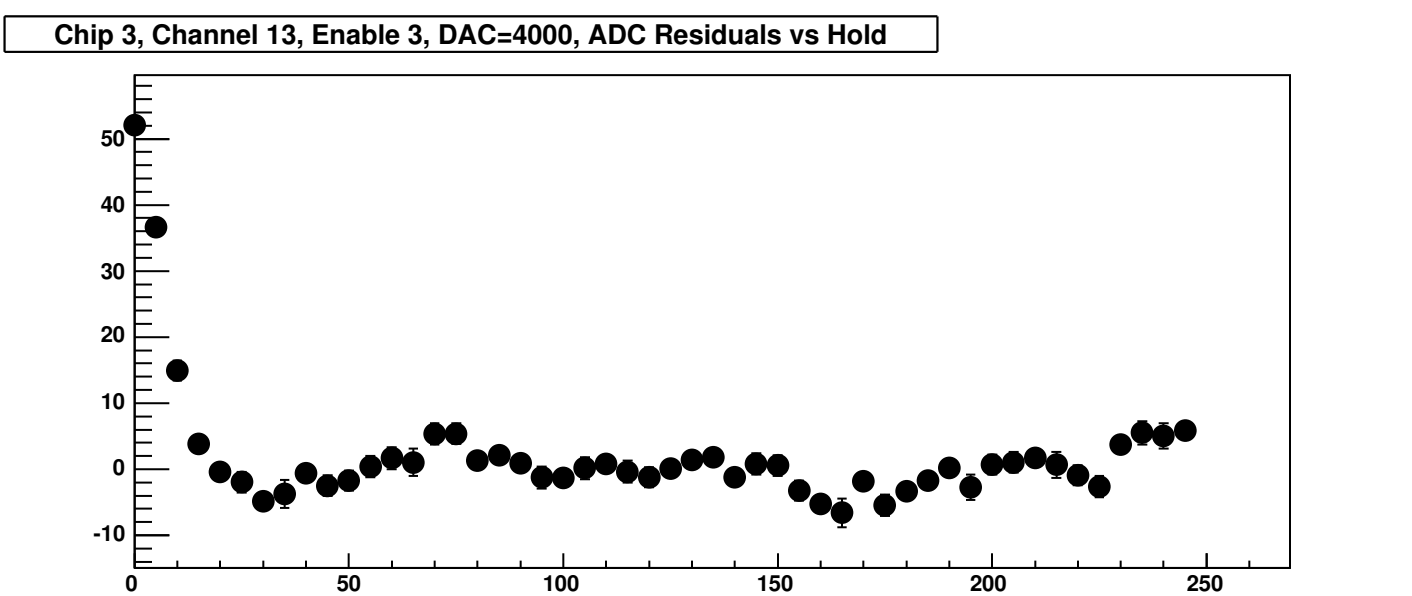


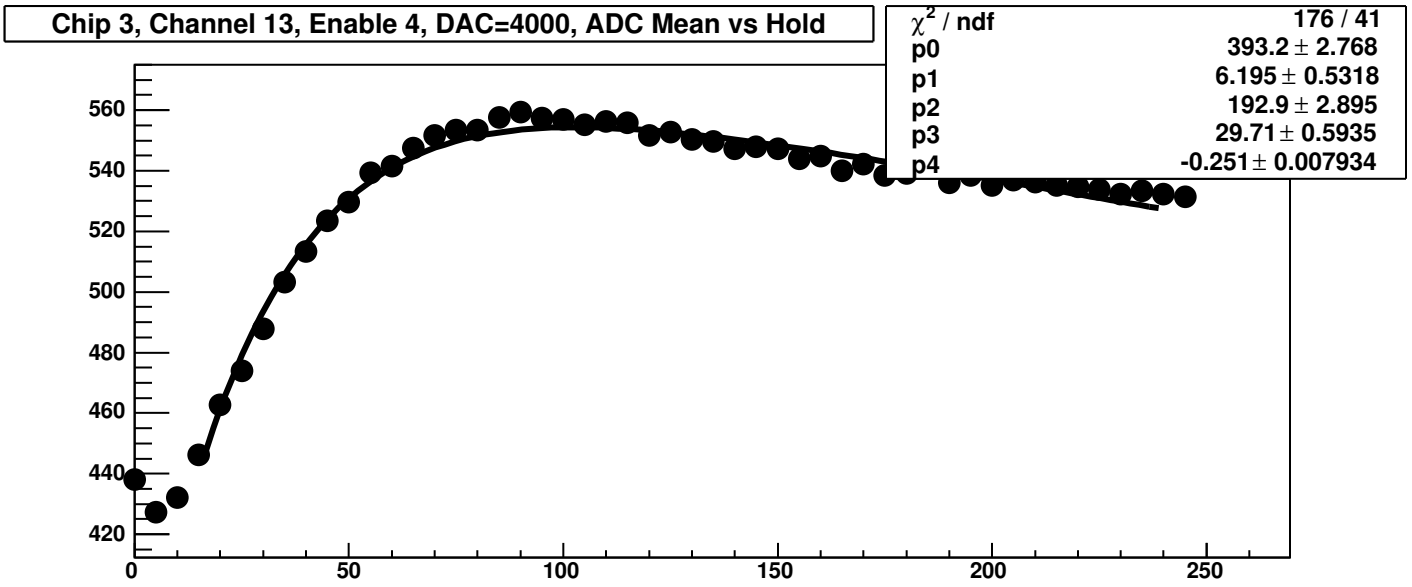
Chip 3, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold

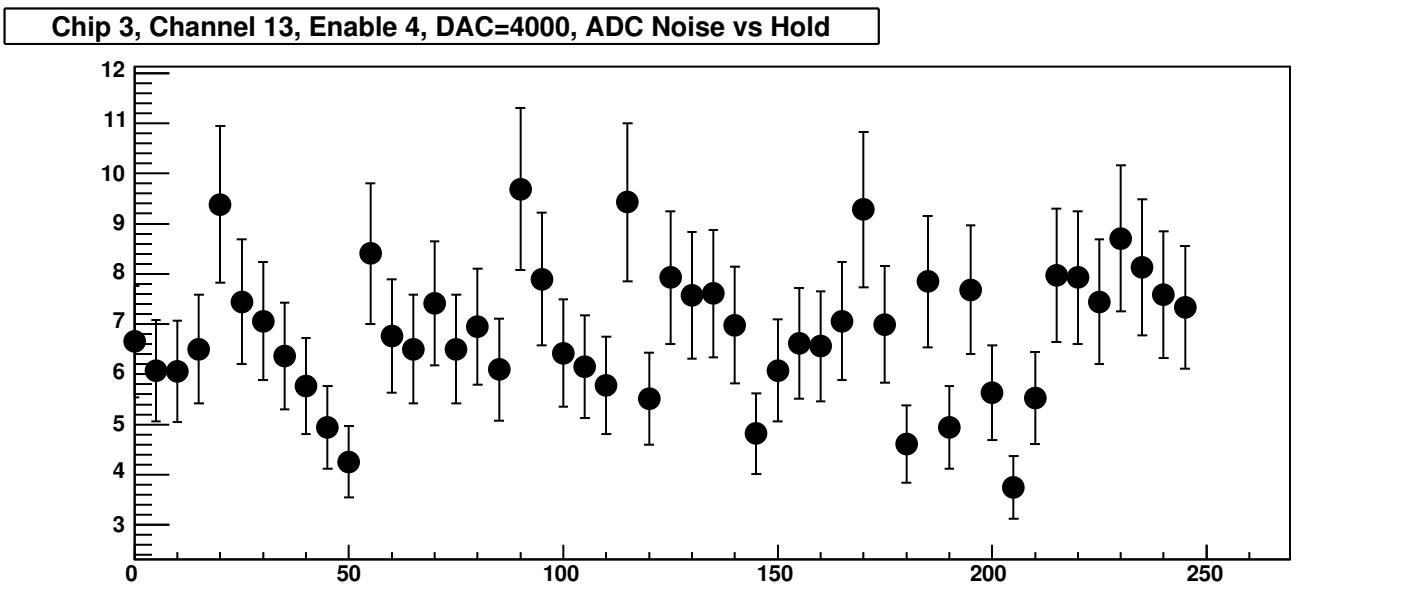


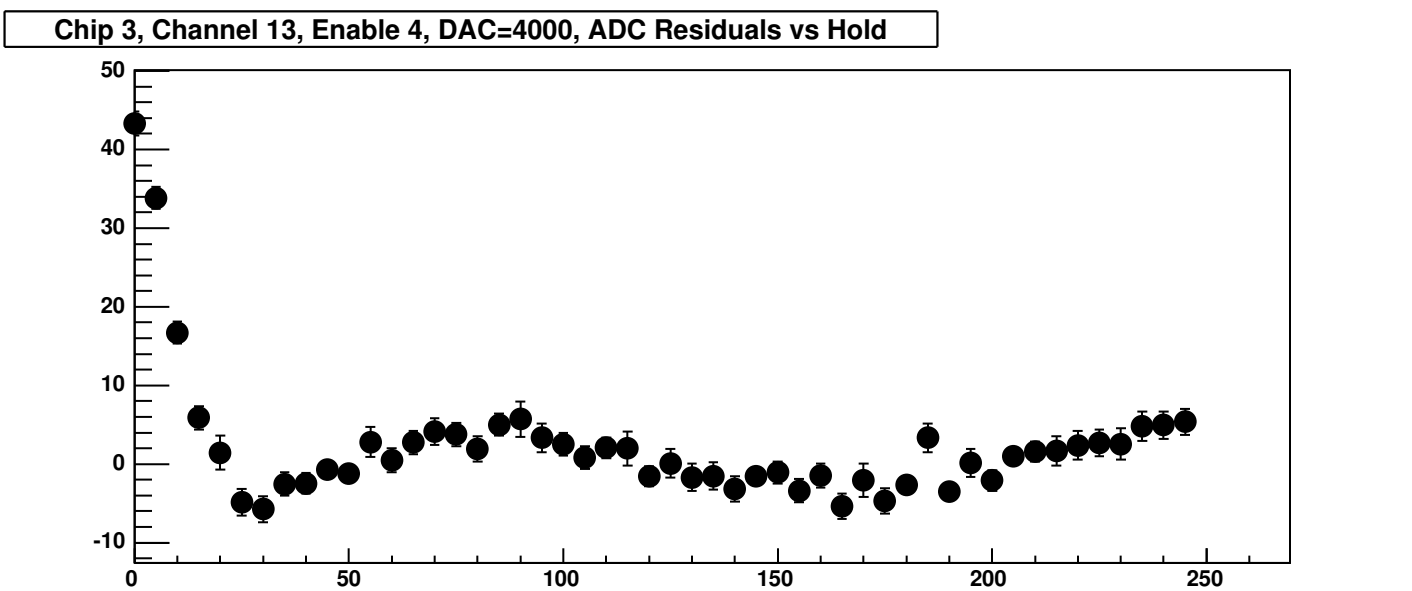

Chip 3, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 144 / 41
p0 387.1 ± 2.778
p1 5.667 ± 0.5276
p2 185.9 ± 2.89
p3 29.28 ± 0.6394
p4 -0.2139 ± 0.007728
560
540
520
500
480
460
440
420
0
50
100
150
200
250


Chip 3, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

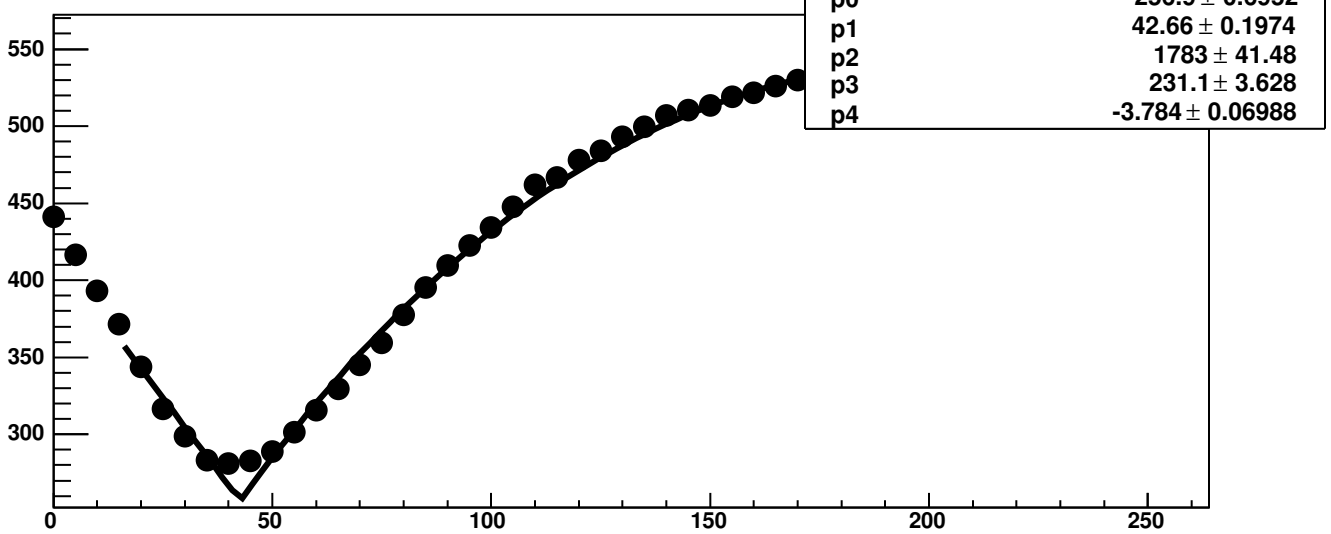

Chip 3, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250


Chip 3, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 176 / 41
p0 393.2 ± 2.768
p1 6.195 ± 0.5318
p2 192.9 ± 2.895
p3 29.71 ± 0.5935
p4 -0.251 ± 0.007934
560
540
520
500
480
460
440
420
0
50
100
150
200
250

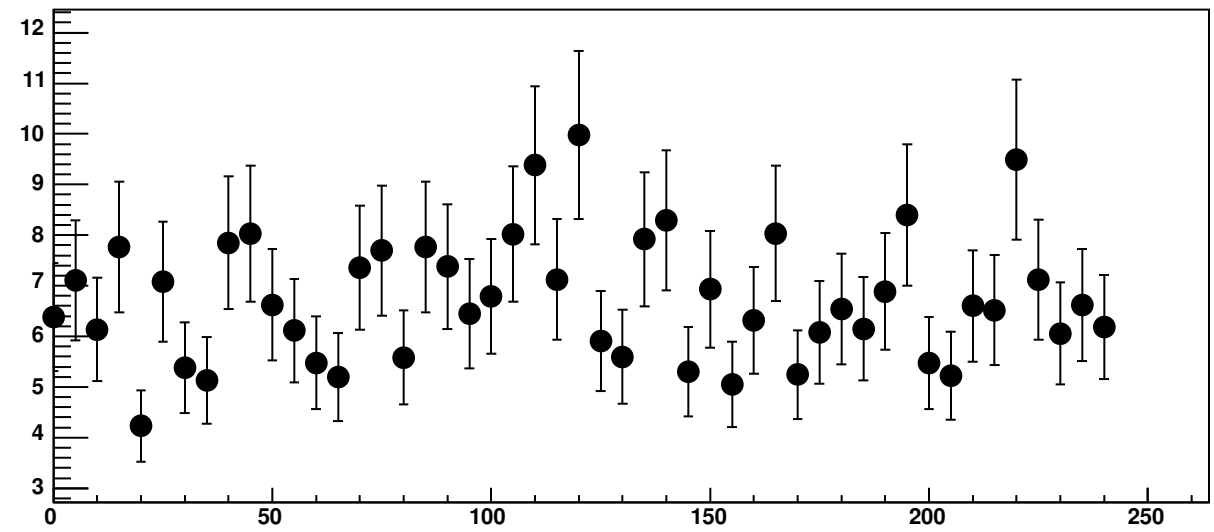

Chip 3, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 3, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250

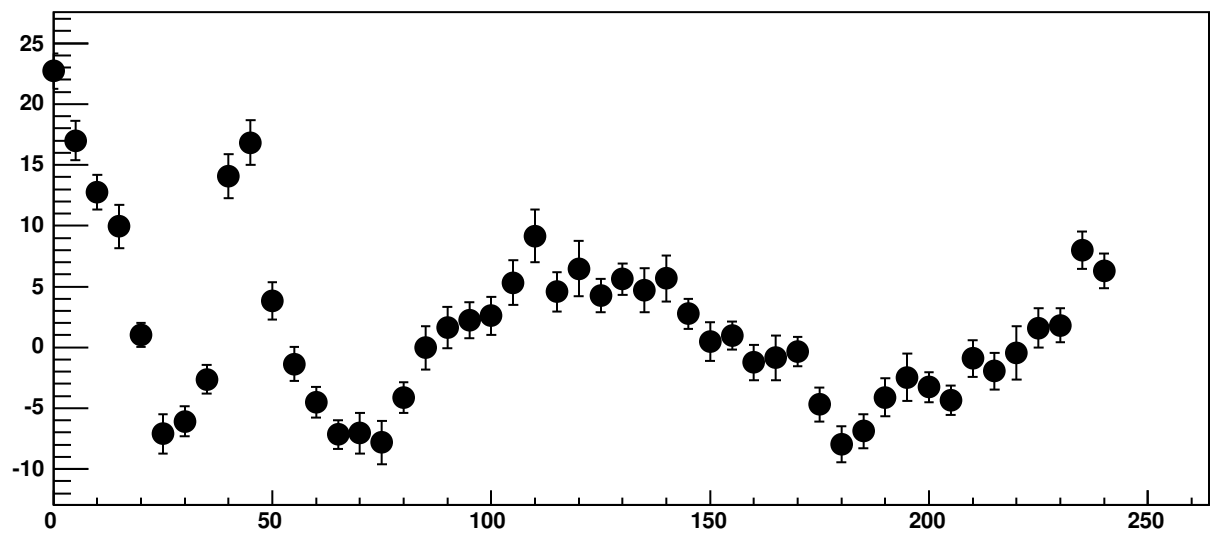
Chip 3, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold



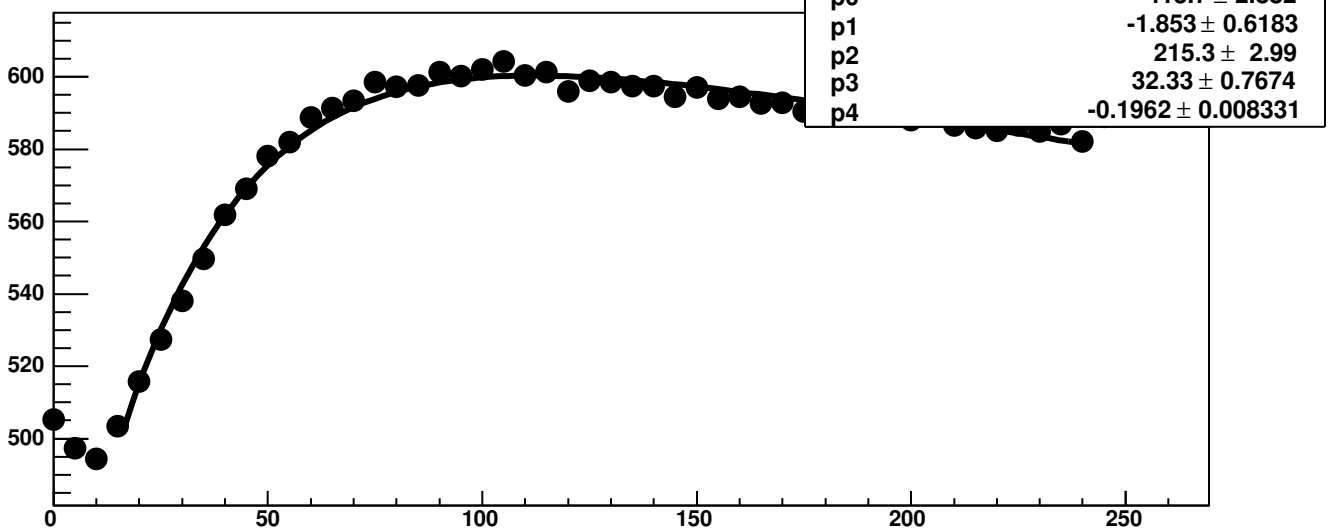
Chip 3, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



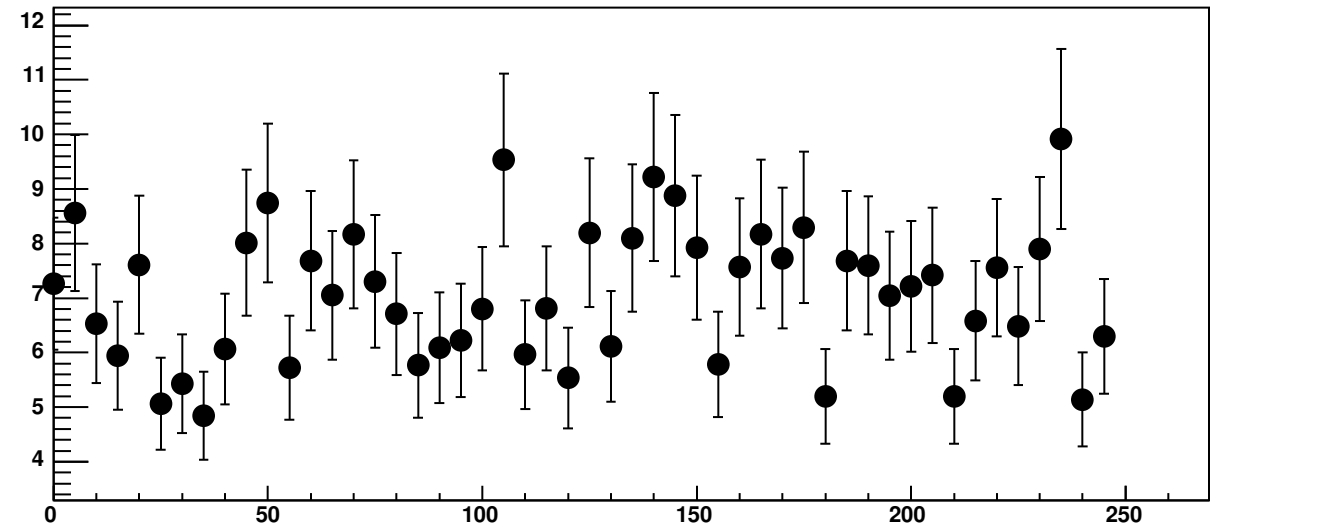
Chip 3, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold



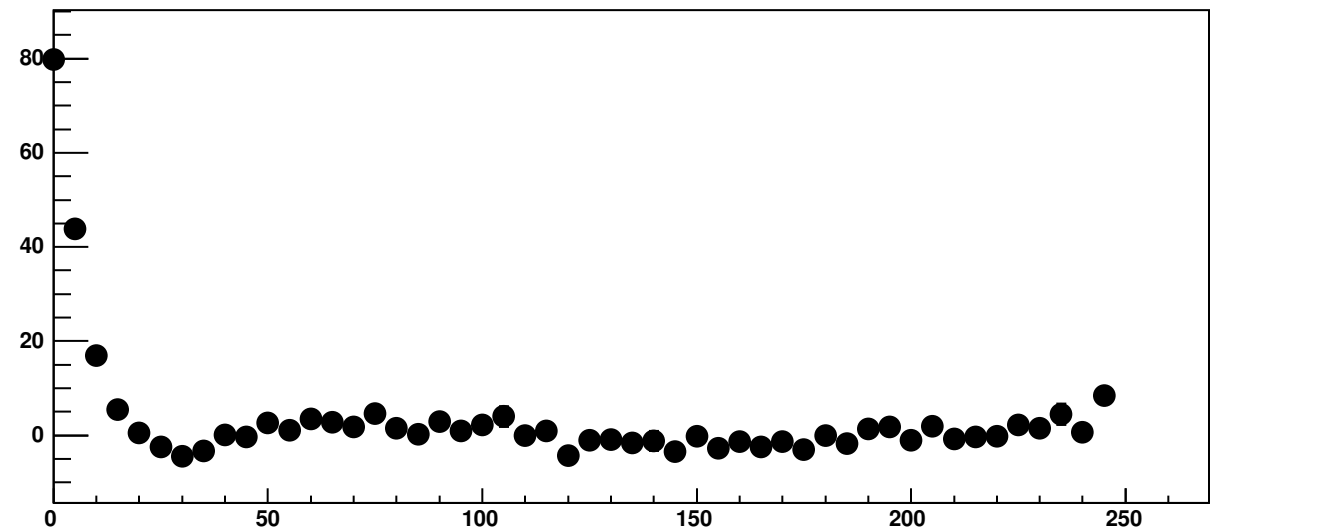
Chip 3, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold



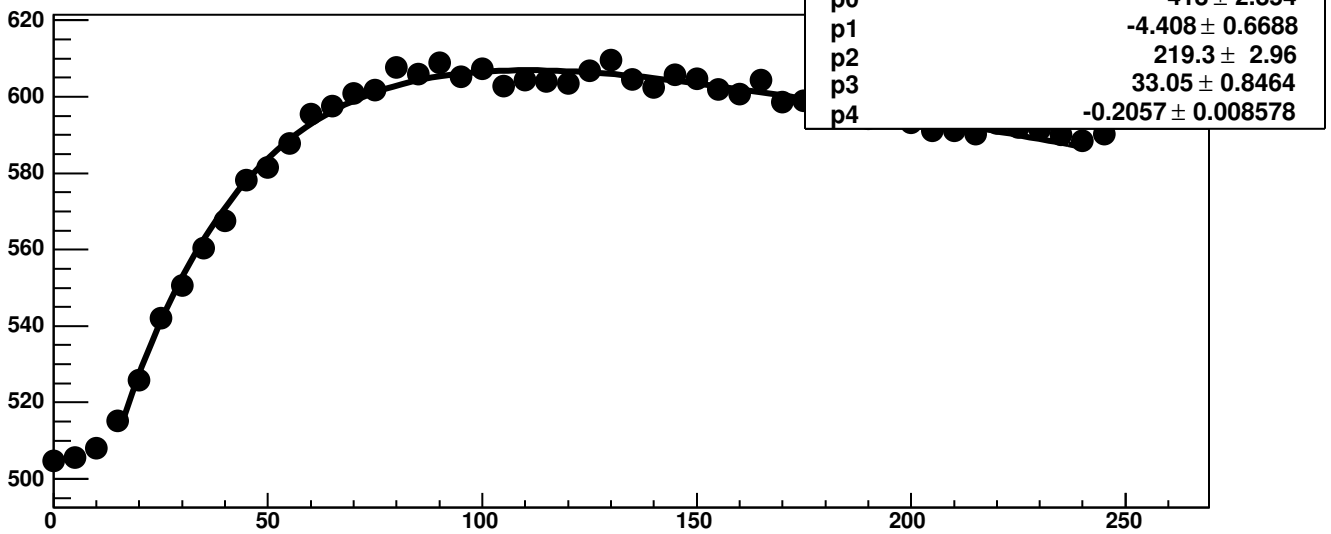
Chip 3, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



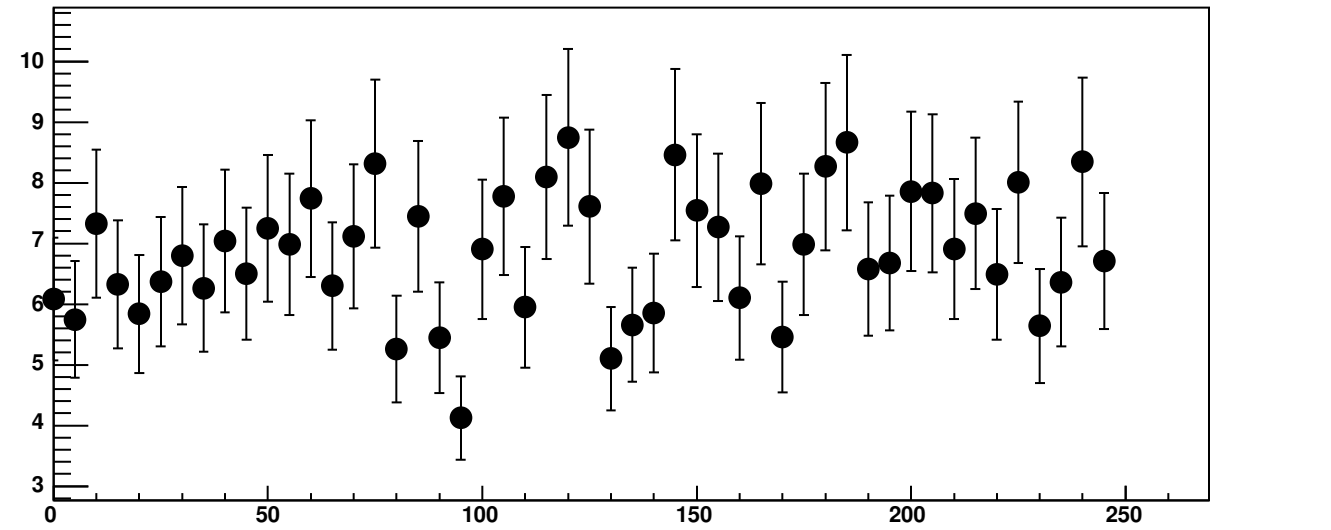
Chip 3, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold



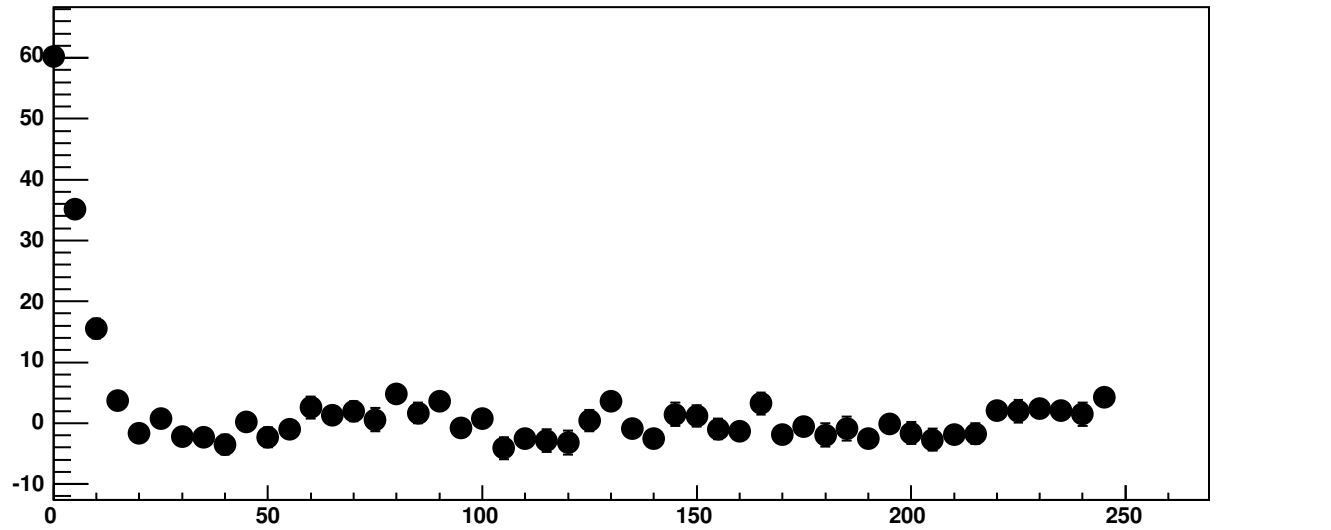
Chip 3, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold



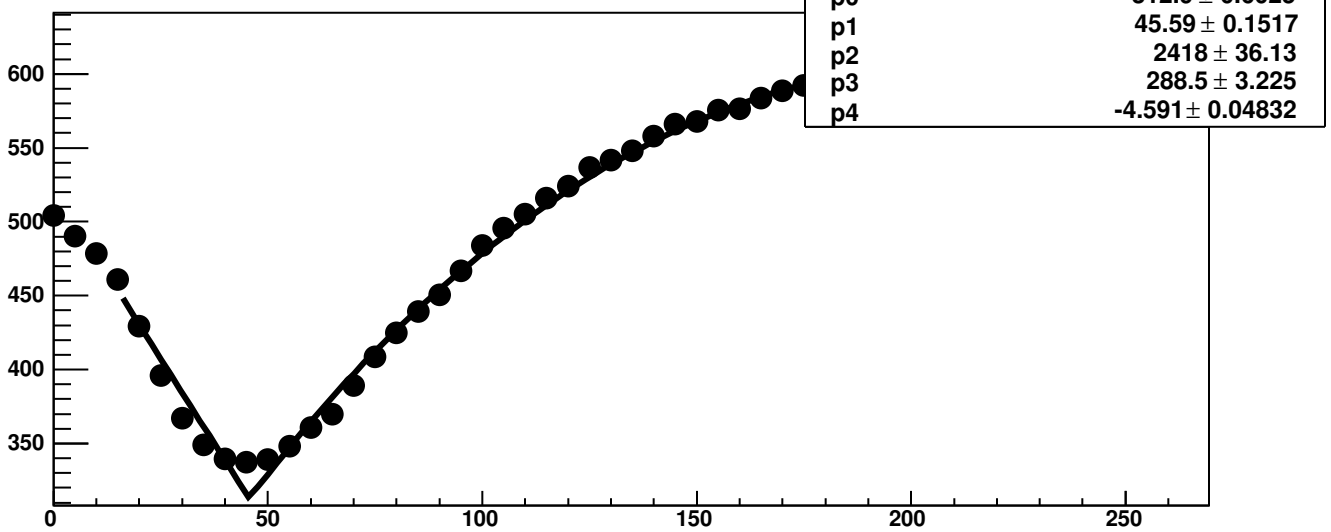
Chip 3, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold



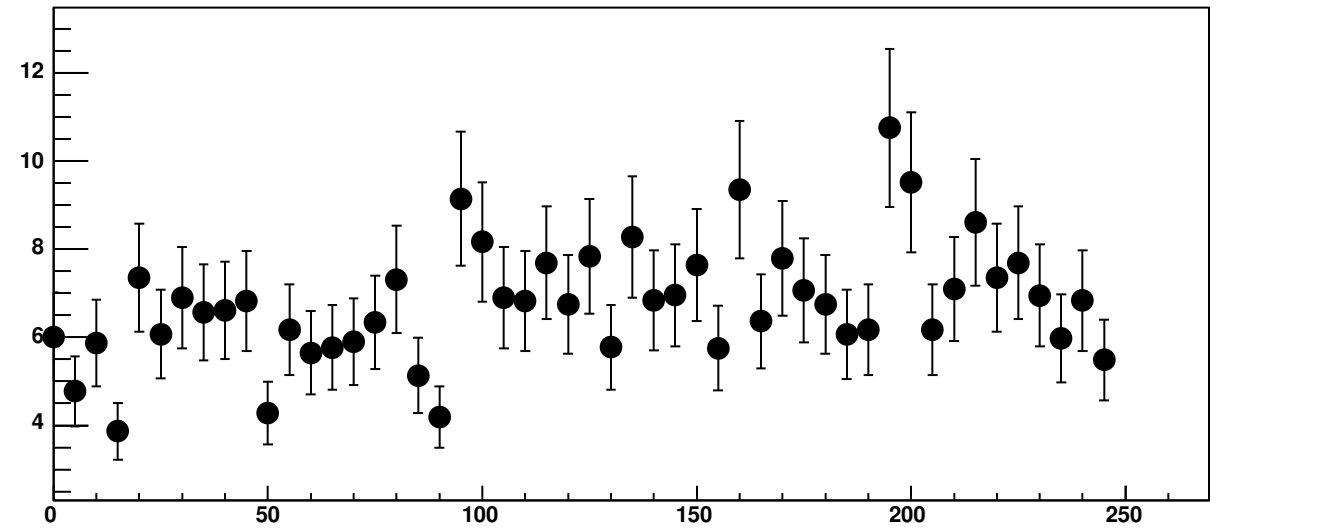
Chip 3, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold



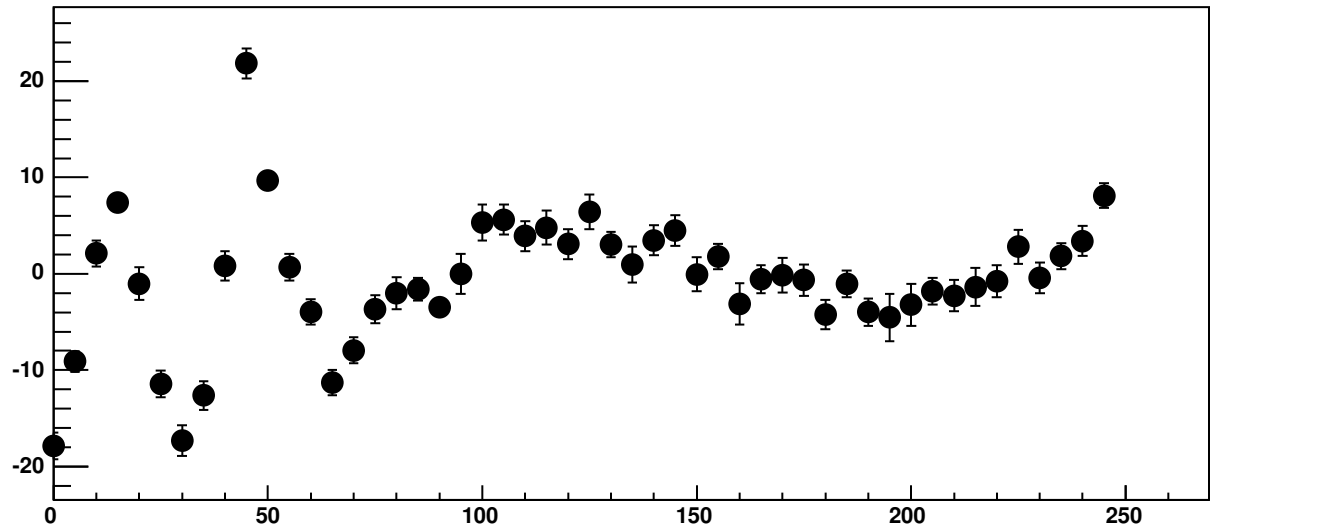
Chip 3, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold

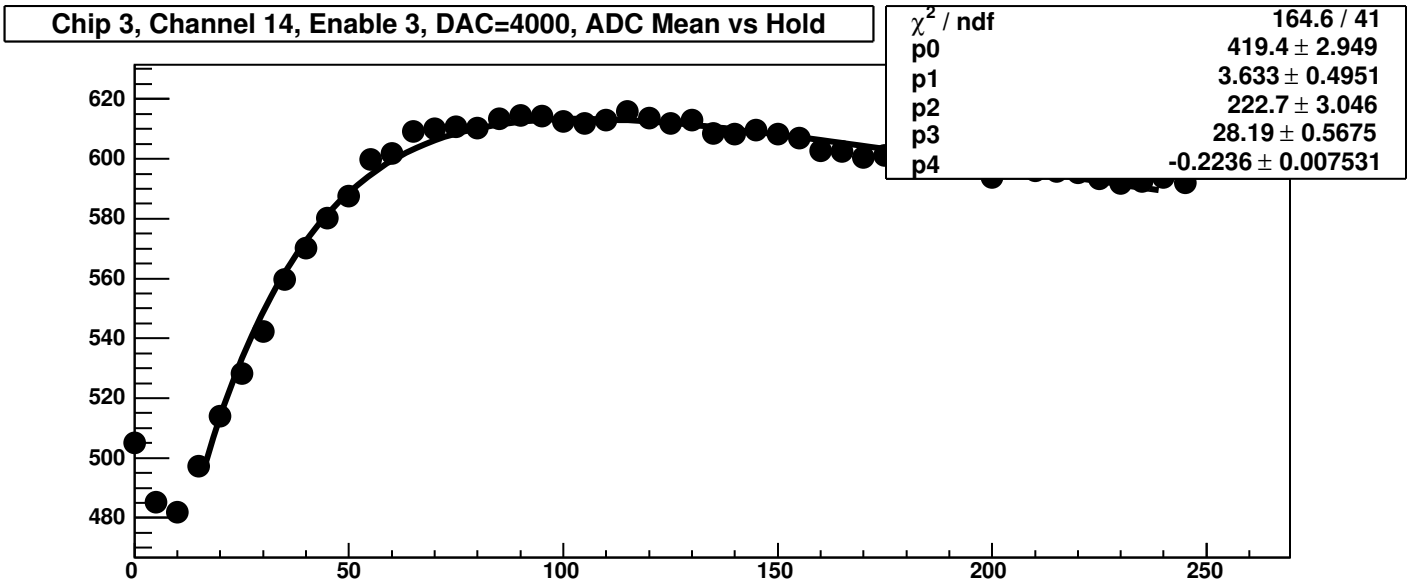


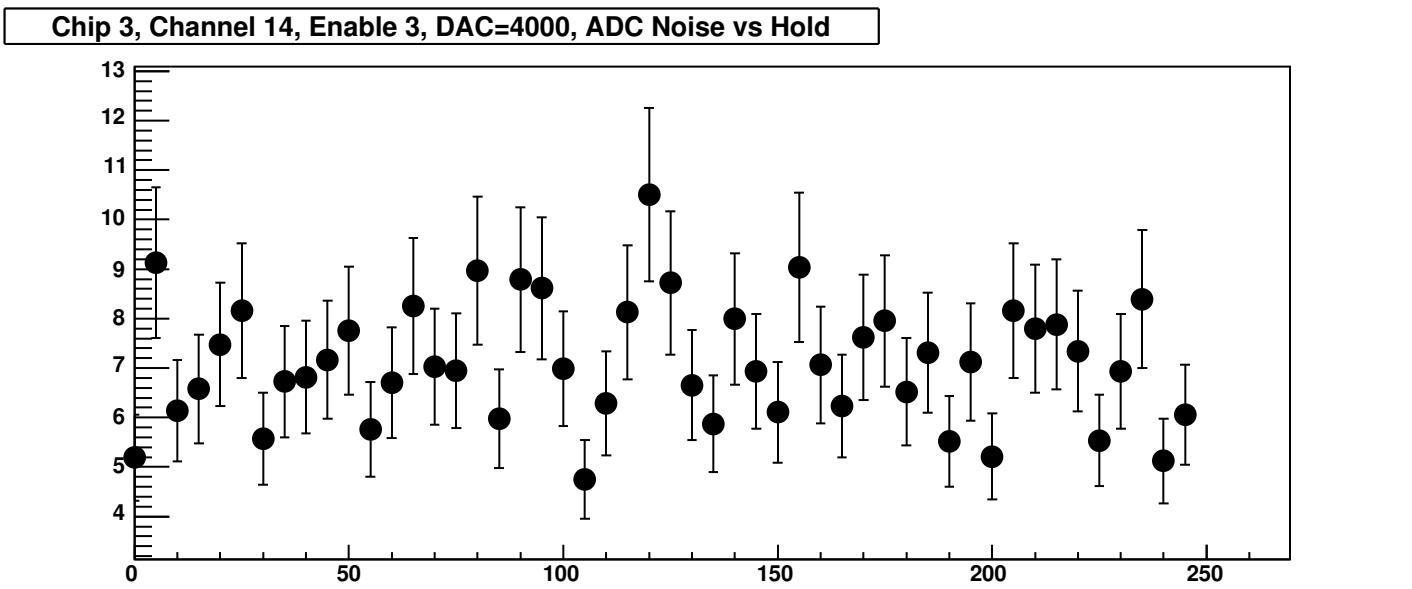
Chip 3, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold

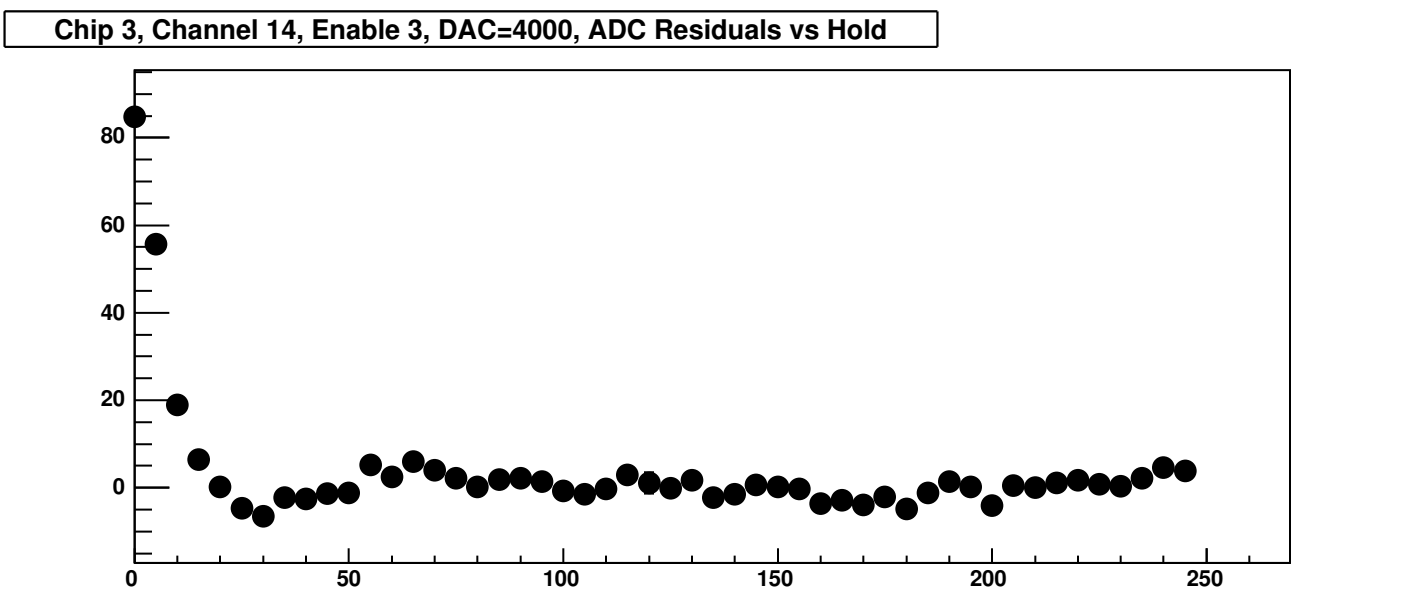


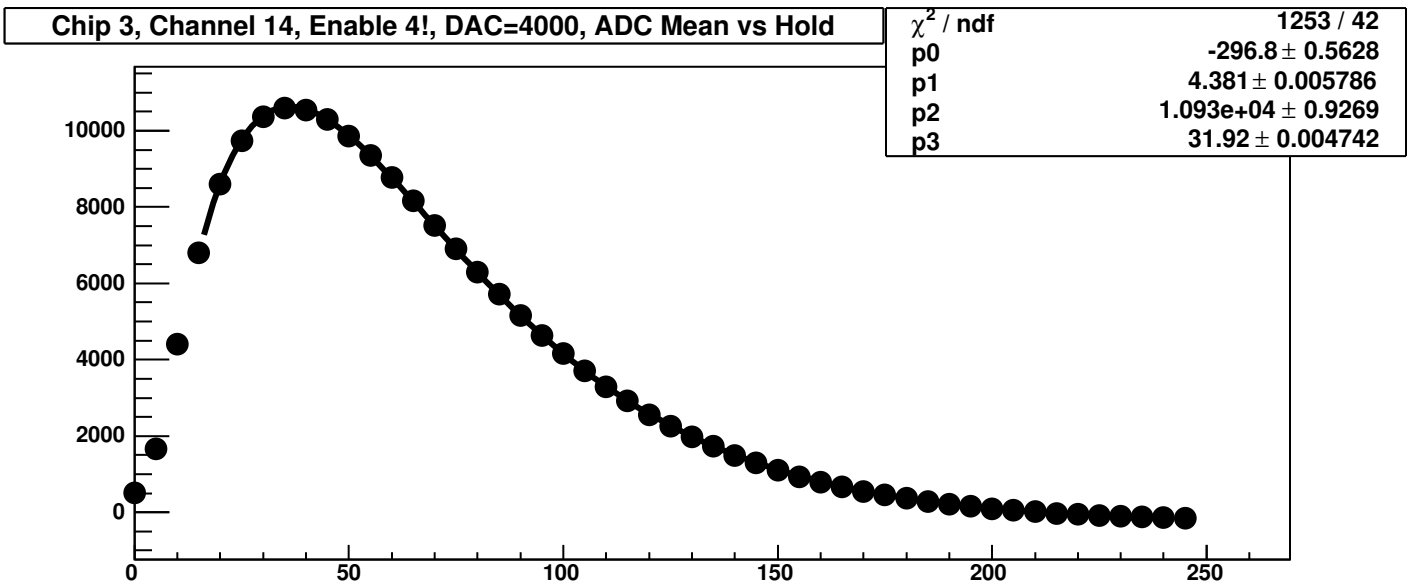
Chip 3, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold

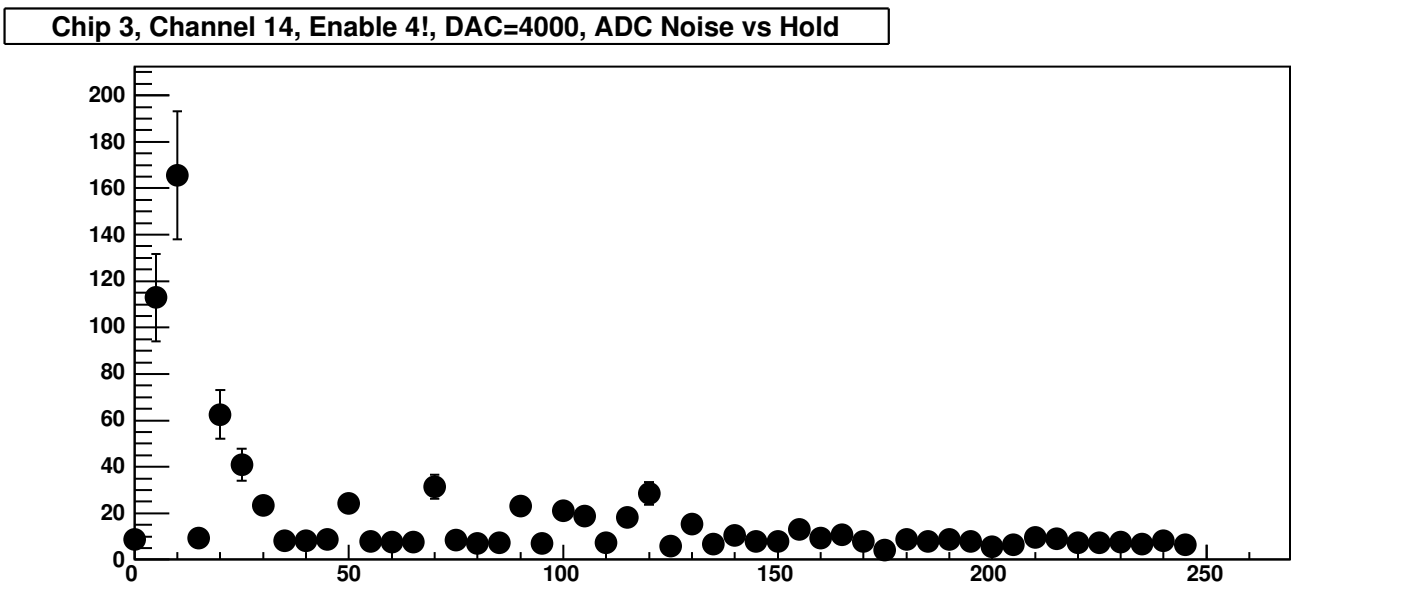


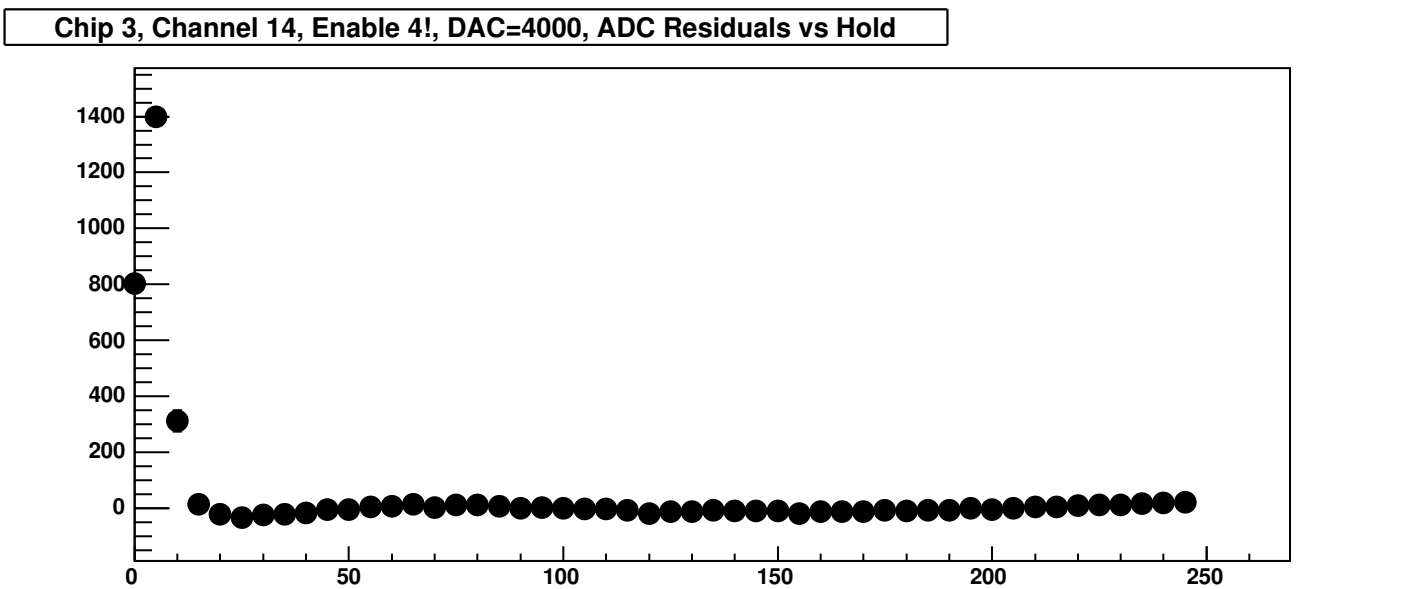

Chip 3, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 164.6 / 41
p0 419.4 ± 2.949
p1 3.633 ± 0.4951
p2 222.7 ± 3.046
p3 28.19 ± 0.5675
p4 -0.2236 ± 0.007531
620
600
580
560
540
520
500
480
0
50
100
150
200
250


Chip 3, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

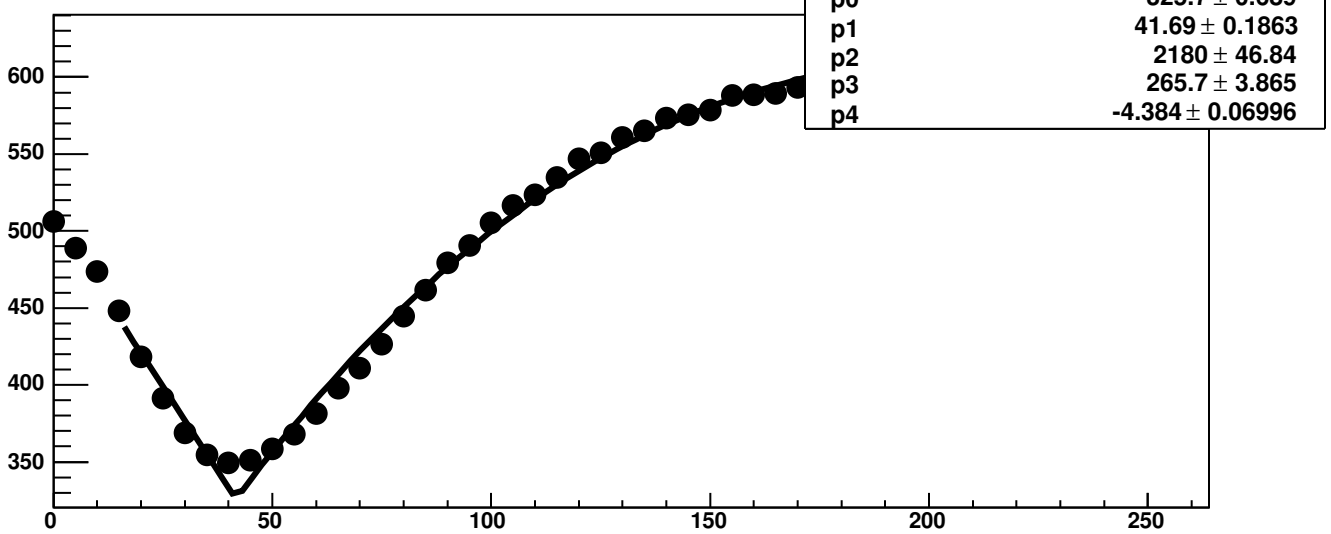

Chip 3, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold
80
60
40
20
0
0
50
100
150
200
250


Chip 3, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1253 / 42
p0 -296.8 ± 0.5628
p1 4.381 ± 0.005786
p2 1.093e+04 ± 0.9269
p3 31.92 ± 0.004742
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

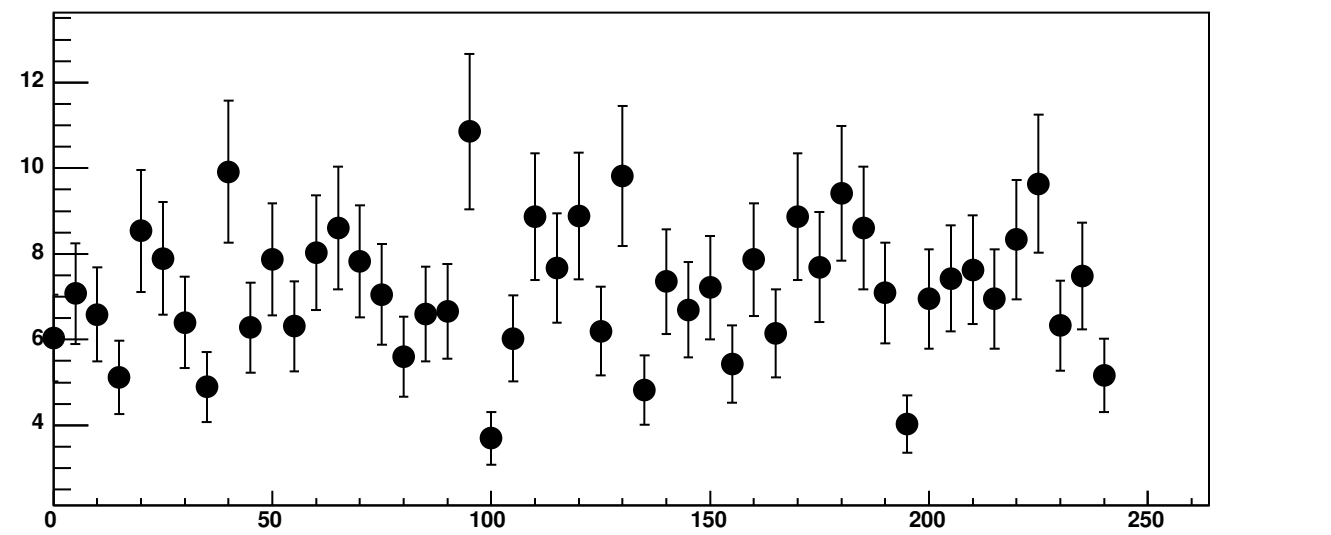

Chip 3, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 3, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

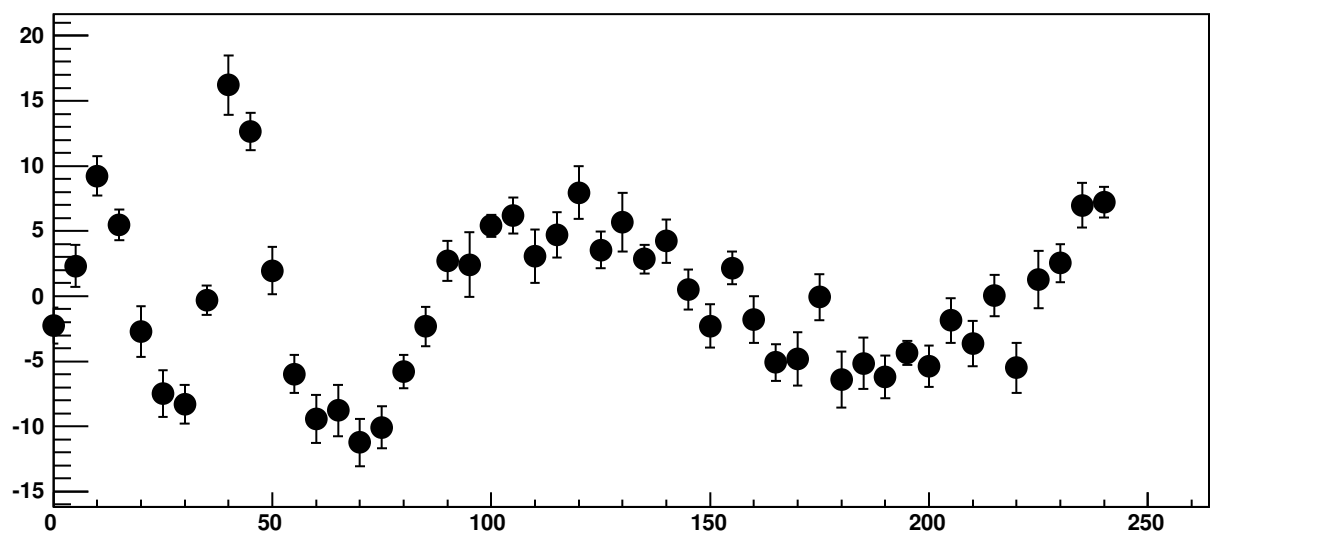
Chip 3, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold



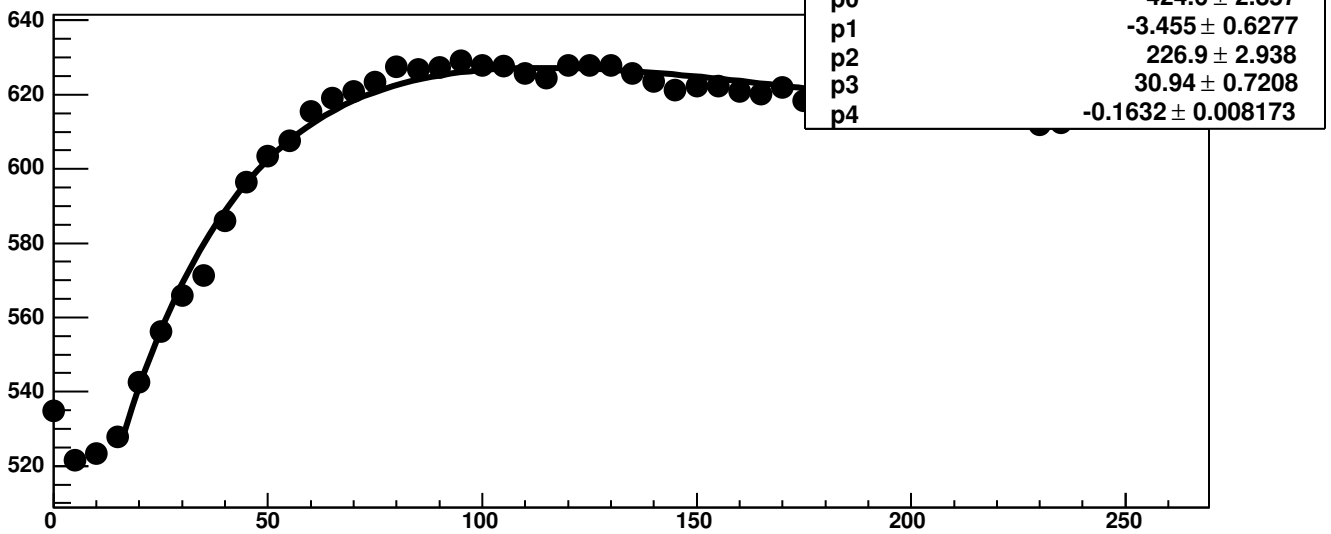
Chip 3, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold



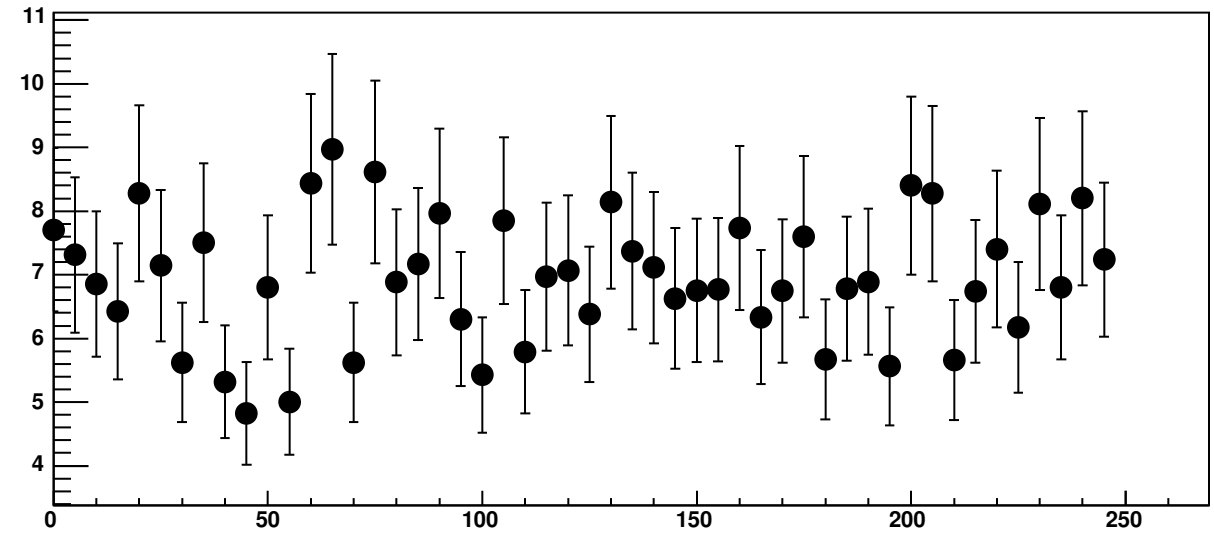
Chip 3, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold



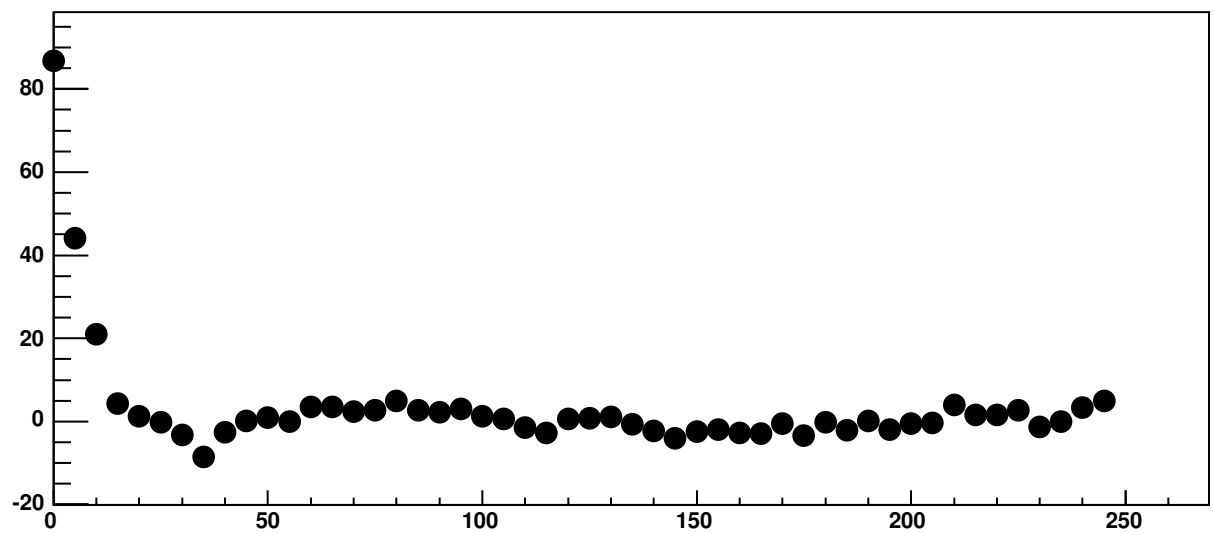
Chip 3, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold



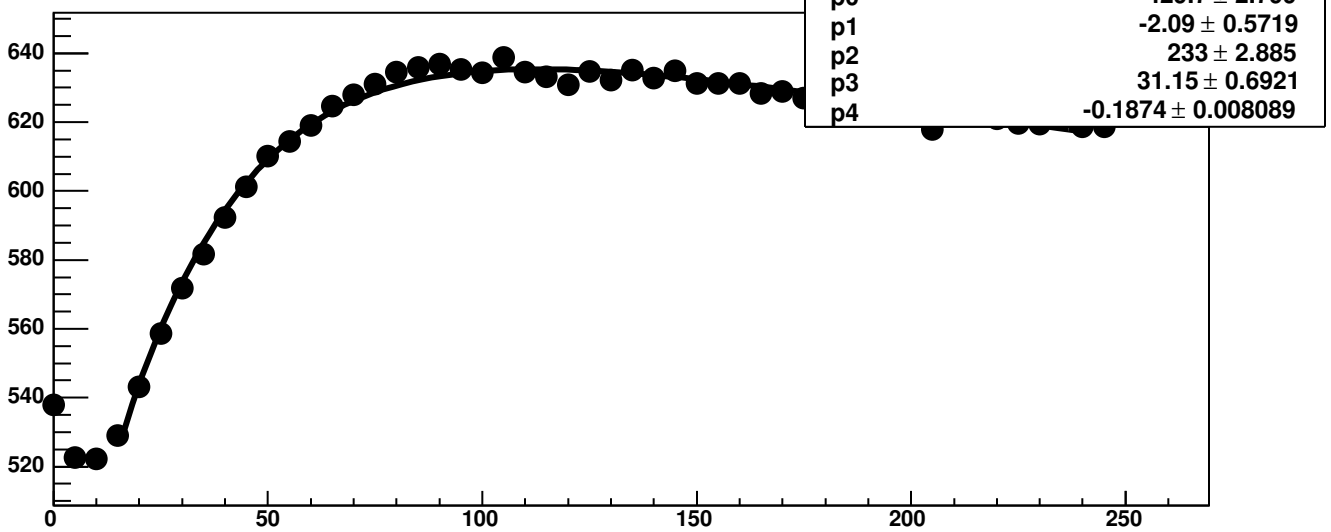
Chip 3, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



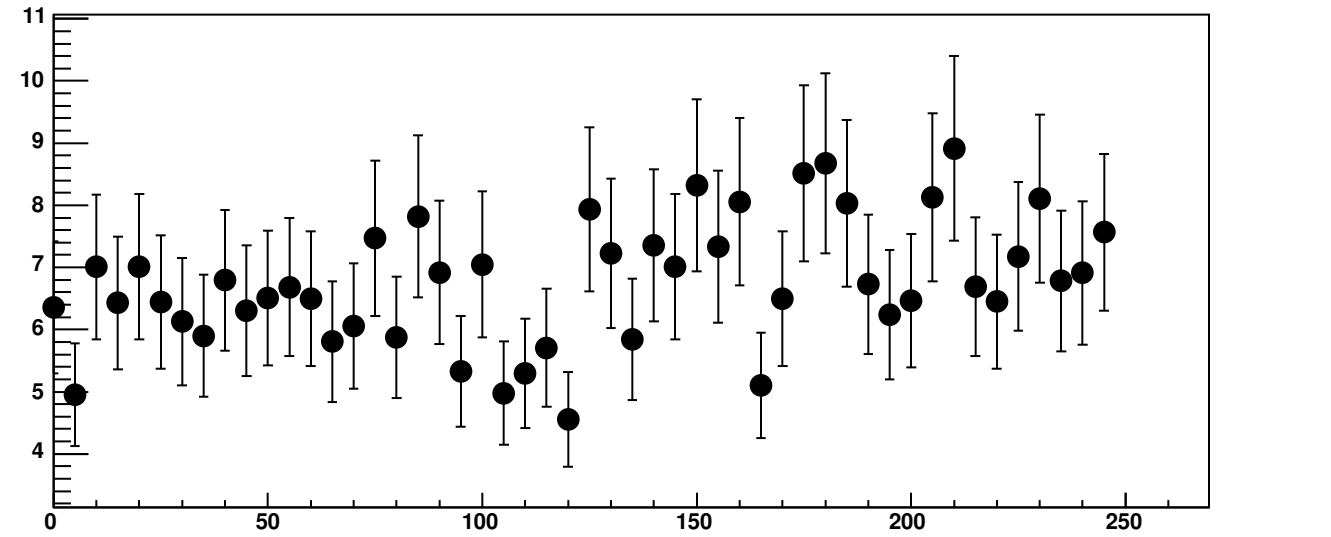
Chip 3, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold



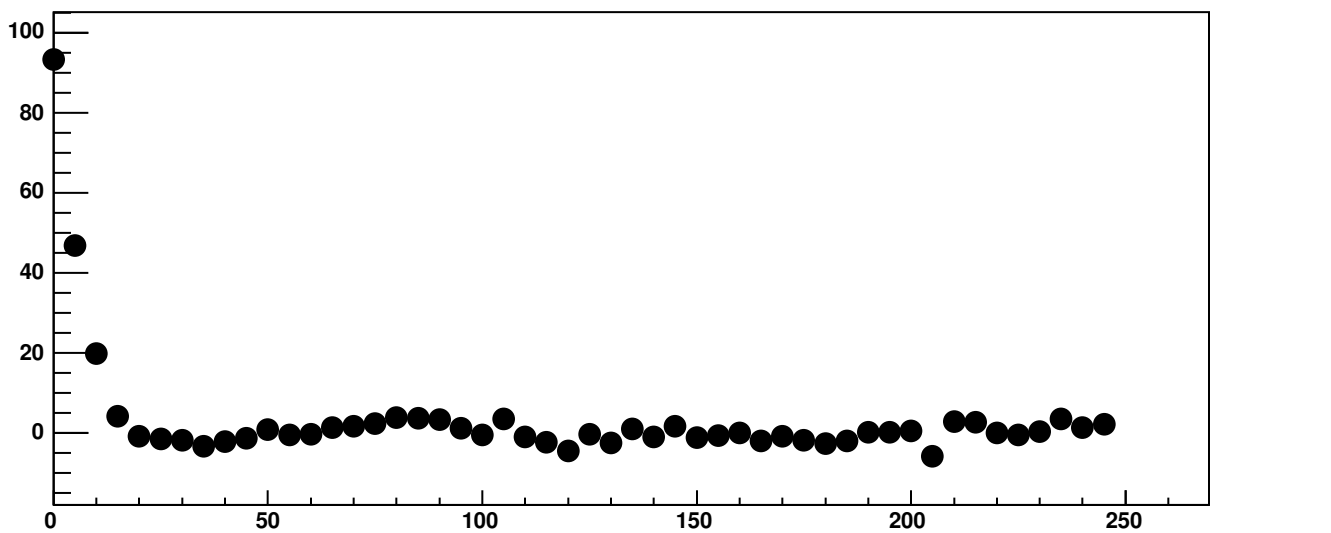
Chip 3, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold



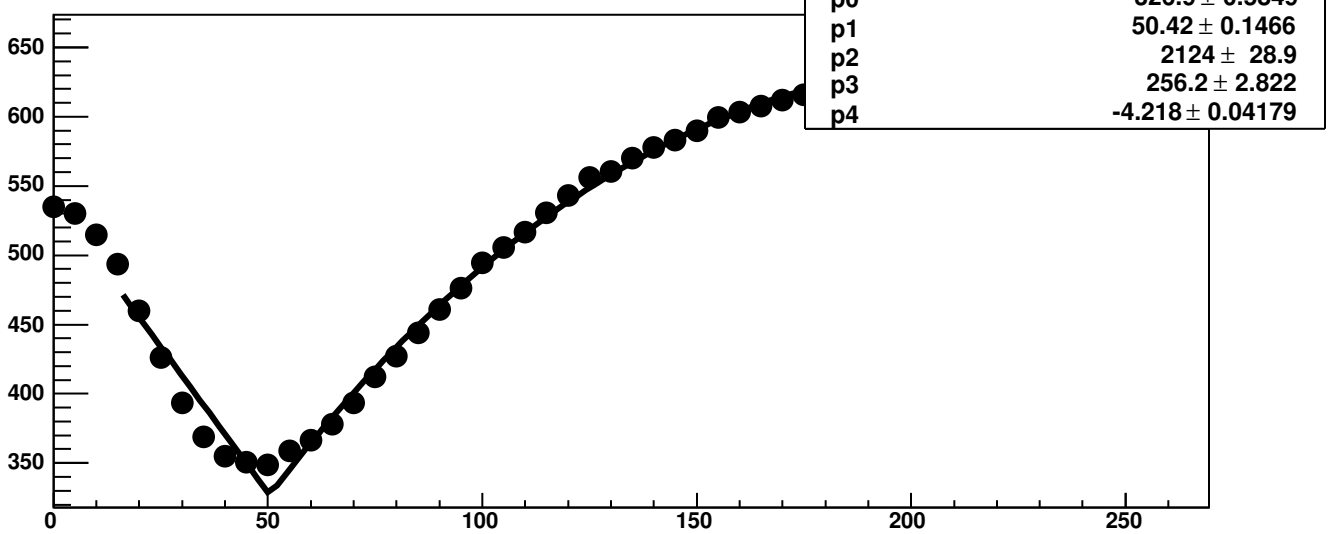
Chip 3, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



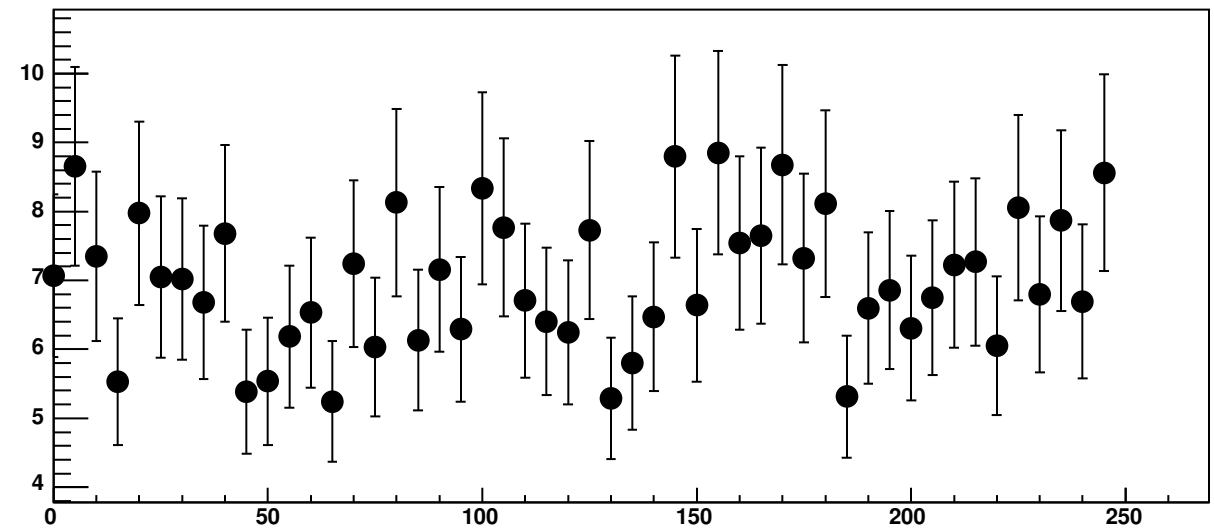
Chip 3, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold



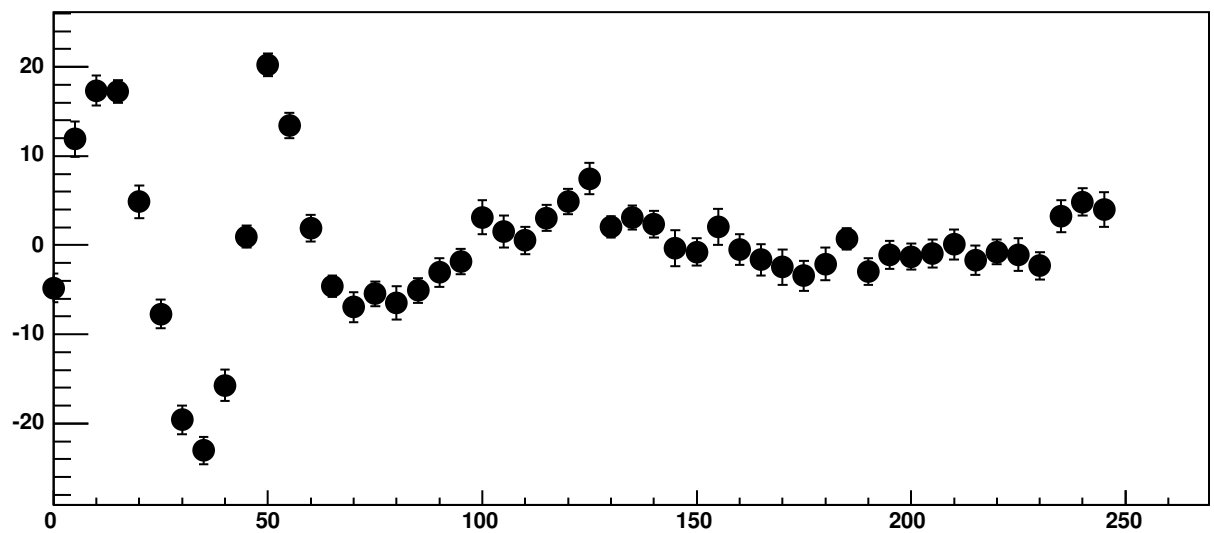
Chip 3, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold



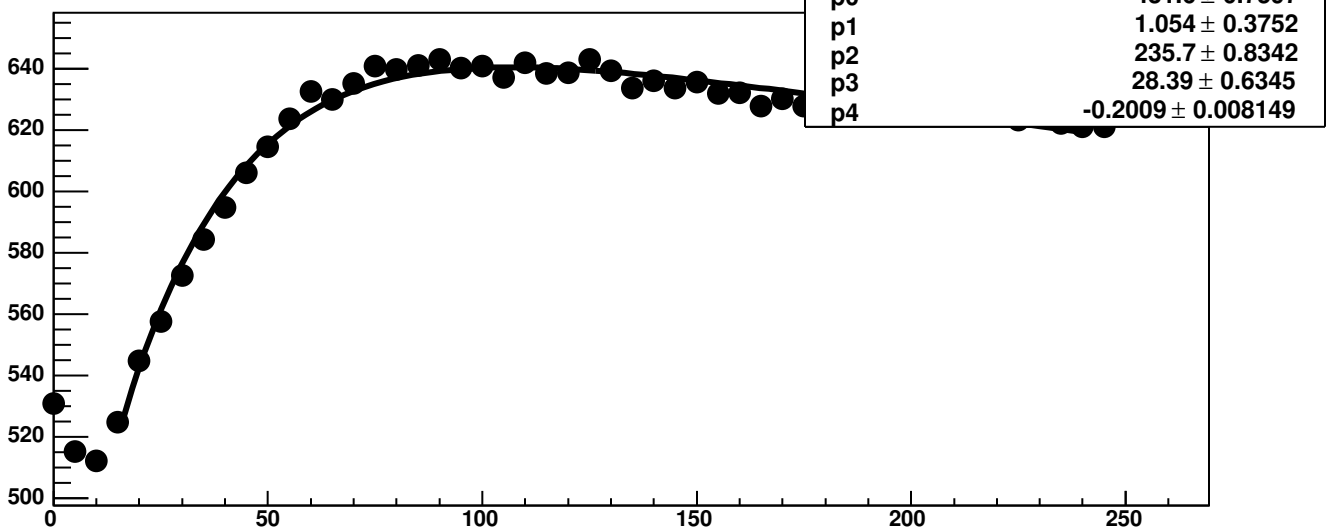
Chip 3, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold



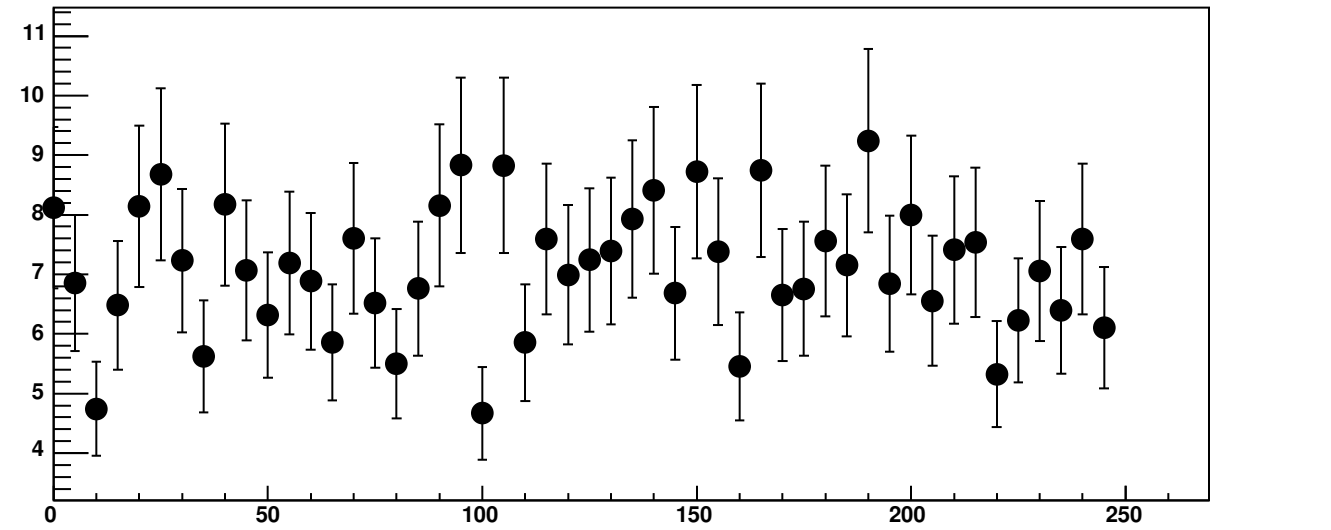
Chip 3, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold



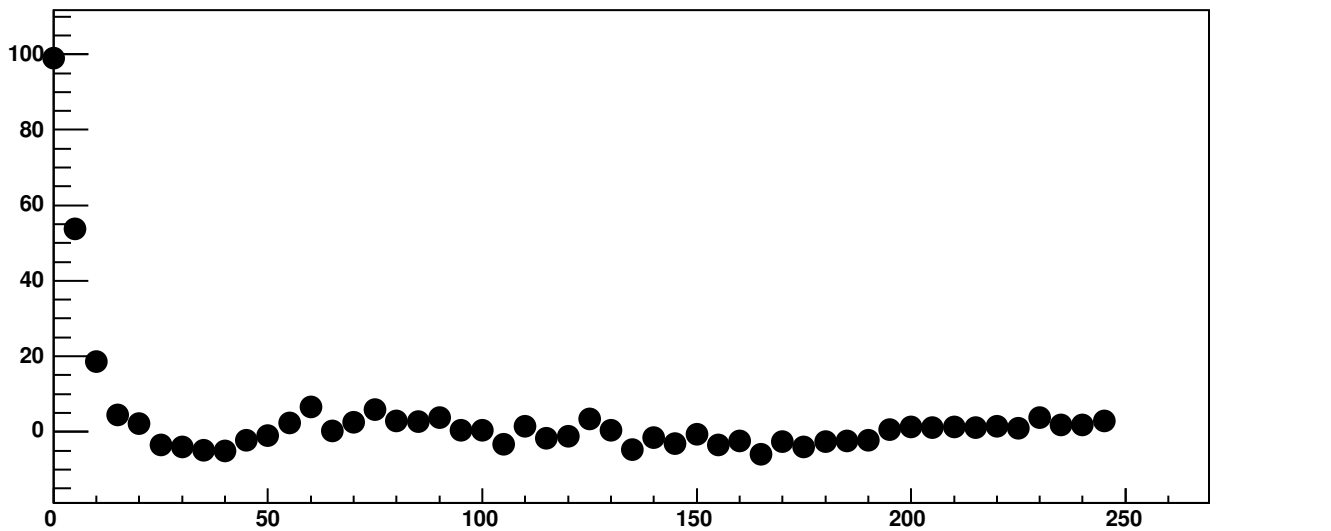
Chip 3, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold

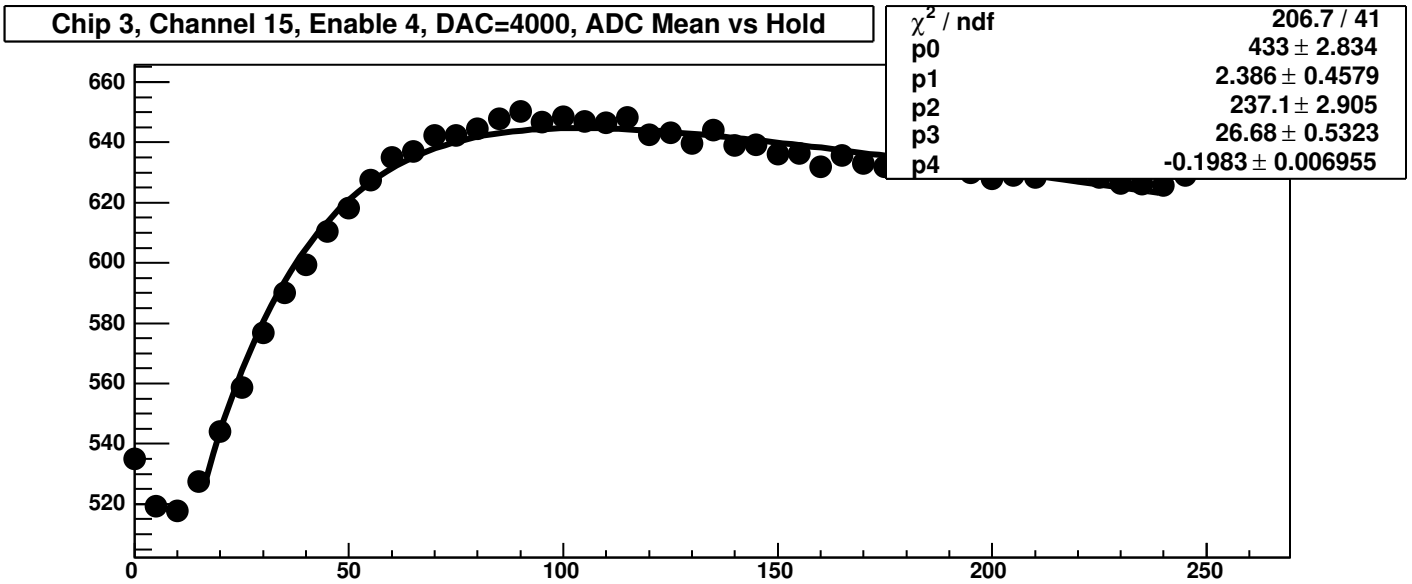


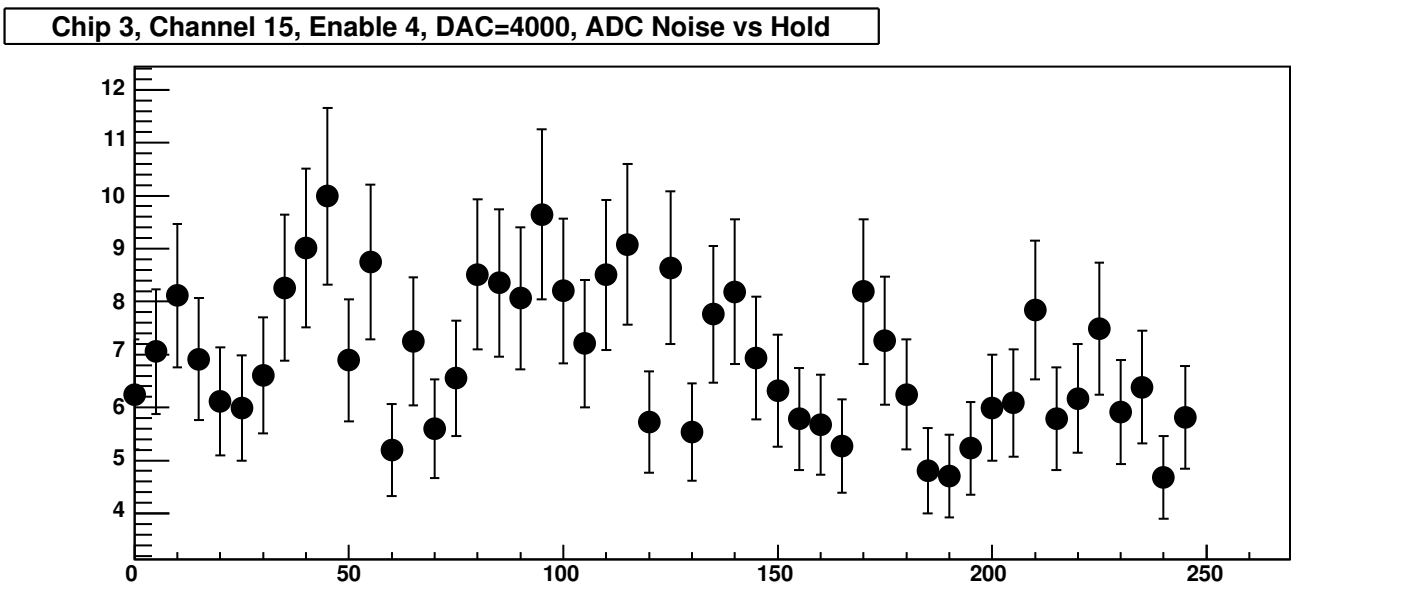
Chip 3, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold

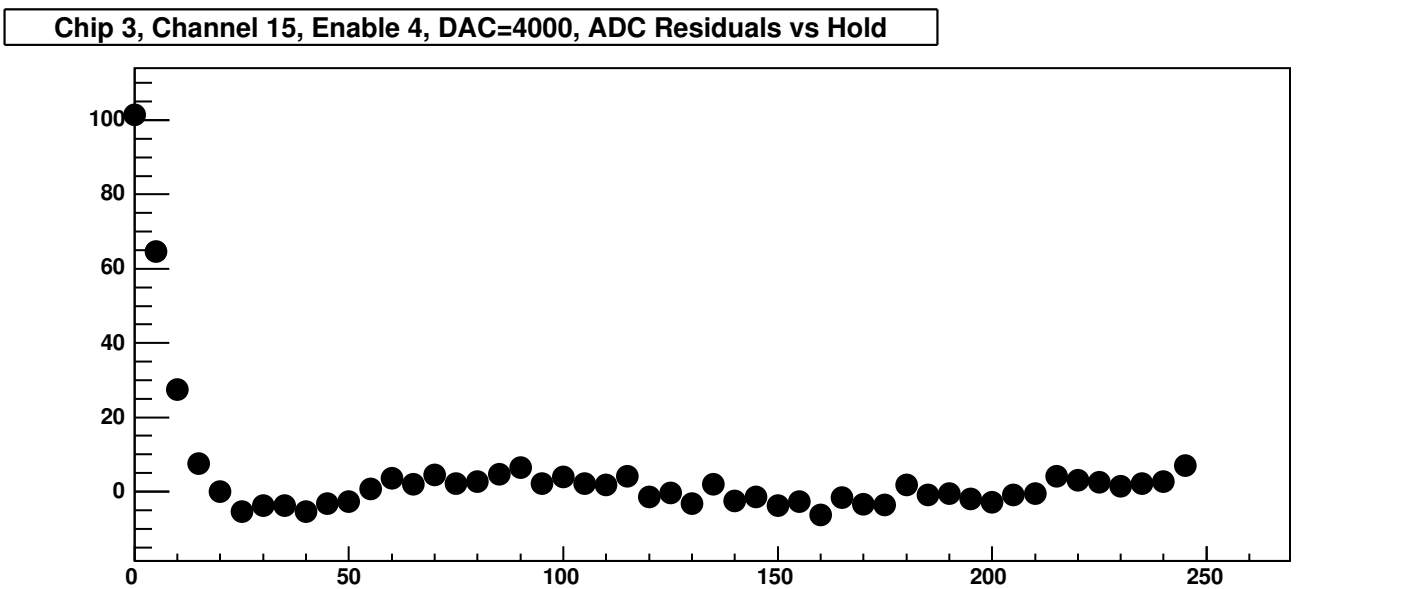


Chip 3, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold



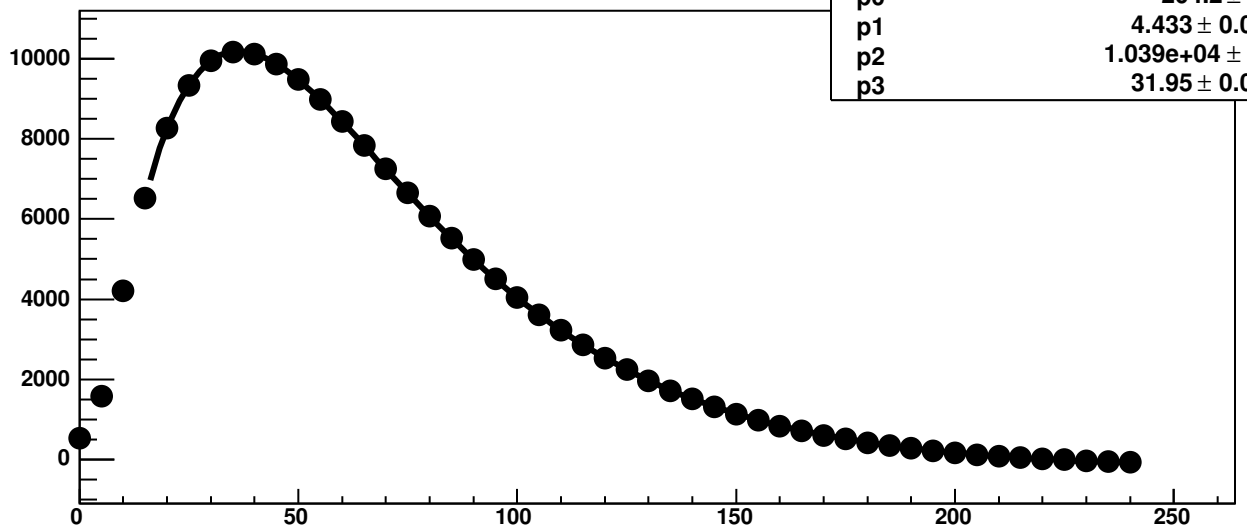

Chip 3, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 206.7 / 41
p0 433 ± 2.834
p1 2.386 ± 0.4579
p2 237.1 ± 2.905
p3 26.68 ± 0.5323
p4 -0.1983 ± 0.006955
660
640
620
600
580
560
540
520
0
50
100
150
200
250


Chip 3, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

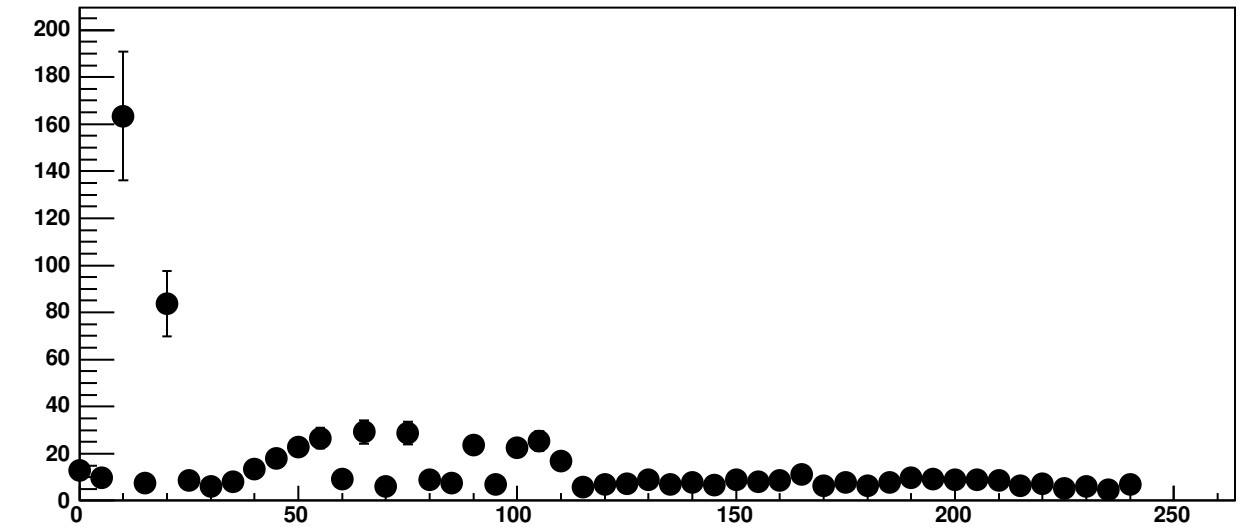

Chip 3, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold
100
80
60
40
20
0
0
50
100
150
200
250

Chip 3, Channel 15, Enable 5!, DAC=4000, ADC Mean vs Hold

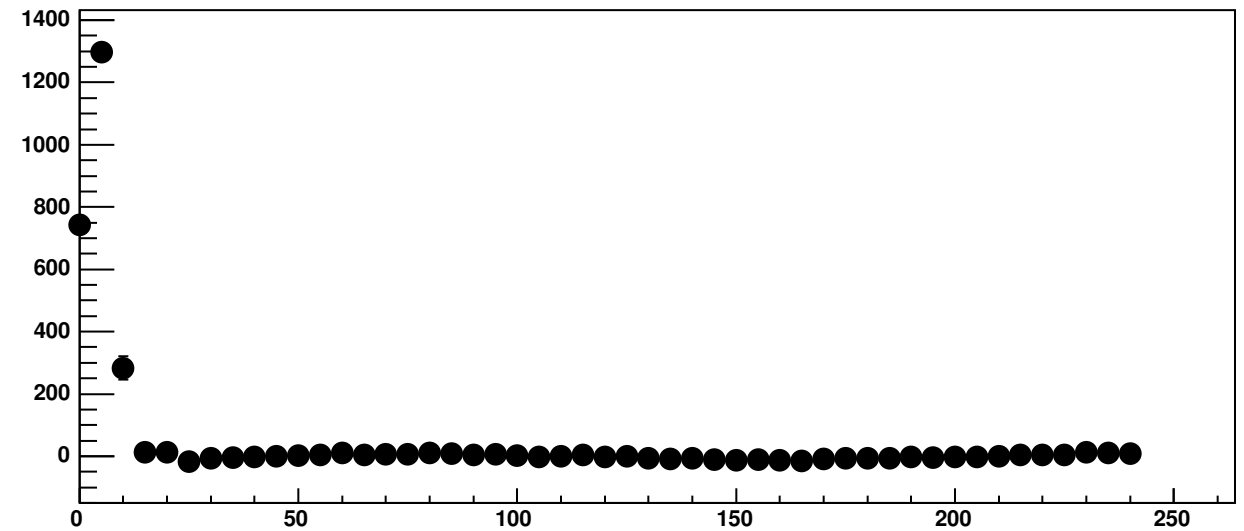
χ^2 / ndf	858.3 / 42
p0	-204.2 ± 0.5147
p1	4.433 ± 0.005364
p2	1.039e+04 ± 0.9027
p3	31.95 ± 0.004454



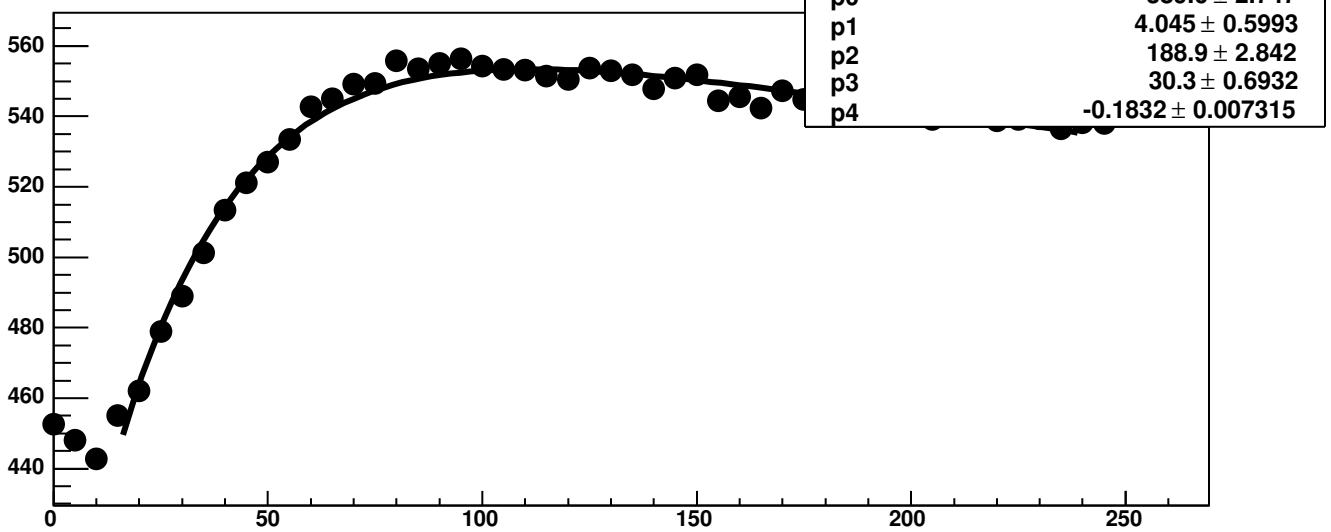
Chip 3, Channel 15, Enable 5!, DAC=4000, ADC Noise vs Hold



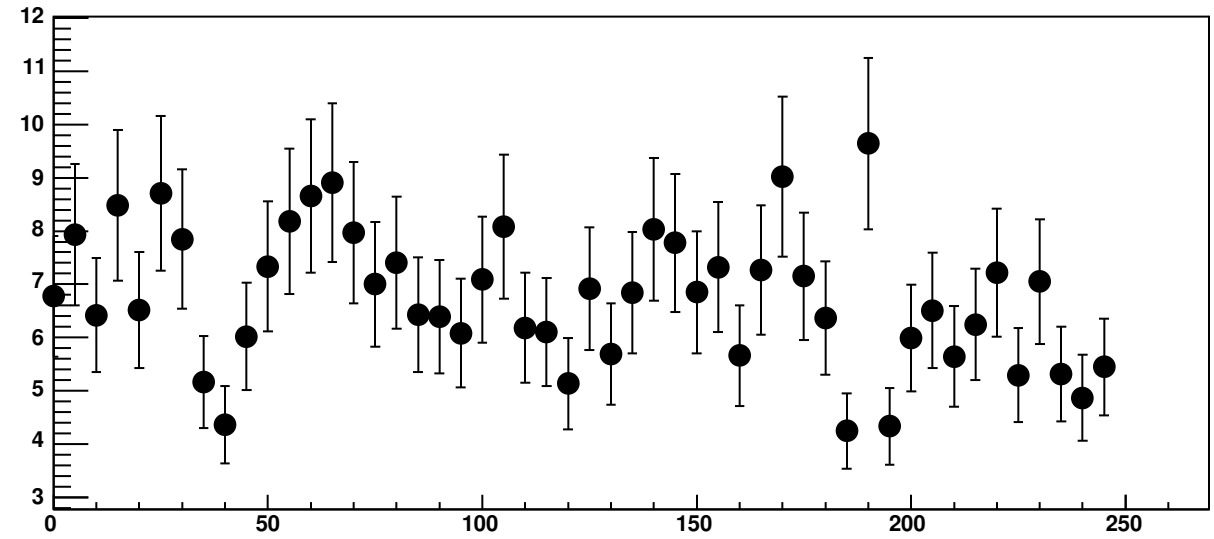
Chip 3, Channel 15, Enable 5!, DAC=4000, ADC Residuals vs Hold



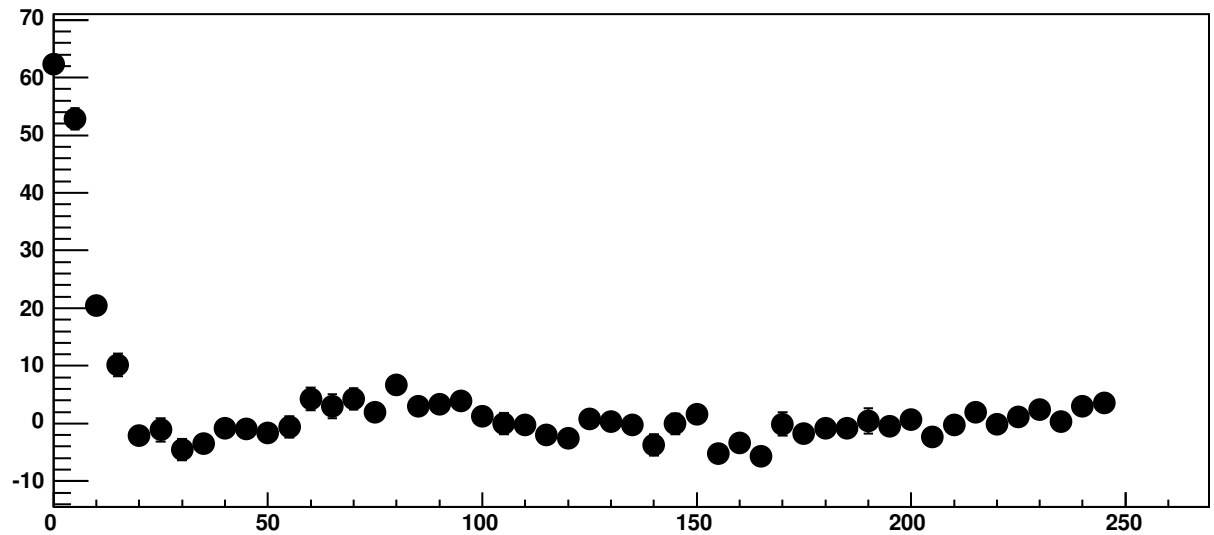
Chip 3, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold



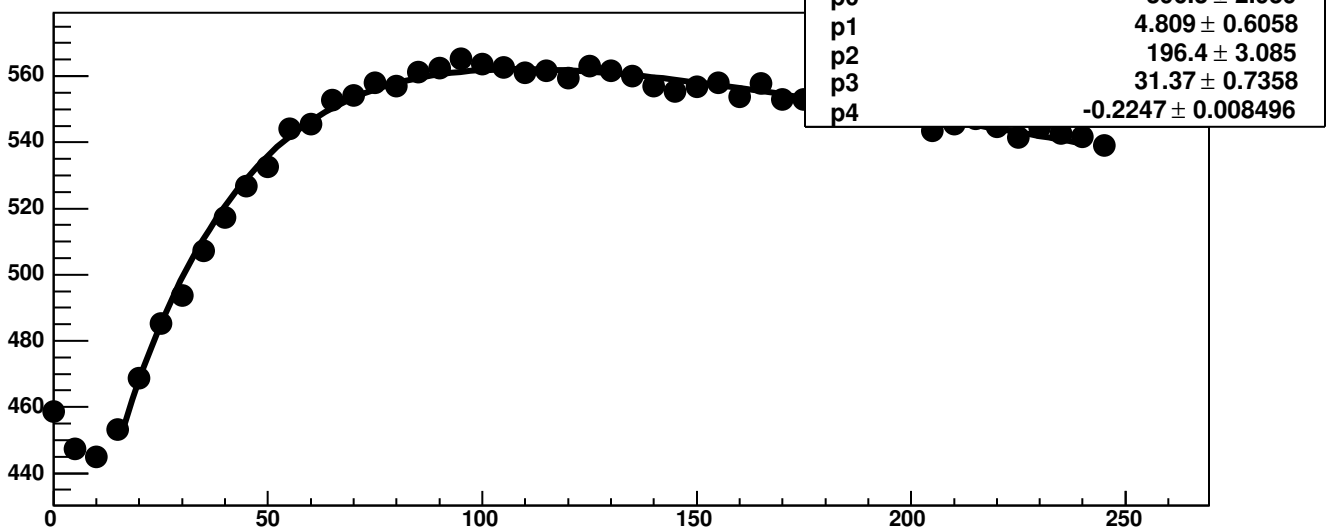
Chip 3, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



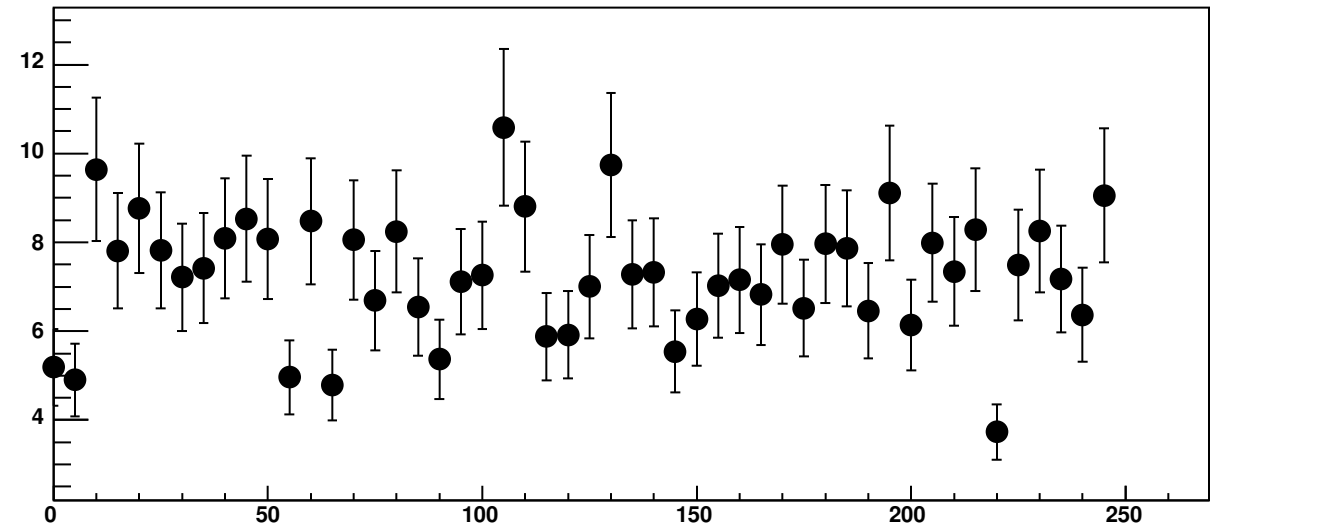
Chip 3, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold



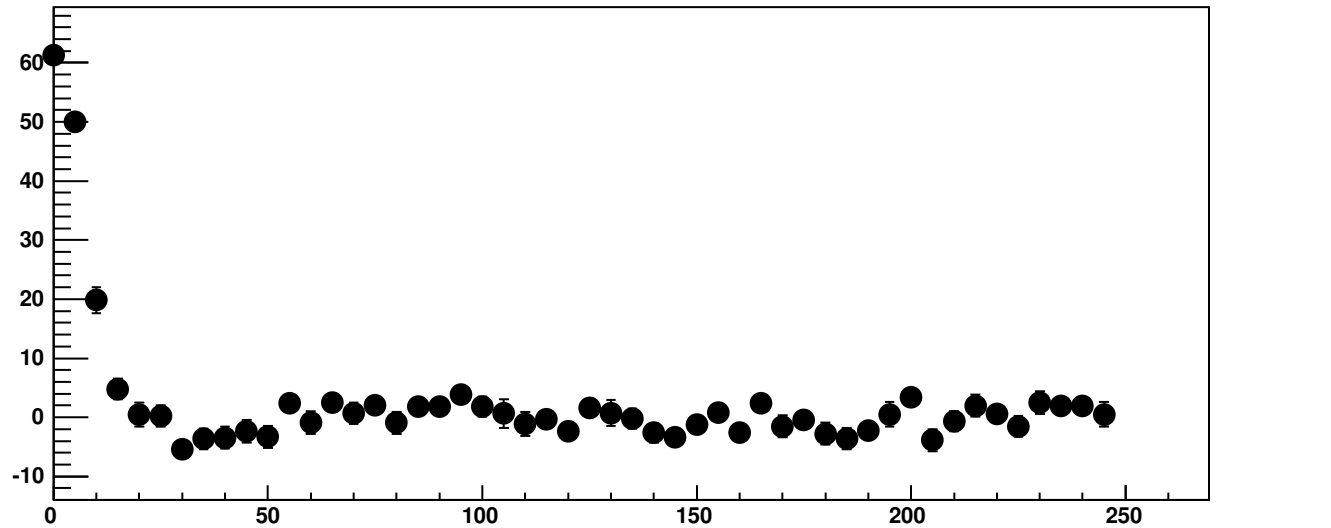
Chip 3, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold

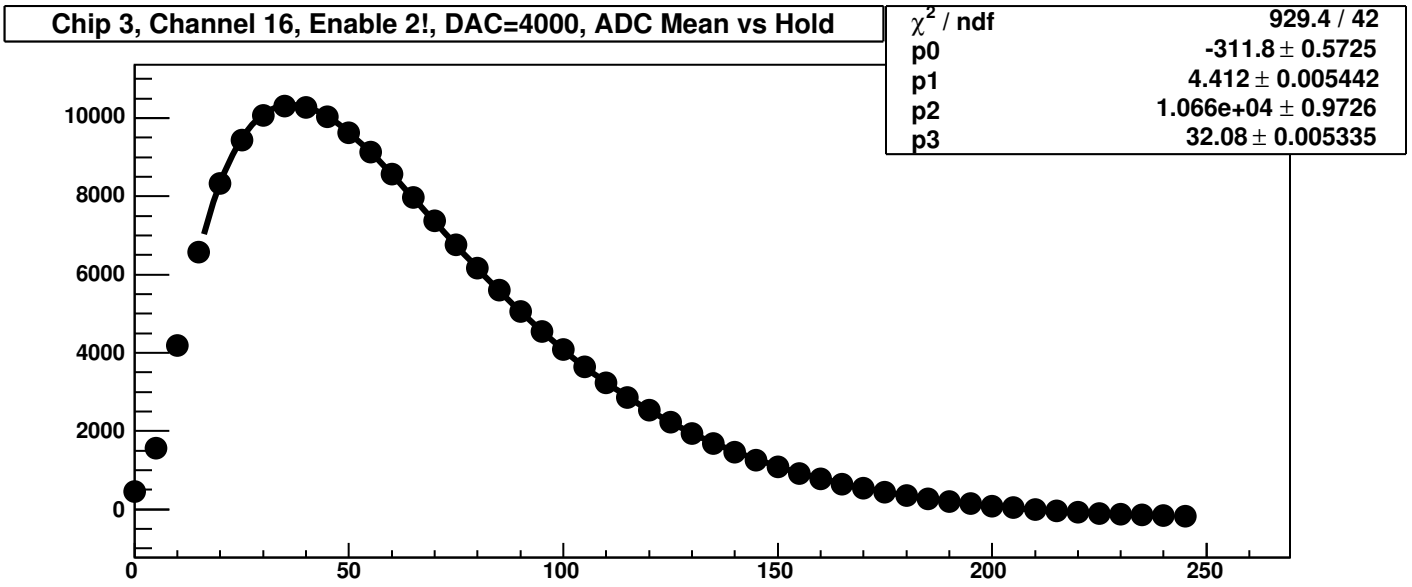


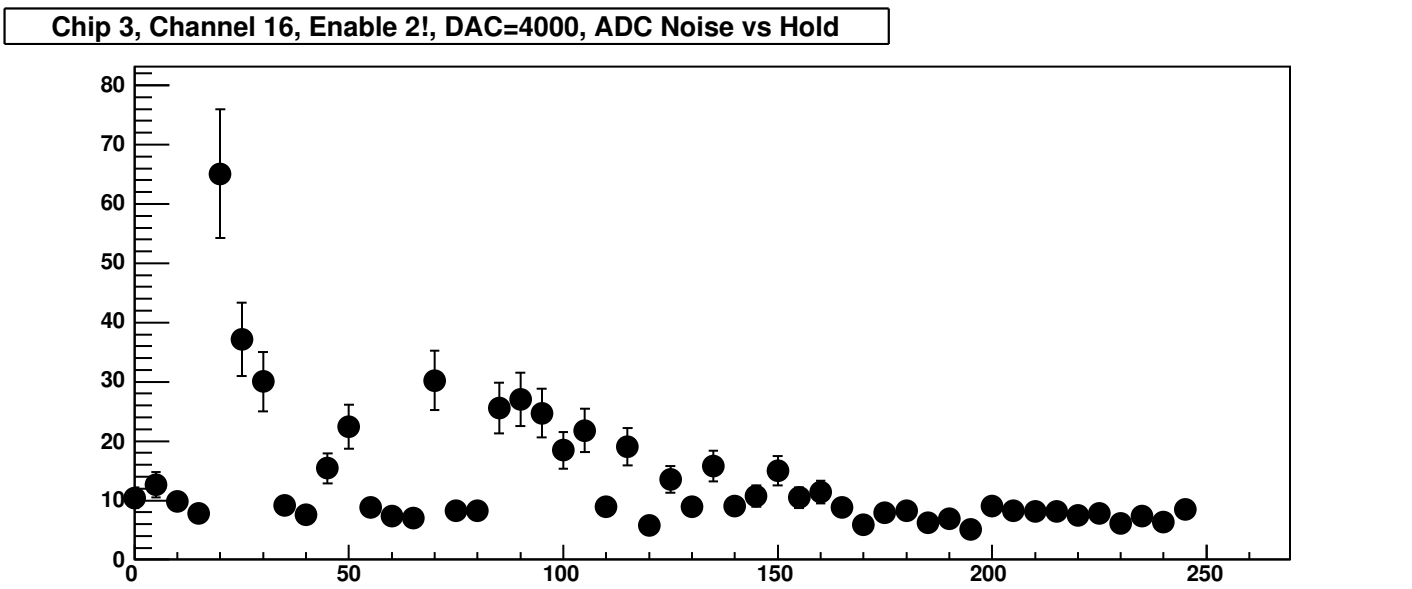
Chip 3, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold

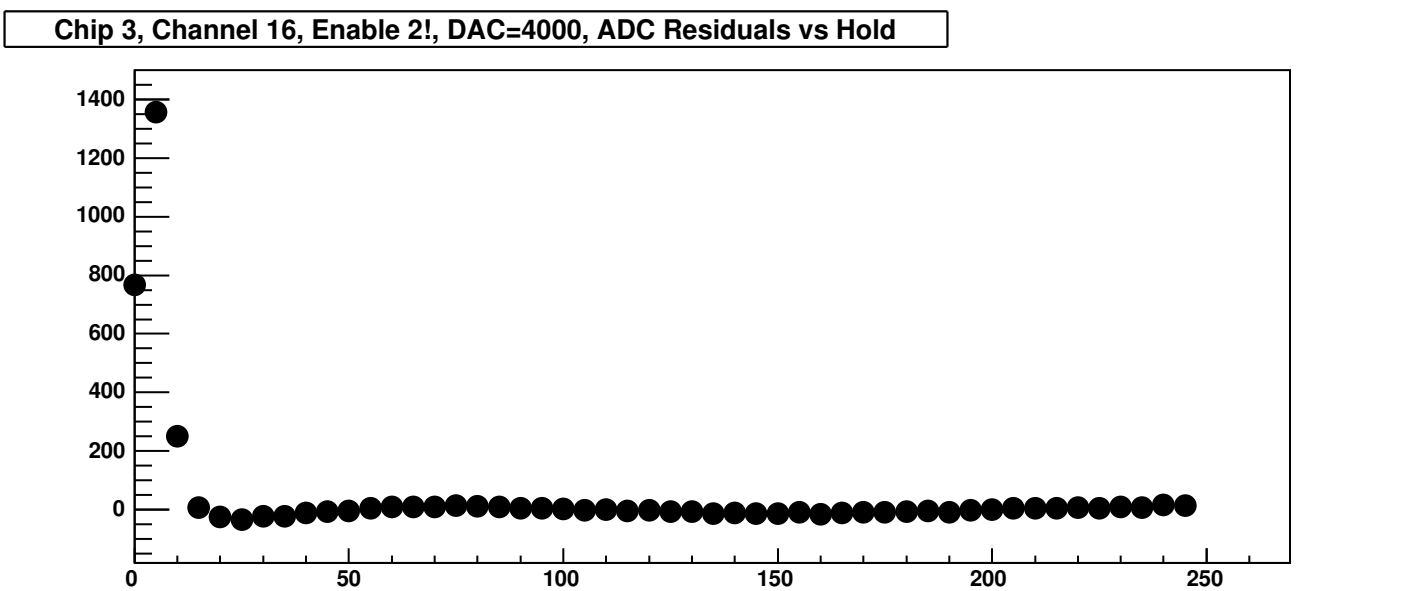


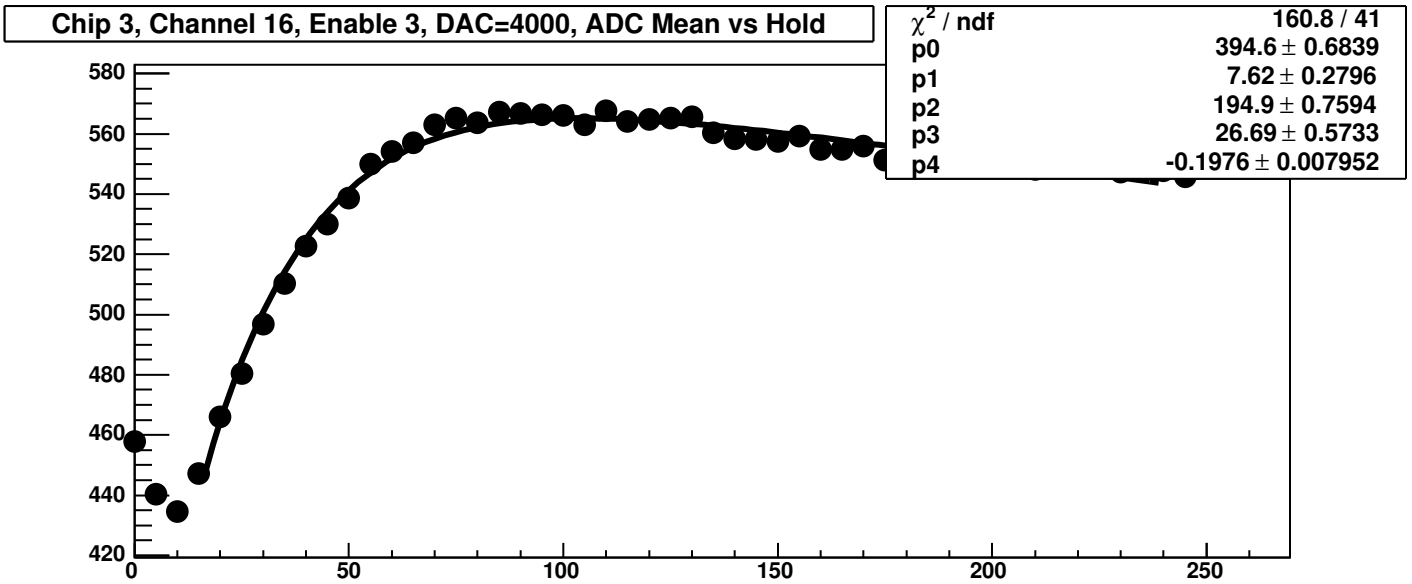
Chip 3, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold

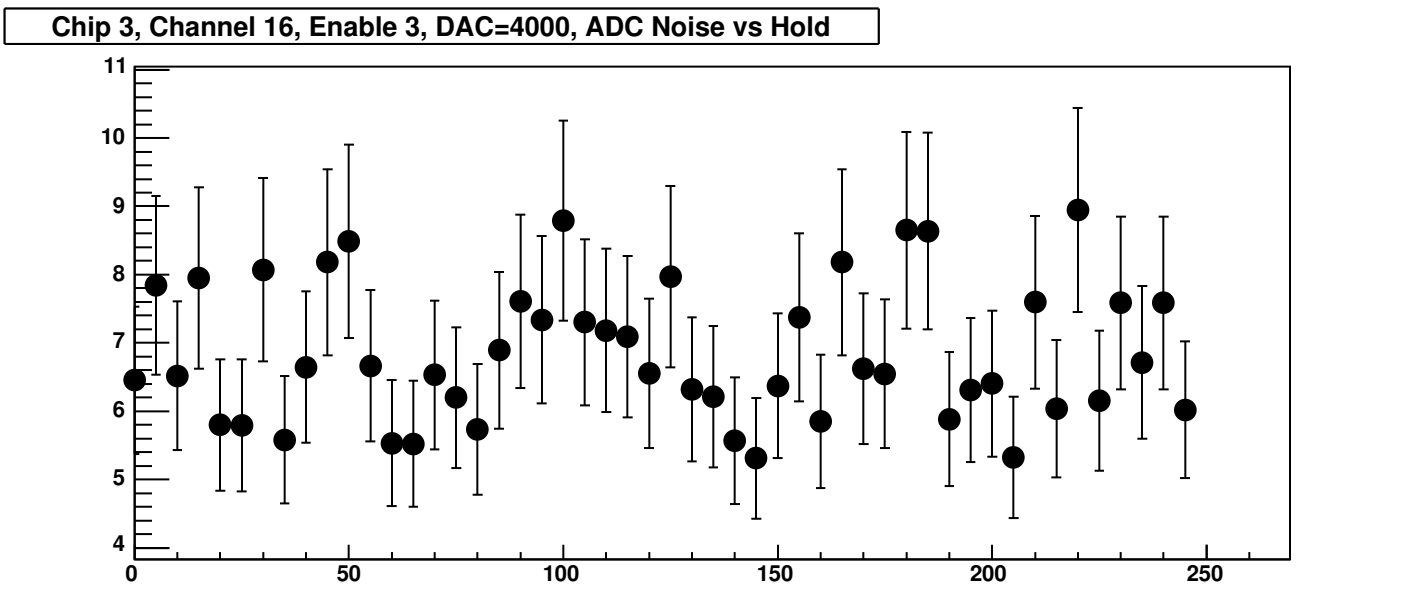


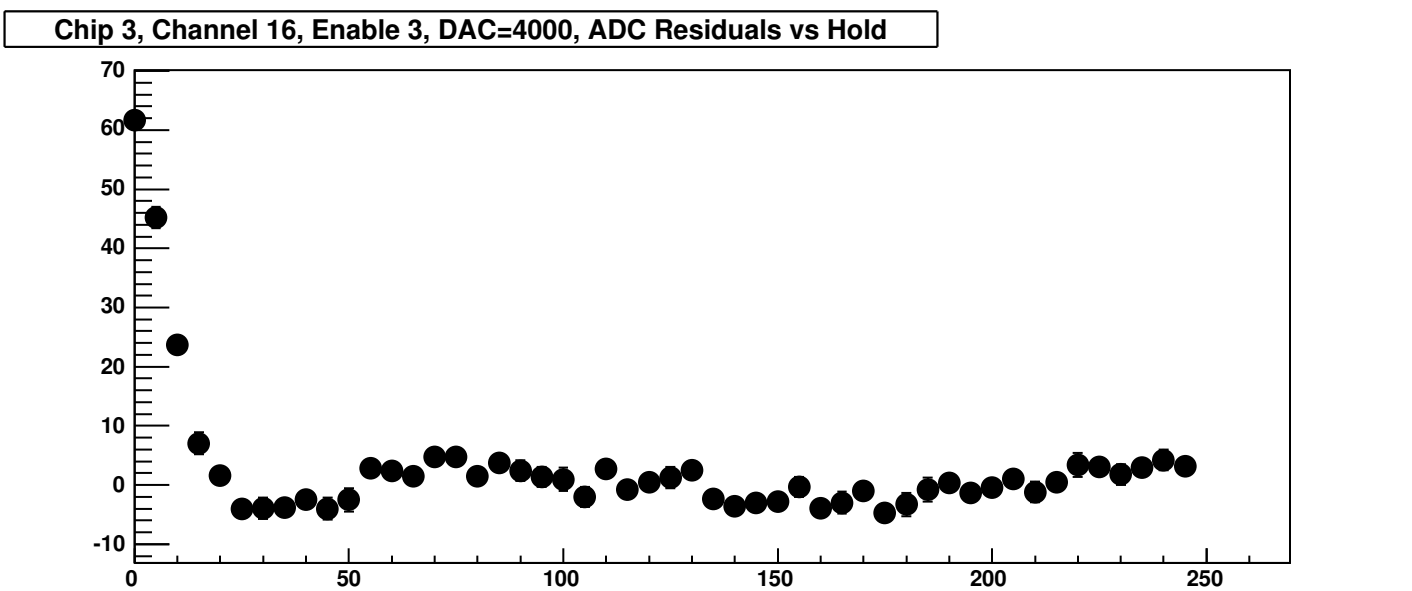

Chip 3, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 929.4 / 42
p0 -311.8 ± 0.5725
p1 4.412 ± 0.005442
p2 1.066e+04 ± 0.9726
p3 32.08 ± 0.005335
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 3, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250

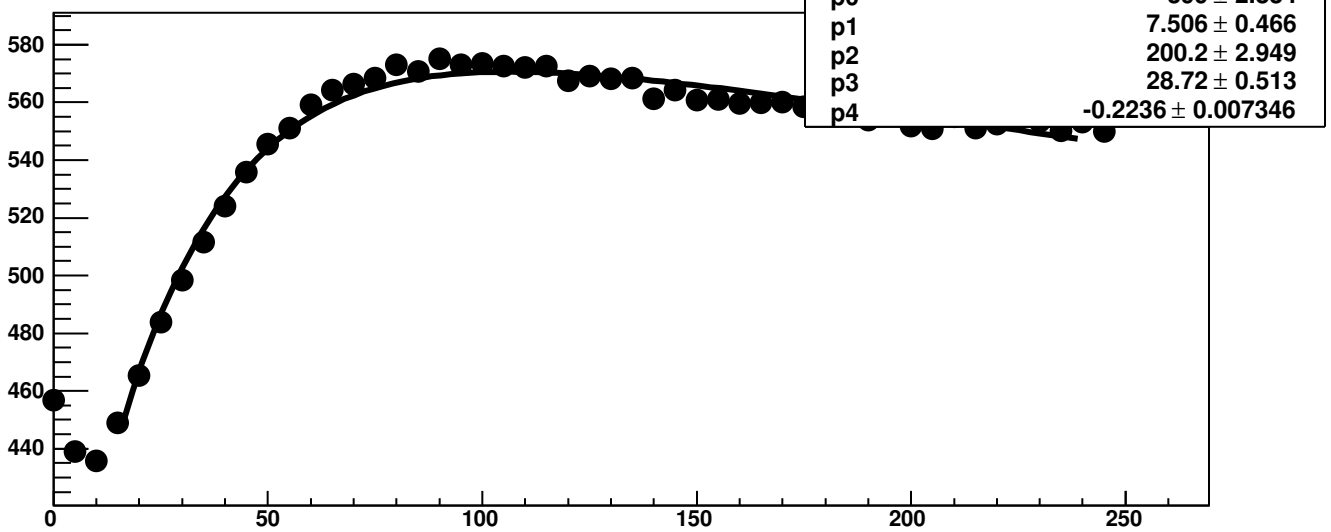

Chip 3, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250


Chip 3, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 160.8 / 41
p0 394.6 ± 0.6839
p1 7.62 ± 0.2796
p2 194.9 ± 0.7594
p3 26.69 ± 0.5733
p4 -0.1976 ± 0.007952
580
560
540
520
500
480
460
440
420
0
50
100
150
200
250

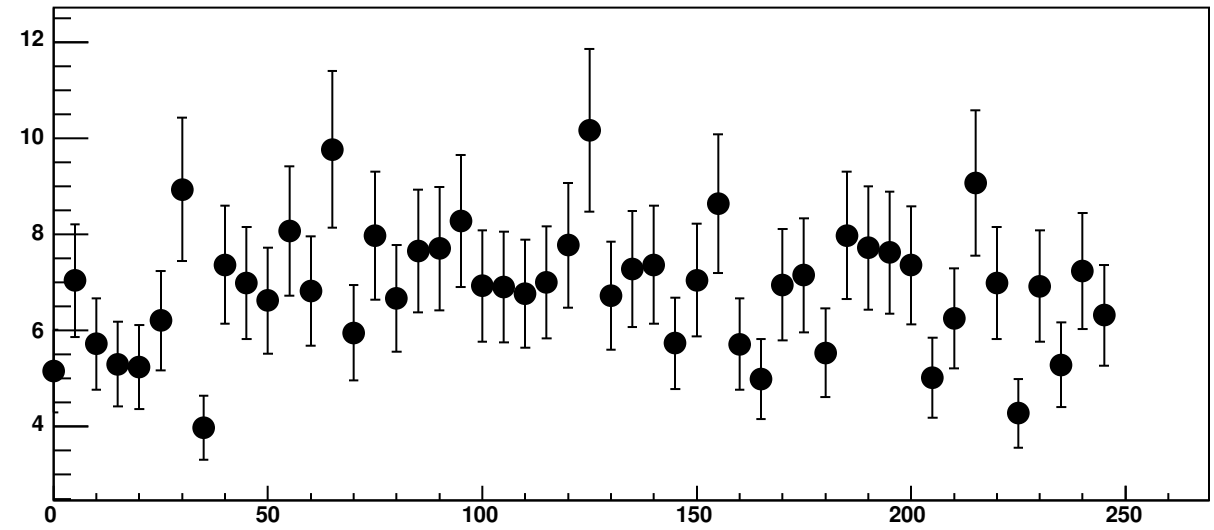

Chip 3, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 3, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold
70
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

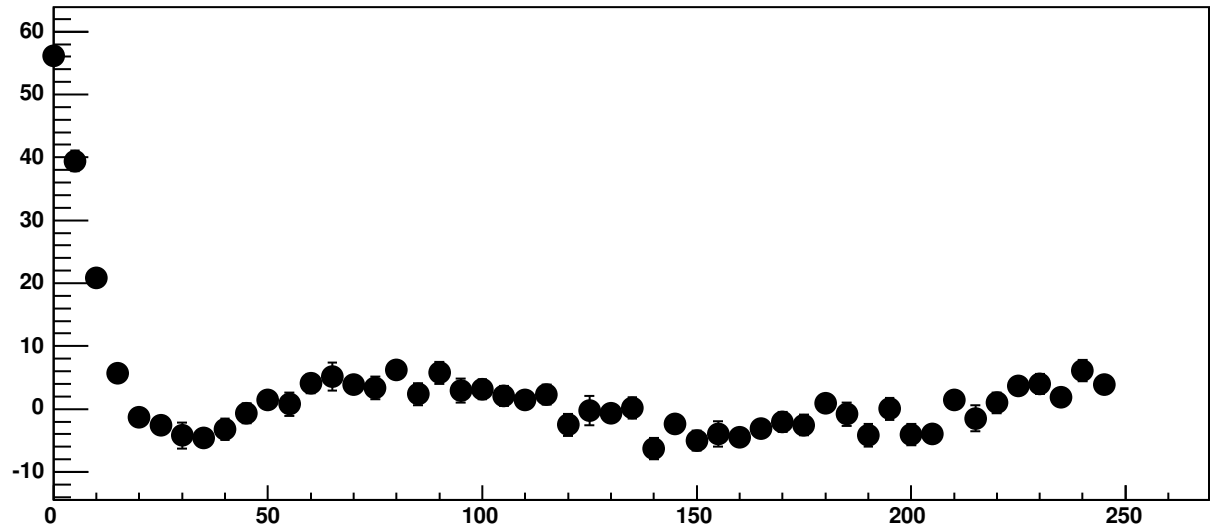
Chip 3, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold



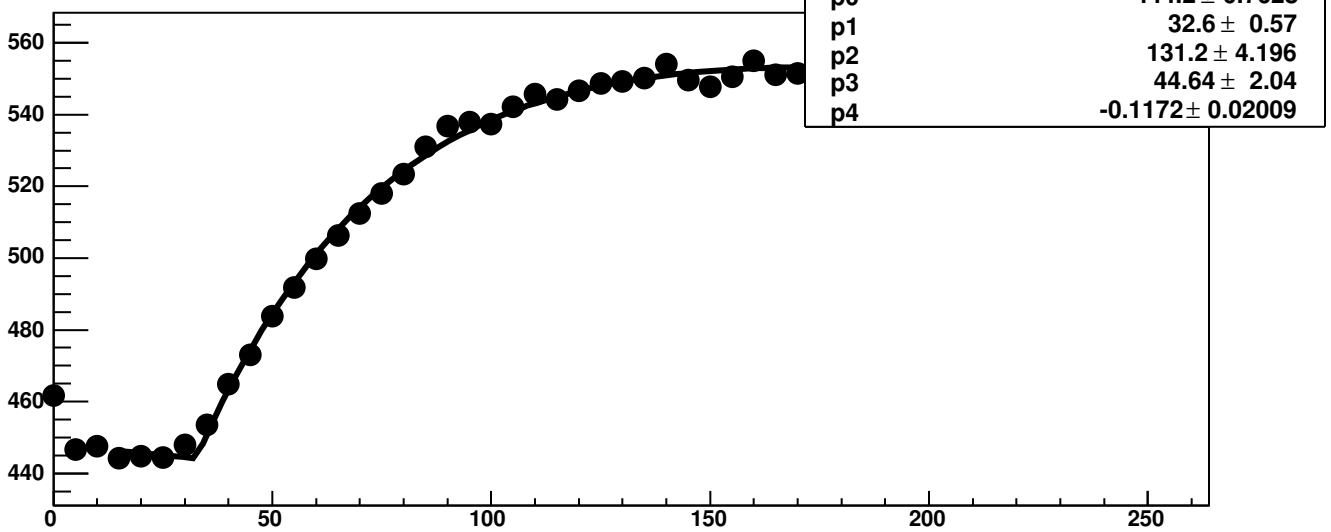
Chip 3, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold



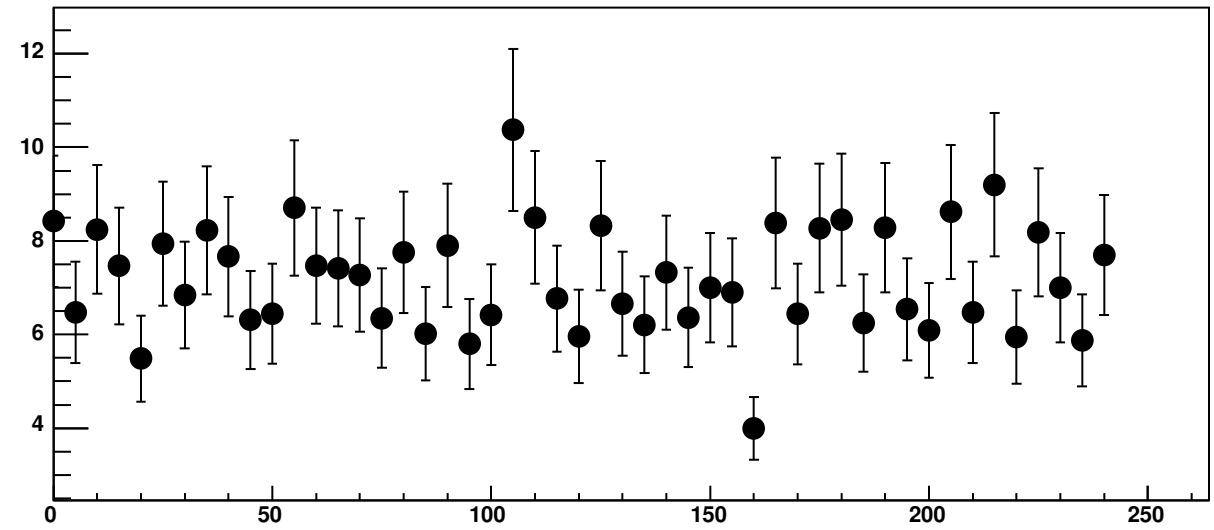
Chip 3, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold



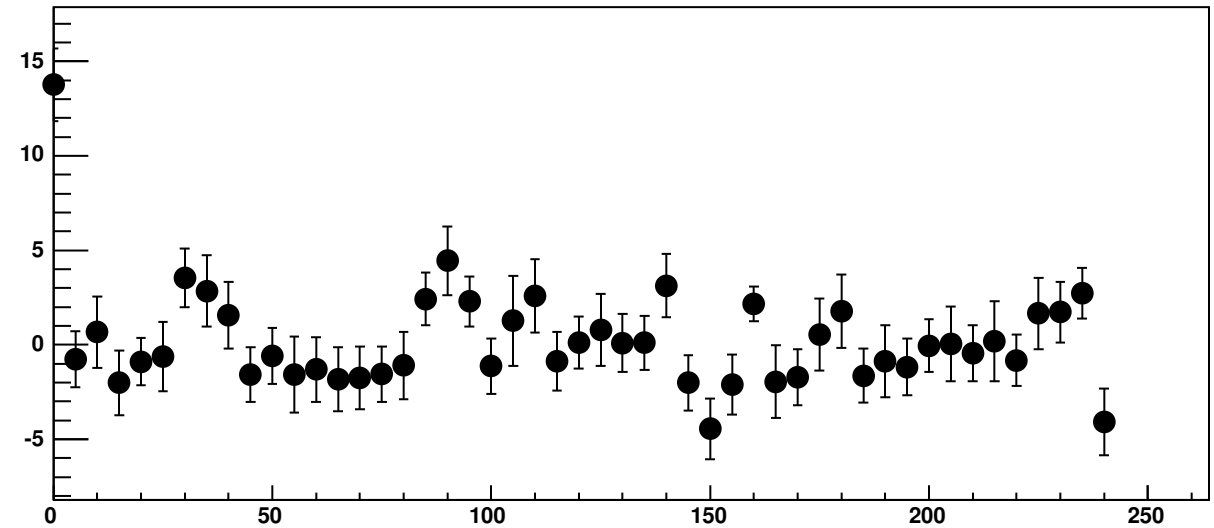
Chip 3, Channel 16, Enable 5, DAC=4000, ADC Mean vs Hold



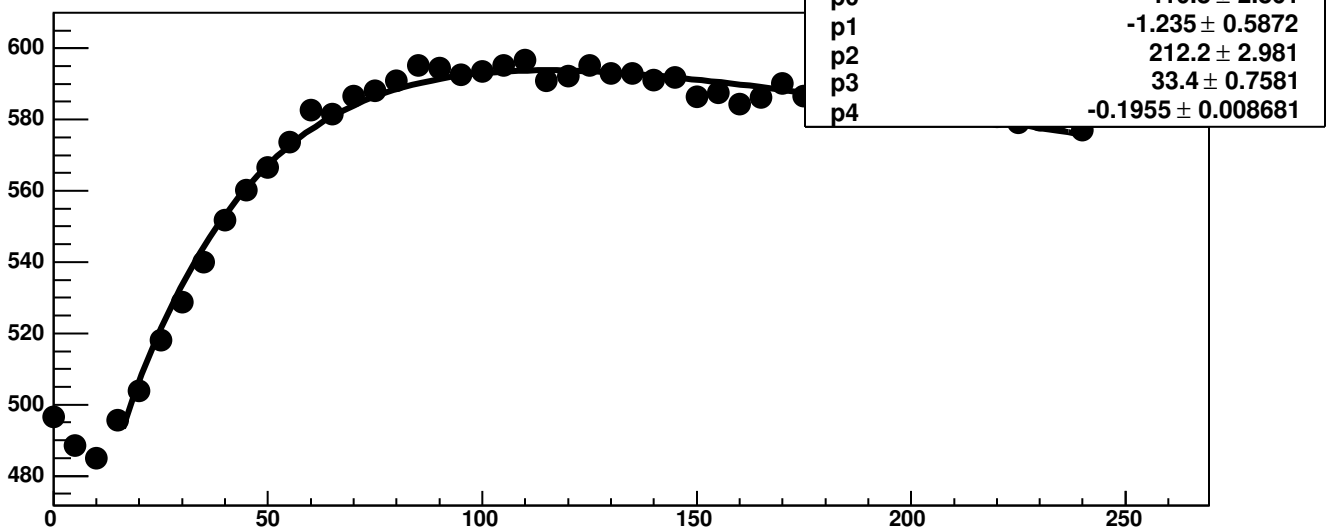
Chip 3, Channel 16, Enable 5, DAC=4000, ADC Noise vs Hold



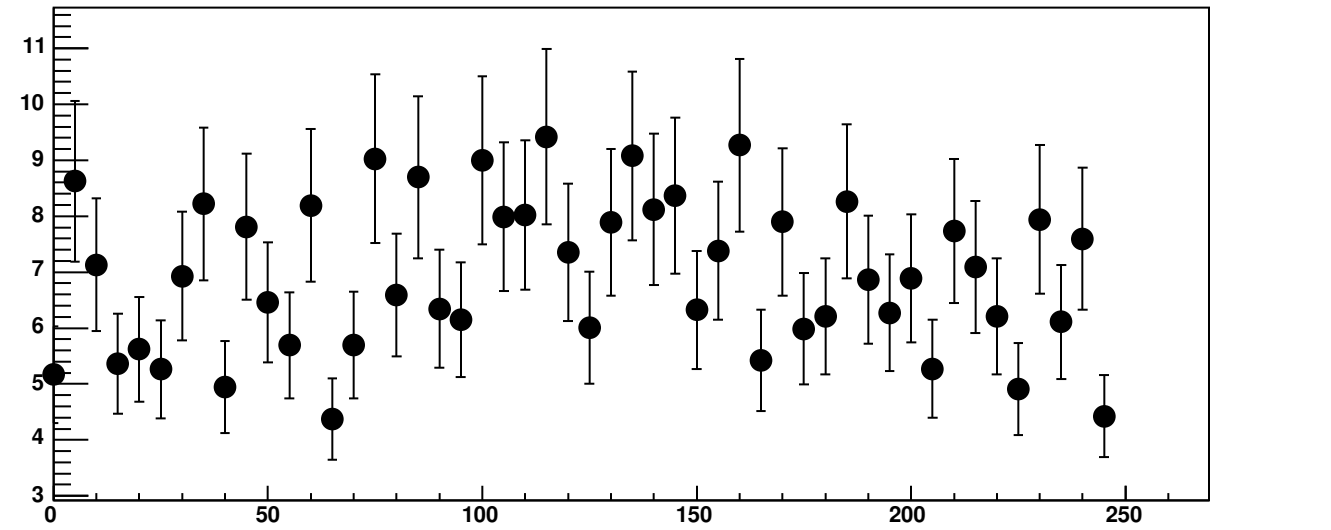
Chip 3, Channel 16, Enable 5, DAC=4000, ADC Residuals vs Hold



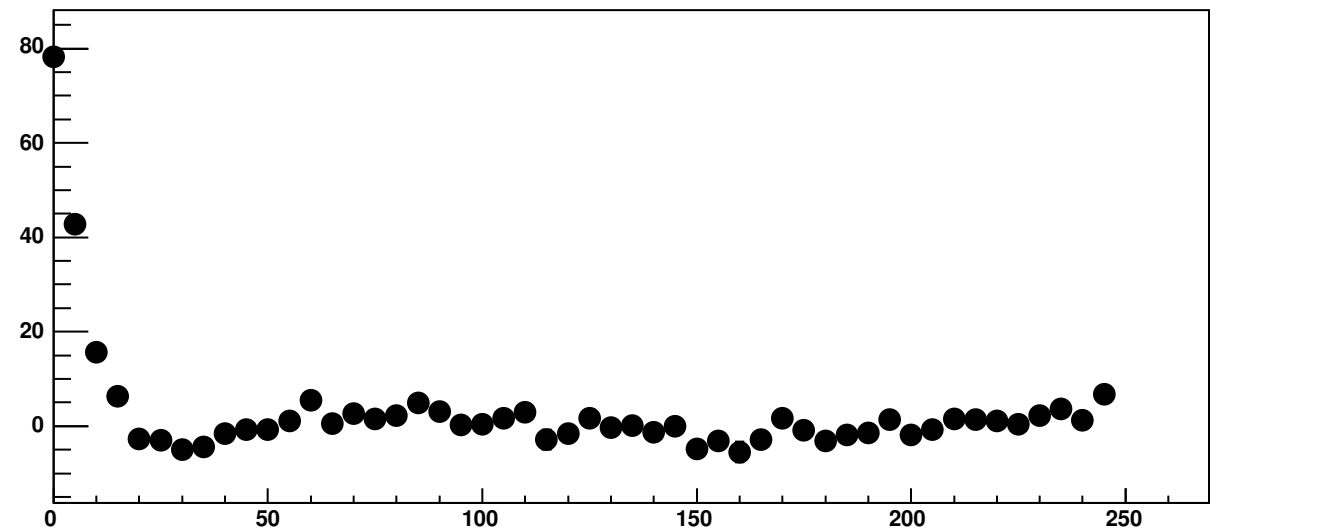
Chip 3, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold



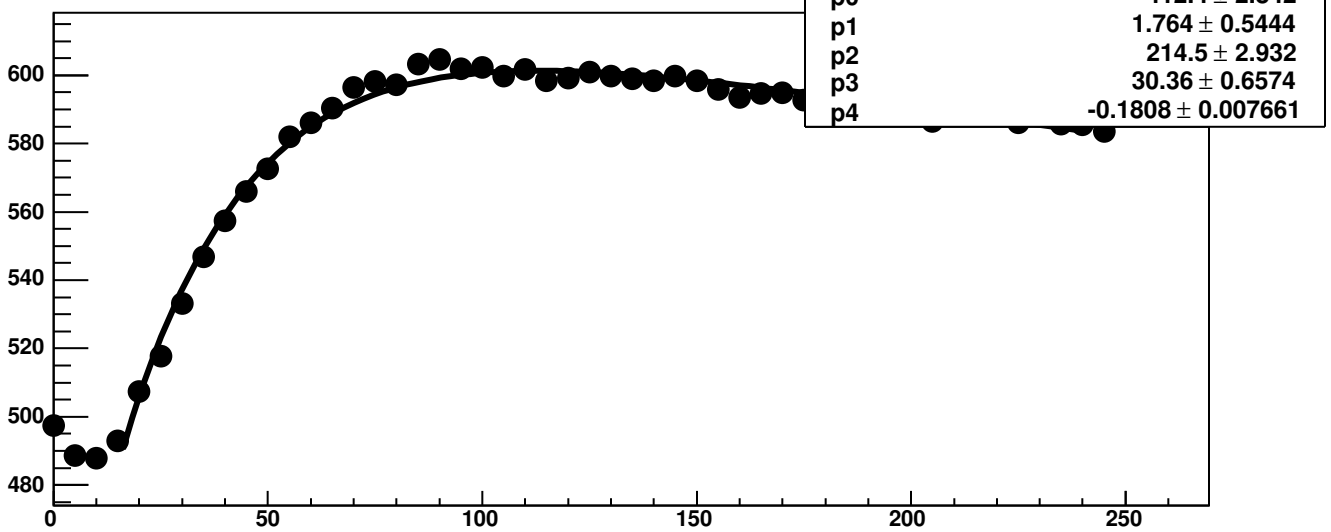
Chip 3, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



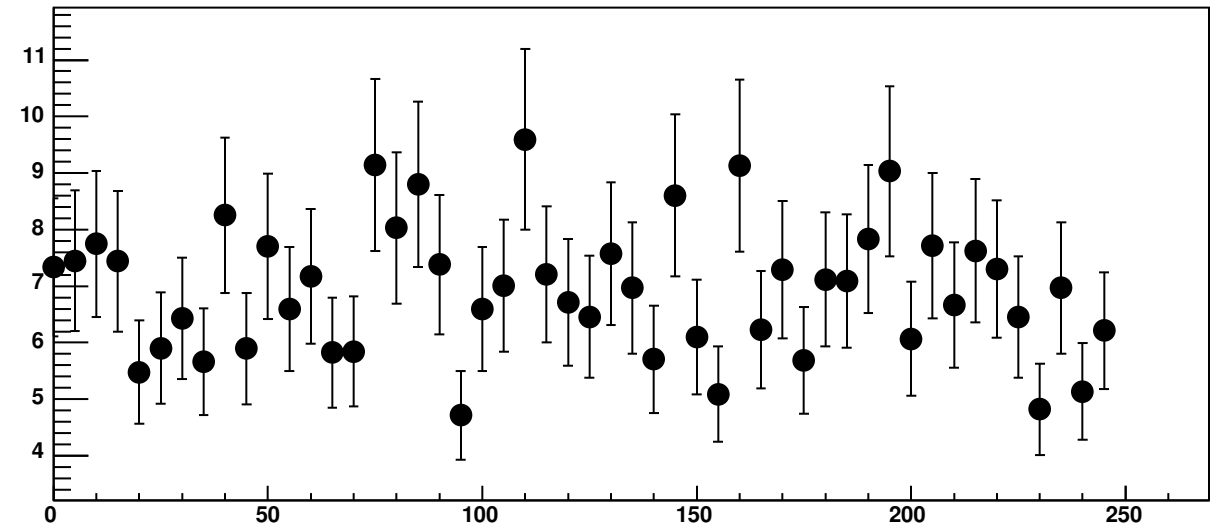
Chip 3, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold



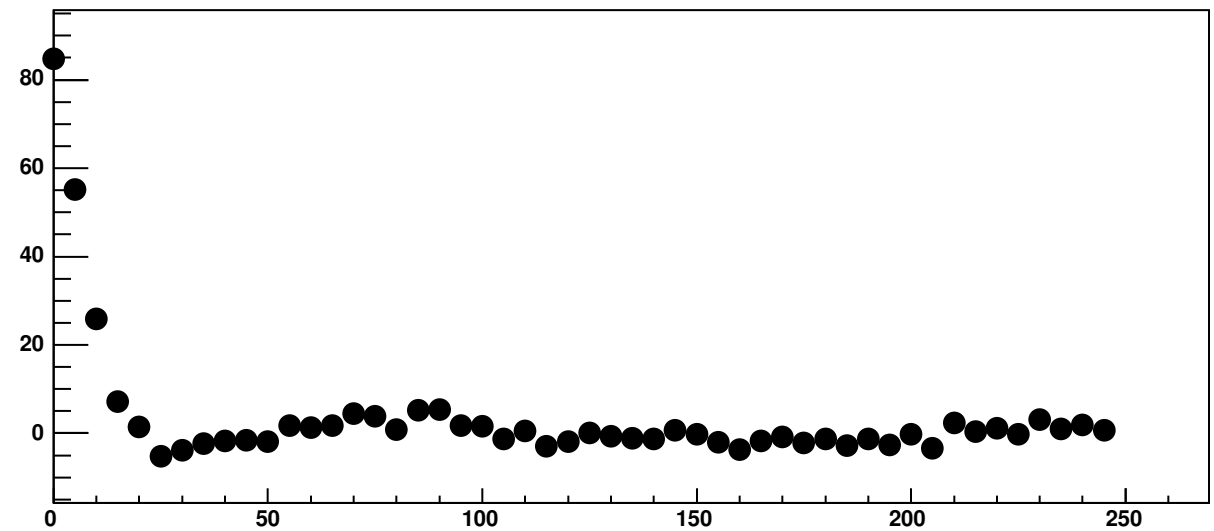
Chip 3, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold



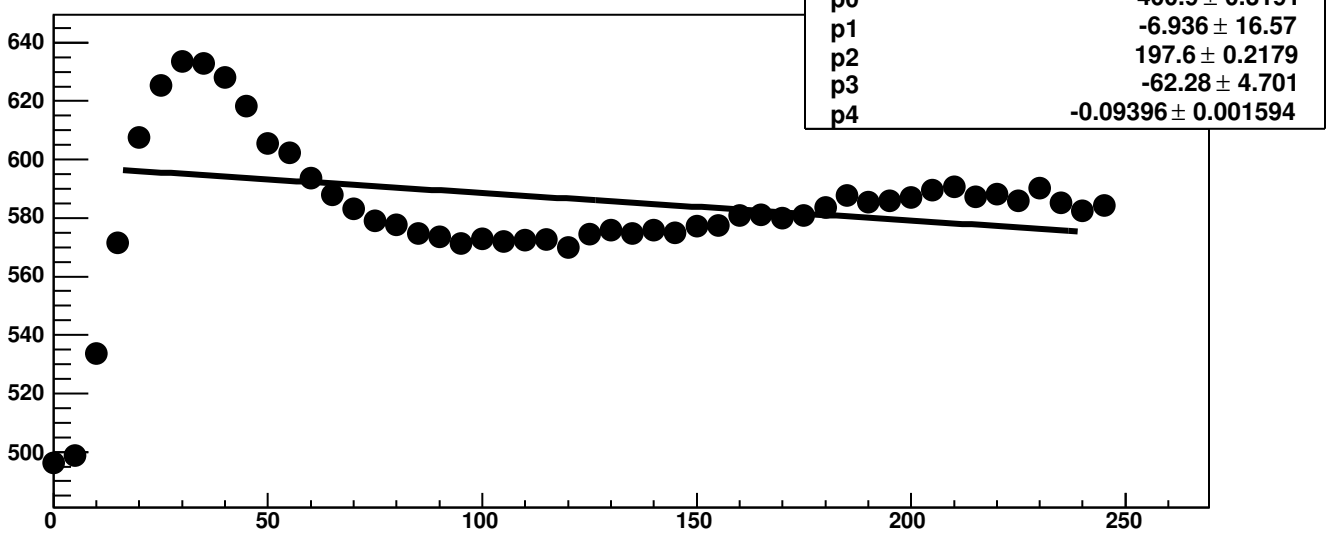
Chip 3, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



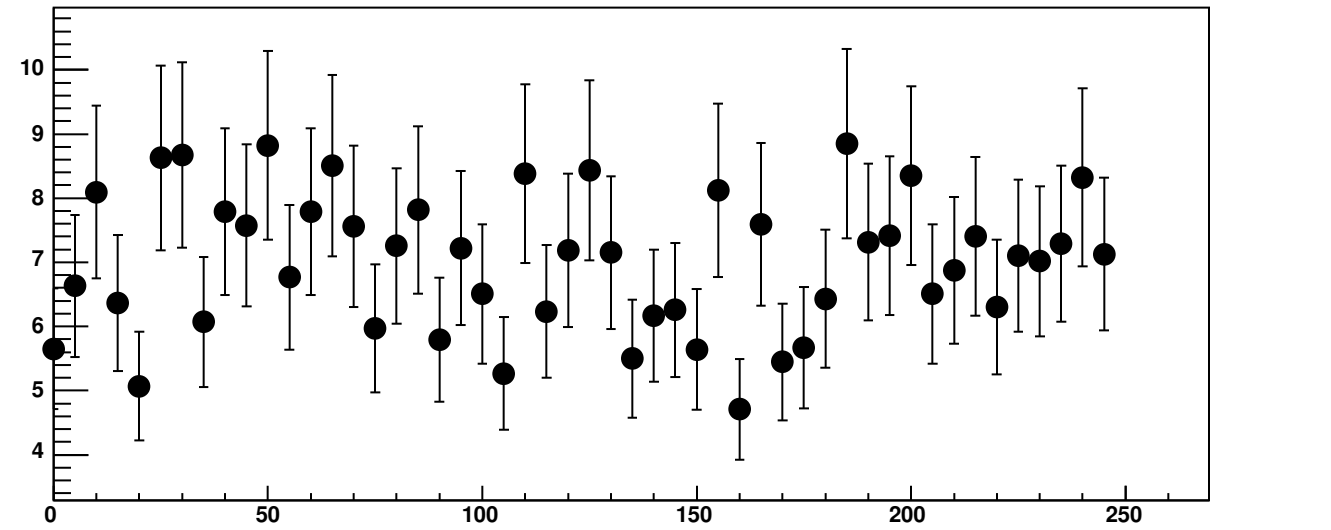
Chip 3, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold



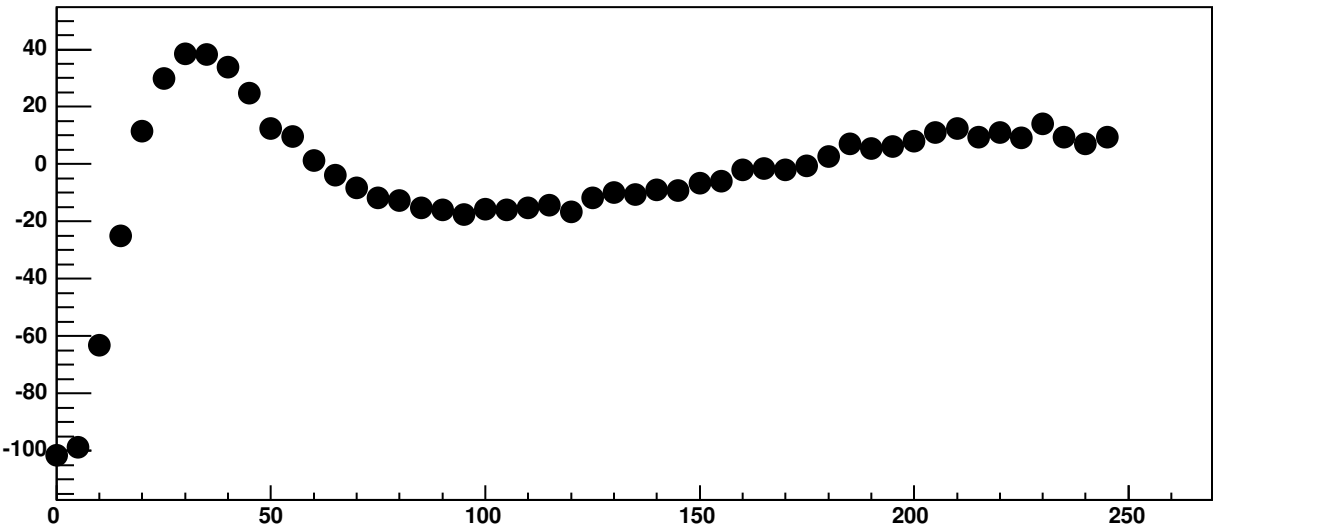
Chip 3, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold



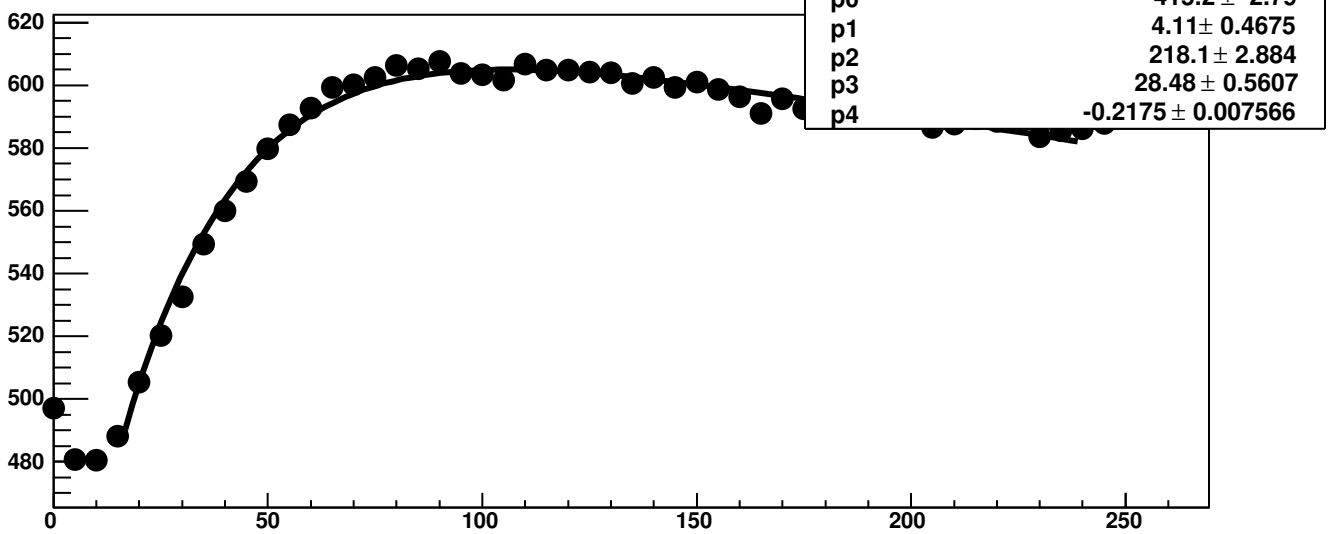
Chip 3, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold



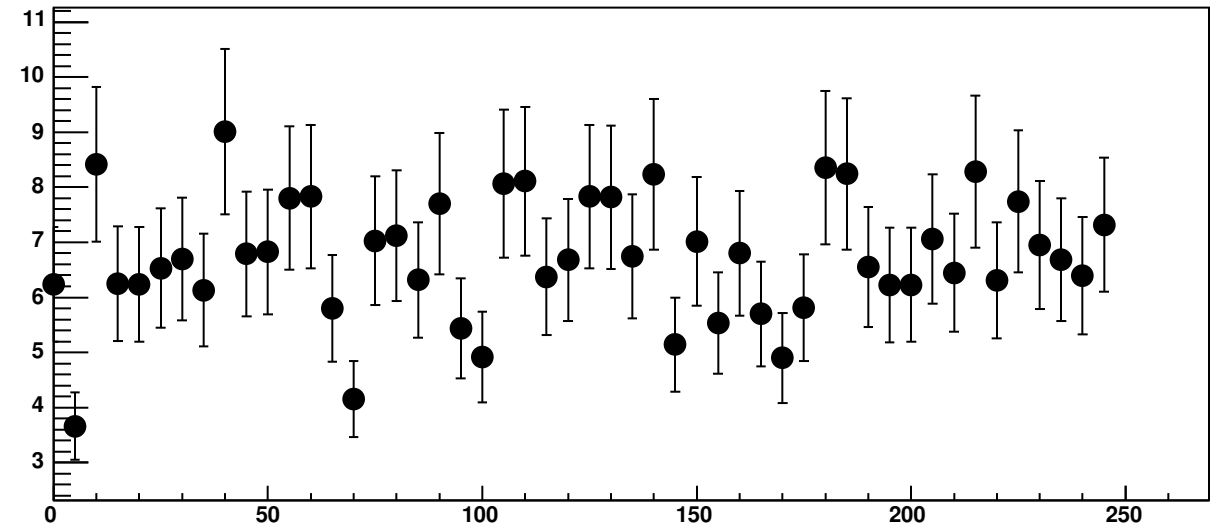
Chip 3, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold



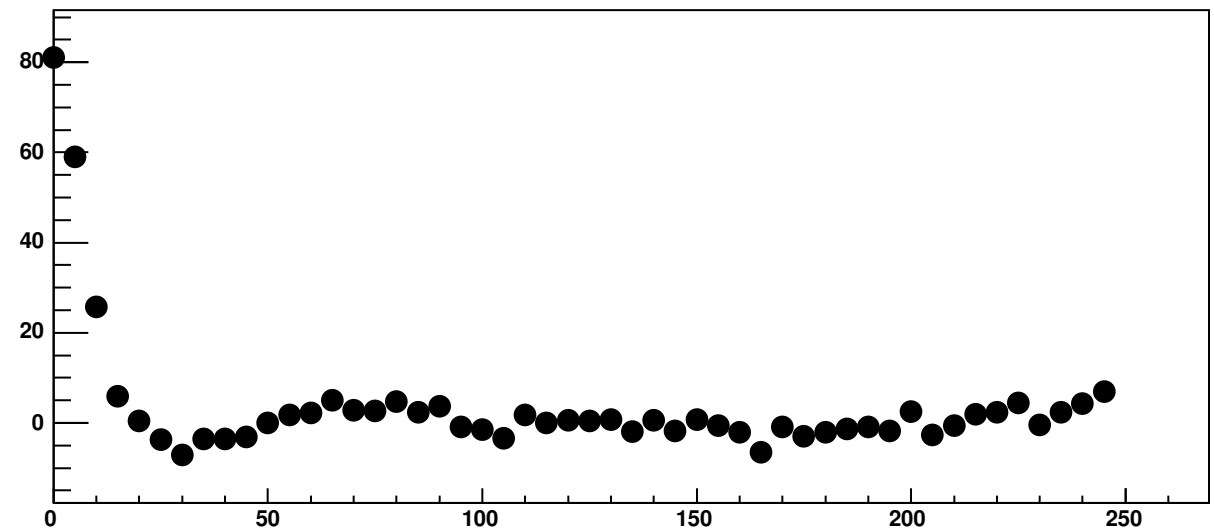
Chip 3, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



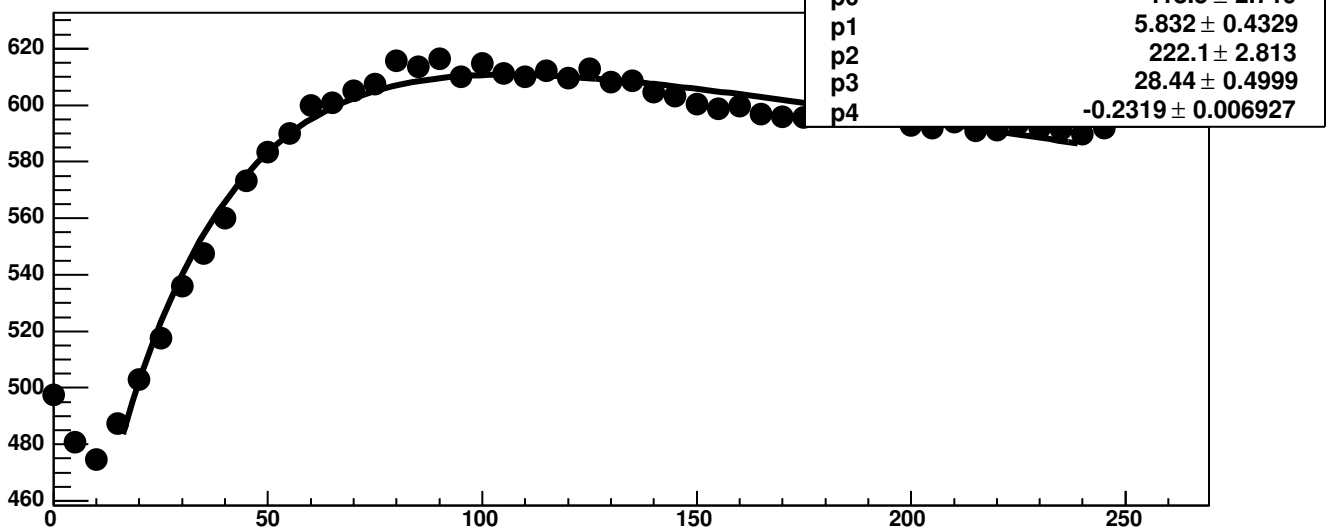
Chip 3, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



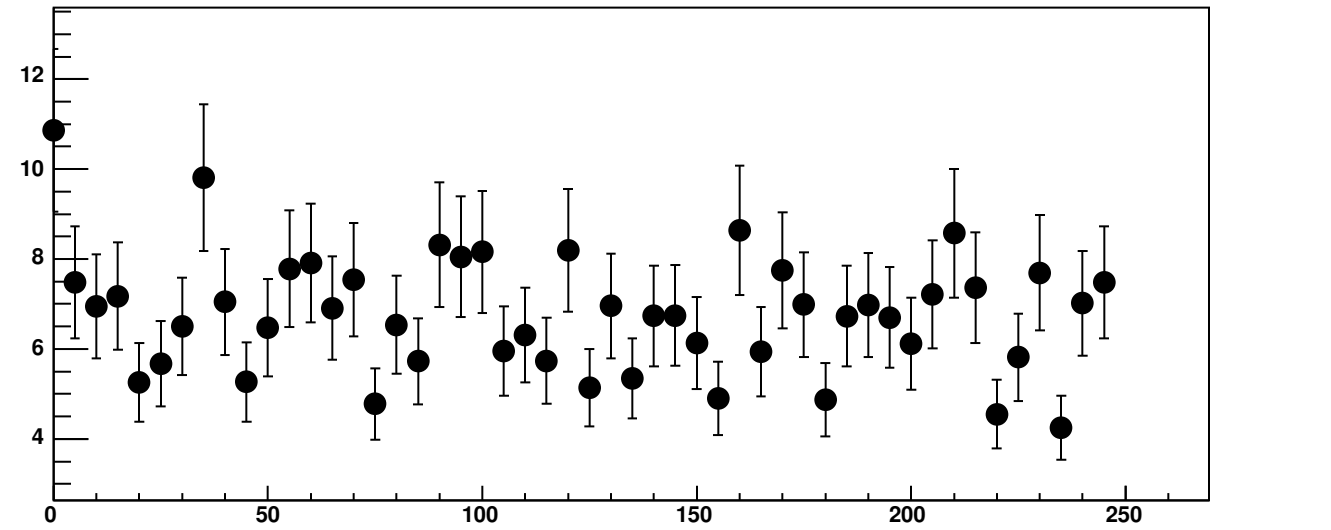
Chip 3, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold



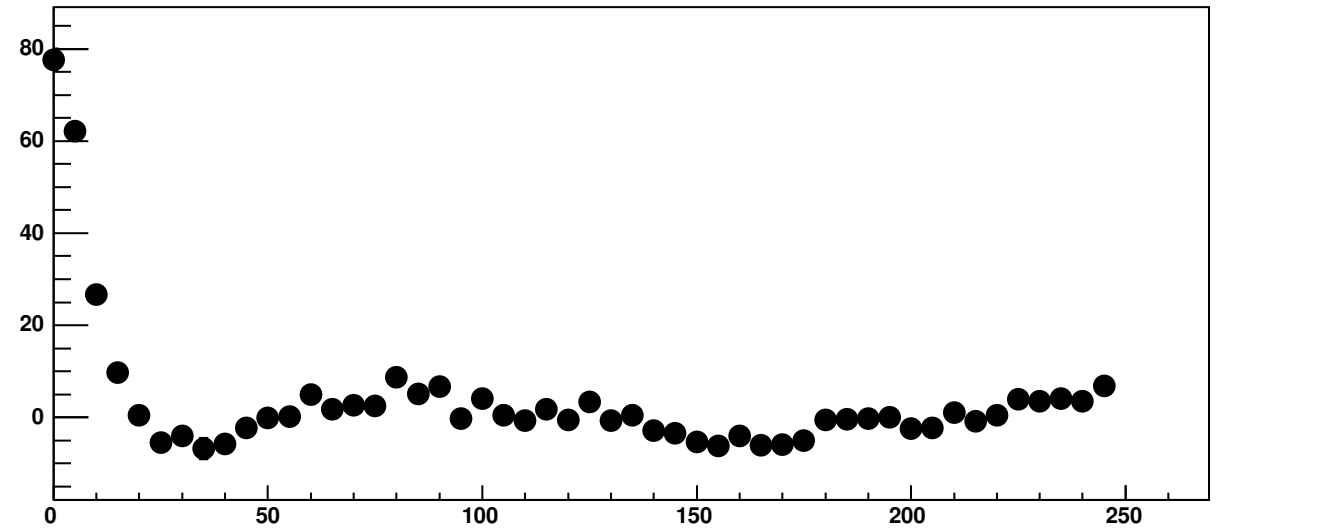
Chip 3, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



Chip 3, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold

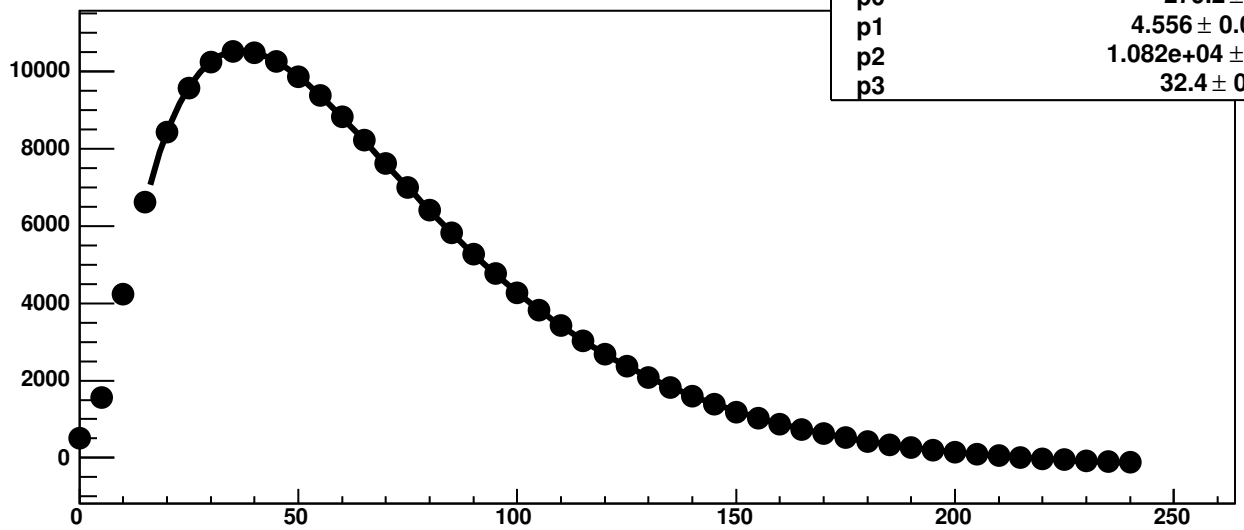


Chip 3, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold

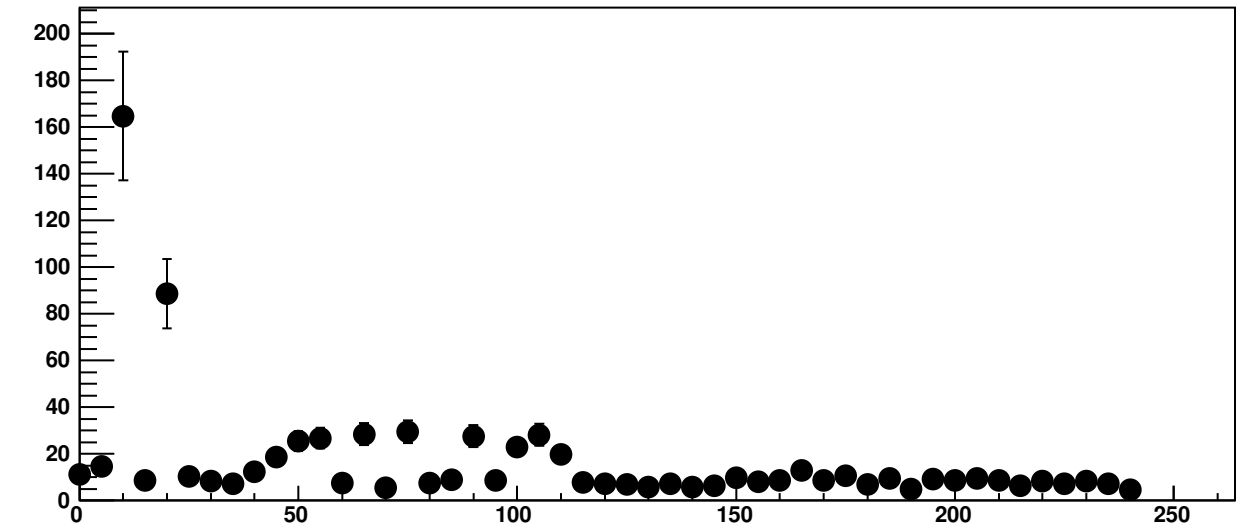


Chip 3, Channel 17, Enable 5!, DAC=4000, ADC Mean vs Hold

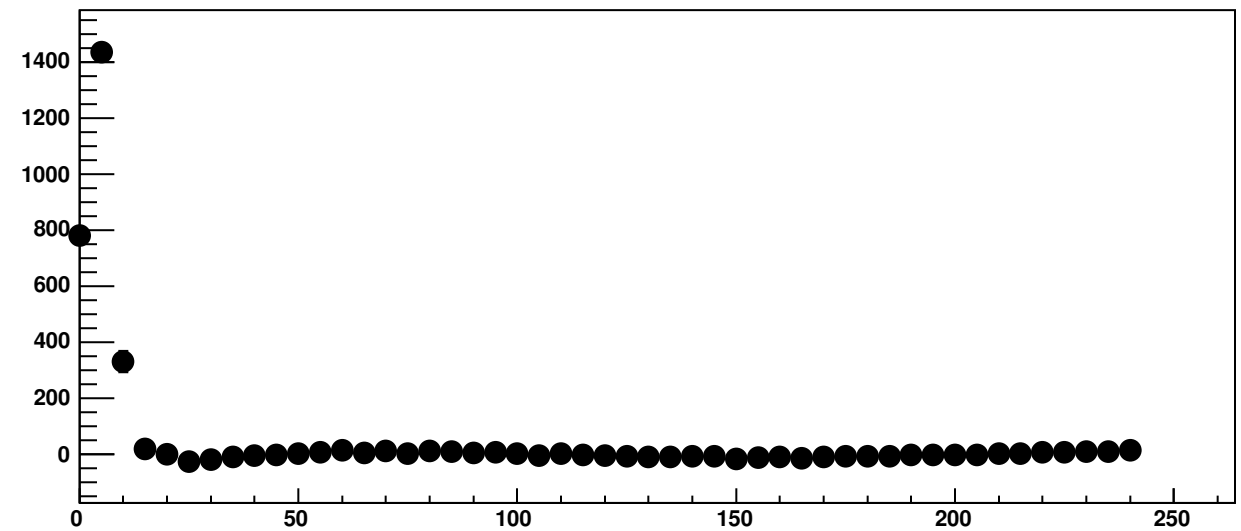
χ^2 / ndf	1212 / 42
p0	-279.2 ± 0.5621
p1	4.556 ± 0.005598
p2	1.082e+04 ± 0.9251
p3	32.4 ± 0.00464

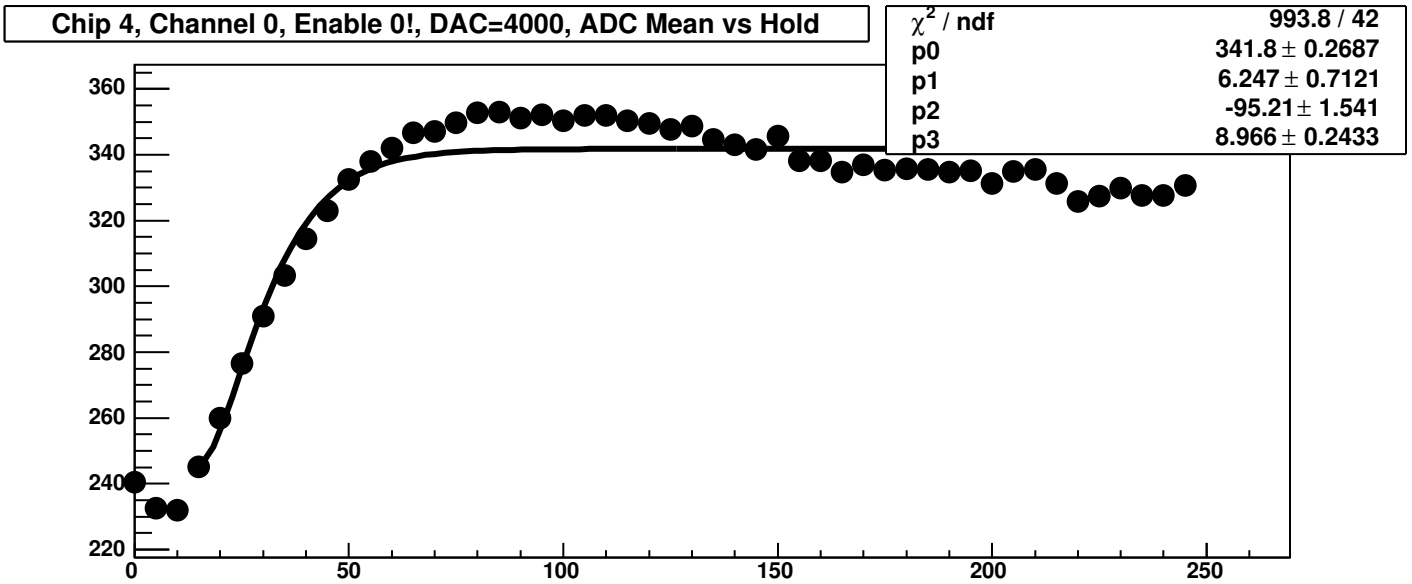


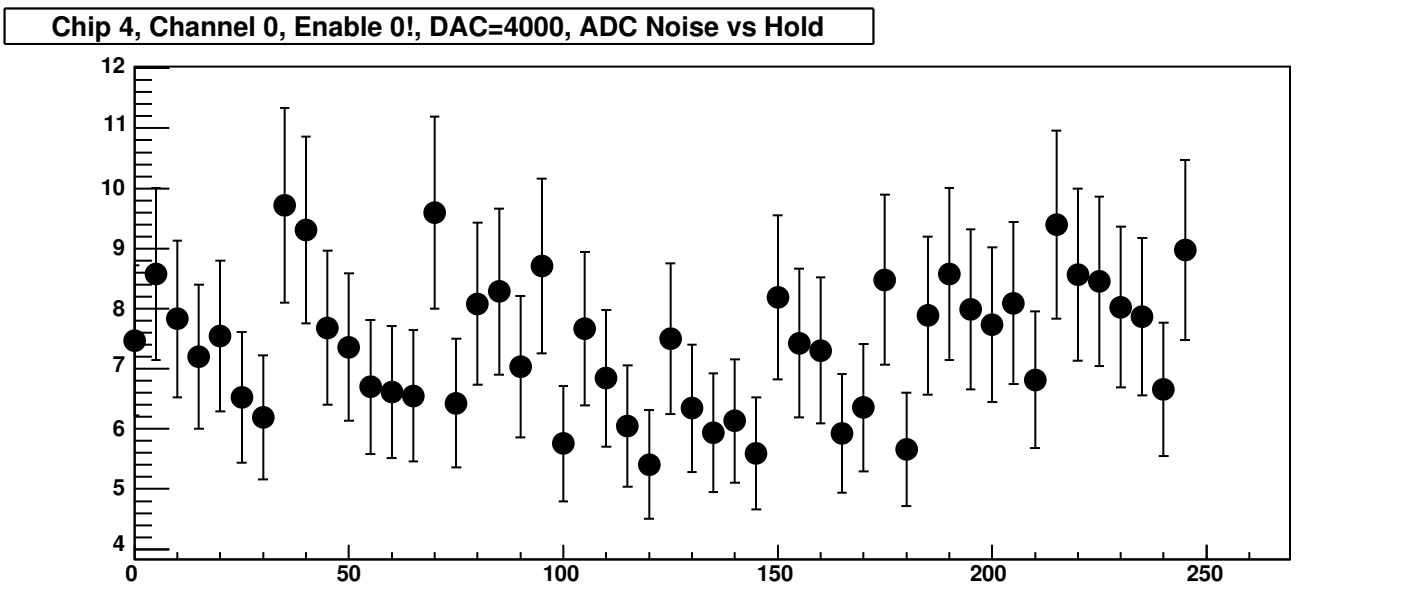
Chip 3, Channel 17, Enable 5!, DAC=4000, ADC Noise vs Hold

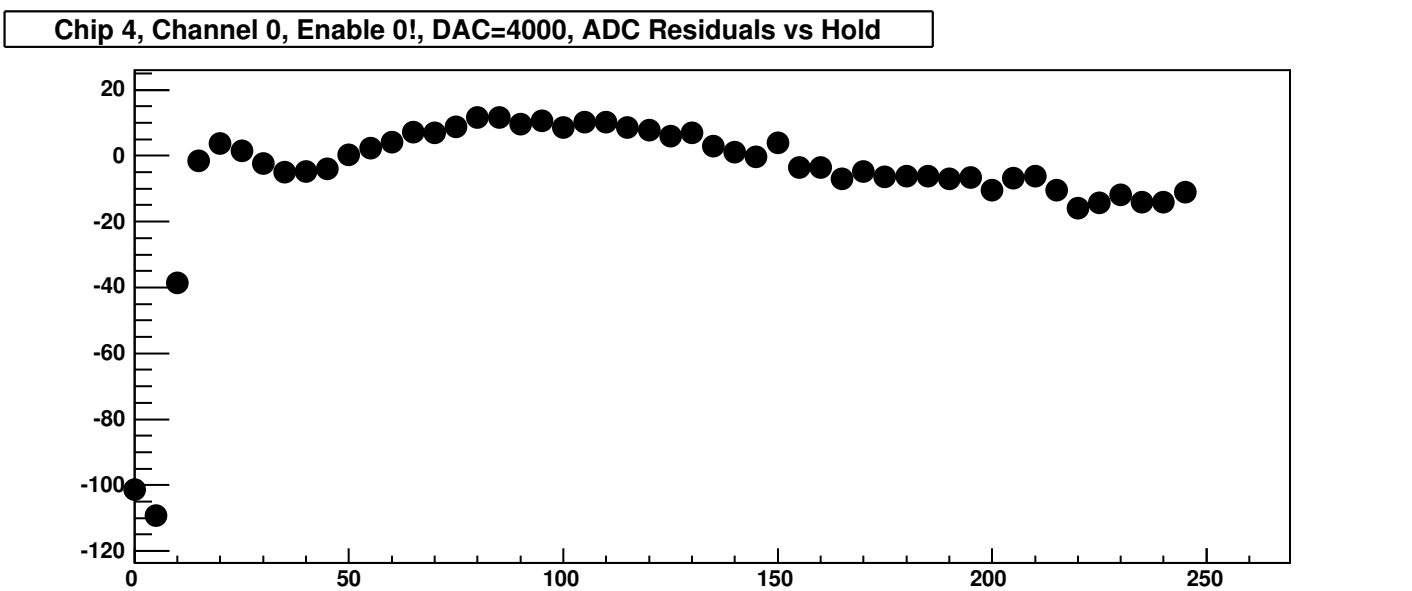


Chip 3, Channel 17, Enable 5!, DAC=4000, ADC Residuals vs Hold

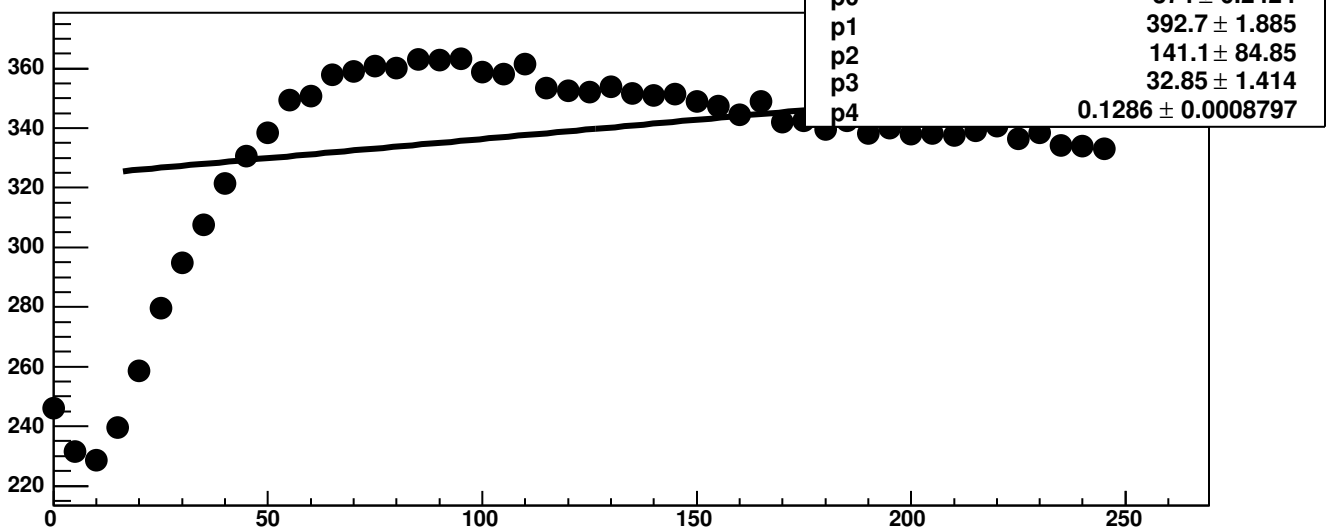



Chip 4, Channel 0, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 993.8 / 42
p0 341.8 ± 0.2687
p1 6.247 ± 0.7121
p2 -95.21 ± 1.541
p3 8.966 ± 0.2433
360
340
320
300
280
260
240
220
0
50
100
150
200
250

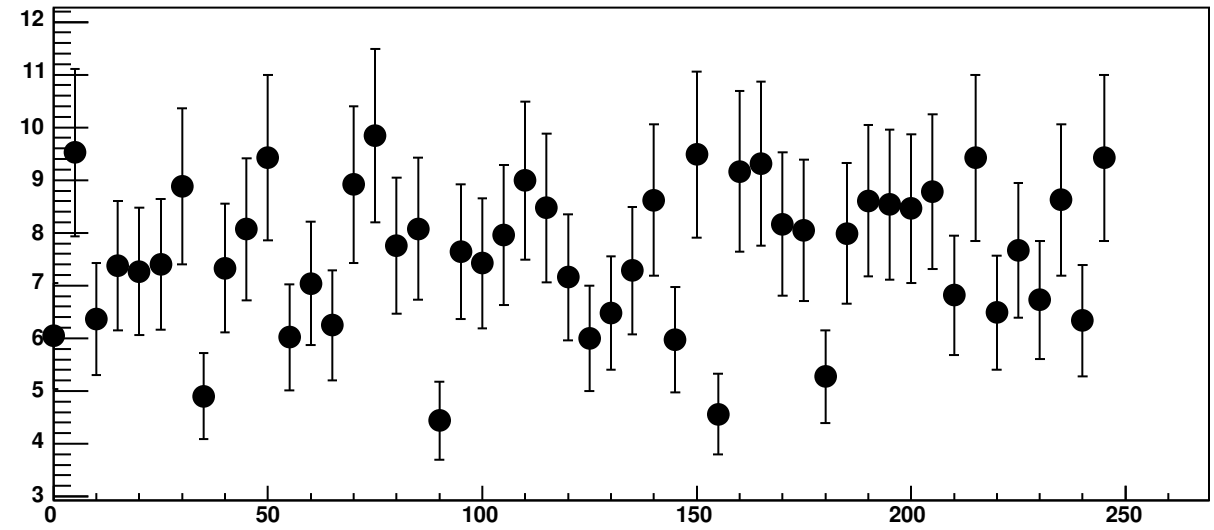

Chip 4, Channel 0, Enable 0!, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 4, Channel 0, Enable 0!, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
-80
-100
-120
0
50
100
150
200
250

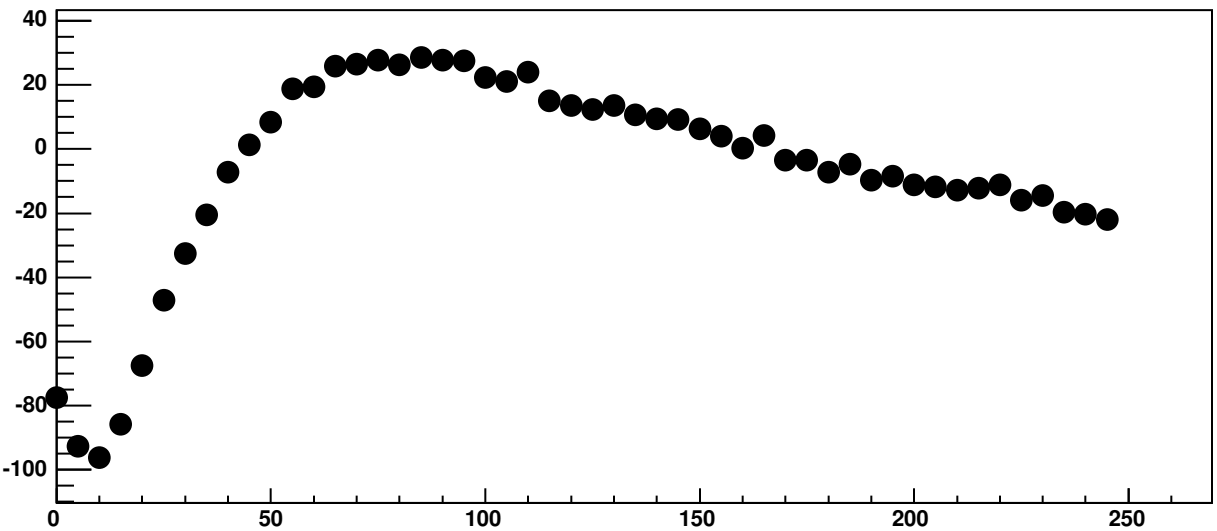
Chip 4, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold



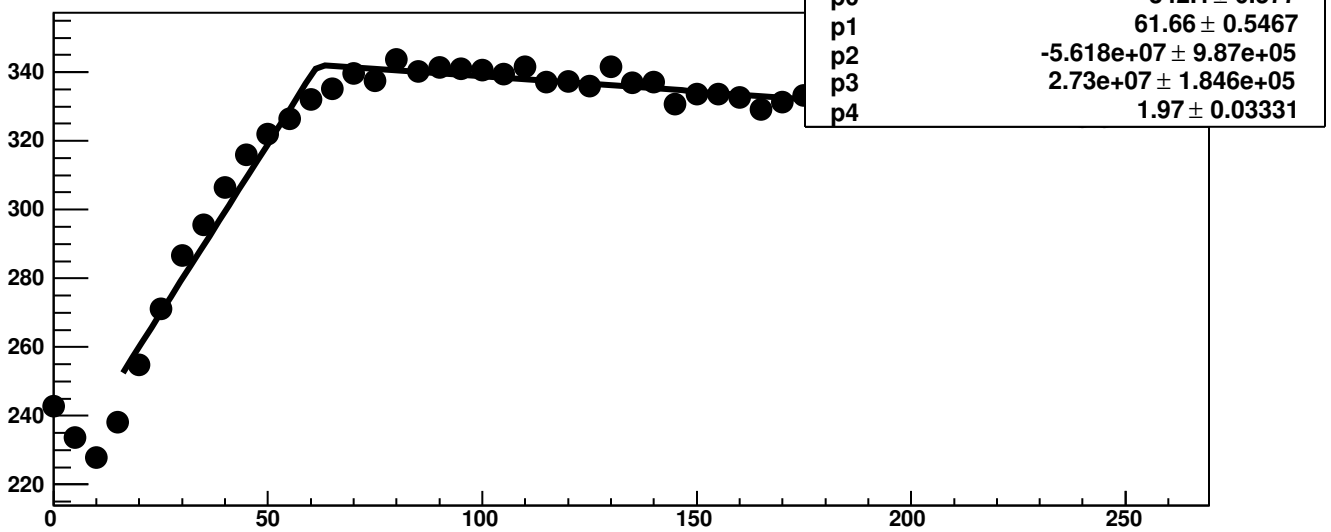
Chip 4, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



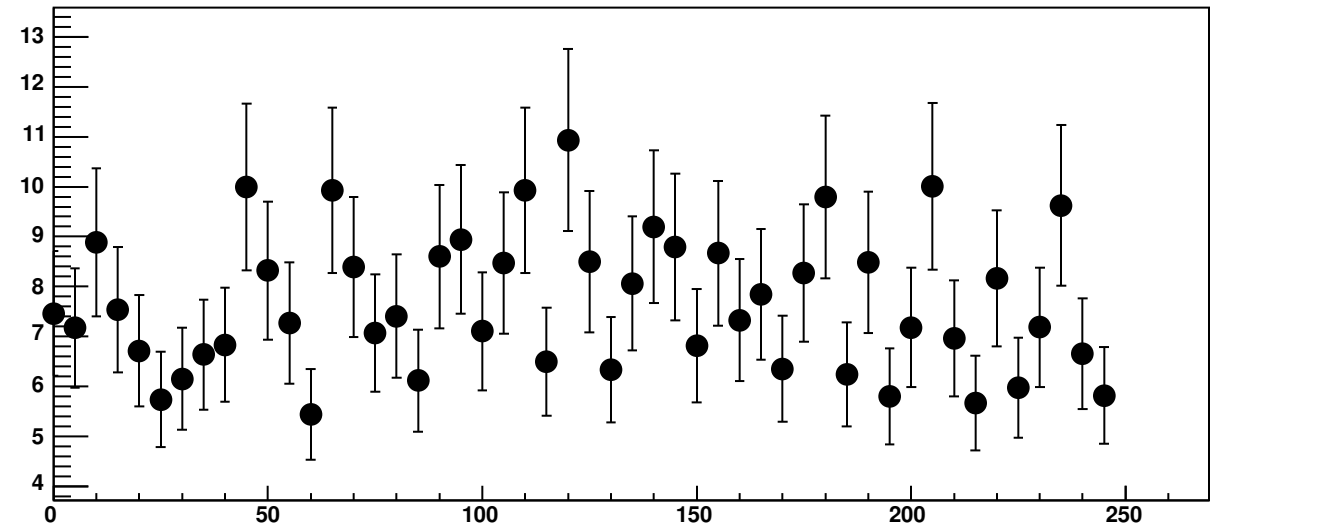
Chip 4, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold



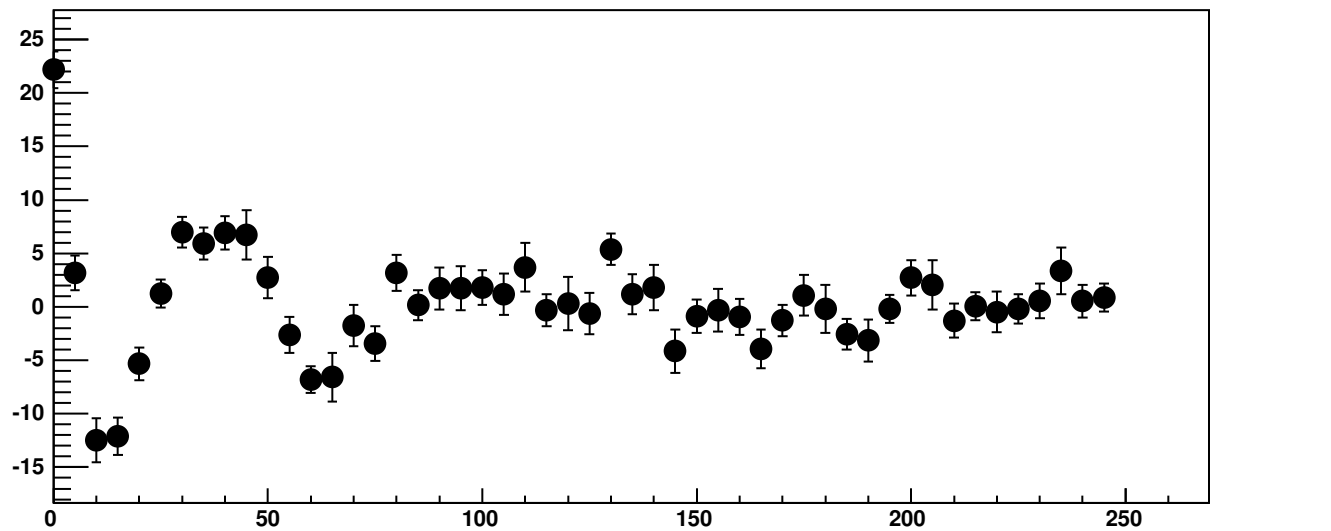
Chip 4, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold



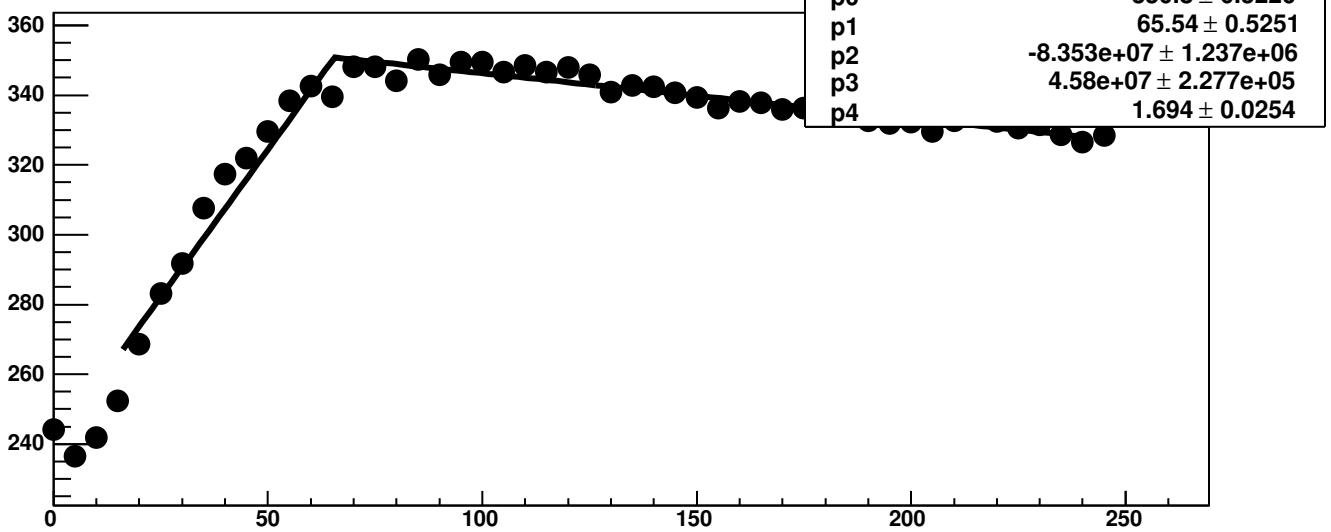
Chip 4, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



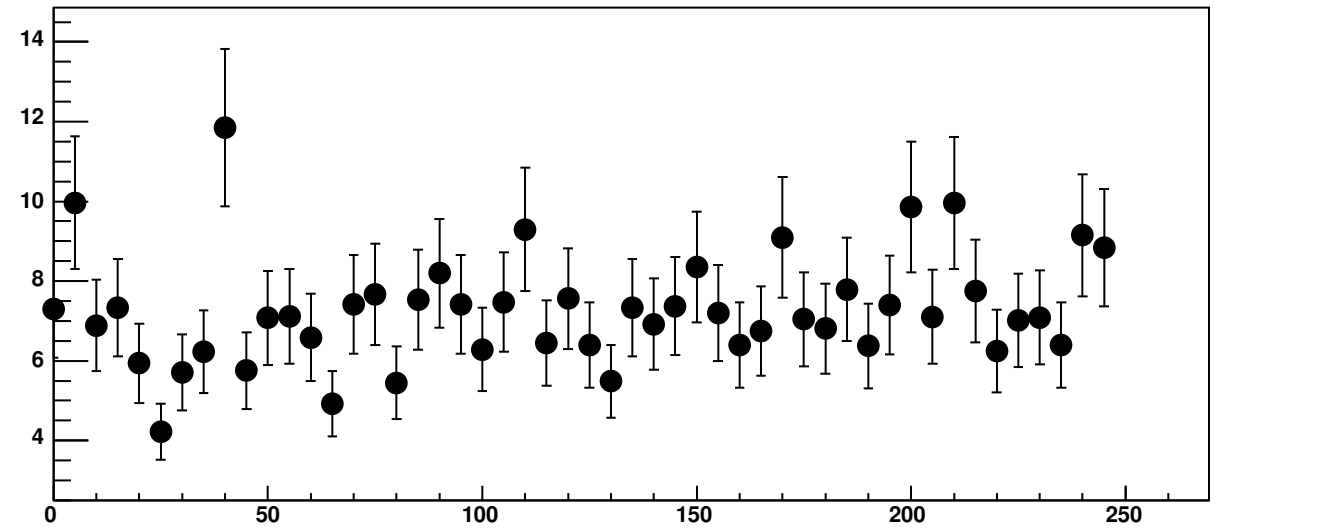
Chip 4, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold



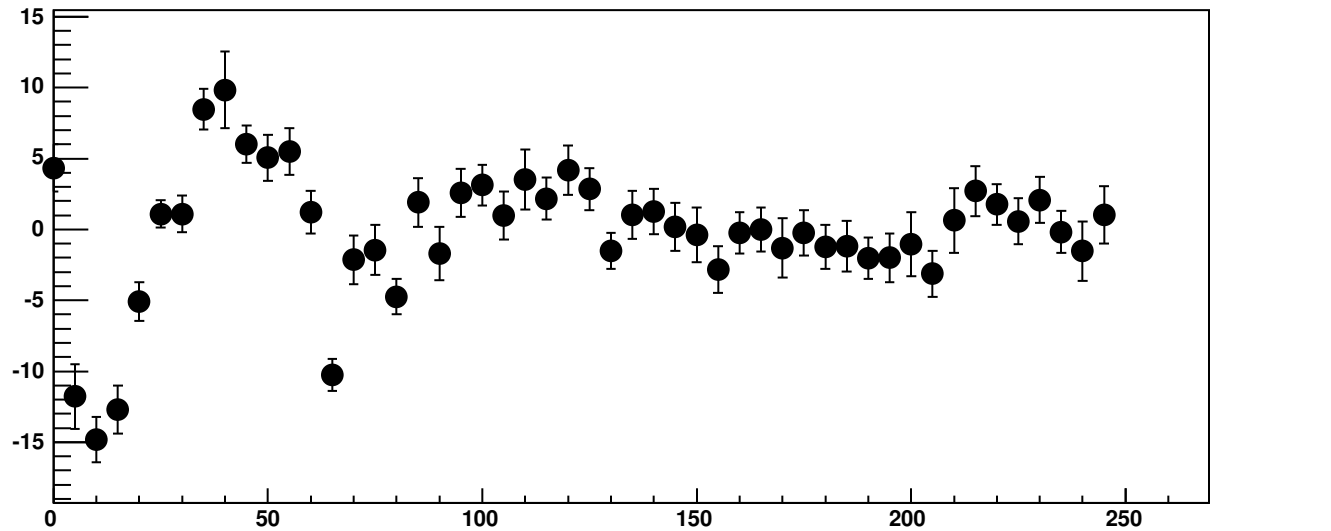
Chip 4, Channel 0, Enable 3, DAC=4000, ADC Mean vs Hold



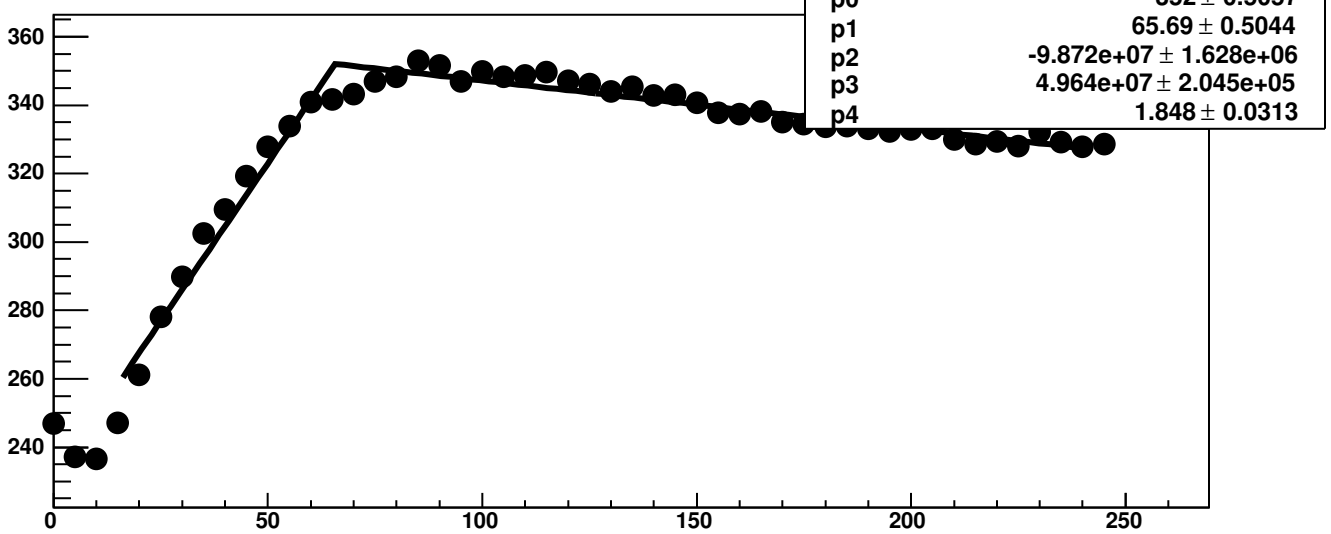
Chip 4, Channel 0, Enable 3, DAC=4000, ADC Noise vs Hold



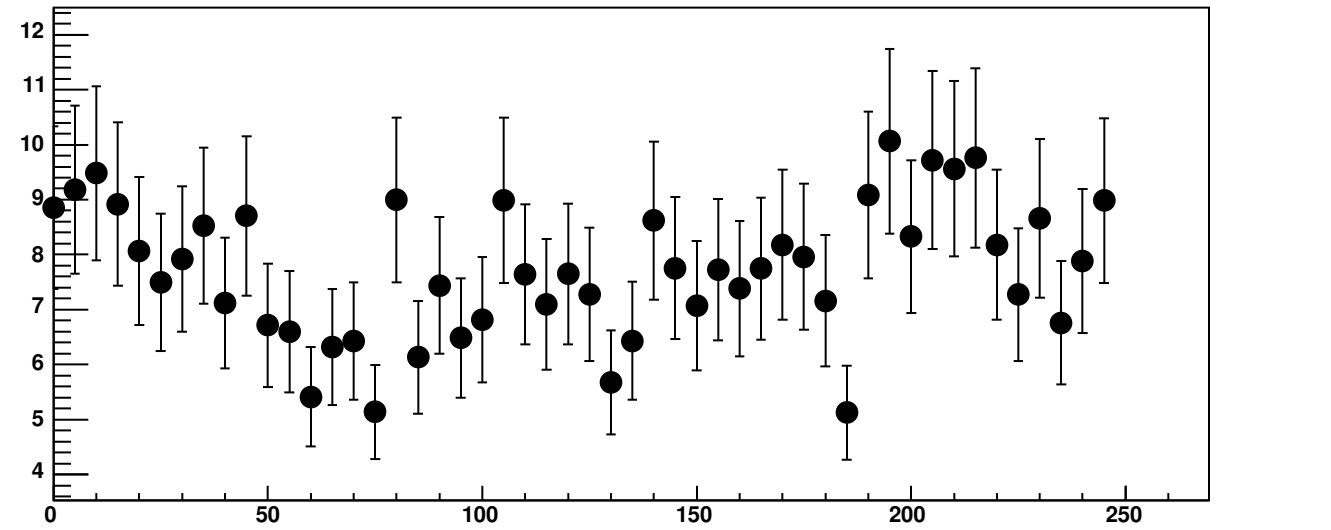
Chip 4, Channel 0, Enable 3, DAC=4000, ADC Residuals vs Hold



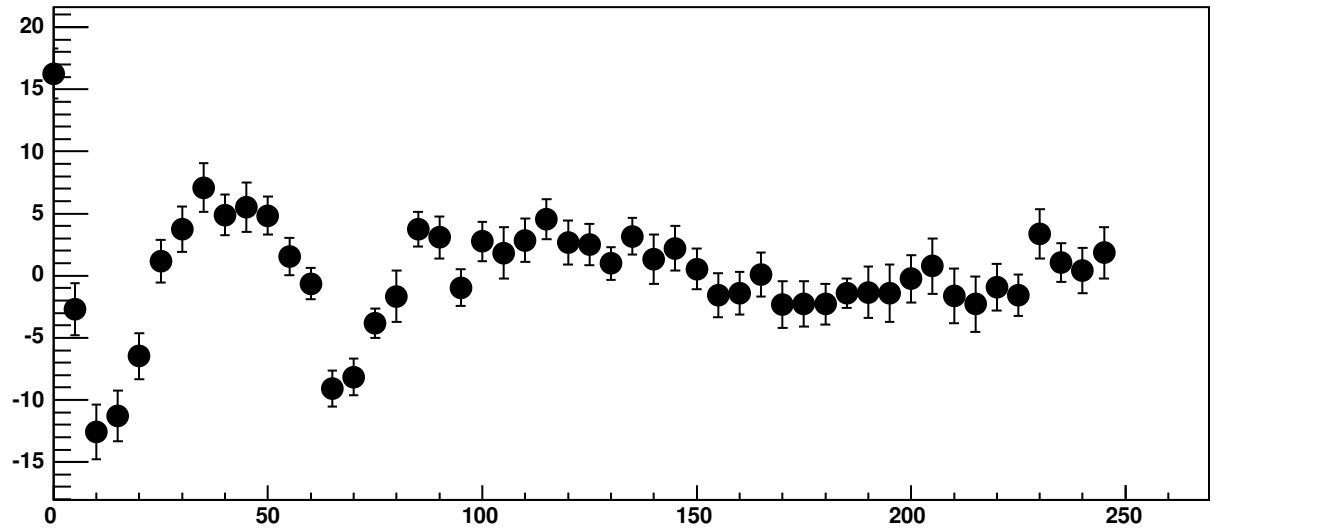
Chip 4, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold



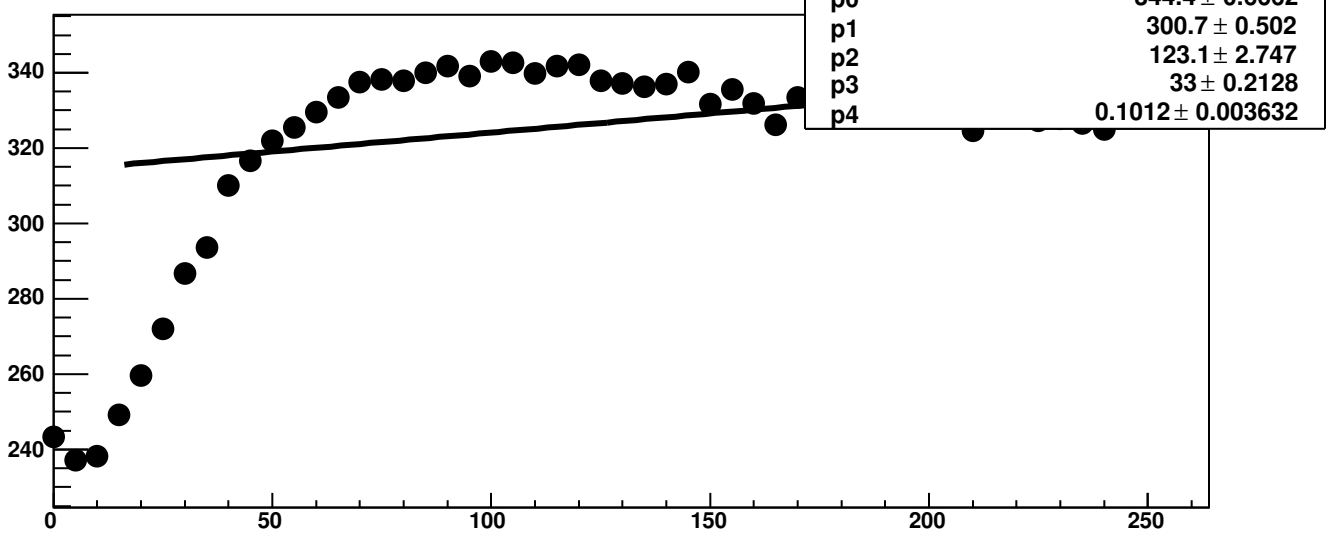
Chip 4, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



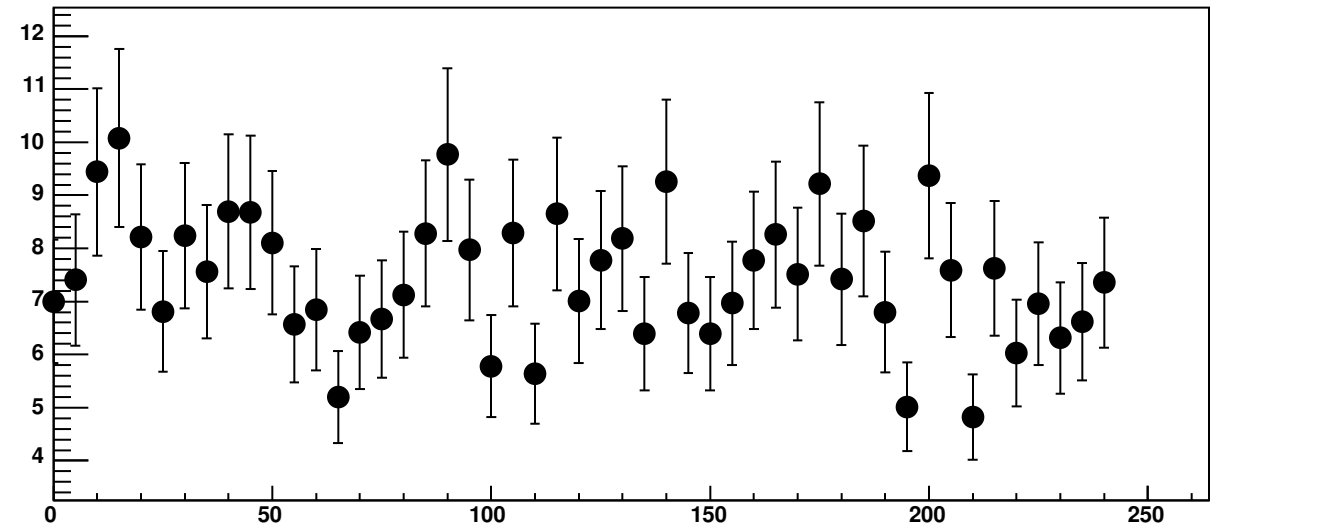
Chip 4, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold



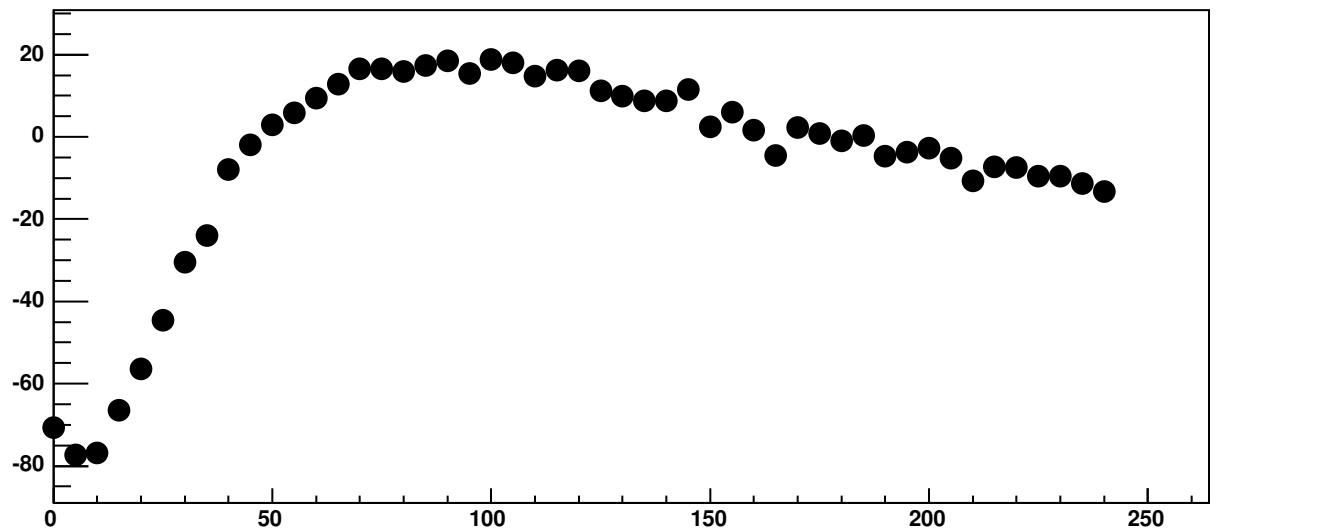
Chip 4, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold

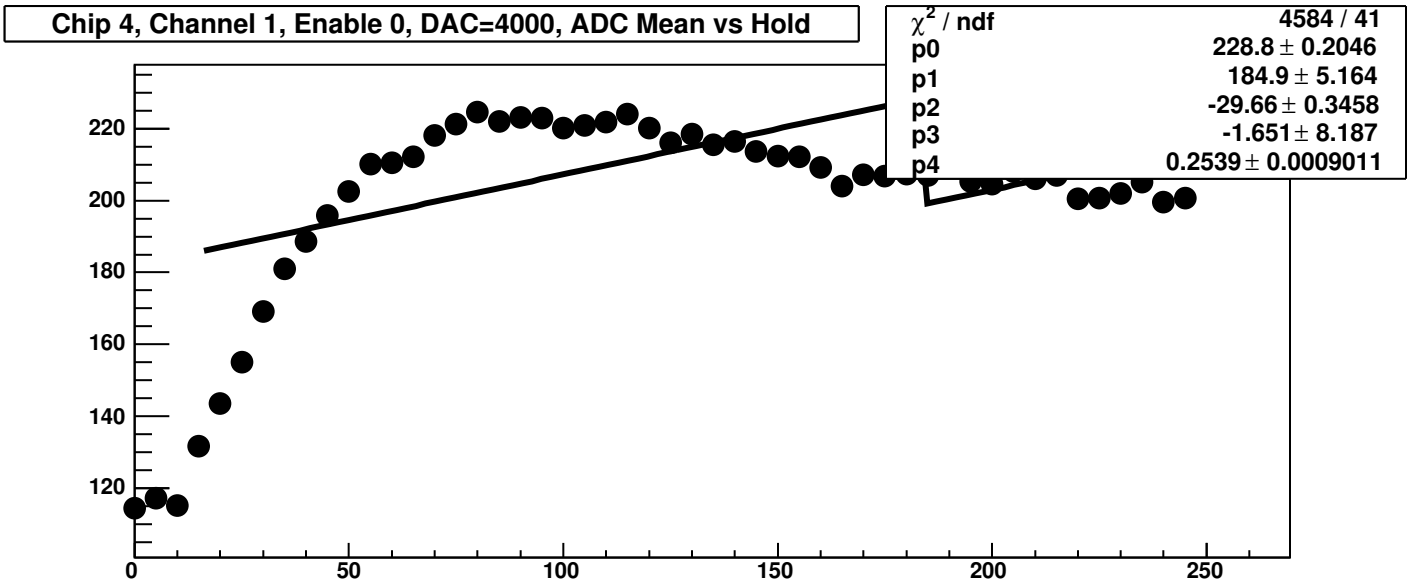


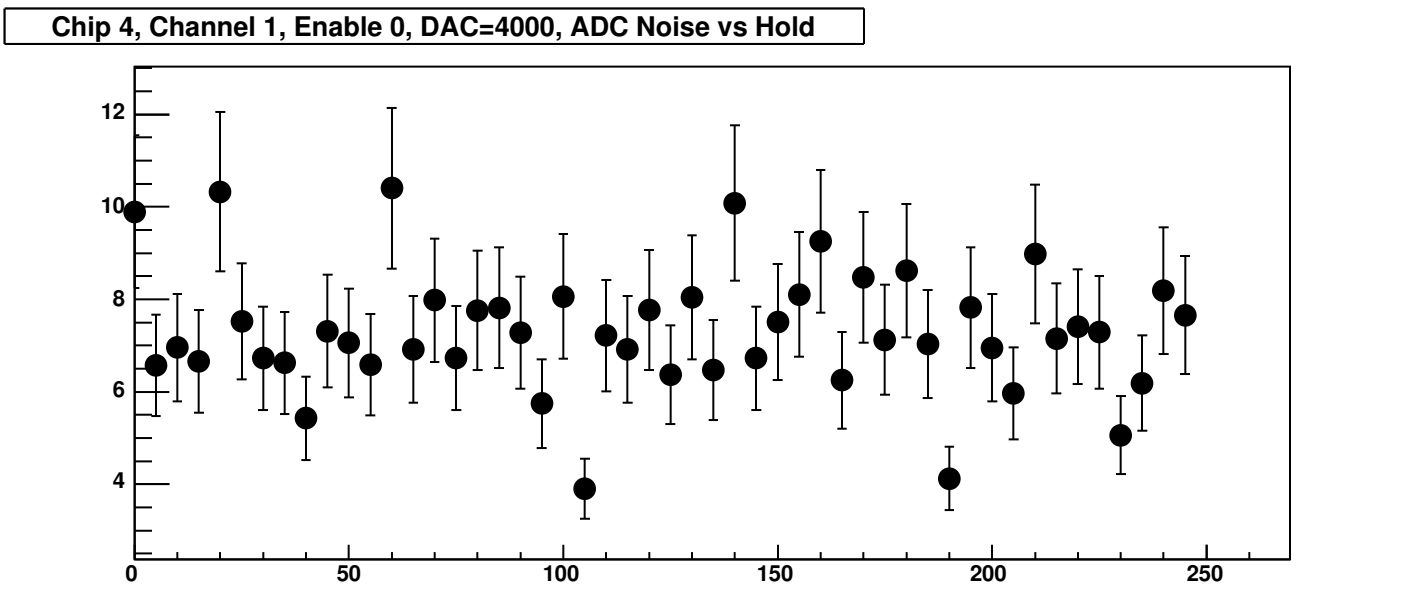
Chip 4, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold

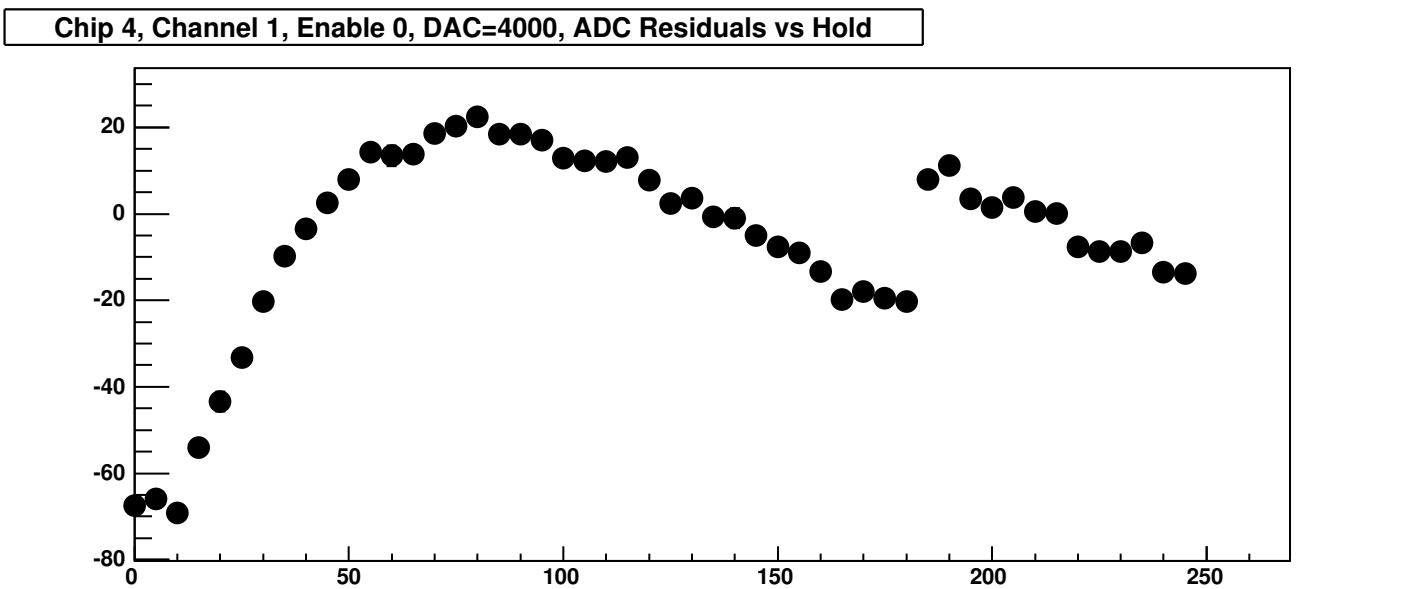


Chip 4, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold

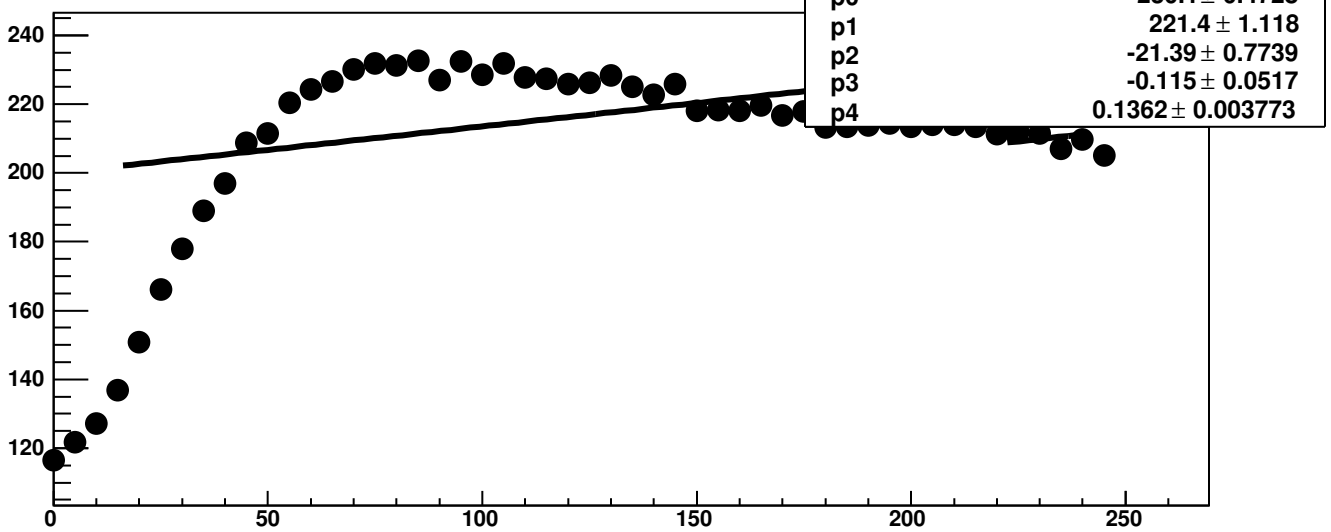



Chip 4, Channel 1, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 4584 / 41
p0 228.8 ± 0.2046
p1 184.9 ± 5.164
p2 -29.66 ± 0.3458
p3 -1.651 ± 8.187
p4 0.2539 ± 0.0009011
220
200
180
160
140
120
0
50
100
150
200
250

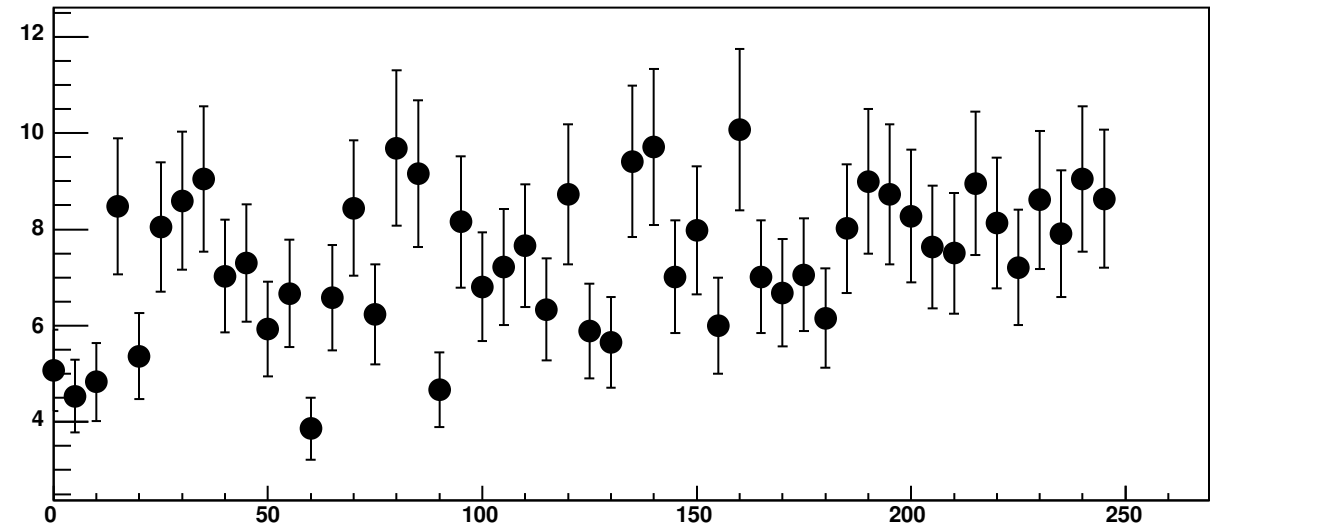

Chip 4, Channel 1, Enable 0, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 4, Channel 1, Enable 0, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
-80
0
50
100
150
200
250

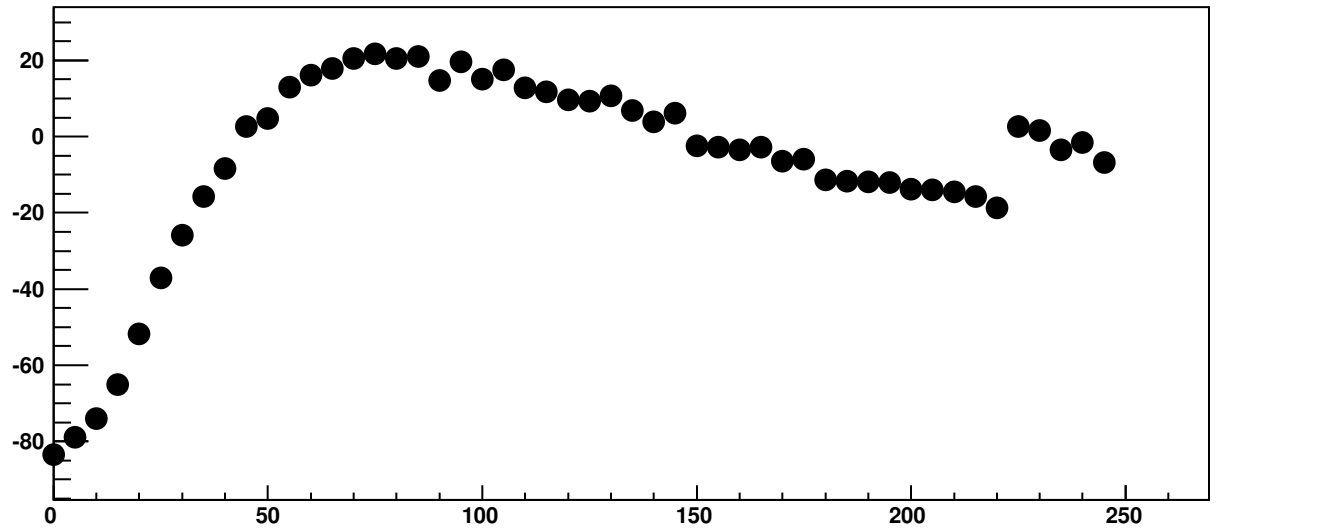
Chip 4, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold



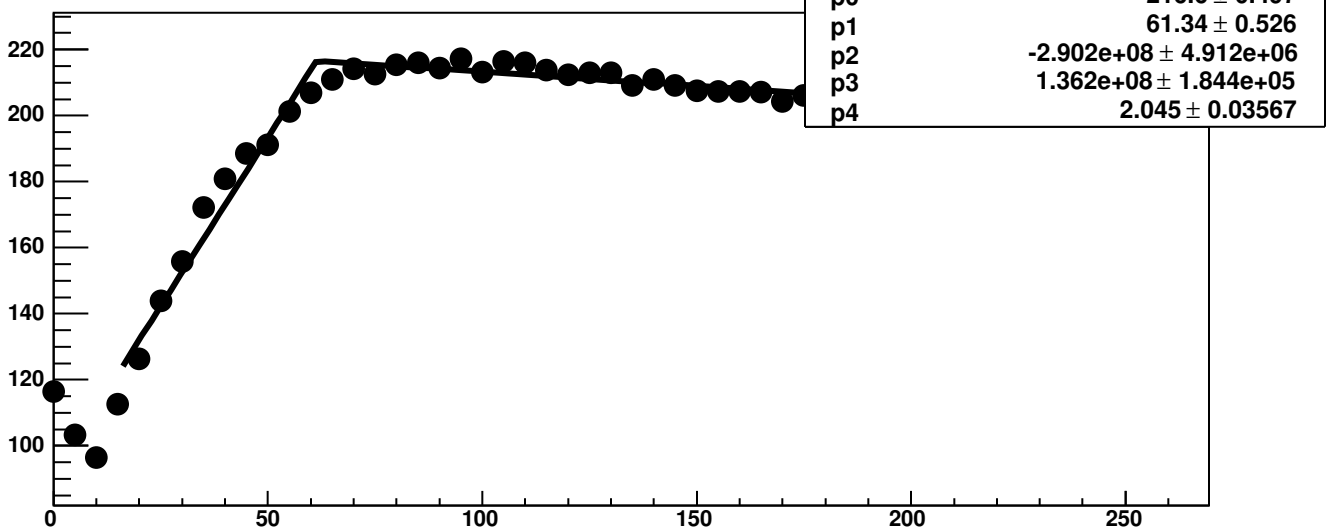
Chip 4, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



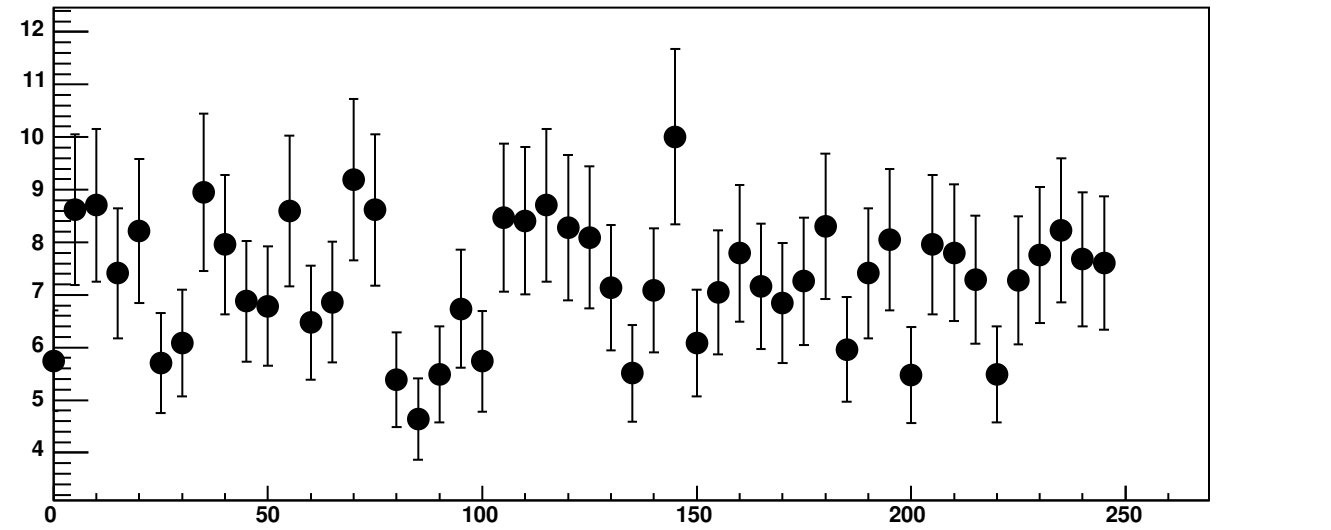
Chip 4, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold



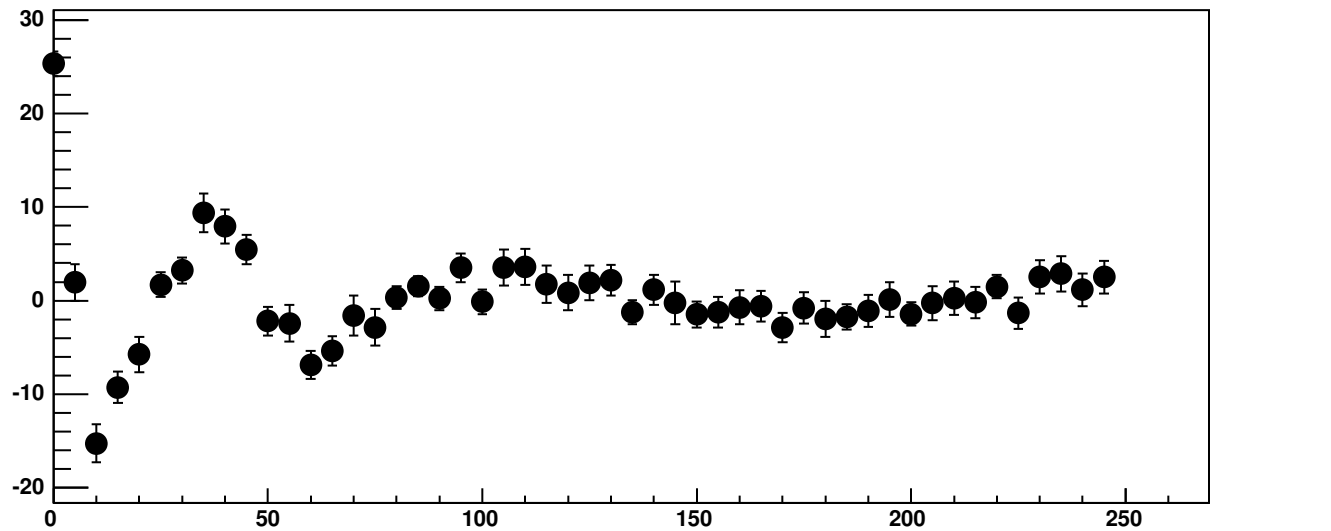
Chip 4, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold

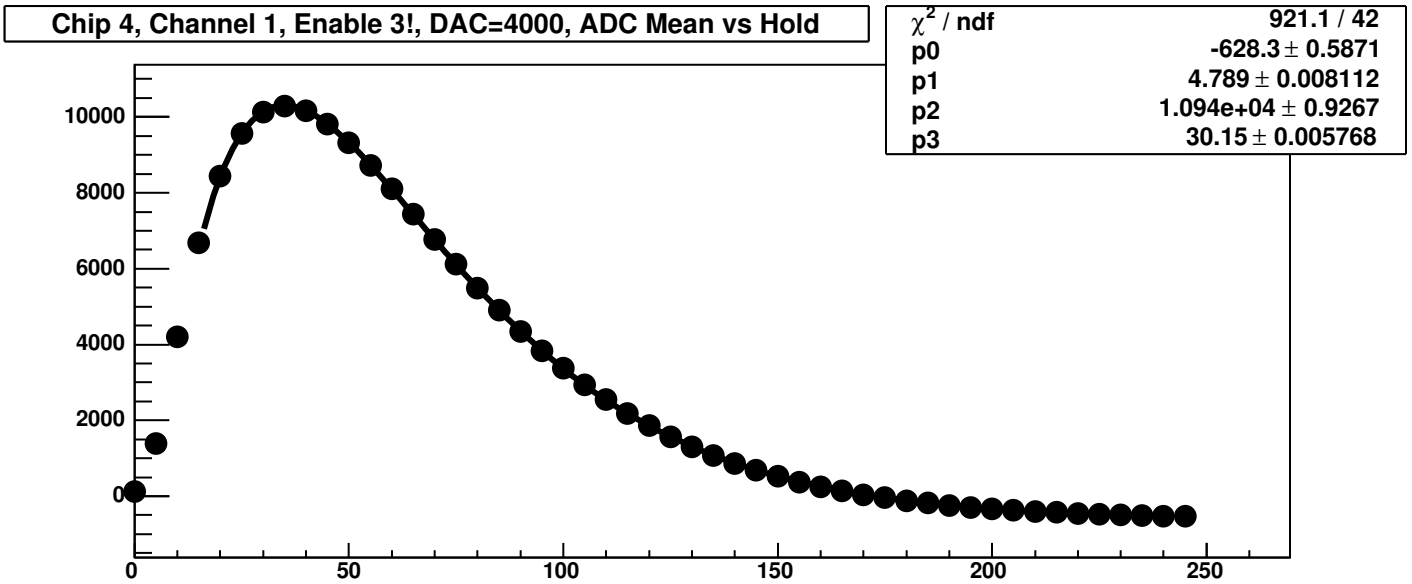


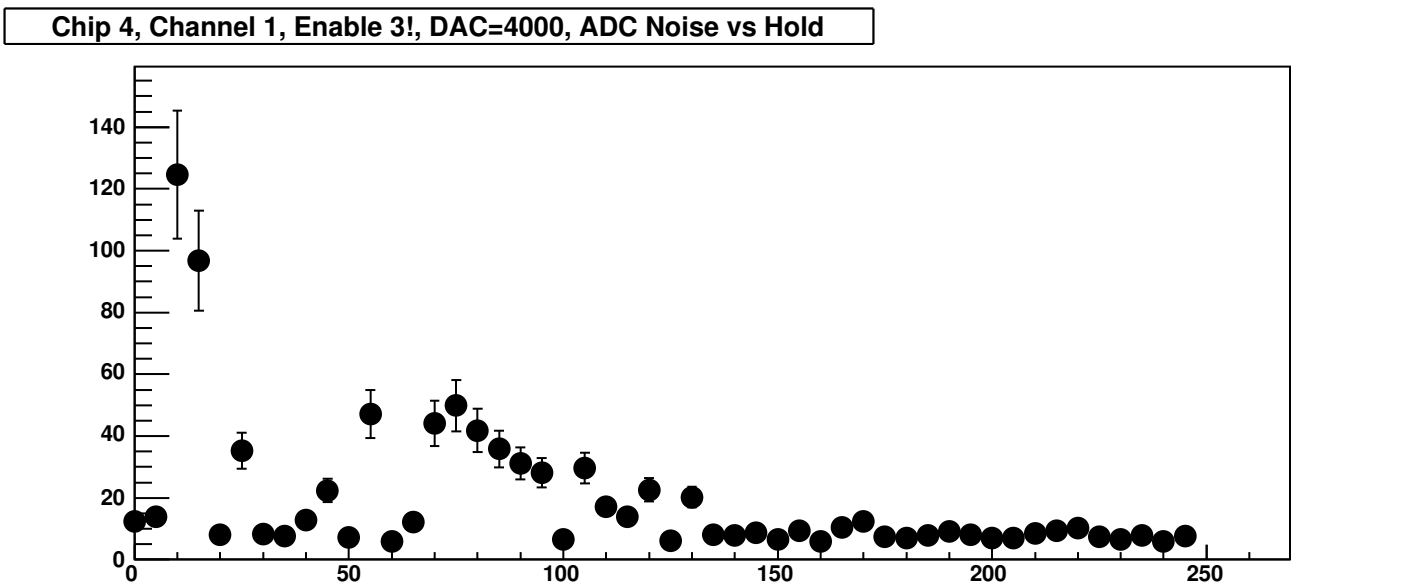
Chip 4, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold

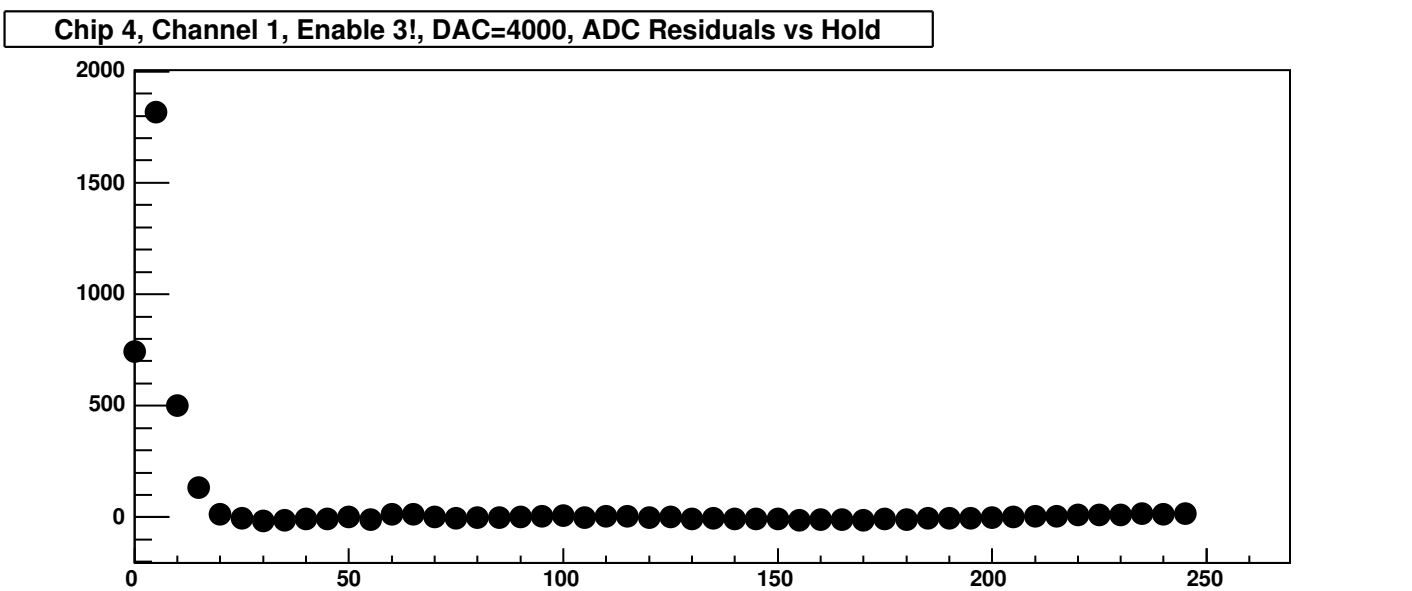


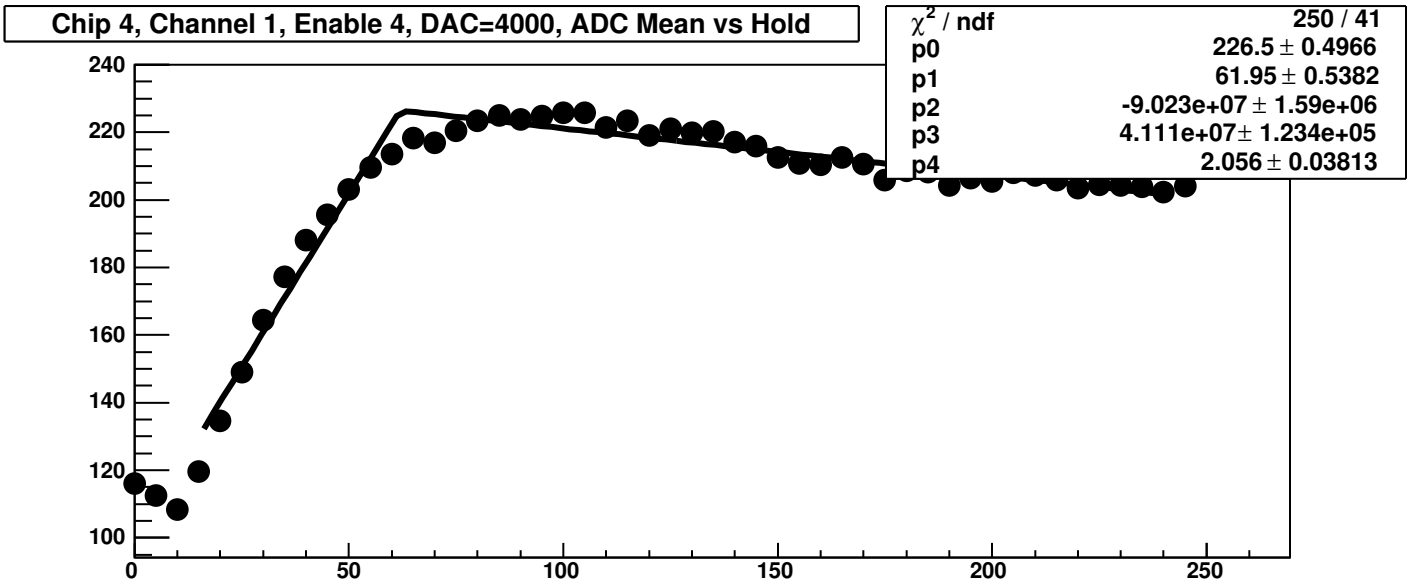
Chip 4, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold

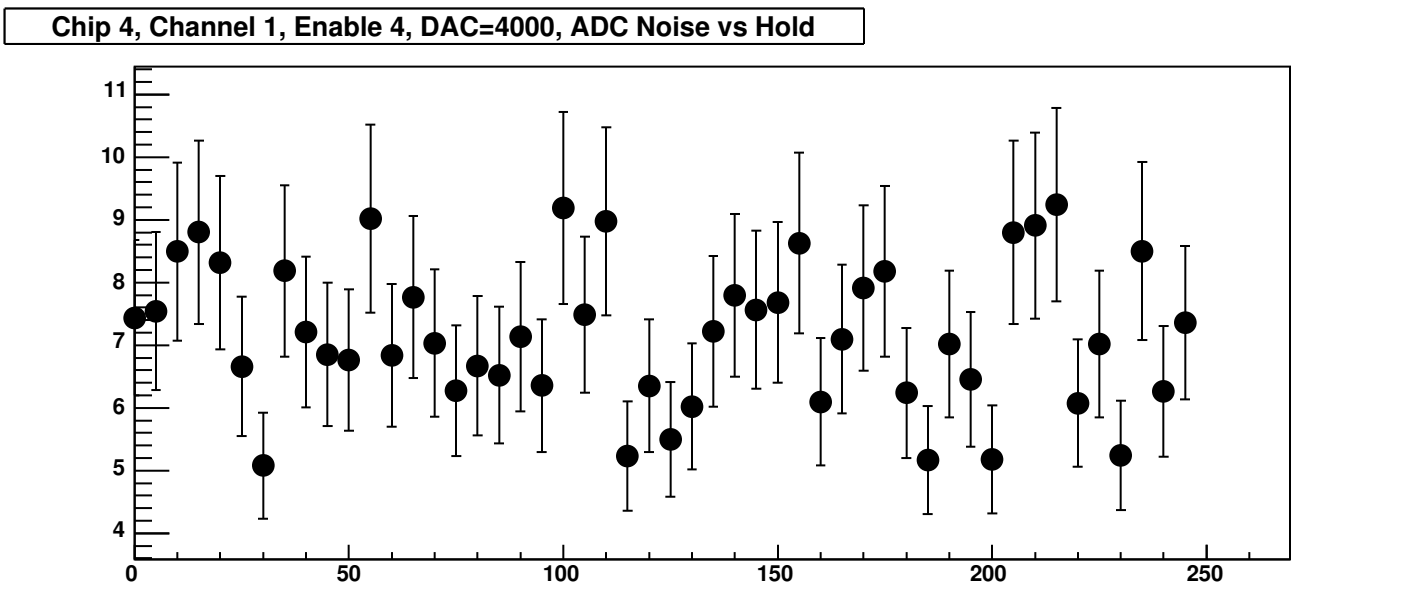


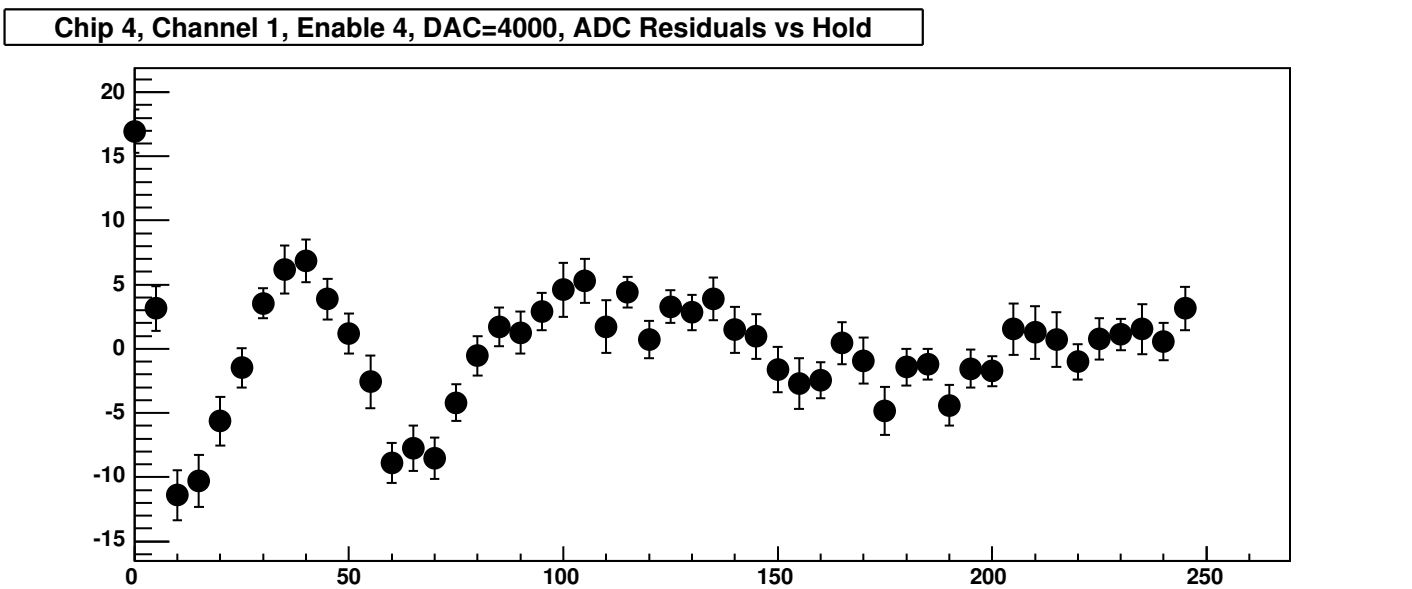

Chip 4, Channel 1, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 921.1 / 42
p0 -628.3 ± 0.5871
p1 4.789 ± 0.008112
p2 1.094e+04 ± 0.9267
p3 30.15 ± 0.005768
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 4, Channel 1, Enable 3!, DAC=4000, ADC Noise vs Hold
140
120
100
80
60
40
20
0
0
50
100
150
200
250

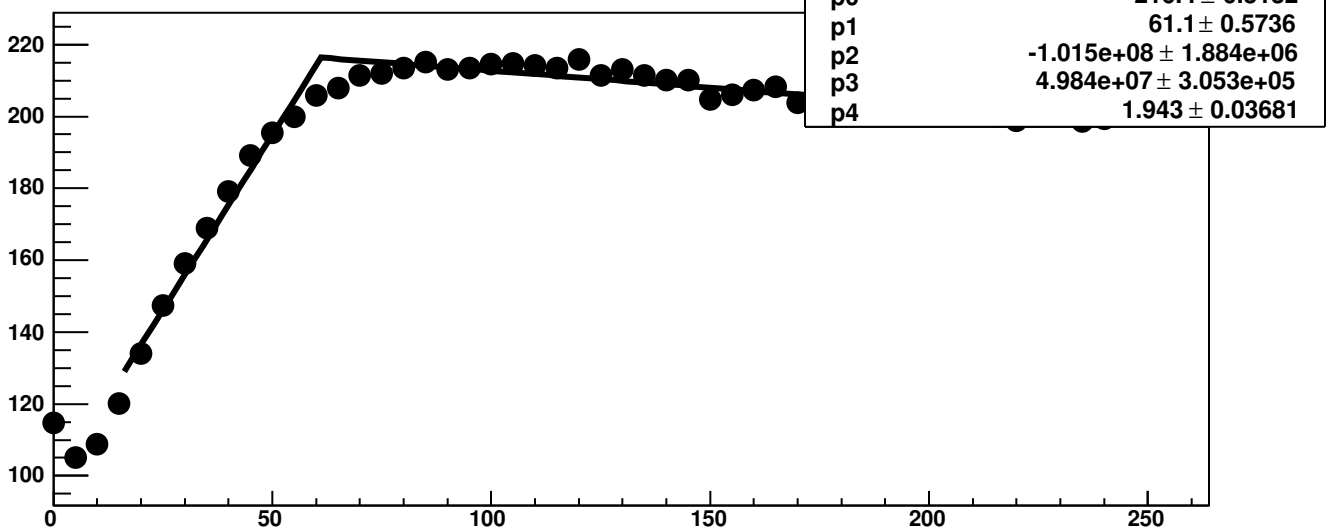

Chip 4, Channel 1, Enable 3!, DAC=4000, ADC Residuals vs Hold
2000
1500
1000
500
0
0
50
100
150
200
250


Chip 4, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 250 / 41
p0 226.5 ± 0.4966
p1 61.95 ± 0.5382
p2 -9.023e+07 ± 1.59e+06
p3 4.111e+07 ± 1.234e+05
p4 2.056 ± 0.03813

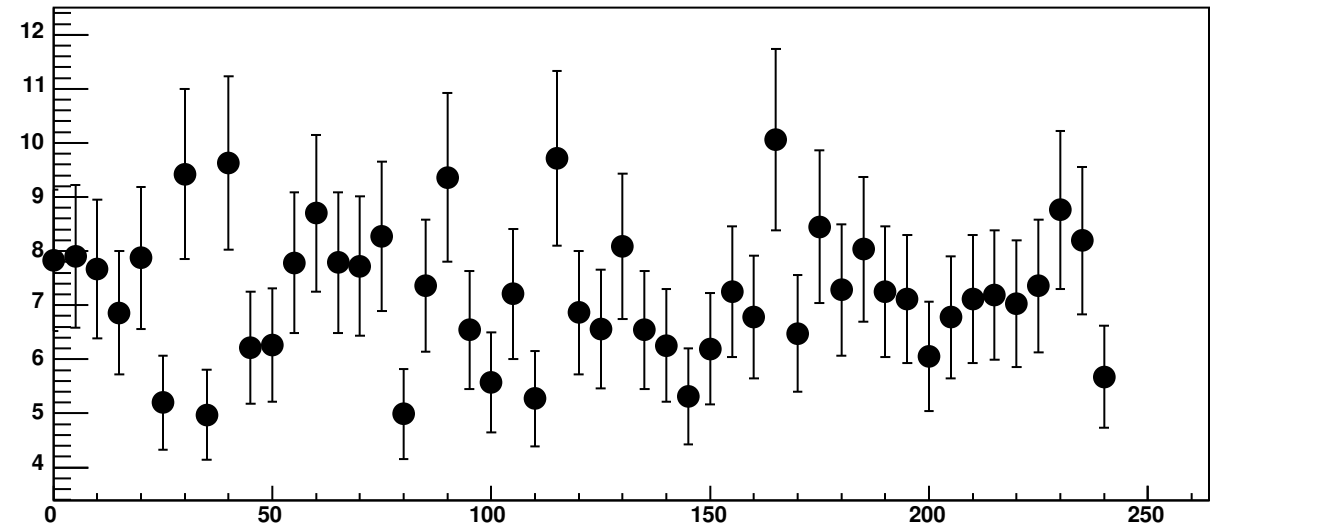

Chip 4, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold


Chip 4, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold

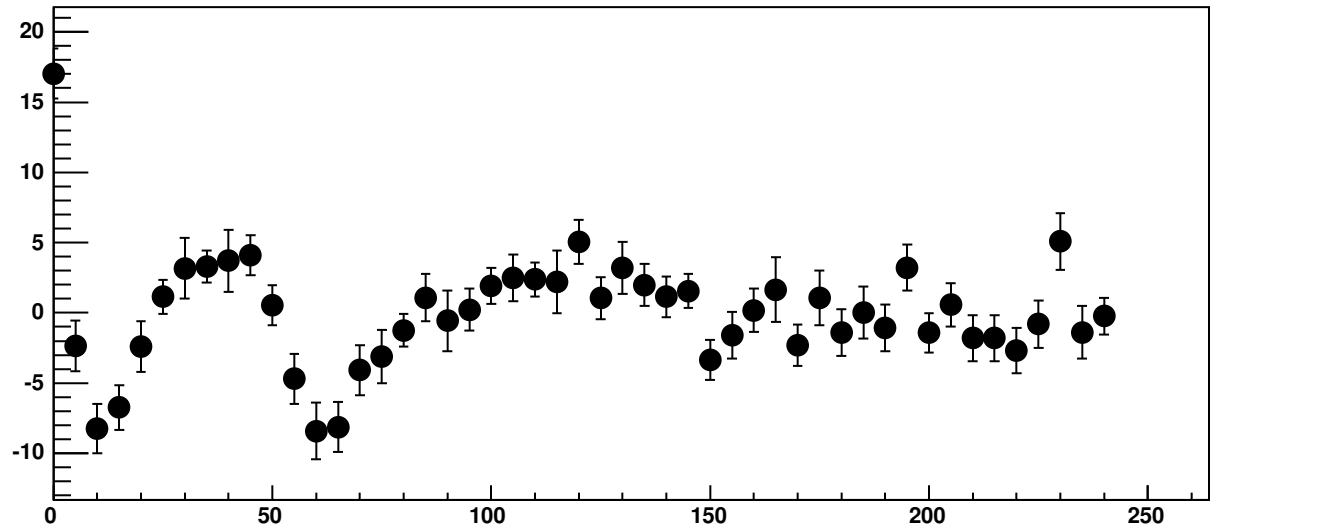
Chip 4, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold

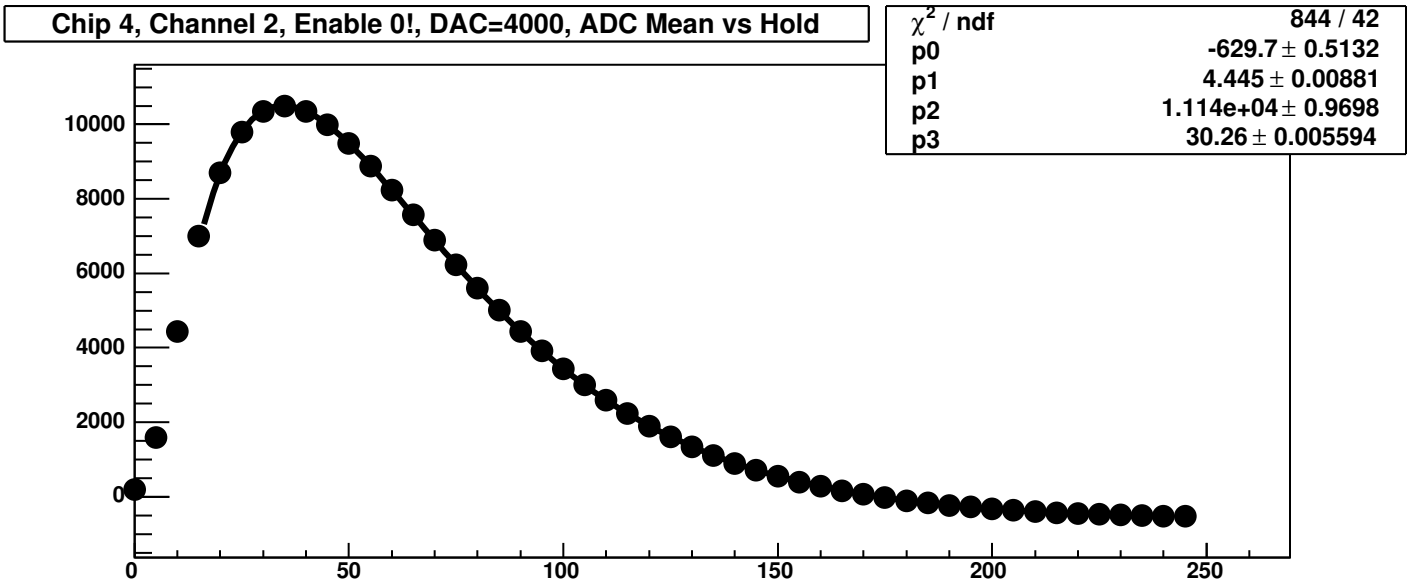


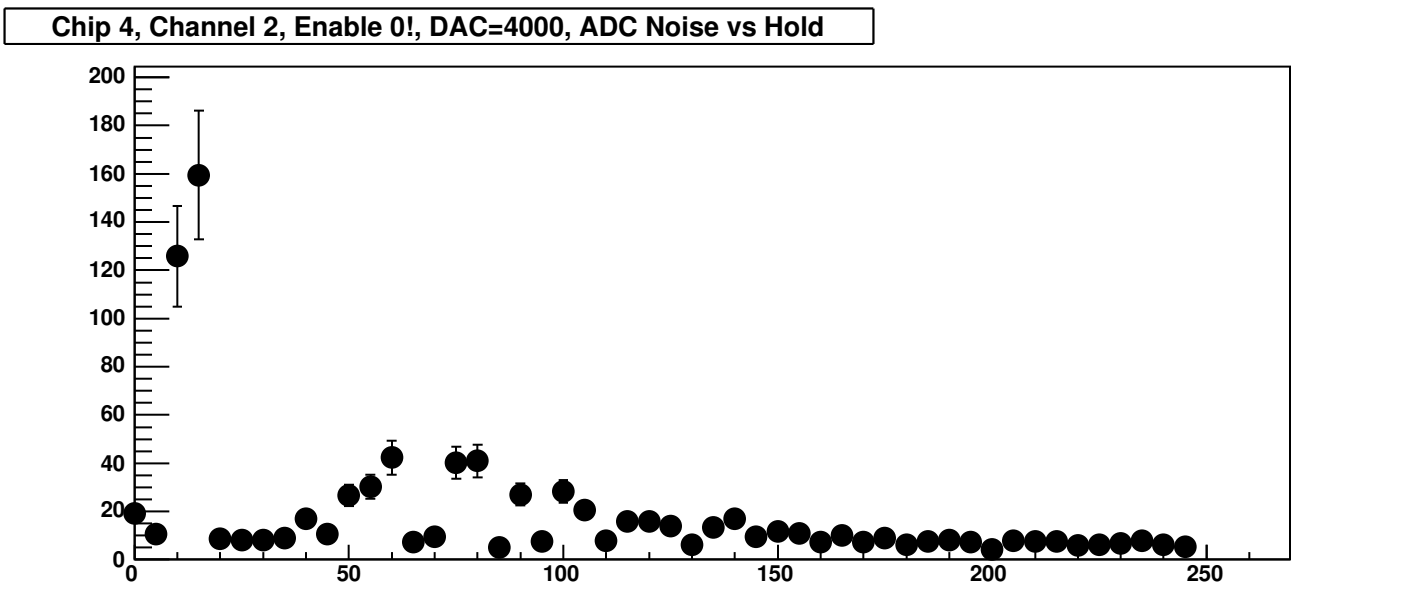
Chip 4, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold

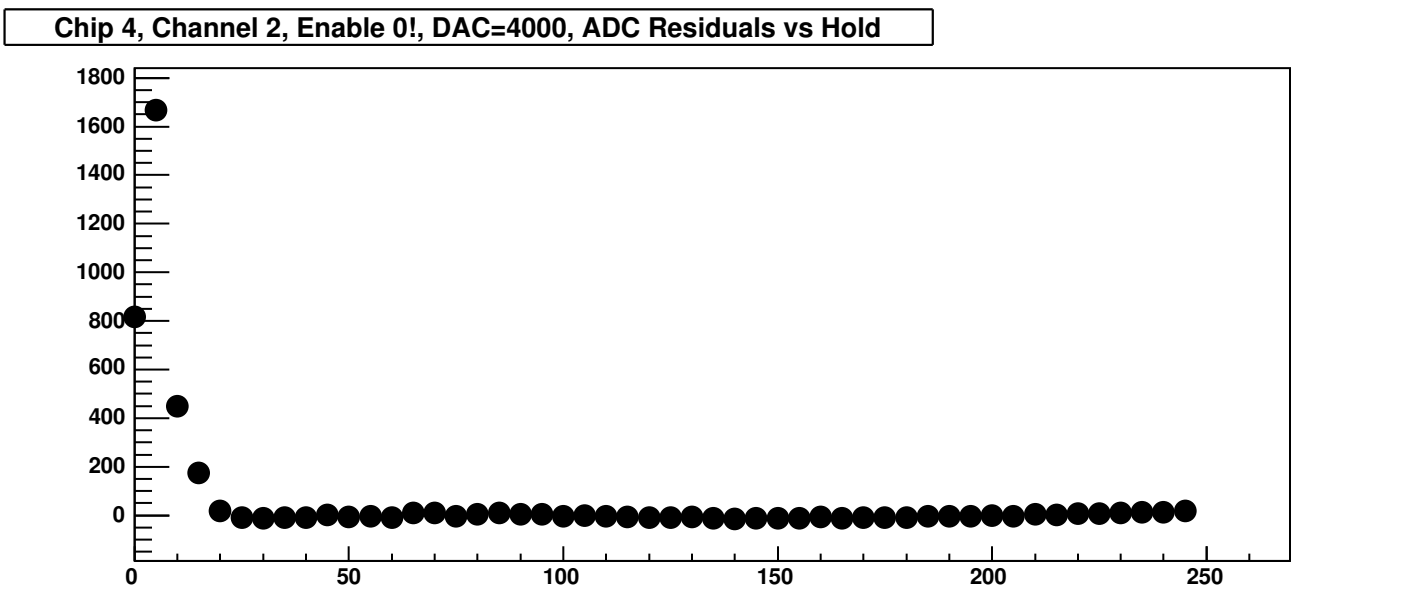


Chip 4, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold

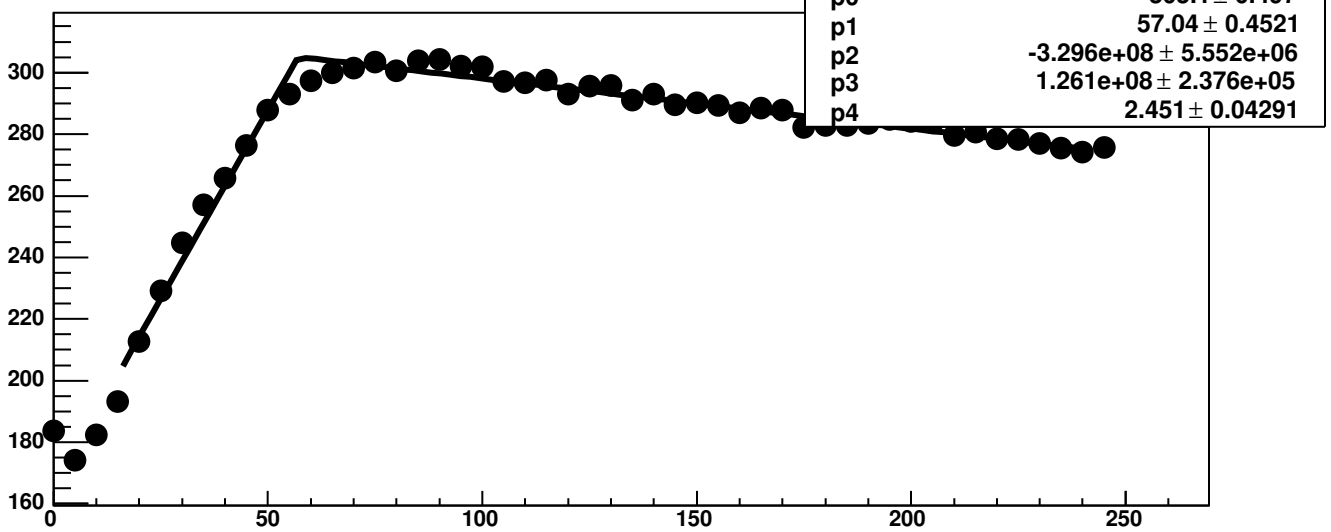



Chip 4, Channel 2, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 844 / 42
p0 -629.7 ± 0.5132
p1 4.445 ± 0.00881
p2 1.114e+04 ± 0.9698
p3 30.26 ± 0.005594
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

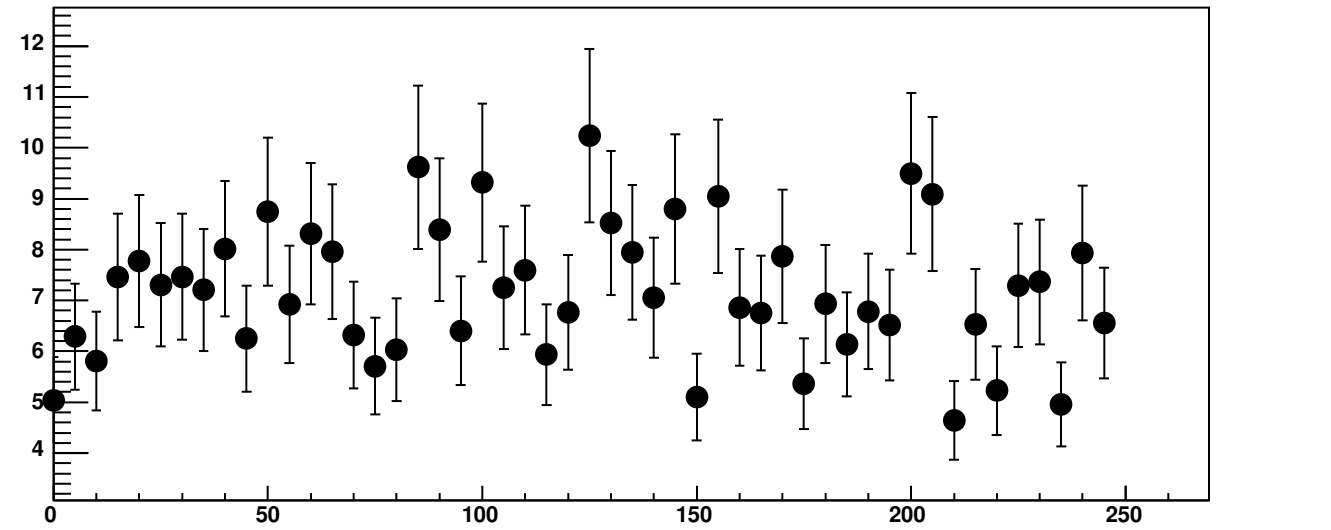

Chip 4, Channel 2, Enable 0!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 4, Channel 2, Enable 0!, DAC=4000, ADC Residuals vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

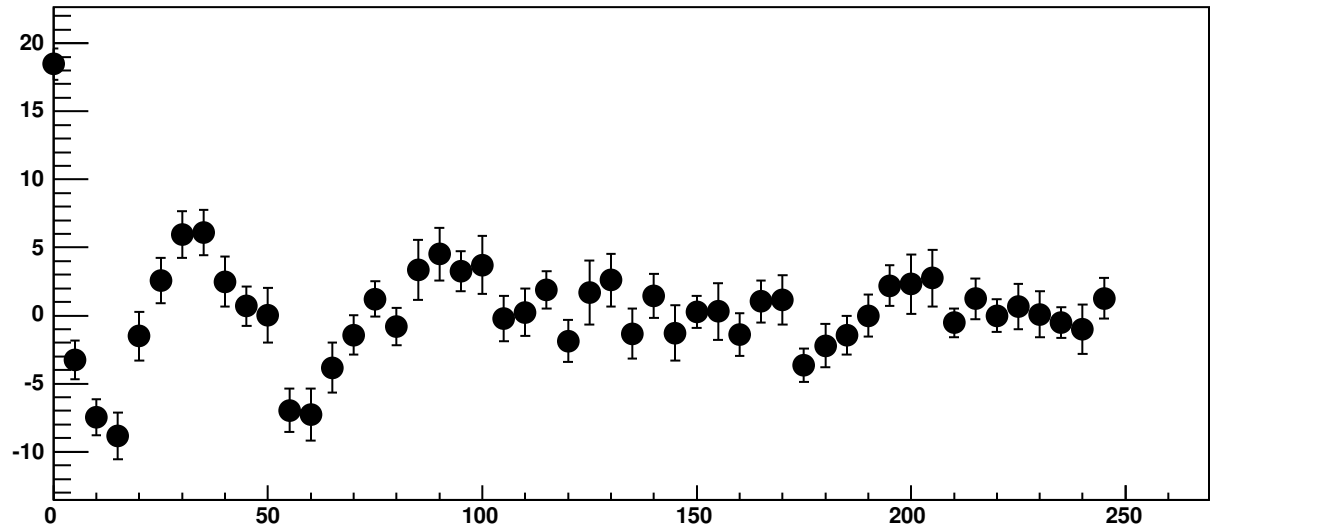
Chip 4, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold



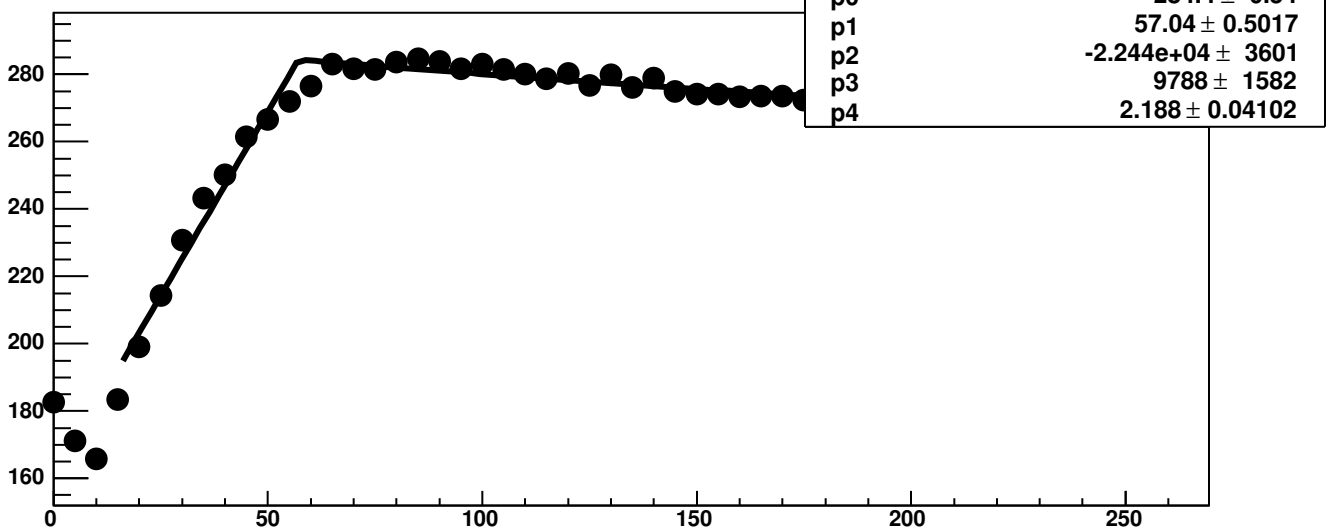
Chip 4, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



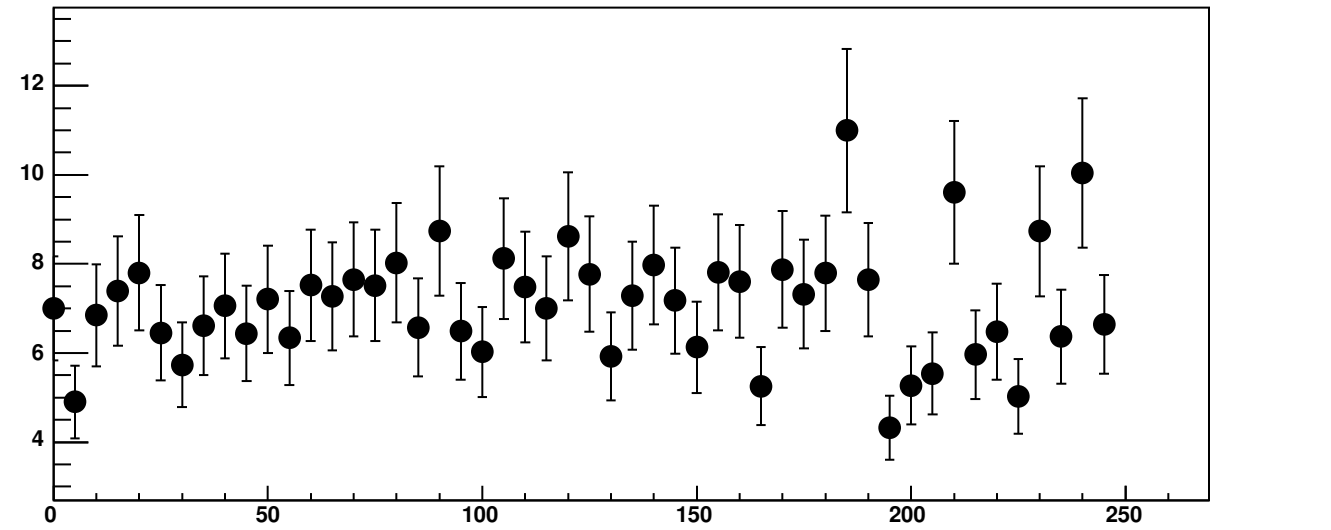
Chip 4, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold



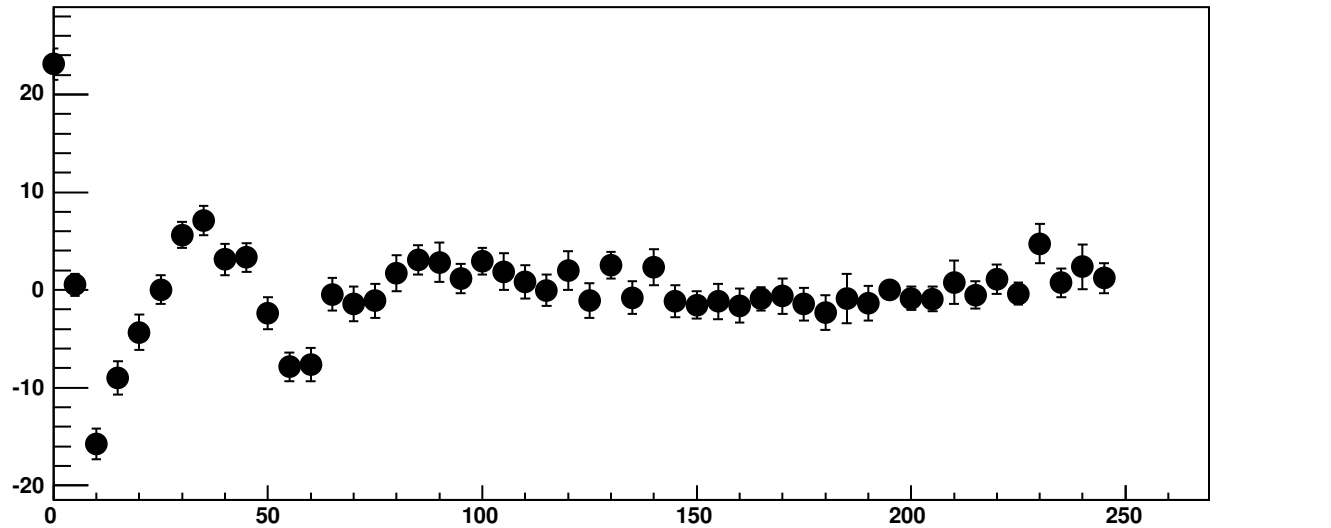
Chip 4, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold



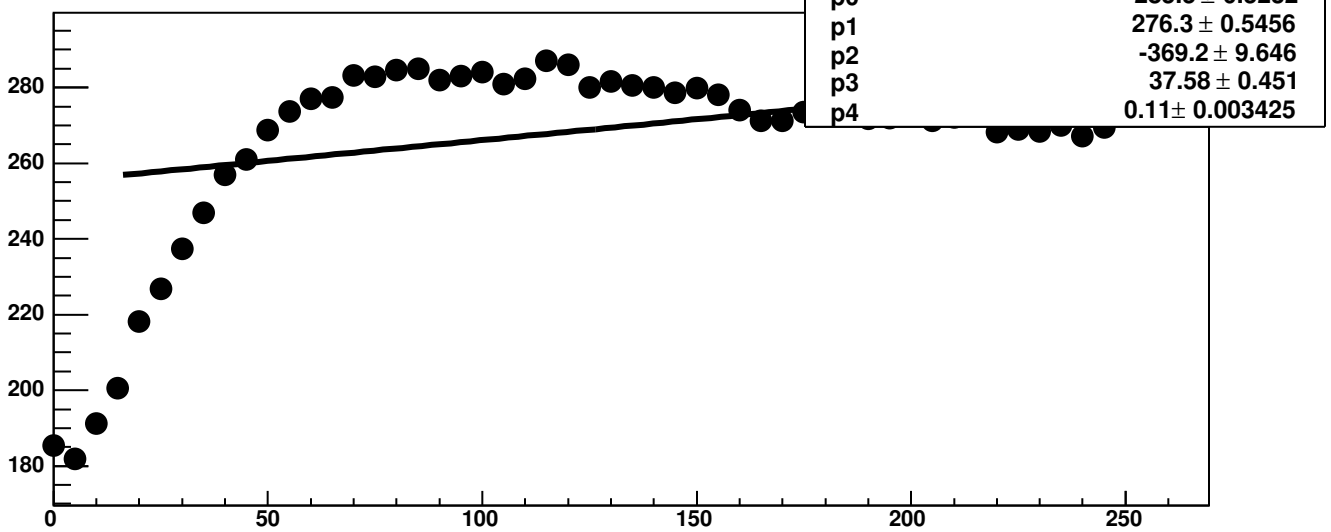
Chip 4, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold



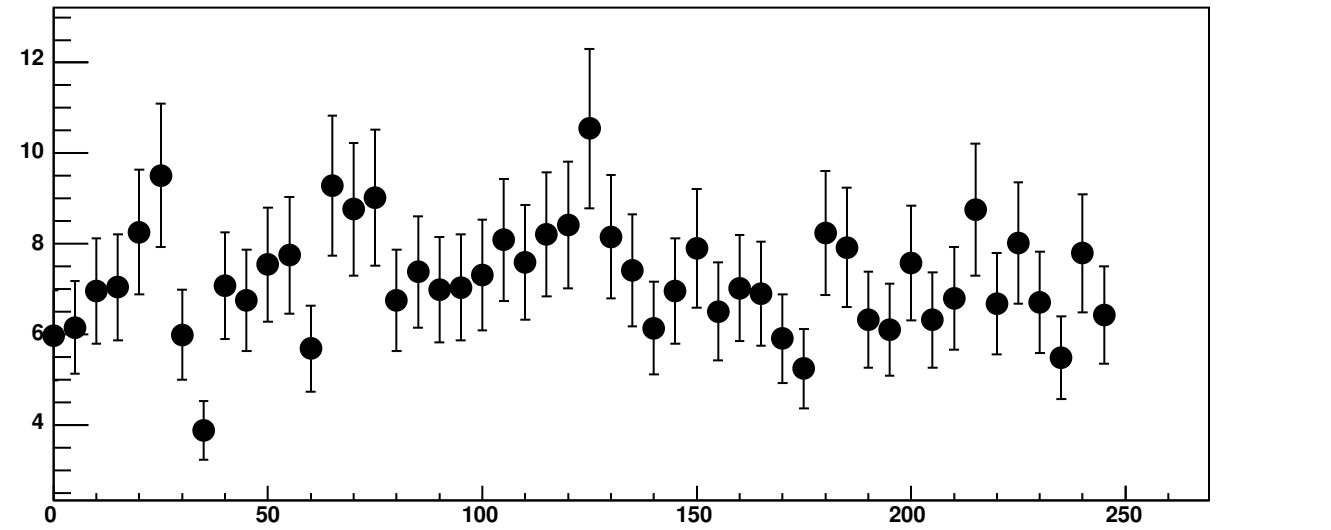
Chip 4, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold



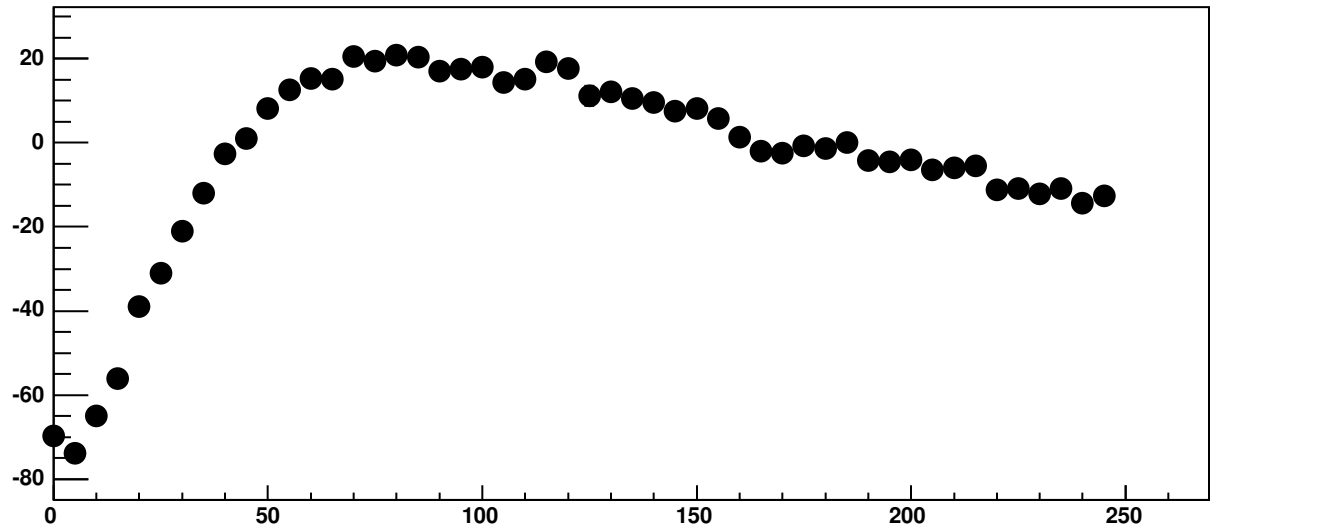
Chip 4, Channel 2, Enable 3, DAC=4000, ADC Mean vs Hold



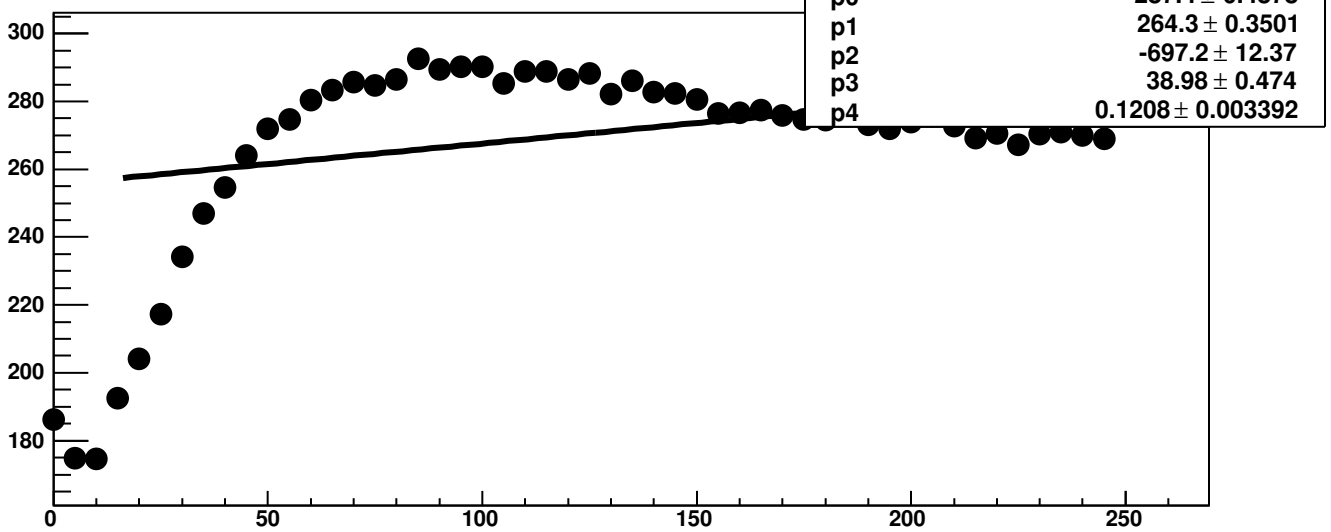
Chip 4, Channel 2, Enable 3, DAC=4000, ADC Noise vs Hold



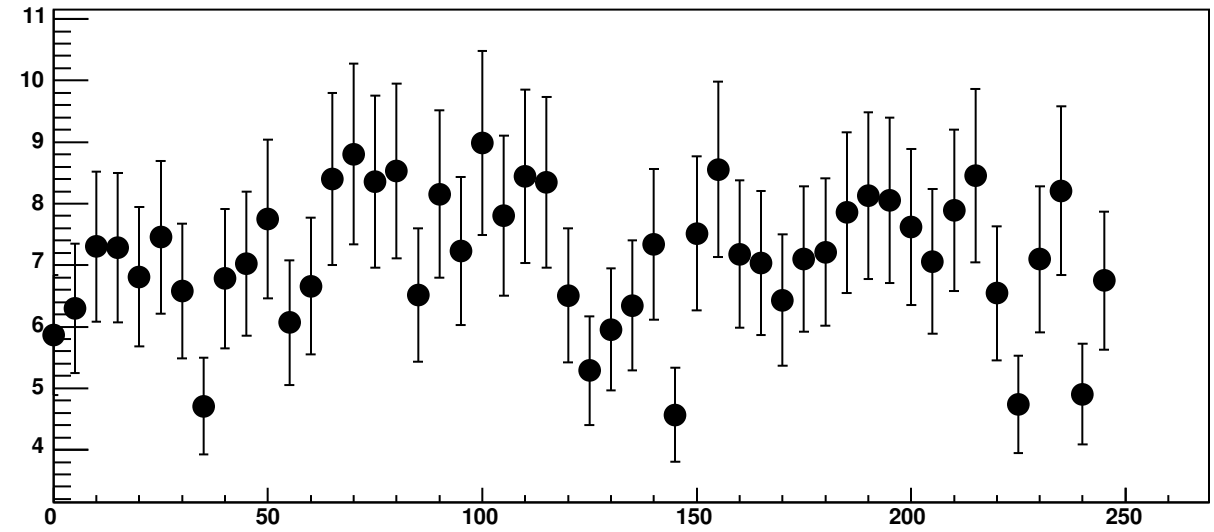
Chip 4, Channel 2, Enable 3, DAC=4000, ADC Residuals vs Hold



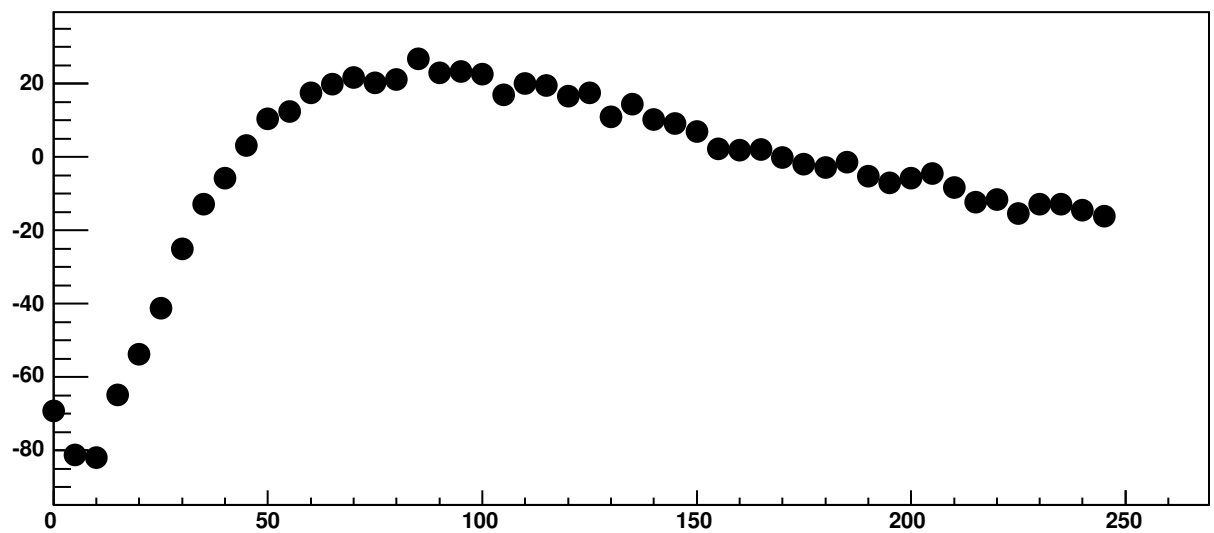
Chip 4, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold



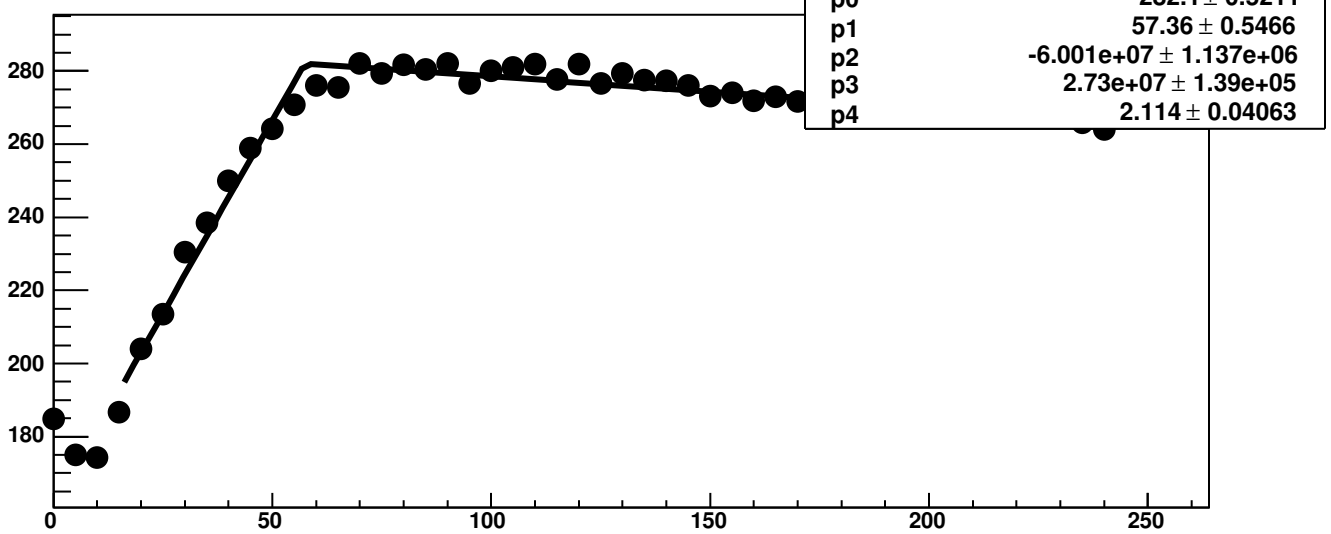
Chip 4, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



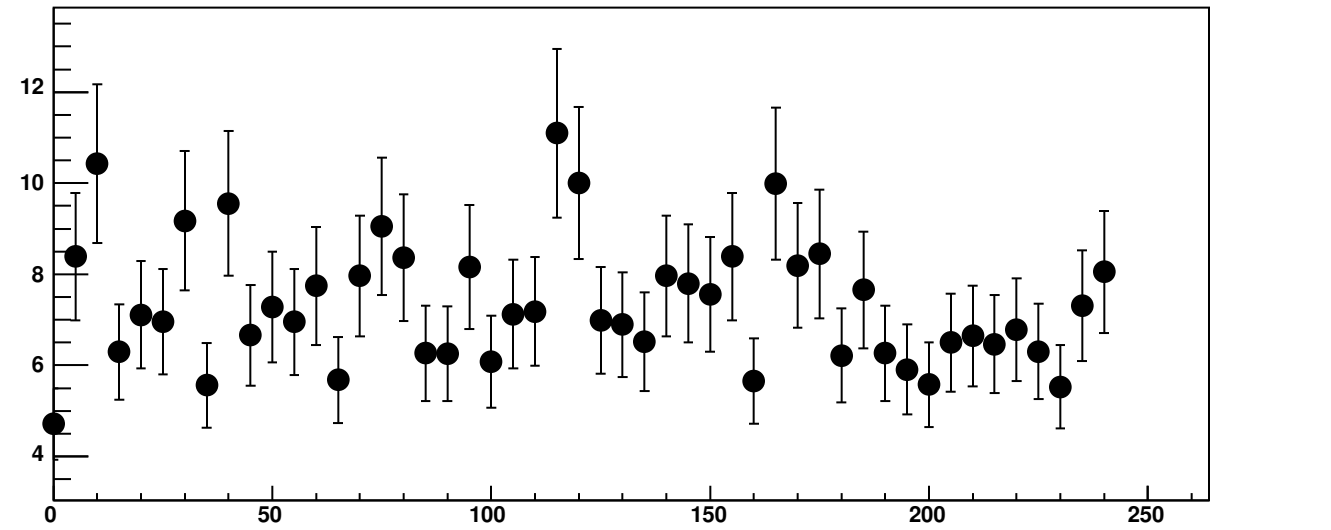
Chip 4, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold



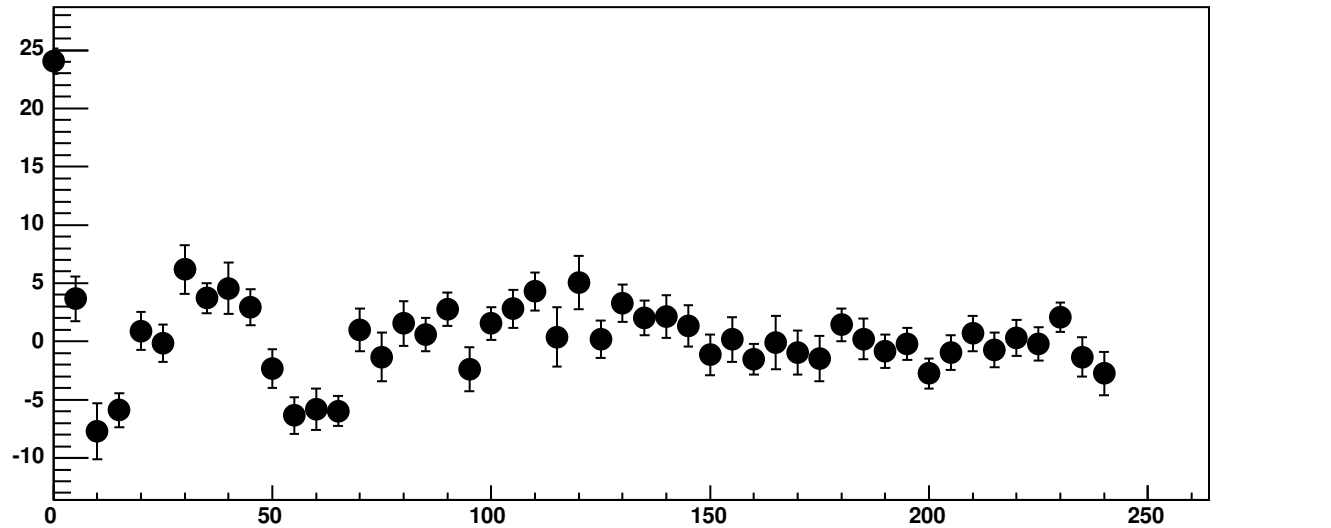
Chip 4, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold

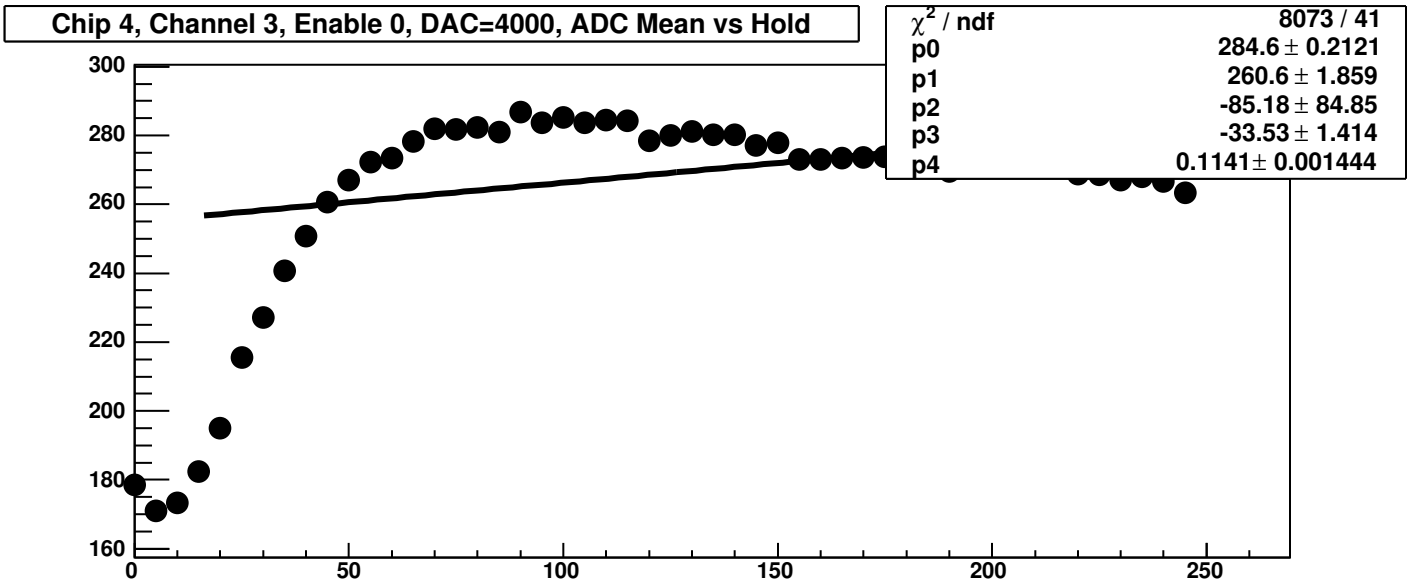


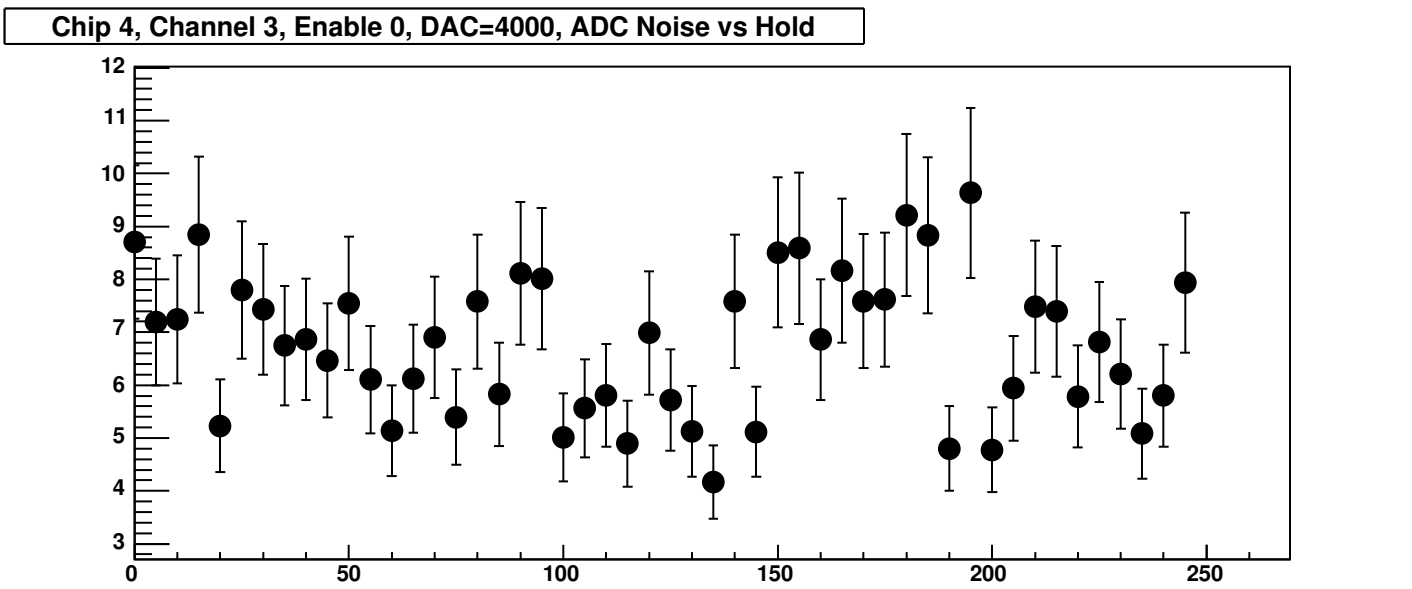
Chip 4, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold

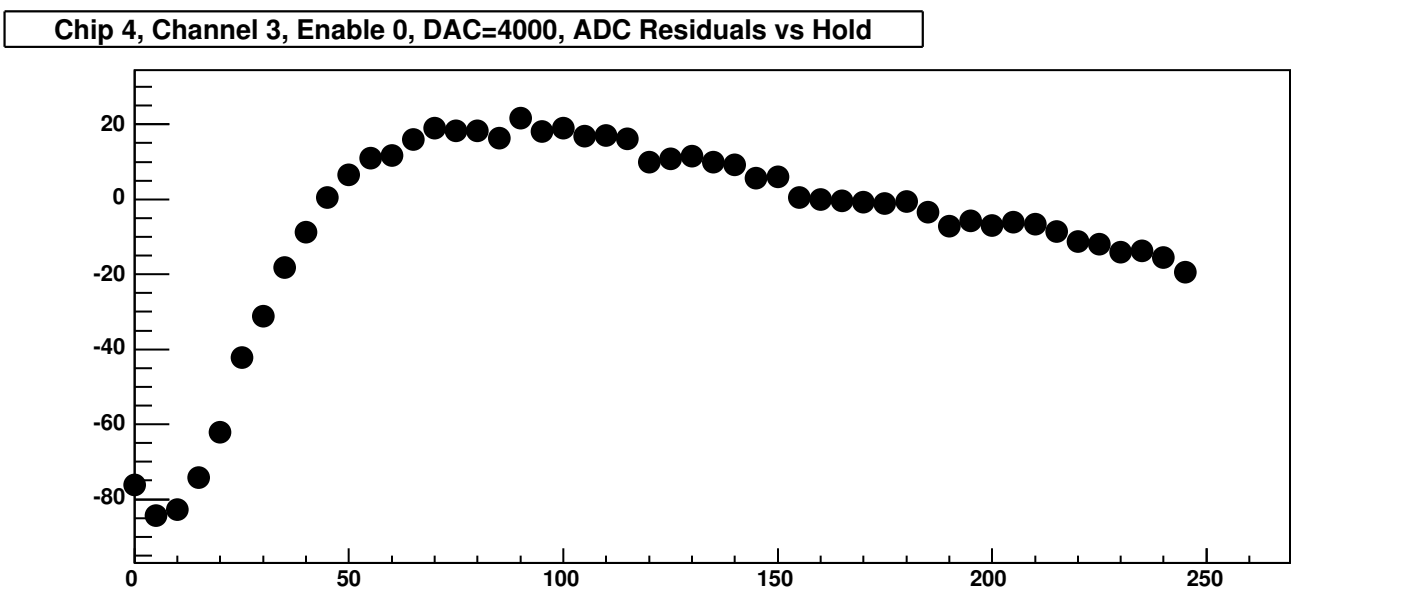


Chip 4, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold

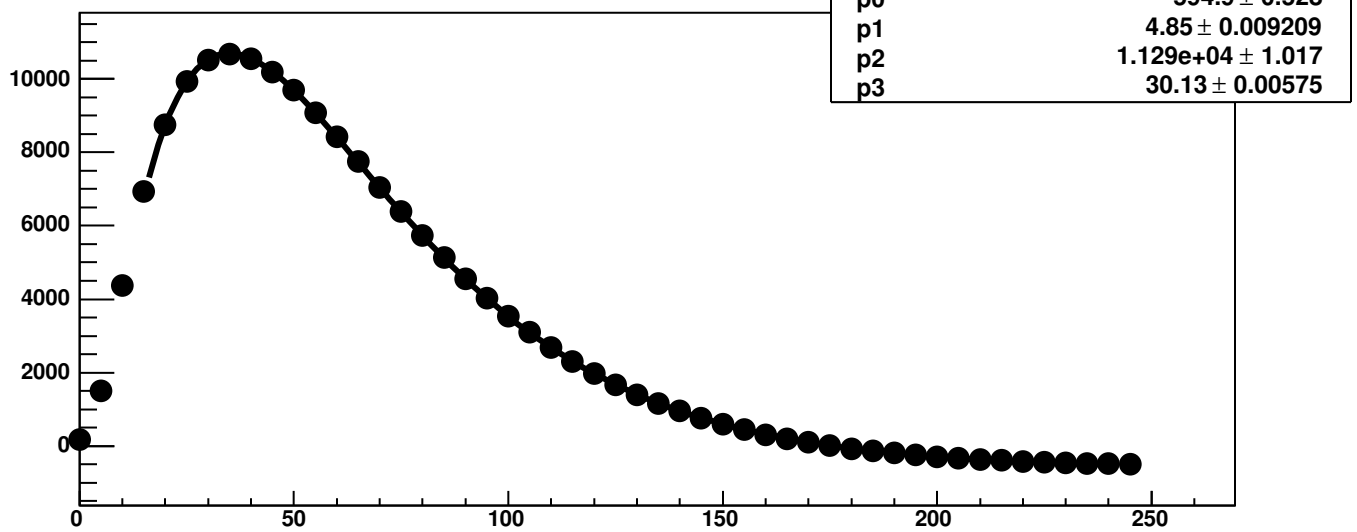



Chip 4, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 8073 / 41
p0 284.6 ± 0.2121
p1 260.6 ± 1.859
p2 -85.18 ± 84.85
p3 -33.53 ± 1.414
p4 0.1141 ± 0.001444
300
280
260
240
220
200
180
160
0
50
100
150
200
250

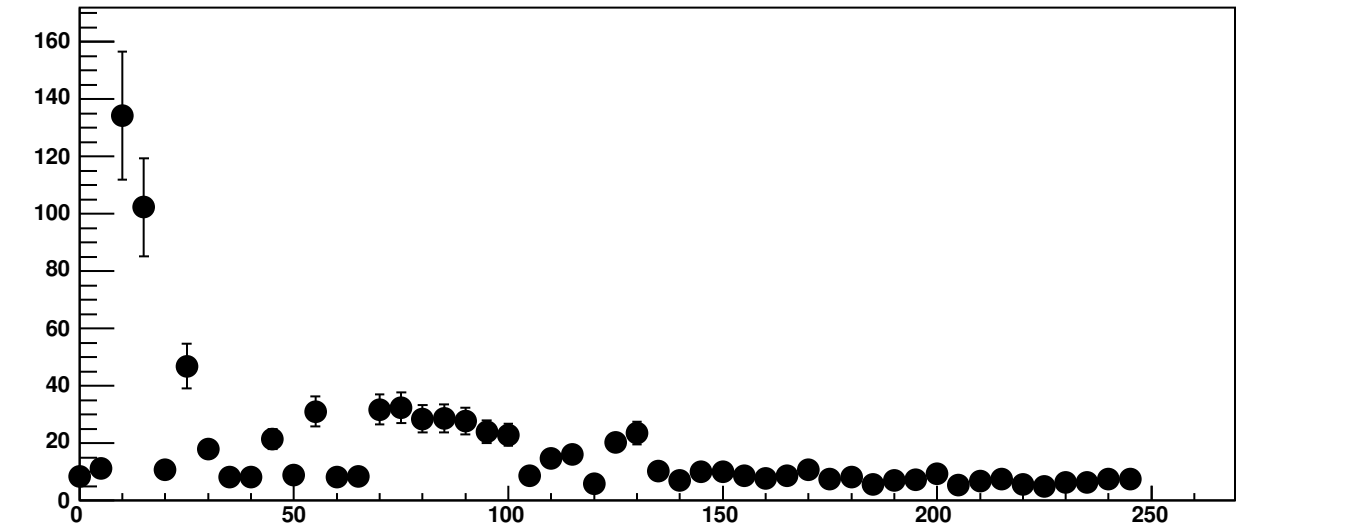

Chip 4, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 4, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
-80
0
50
100
150
200
250

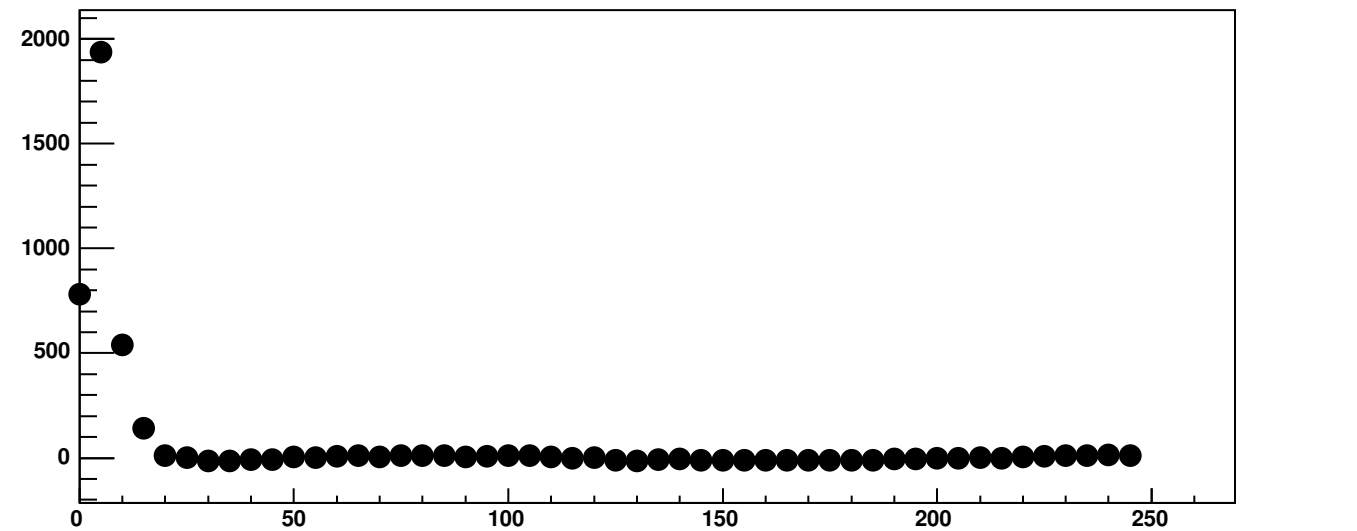
Chip 4, Channel 3, Enable 1!, DAC=4000, ADC Mean vs Hold



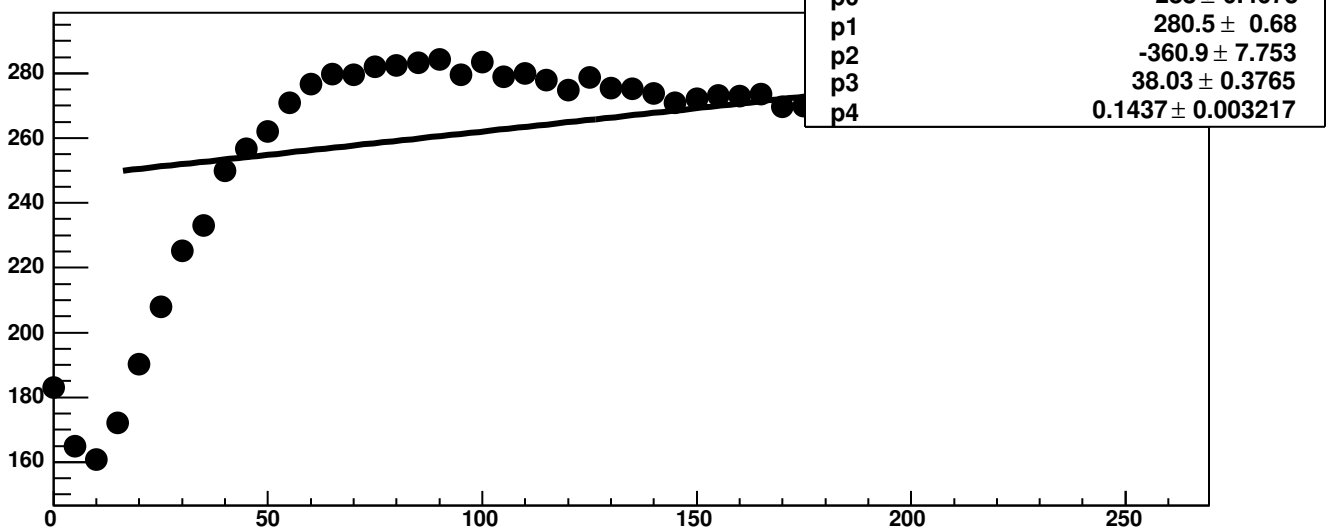
Chip 4, Channel 3, Enable 1!, DAC=4000, ADC Noise vs Hold



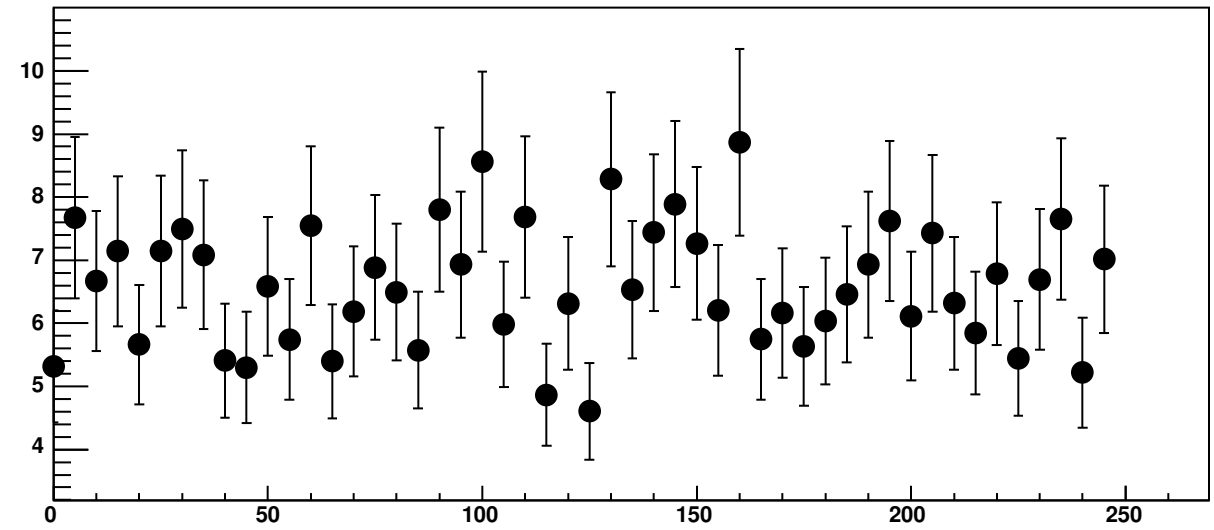
Chip 4, Channel 3, Enable 1!, DAC=4000, ADC Residuals vs Hold



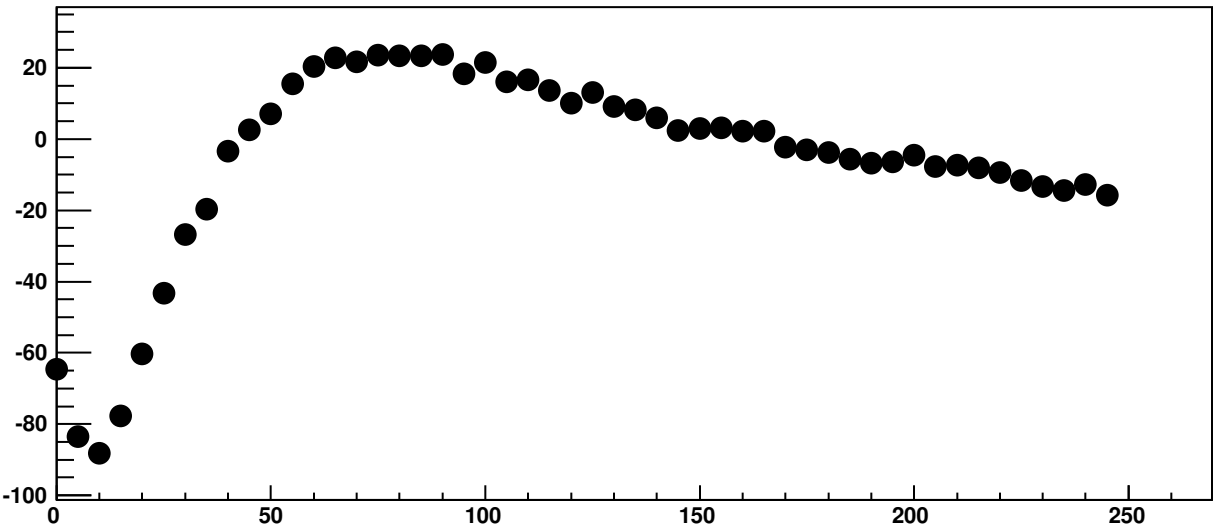
Chip 4, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold



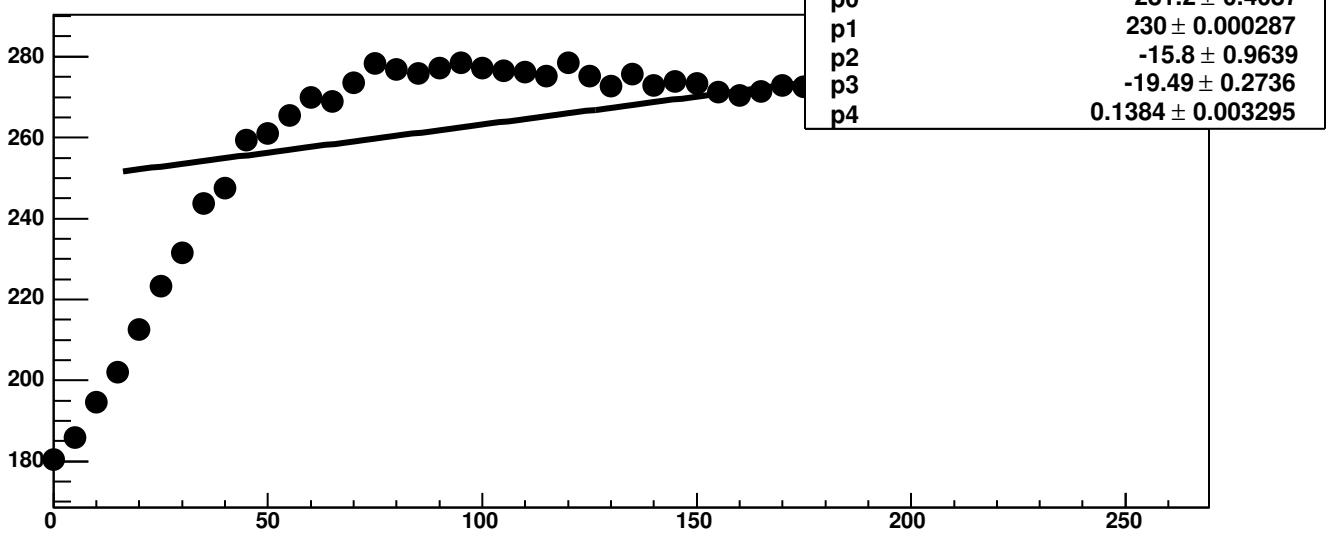
Chip 4, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold



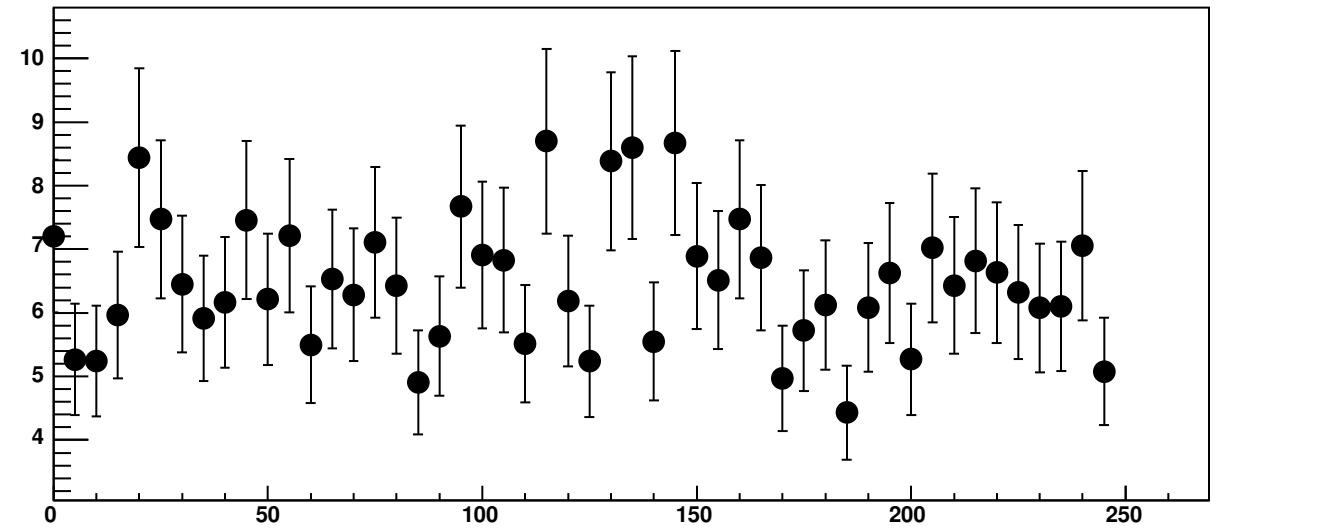
Chip 4, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold



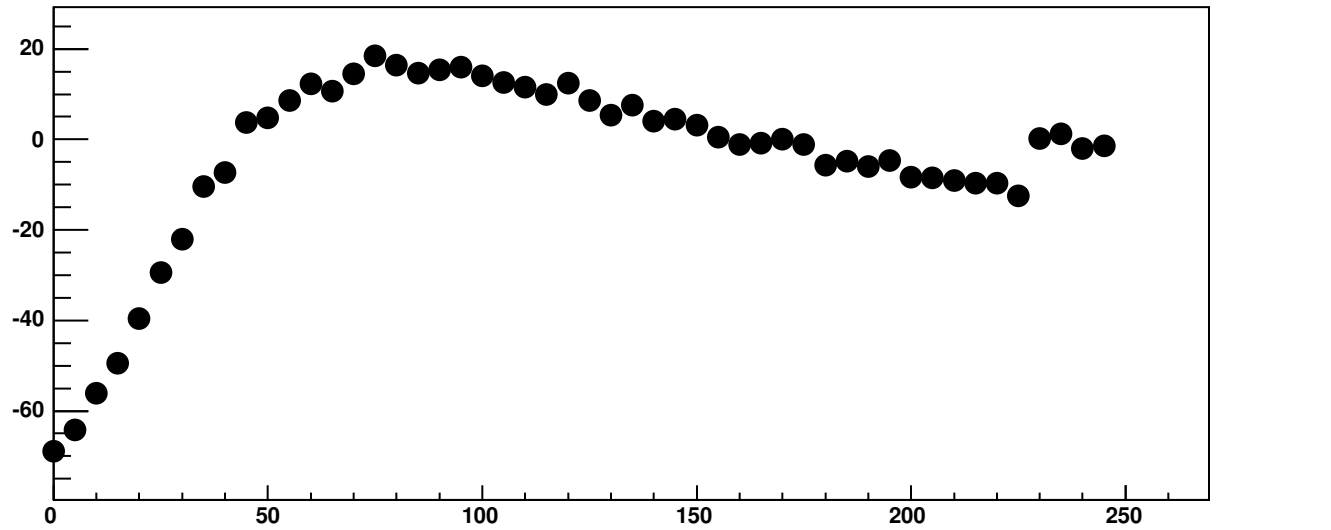
Chip 4, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold

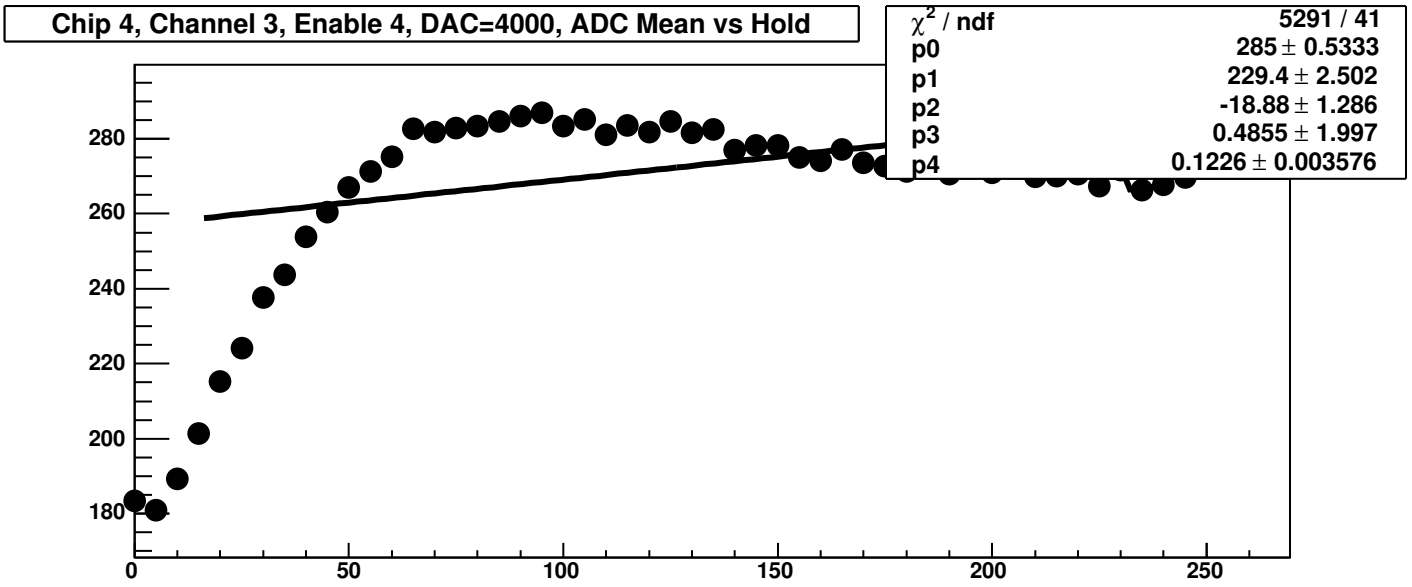


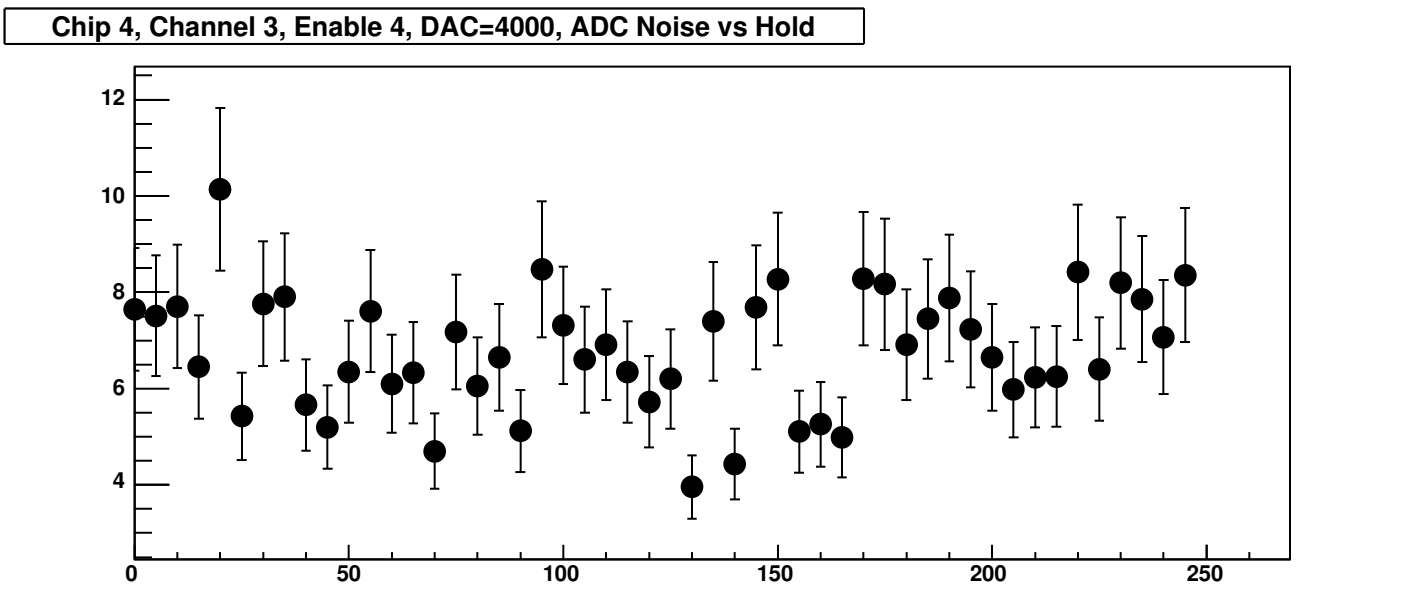
Chip 4, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold

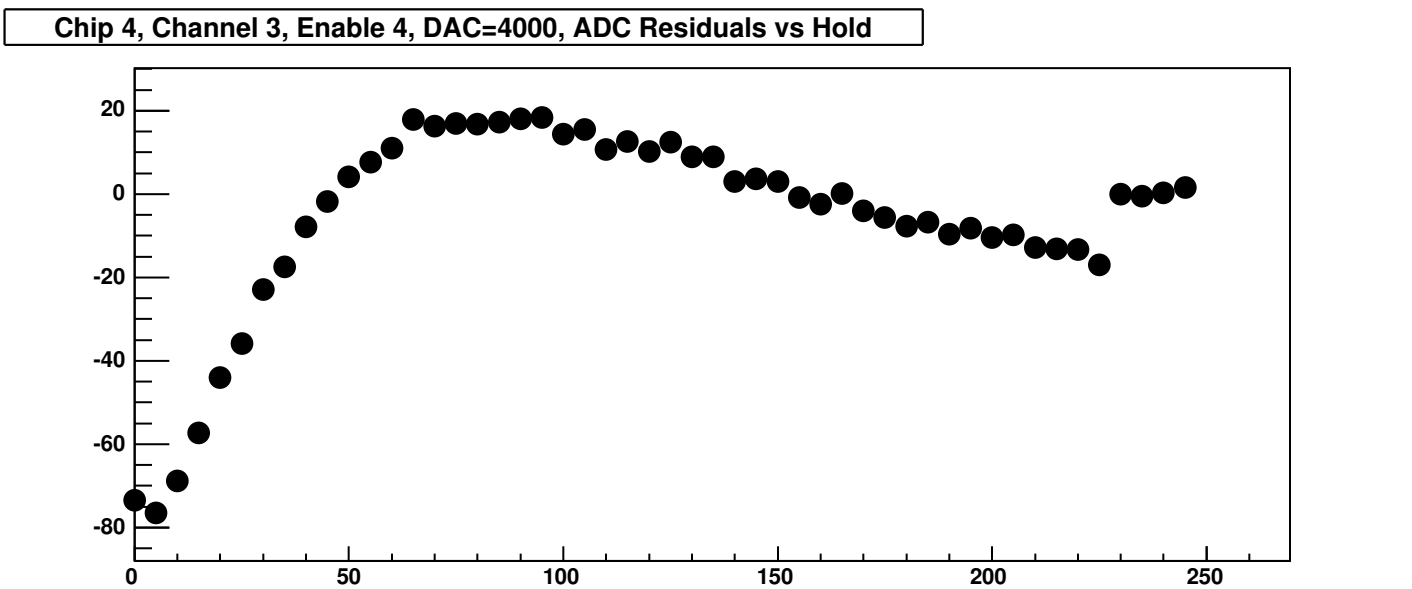


Chip 4, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold

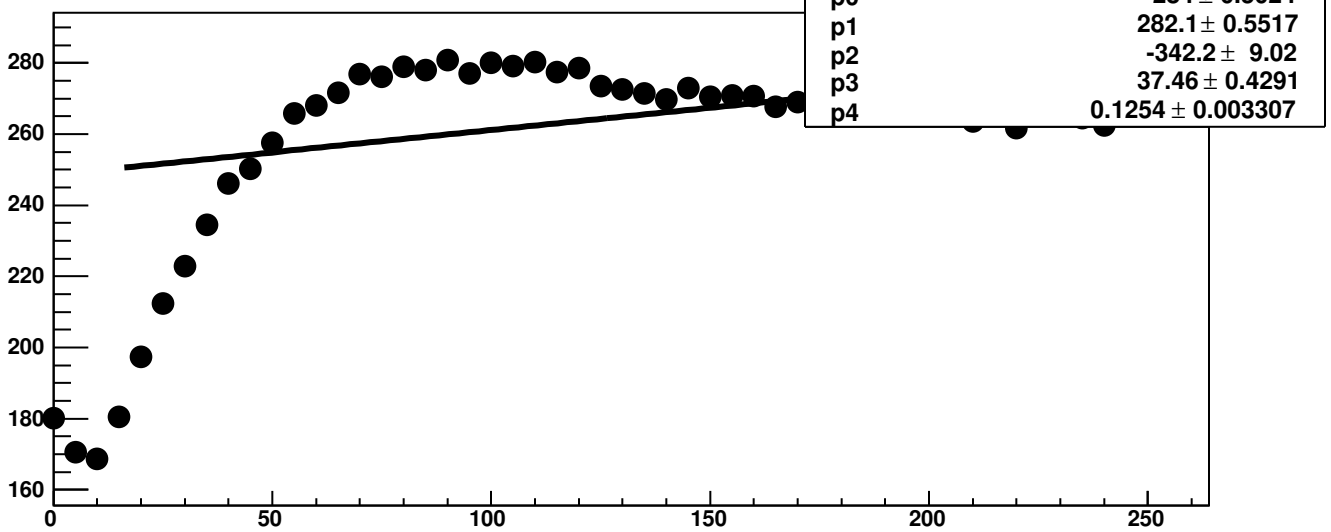



Chip 4, Channel 3, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 5291 / 41
p0 285 ± 0.5333
p1 229.4 ± 2.502
p2 -18.88 ± 1.286
p3 0.4855 ± 1.997
p4 0.1226 ± 0.003576
280
260
240
220
200
180
0
50
100
150
200
250

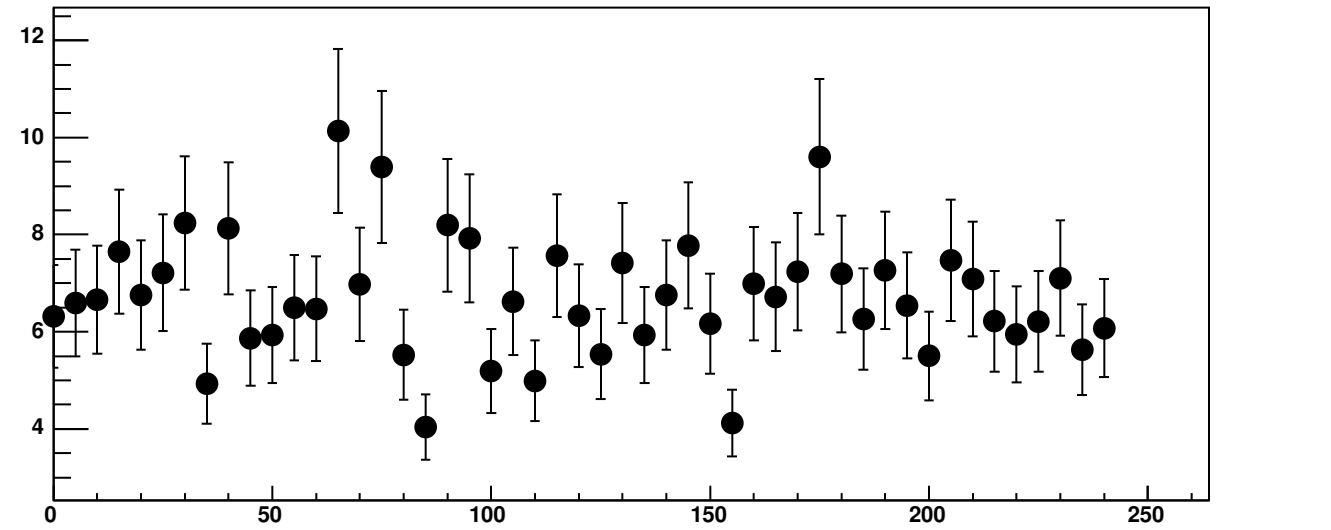

Chip 4, Channel 3, Enable 4, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250


Chip 4, Channel 3, Enable 4, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
-80
0
50
100
150
200
250

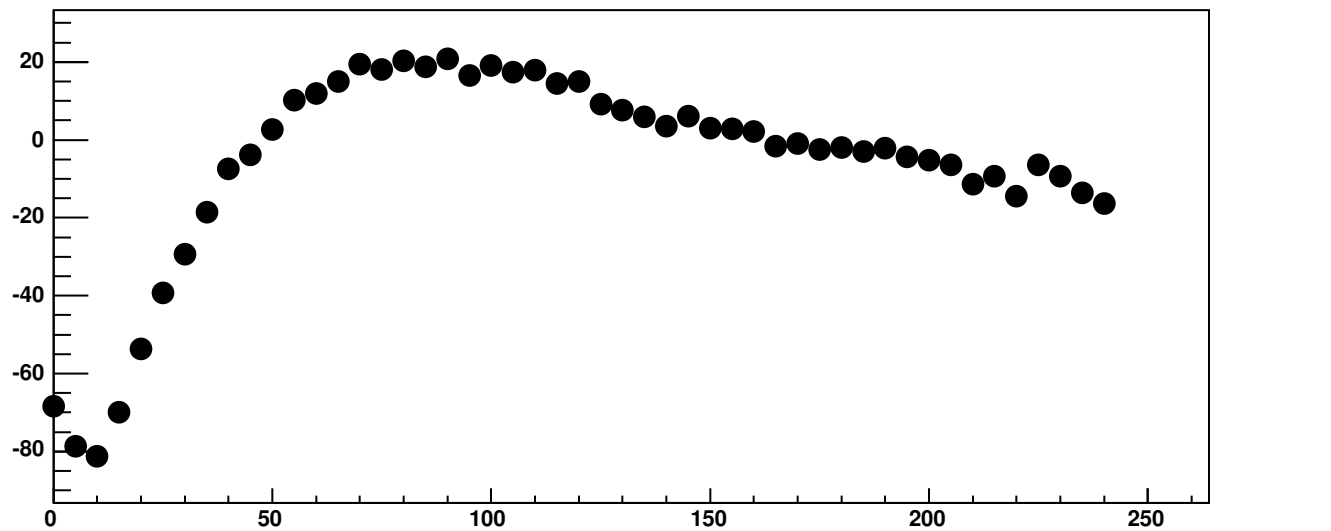
Chip 4, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold



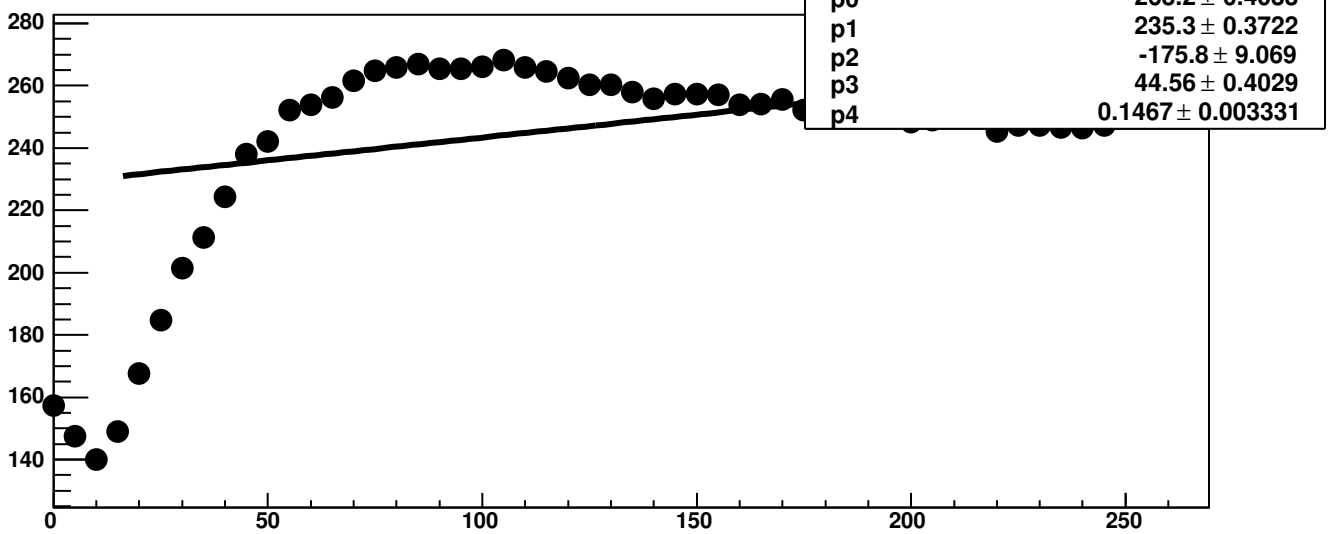
Chip 4, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



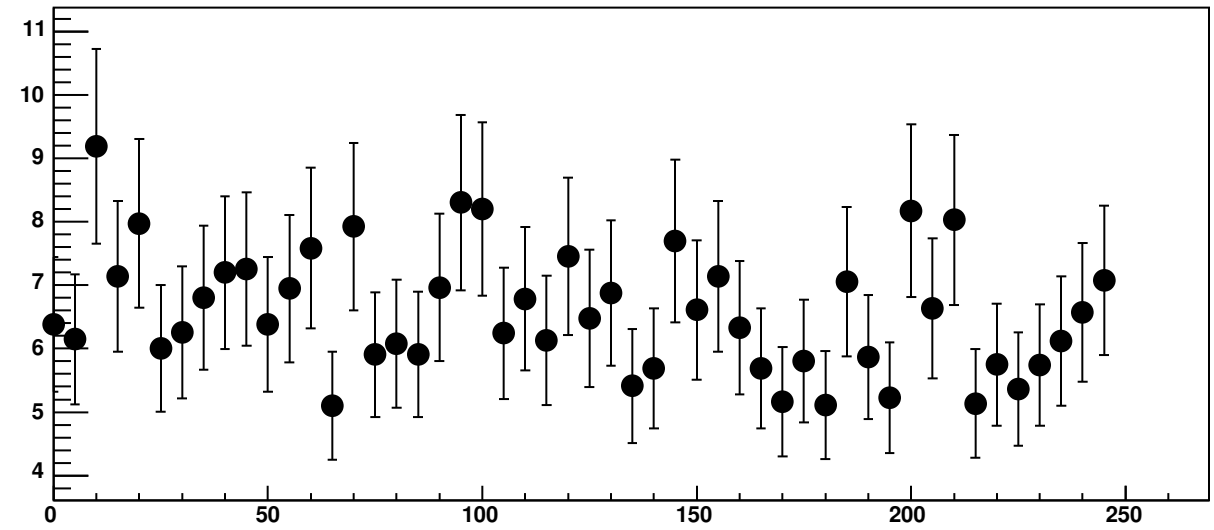
Chip 4, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold



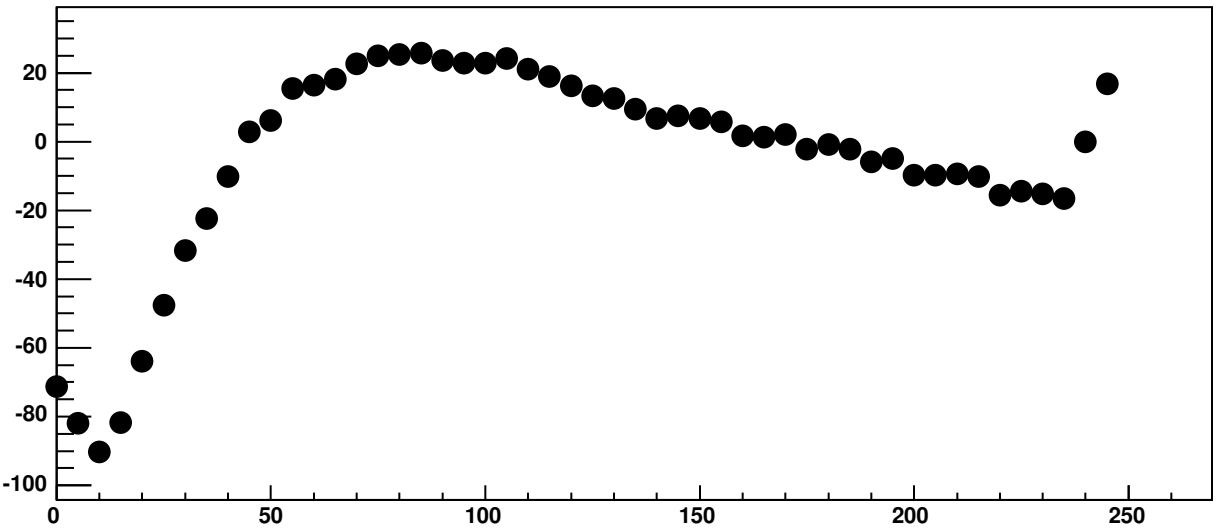
Chip 4, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

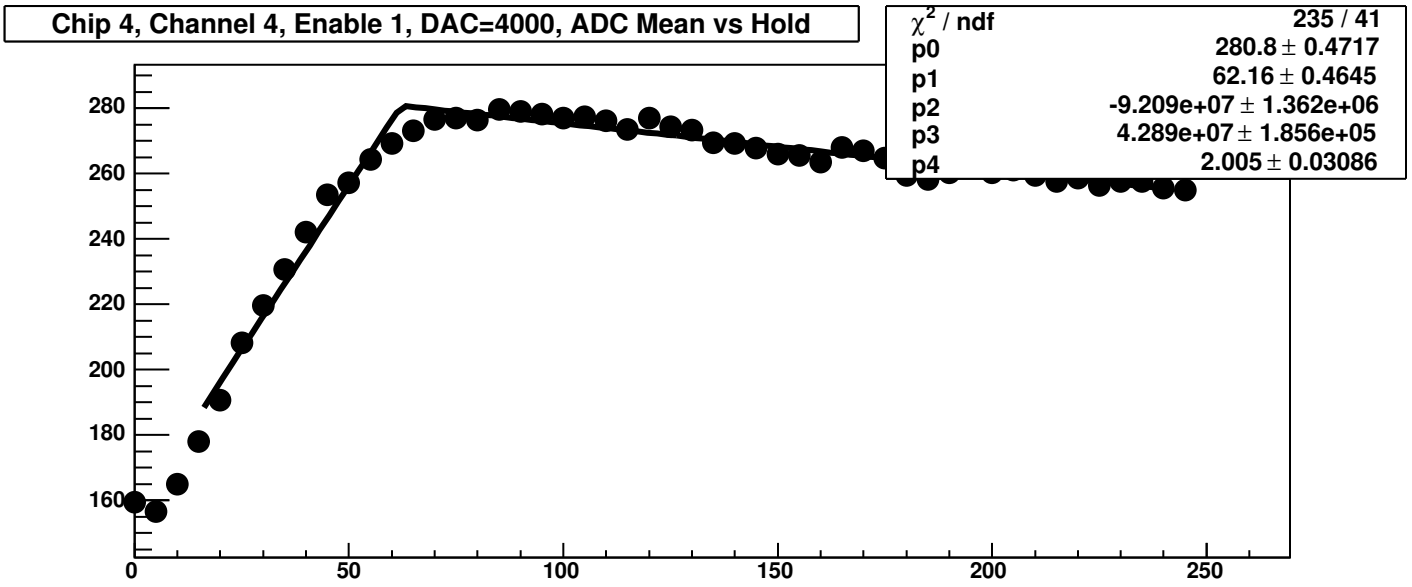


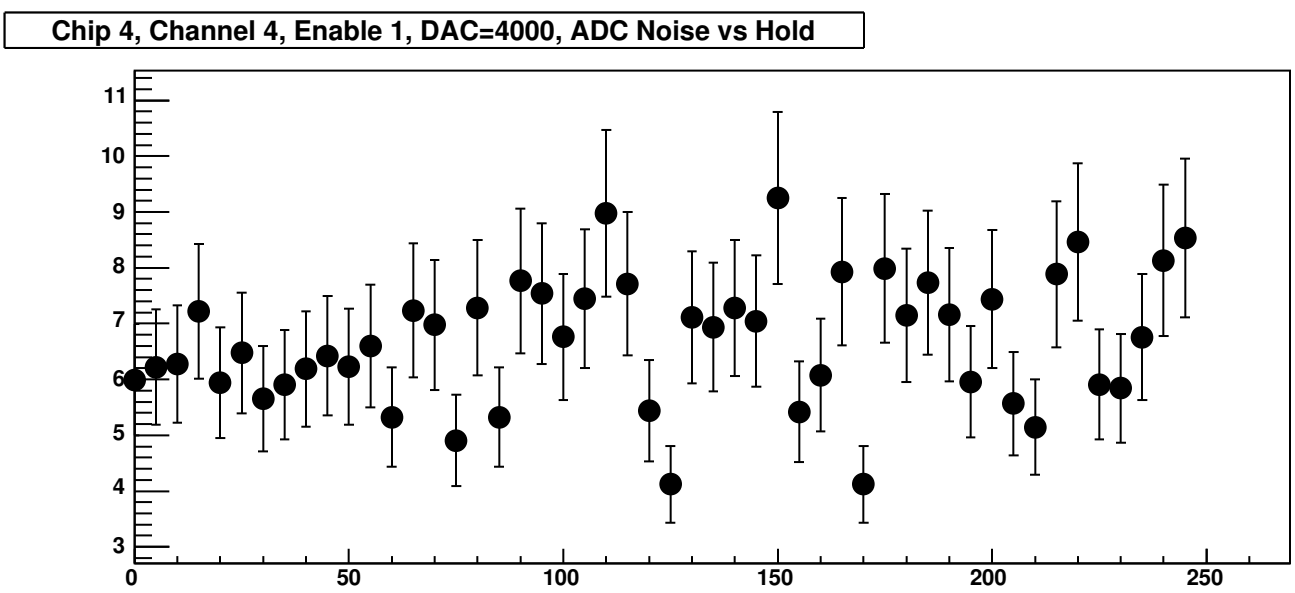
Chip 4, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold

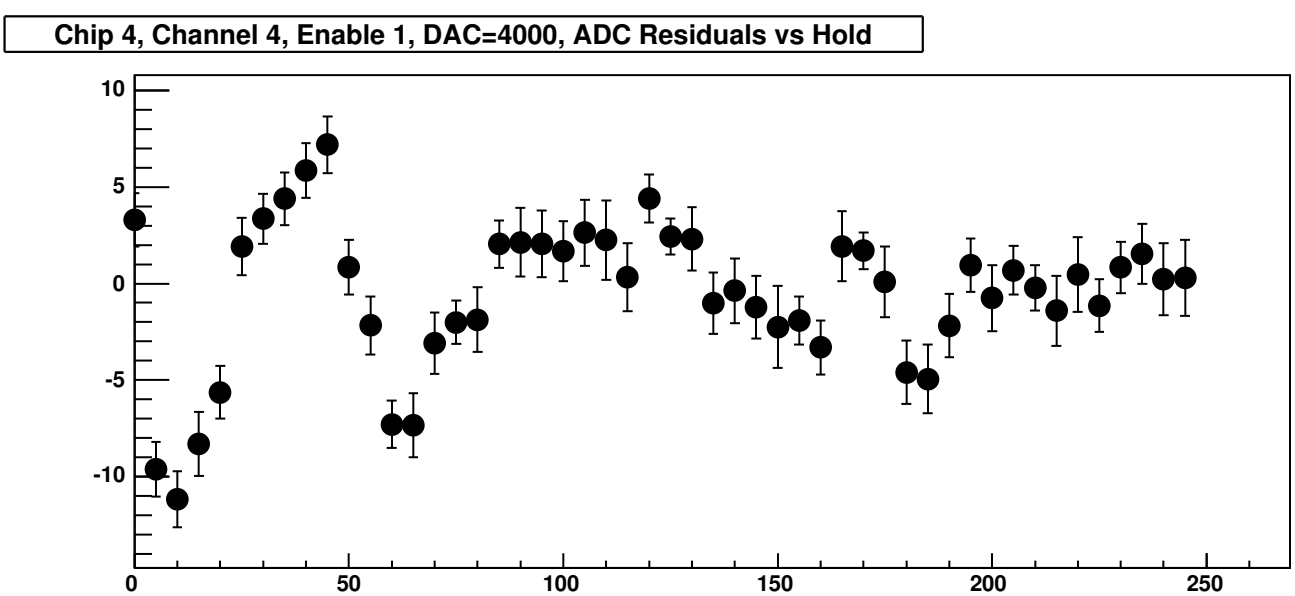


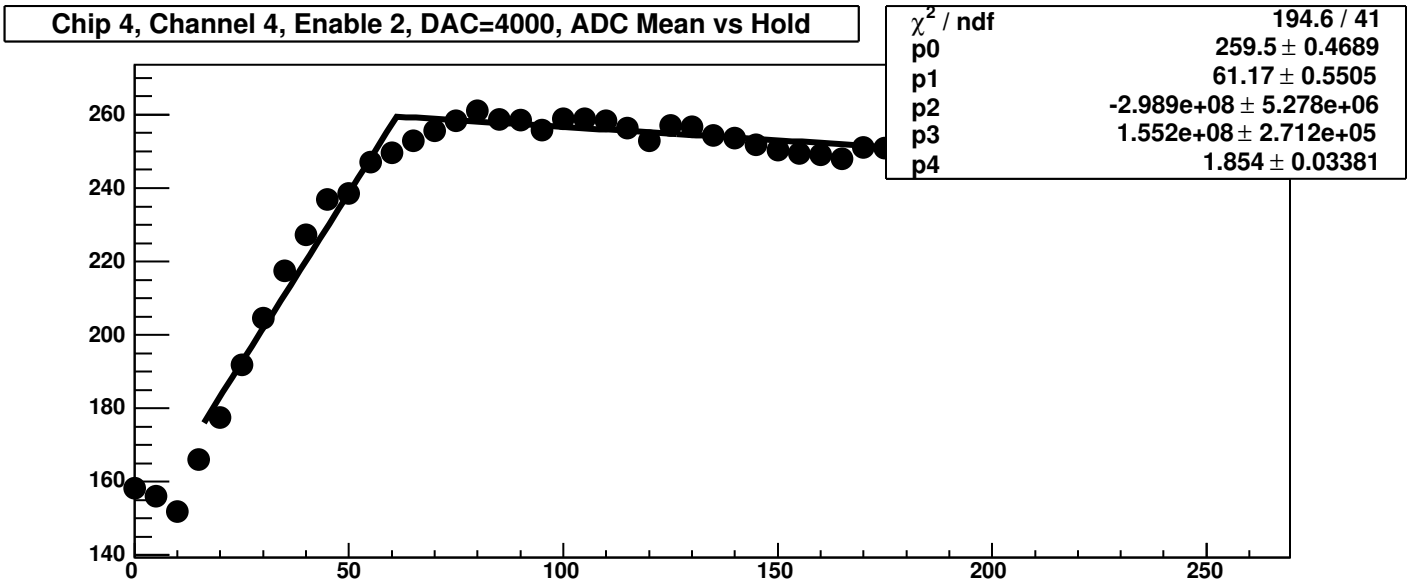
Chip 4, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

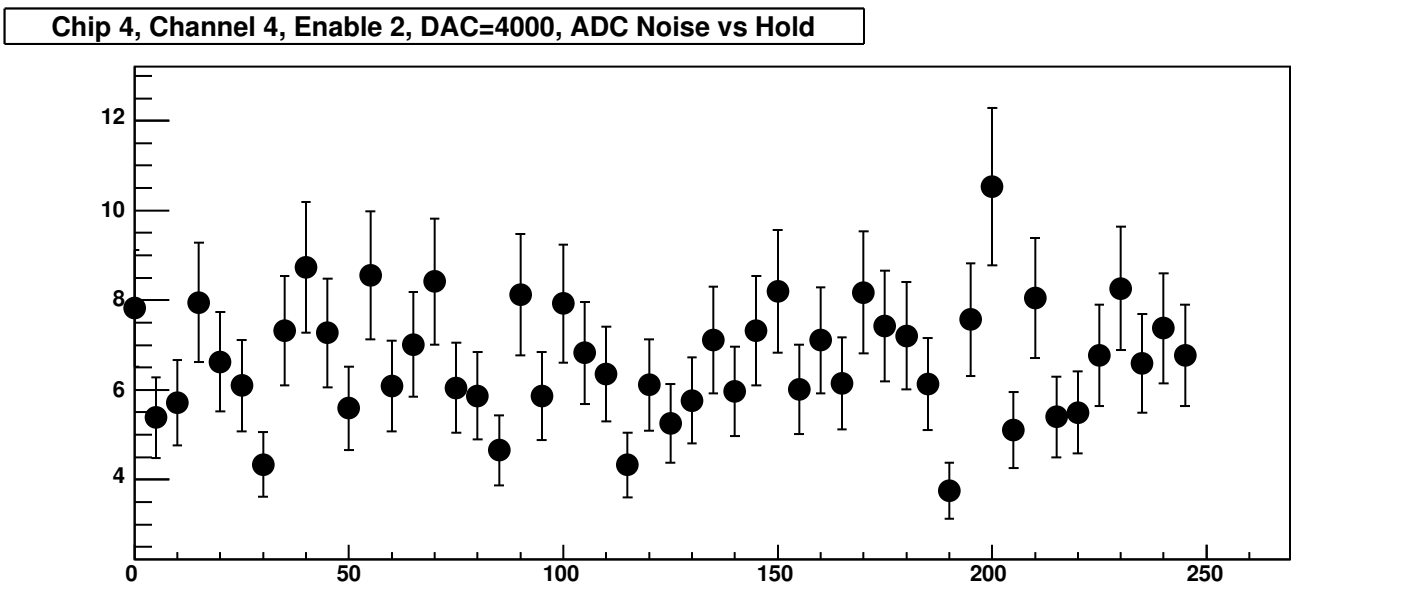


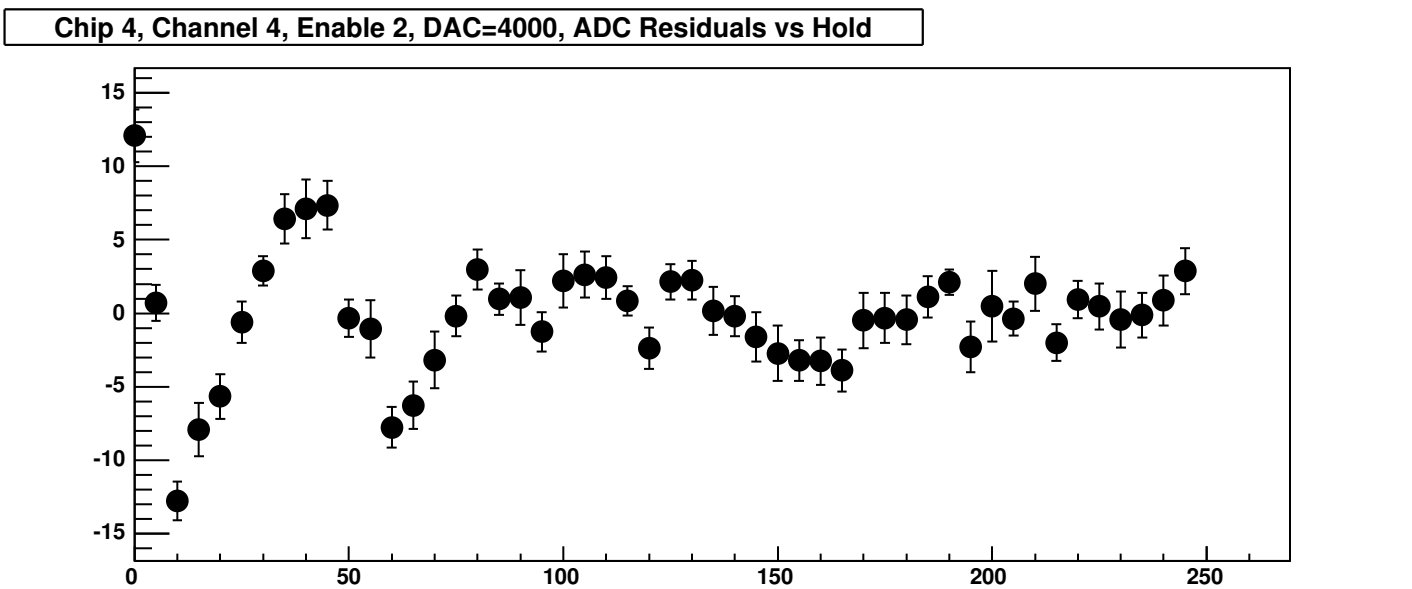

Chip 4, Channel 4, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 235 / 41
p0 280.8 ± 0.4717
p1 62.16 ± 0.4645
p2 -9.209e+07 ± 1.362e+06
p3 4.289e+07 ± 1.856e+05
p4 2.005 ± 0.03086
280
260
240
220
200
180
160
0
50
100
150
200
250

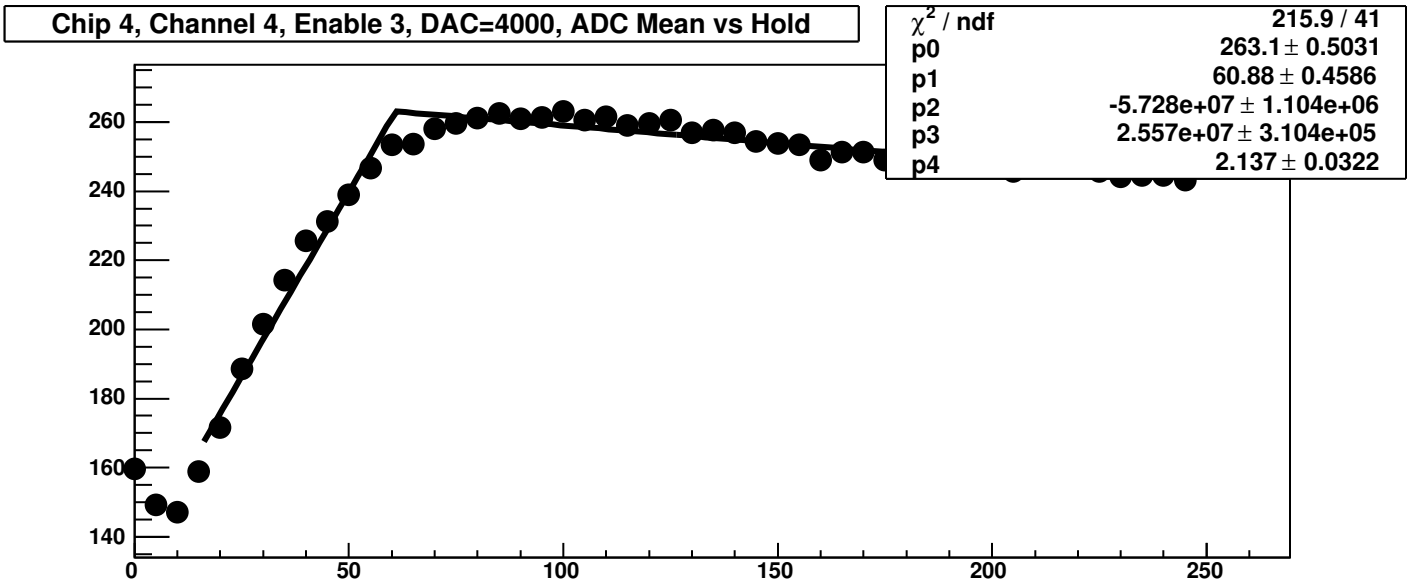

Chip 4, Channel 4, Enable 1, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

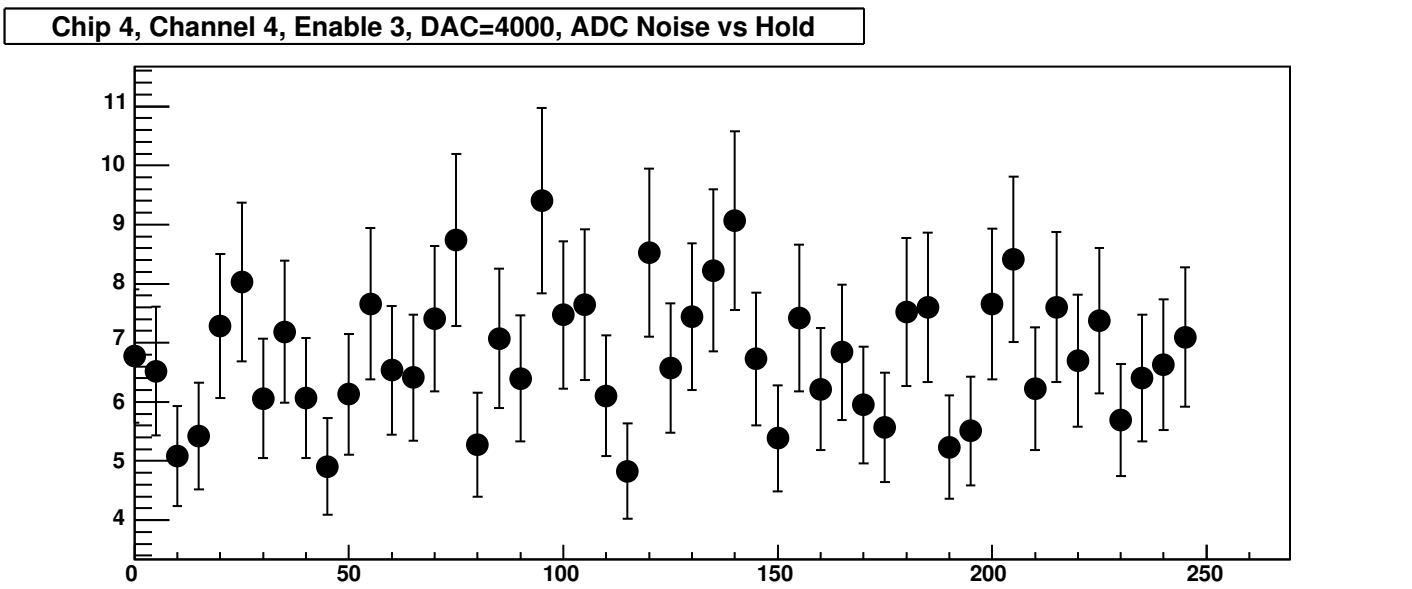

Chip 4, Channel 4, Enable 1, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250

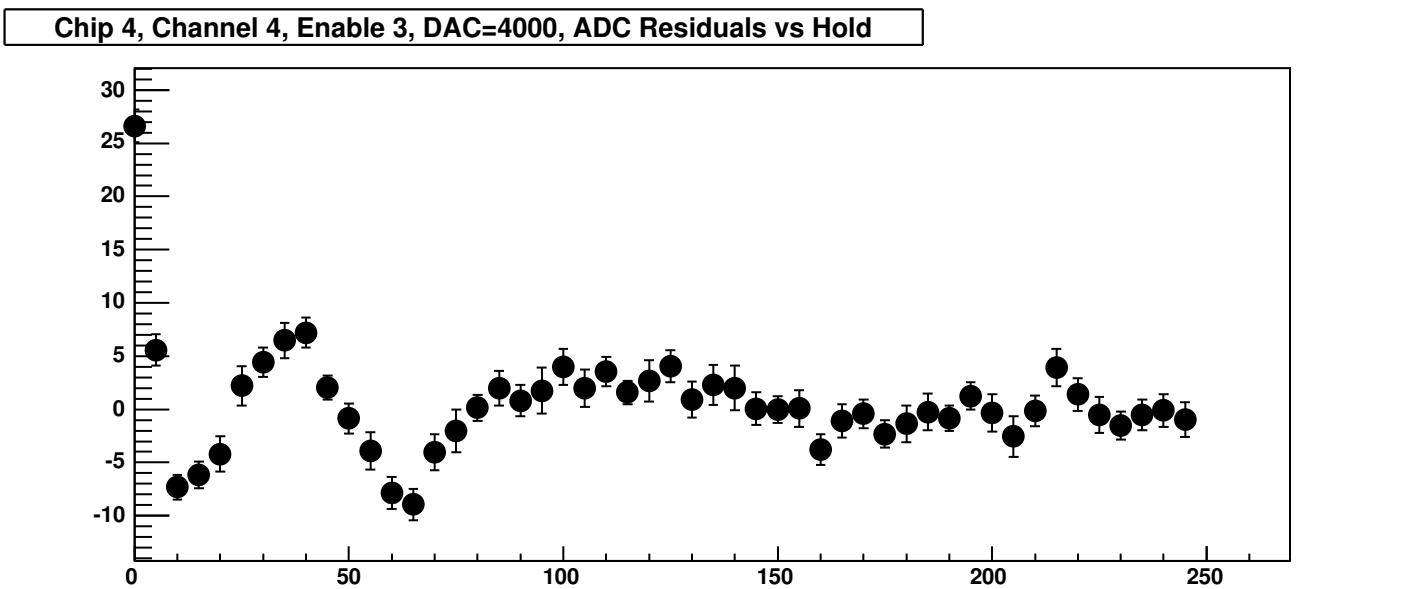

Chip 4, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 194.6 / 41
p0 259.5 ± 0.4689
p1 61.17 ± 0.5505
p2 -2.989e+08 ± 5.278e+06
p3 1.552e+08 ± 2.712e+05
p4 1.854 ± 0.03381
260
240
220
200
180
160
140
0
50
100
150
200
250

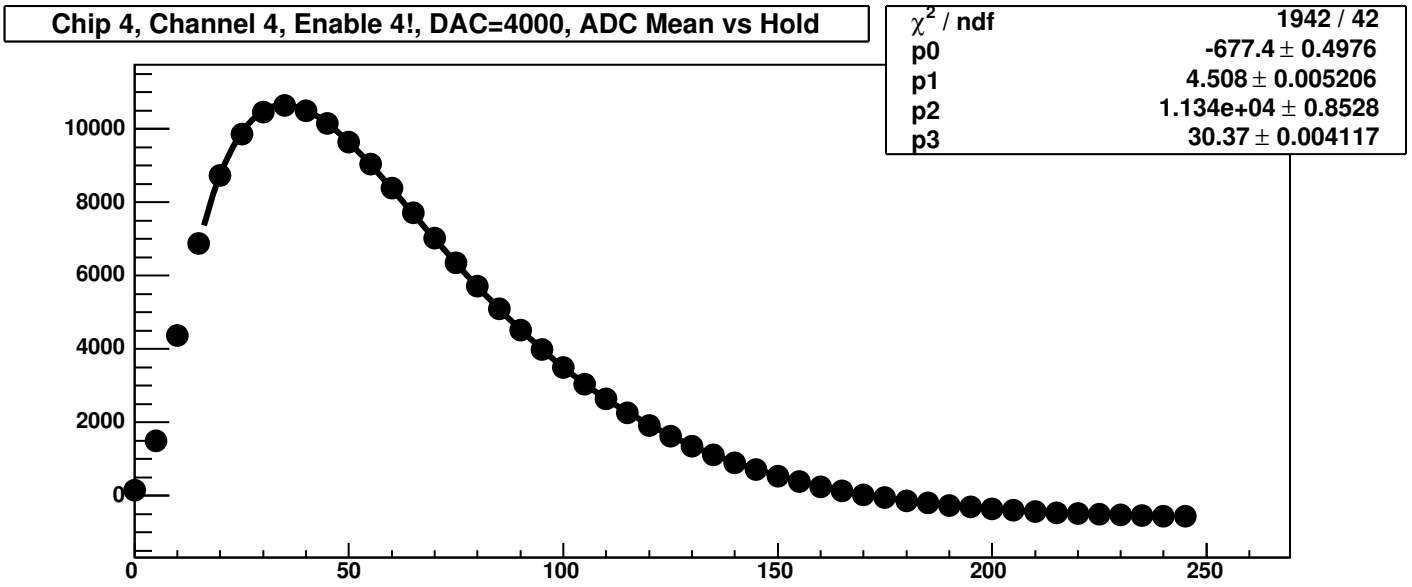

Chip 4, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold
12
10
8
6
4
0
50
100
150
200
250

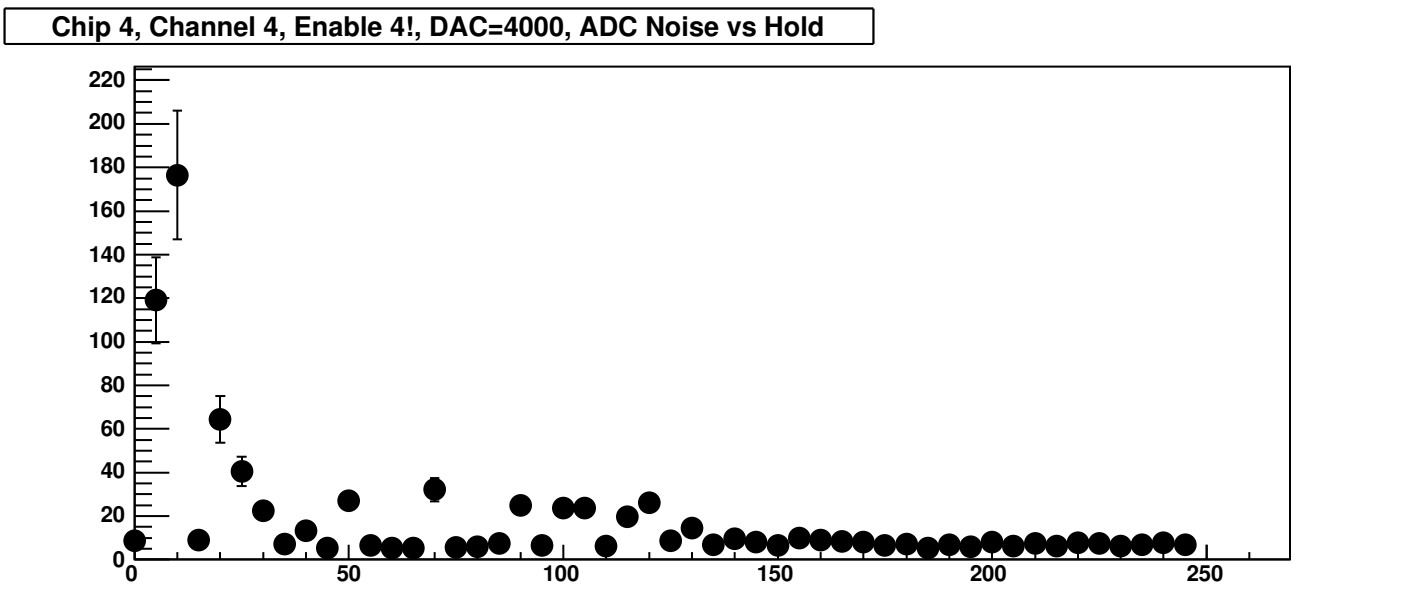

Chip 4, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

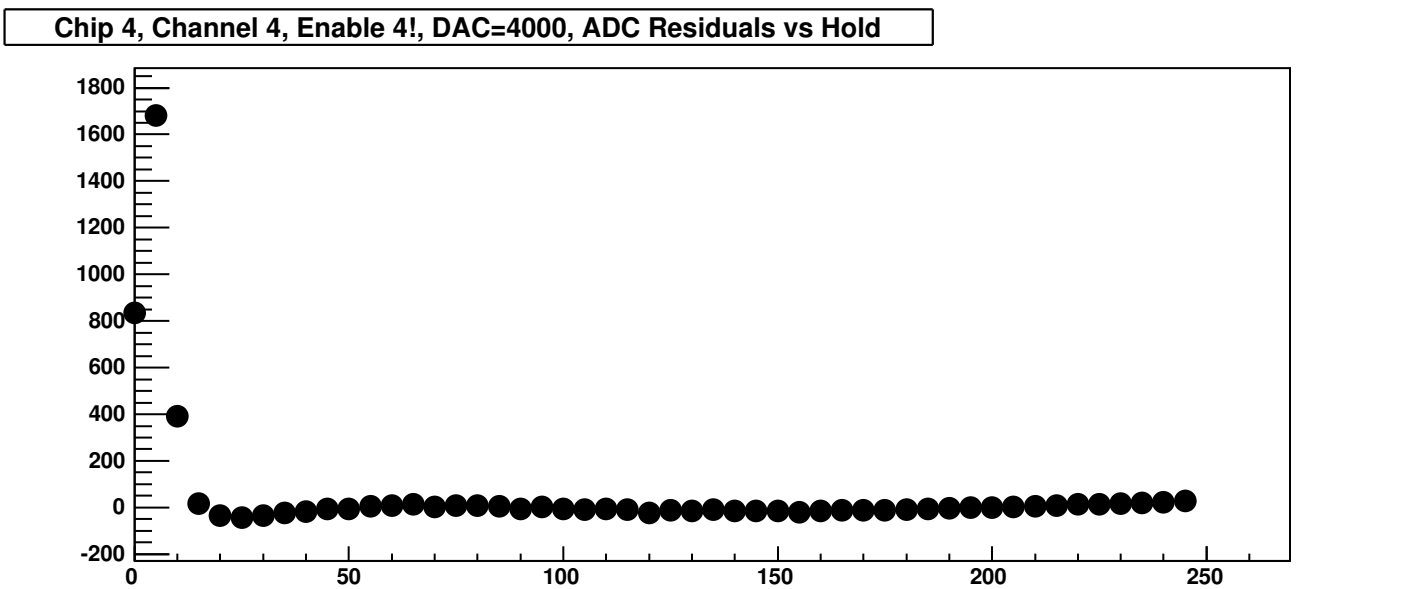

Chip 4, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 215.9 / 41
p0 263.1 ± 0.5031
p1 60.88 ± 0.4586
p2 -5.728e+07 ± 1.104e+06
p3 2.557e+07 ± 3.104e+05
p4 2.137 ± 0.0322
260
240
220
200
180
160
140
0
50
100
150
200
250

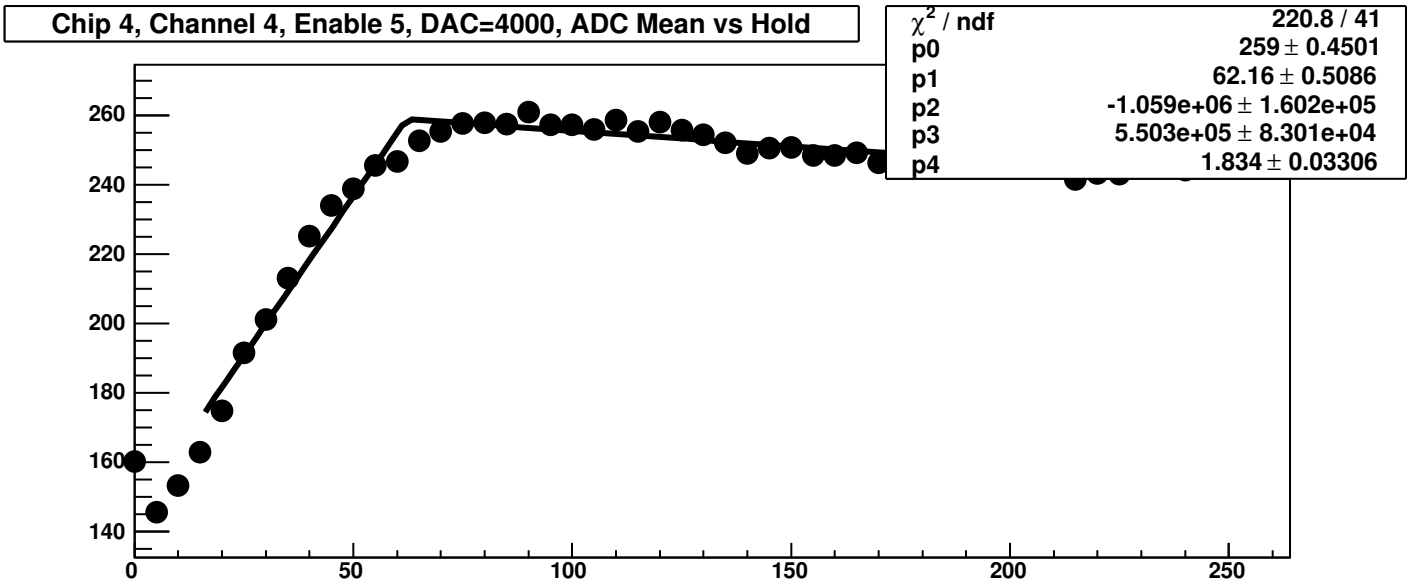

Chip 4, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250

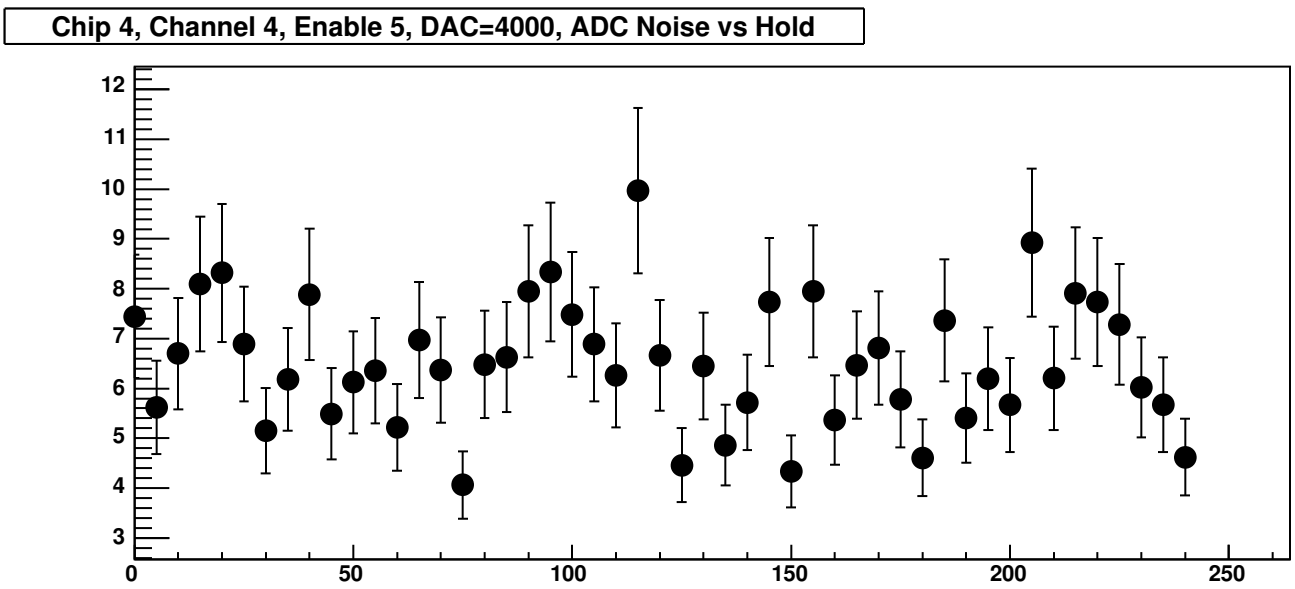

Chip 4, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
0
50
100
150
200
250

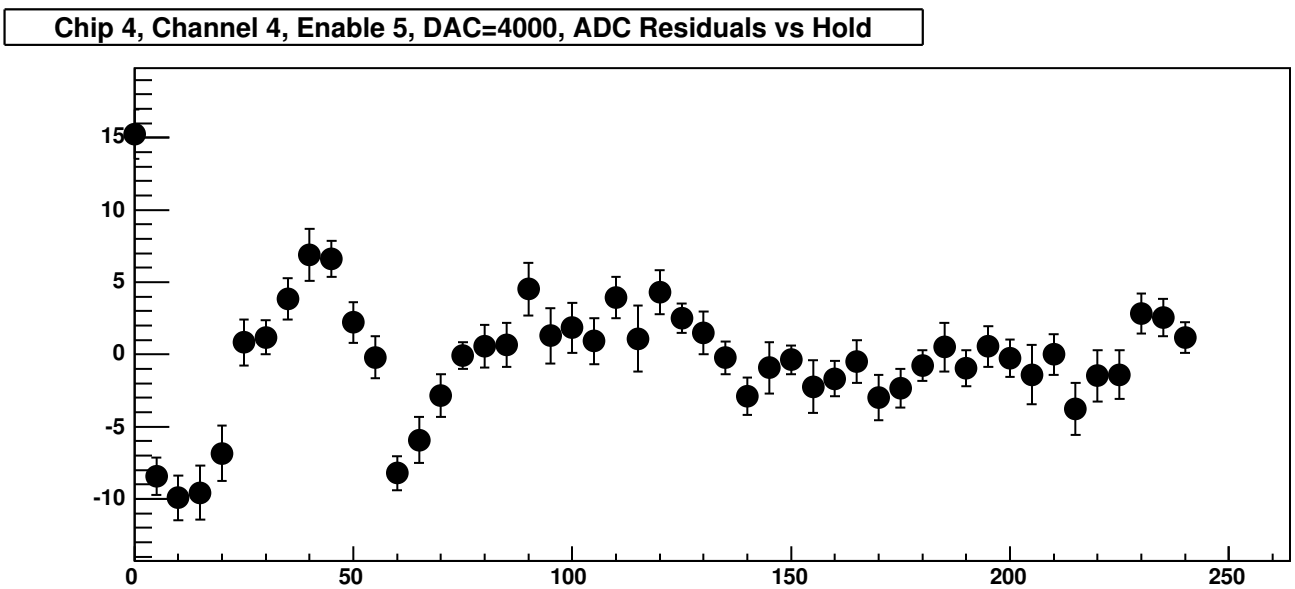

Chip 4, Channel 4, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1942 / 42
p0 -677.4 ± 0.4976
p1 4.508 ± 0.005206
p2 1.134e+04 ± 0.8528
p3 30.37 ± 0.004117
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 4, Channel 4, Enable 4!, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

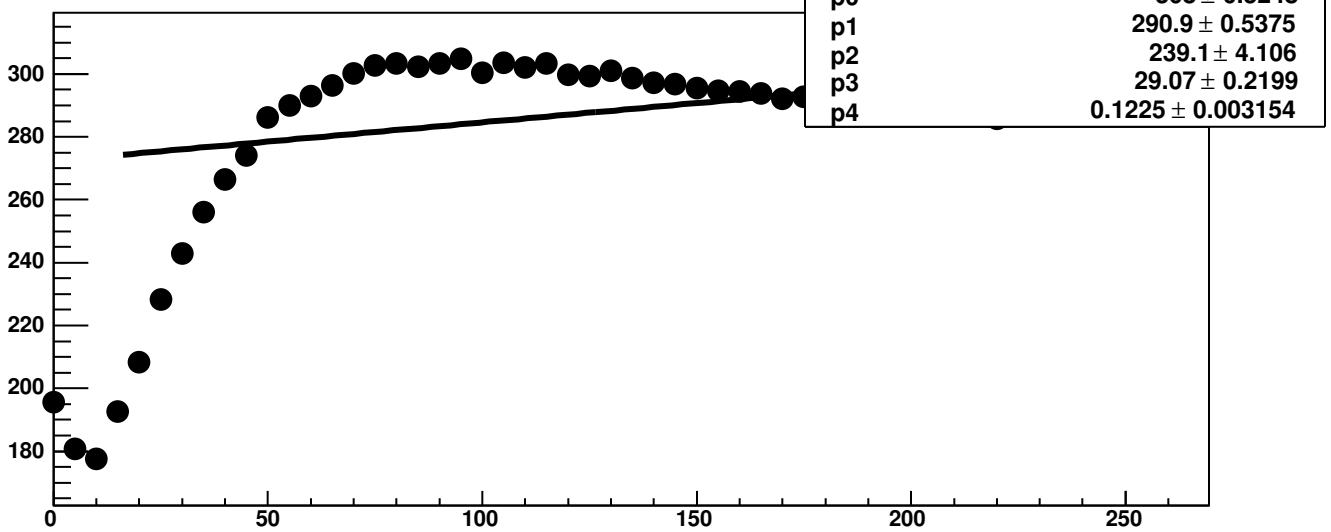

Chip 4, Channel 4, Enable 4!, DAC=4000, ADC Residuals vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250


Chip 4, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 220.8 / 41
p0 259 ± 0.4501
p1 62.16 ± 0.5086
p2 -1.059e+06 ± 1.602e+05
p3 5.503e+05 ± 8.301e+04
p4 1.834 ± 0.03306
260
240
220
200
180
160
140
0
50
100
150
200
250

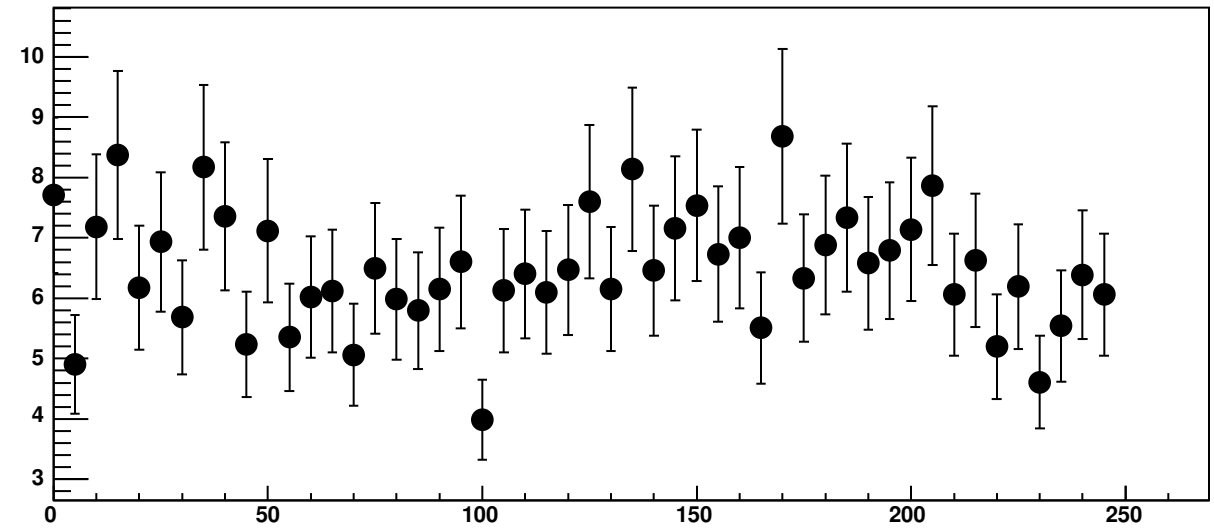

Chip 4, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 4, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
0
50
100
150
200
250

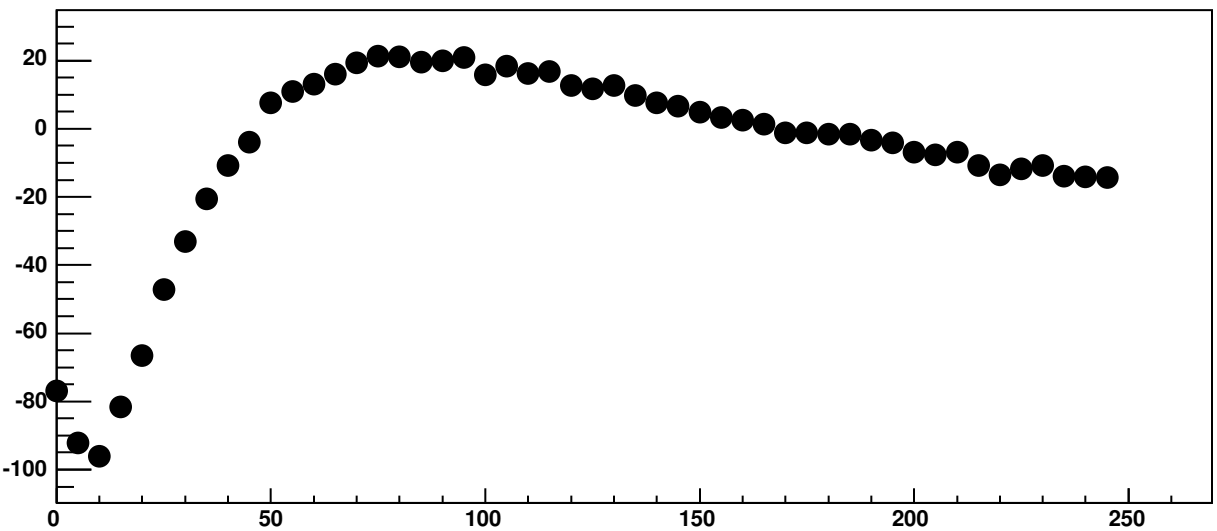
Chip 4, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold



Chip 4, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold

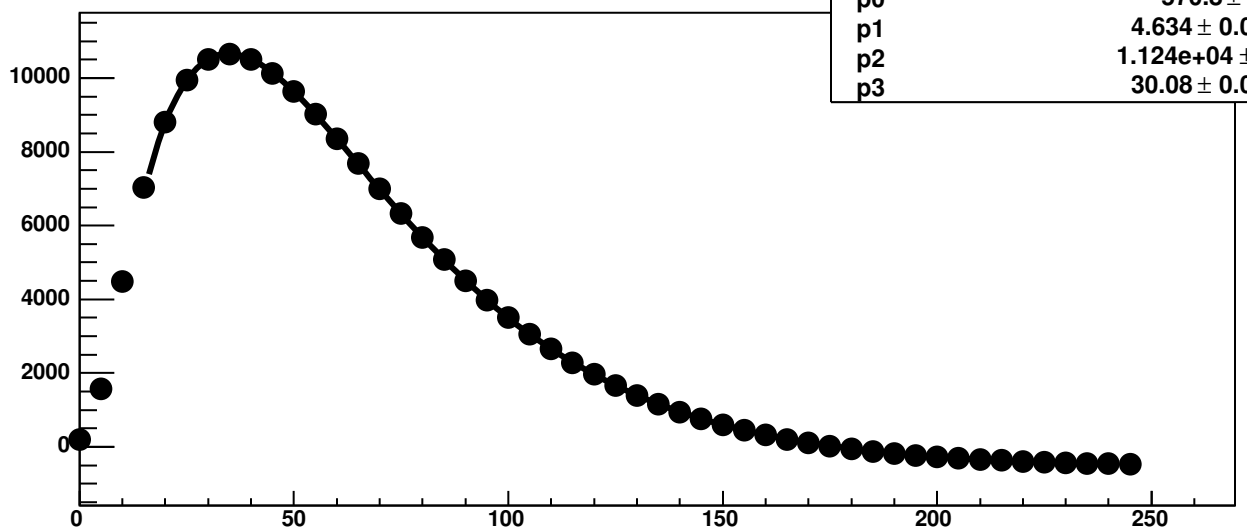


Chip 4, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold

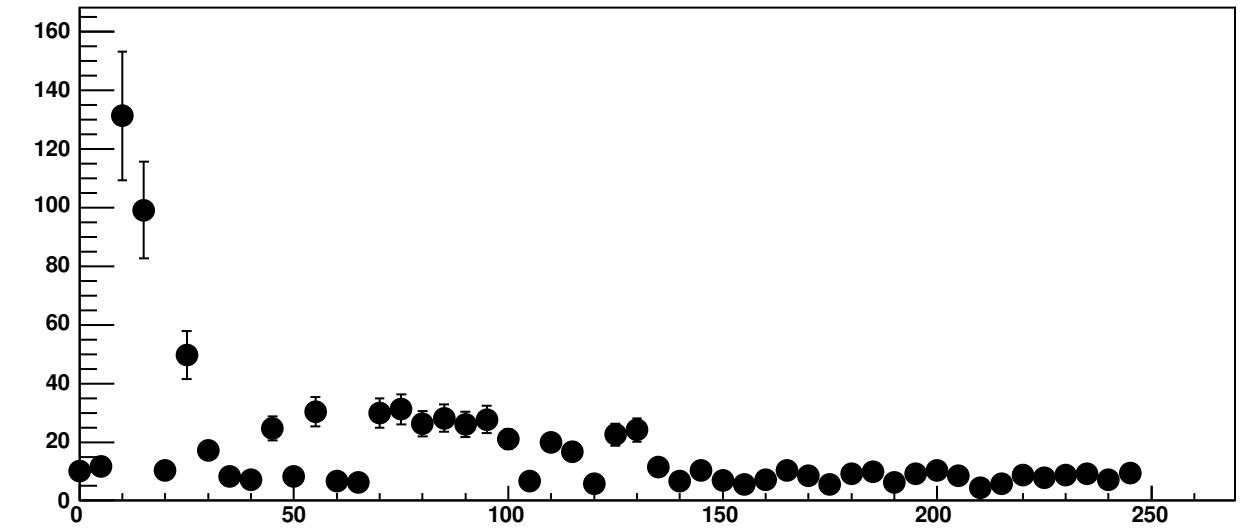


Chip 4, Channel 5, Enable 1!, DAC=4000, ADC Mean vs Hold

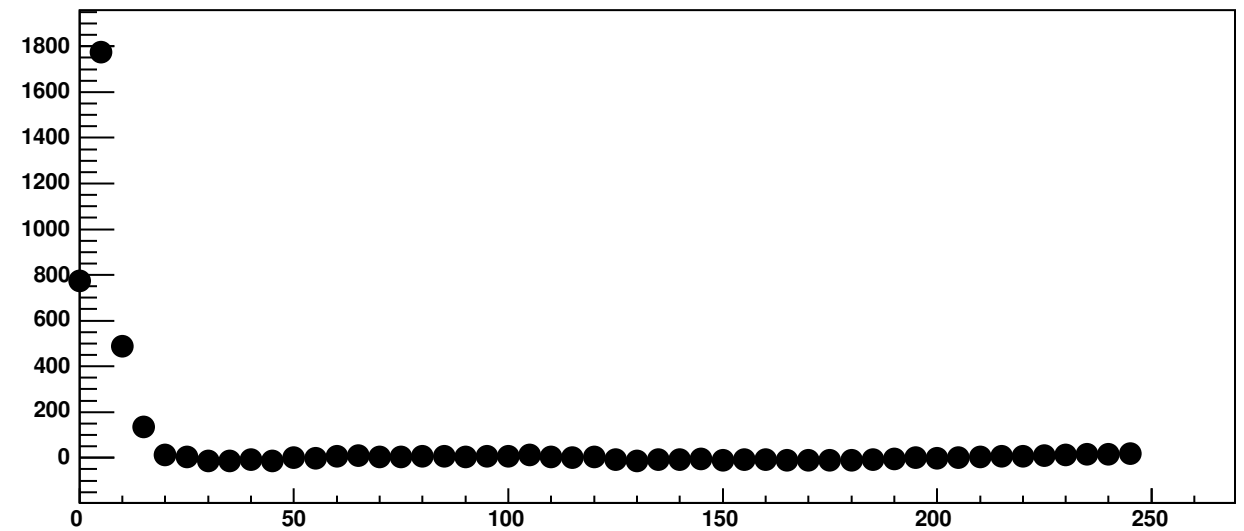
χ^2 / ndf	844.7 / 42
p0	-570.8 ± 0.5637
p1	4.634 ± 0.008984
p2	1.124e+04 ± 0.976
p3	30.08 ± 0.005644



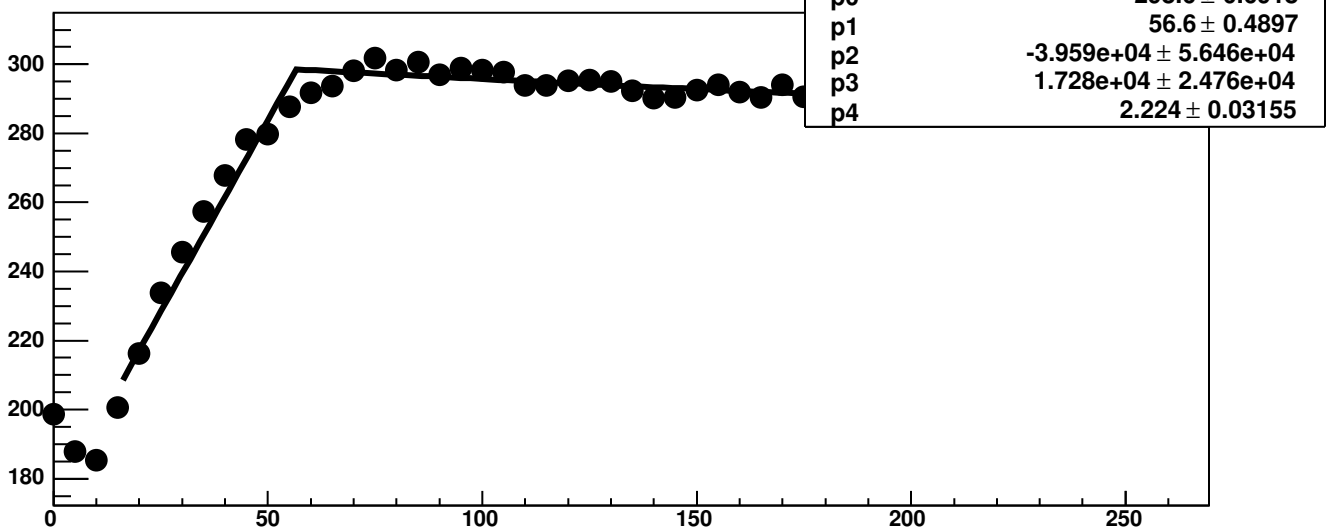
Chip 4, Channel 5, Enable 1!, DAC=4000, ADC Noise vs Hold



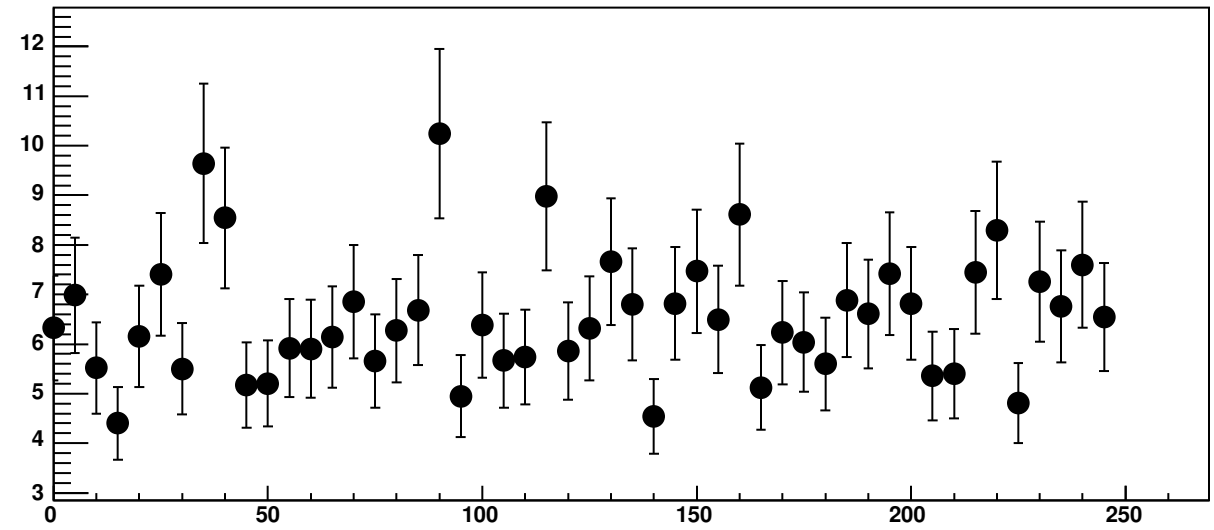
Chip 4, Channel 5, Enable 1!, DAC=4000, ADC Residuals vs Hold



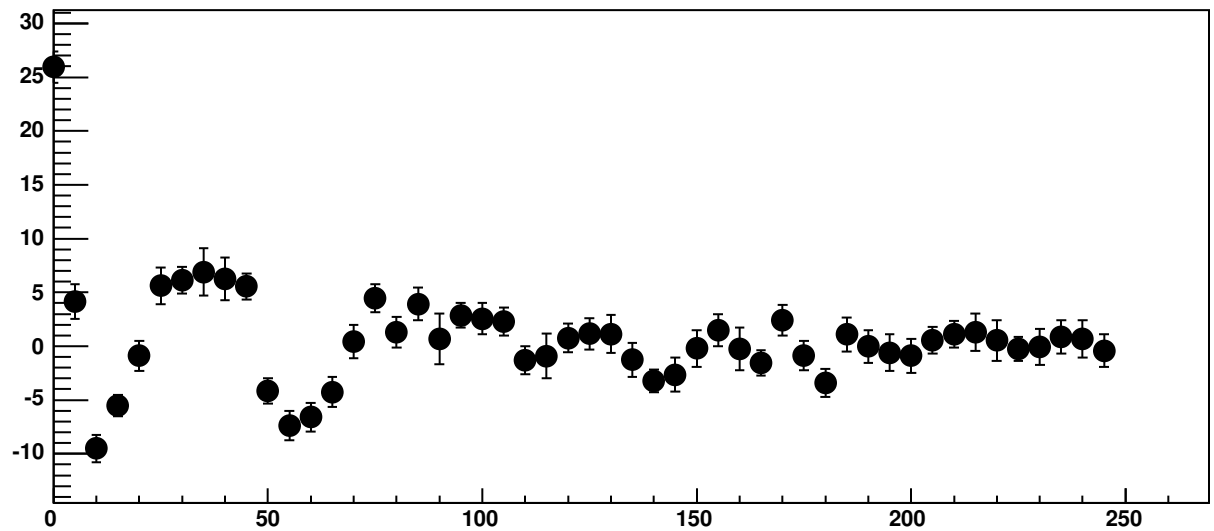
Chip 4, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold



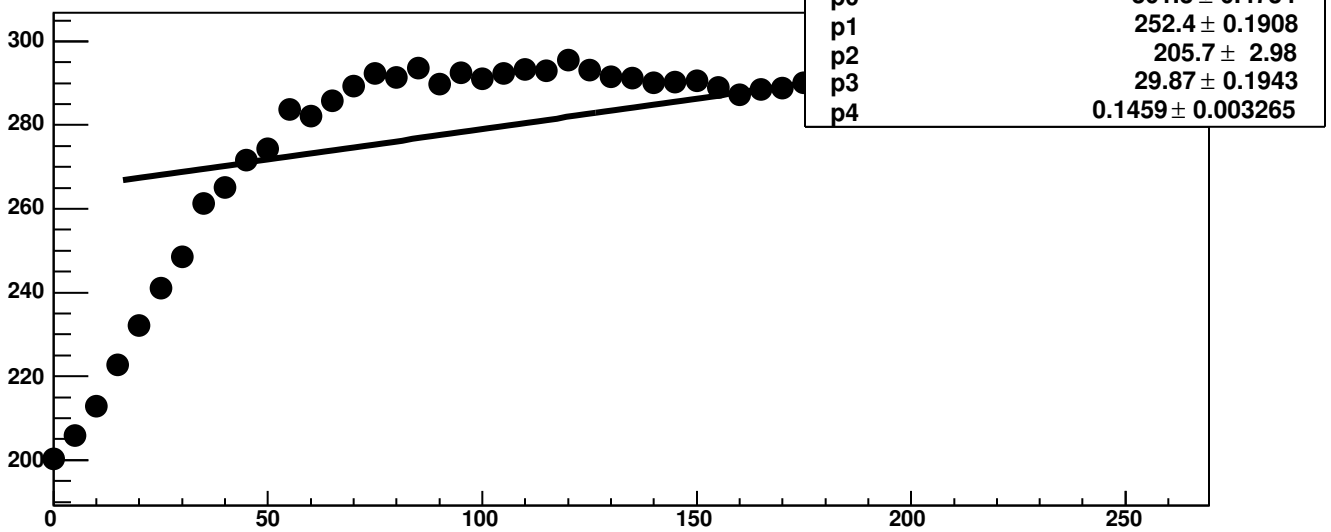
Chip 4, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



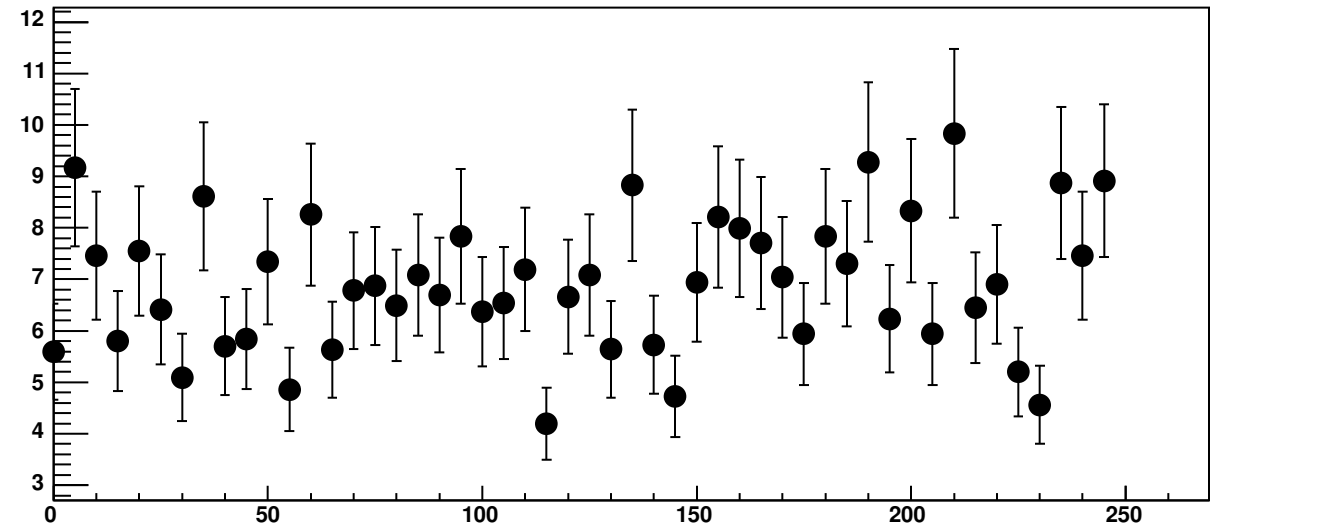
Chip 4, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold



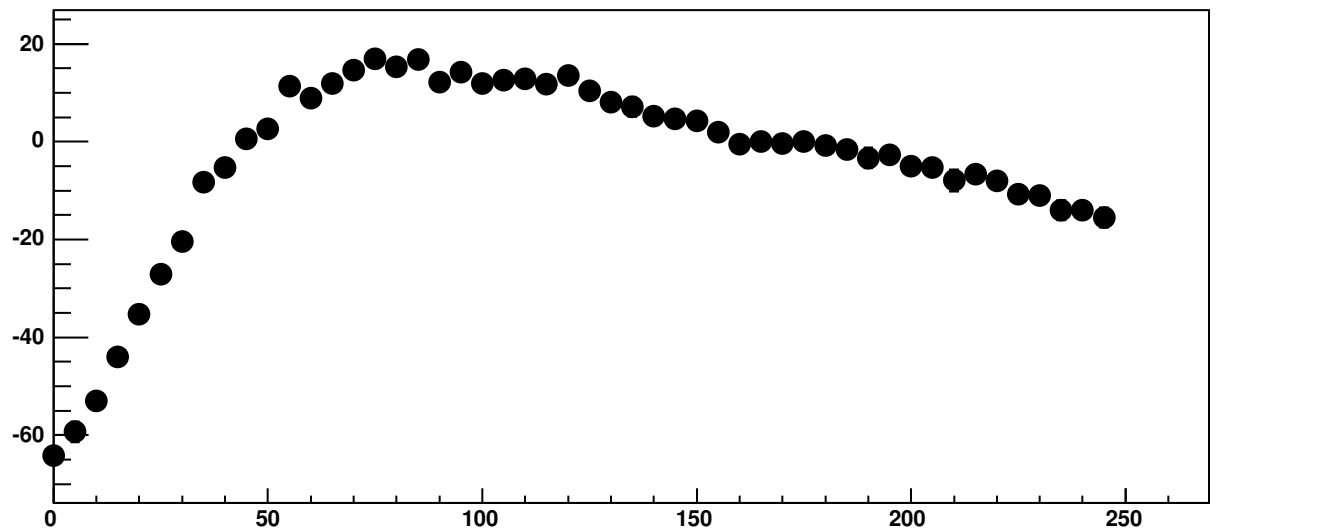
Chip 4, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold



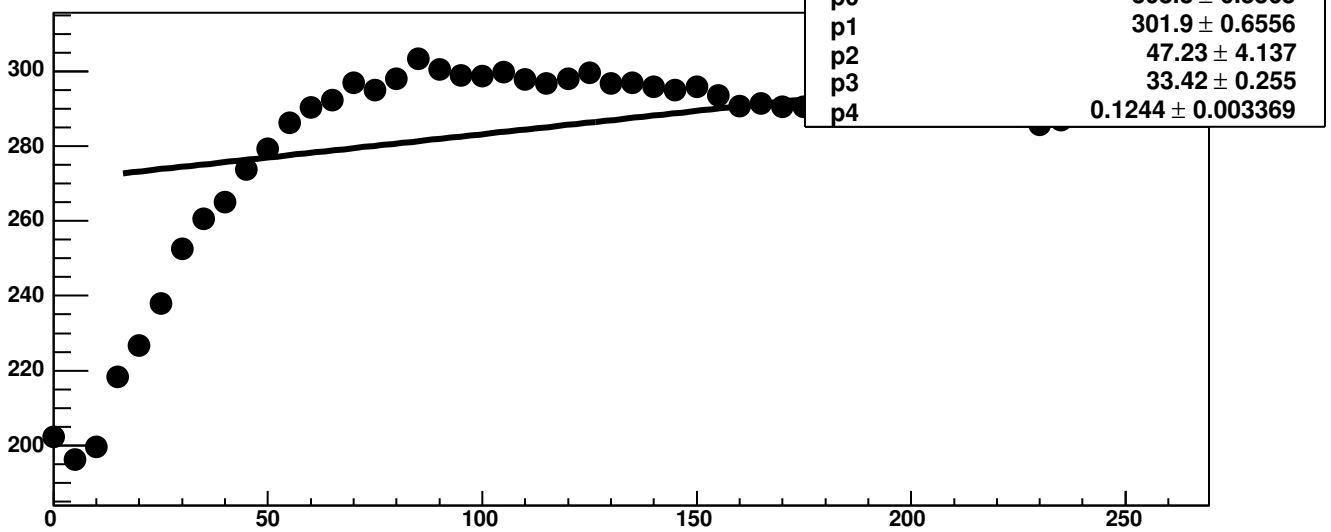
Chip 4, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold



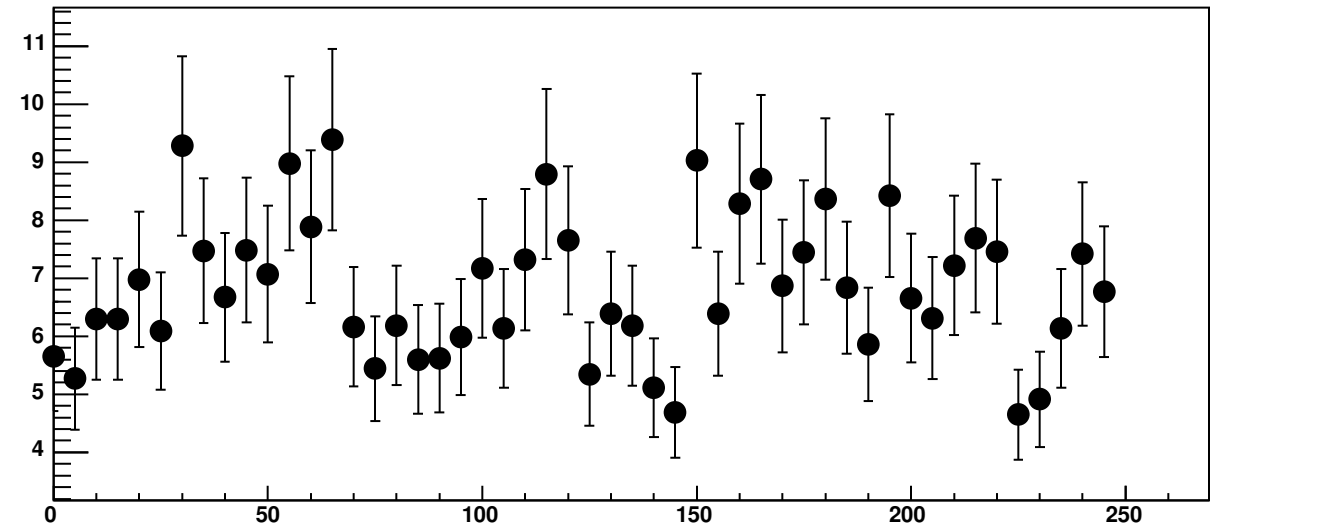
Chip 4, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold



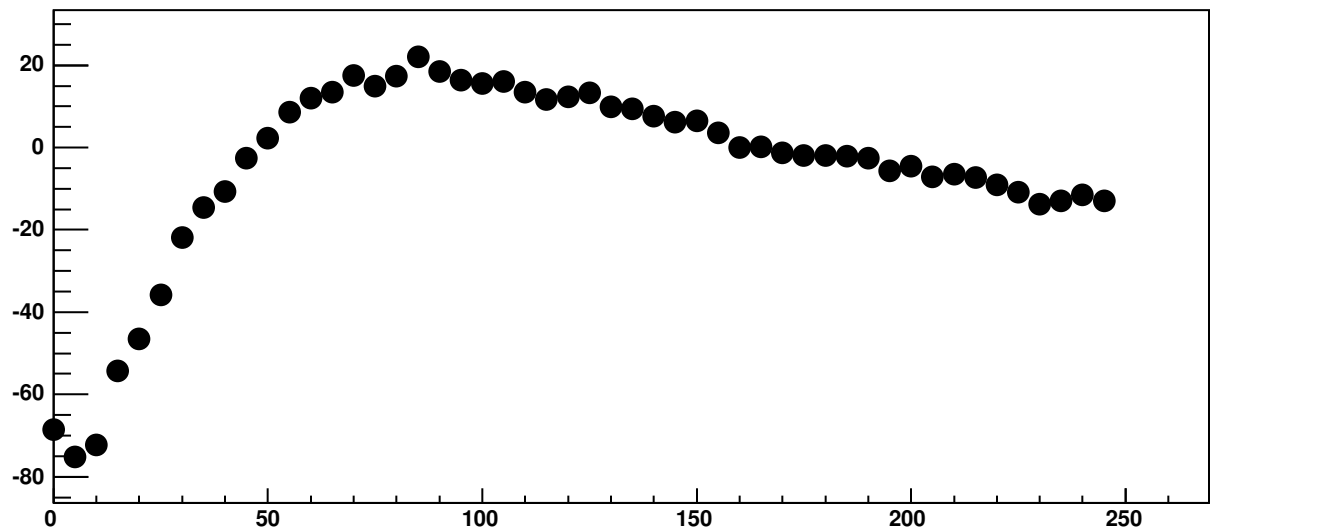
Chip 4, Channel 5, Enable 4, DAC=4000, ADC Mean vs Hold

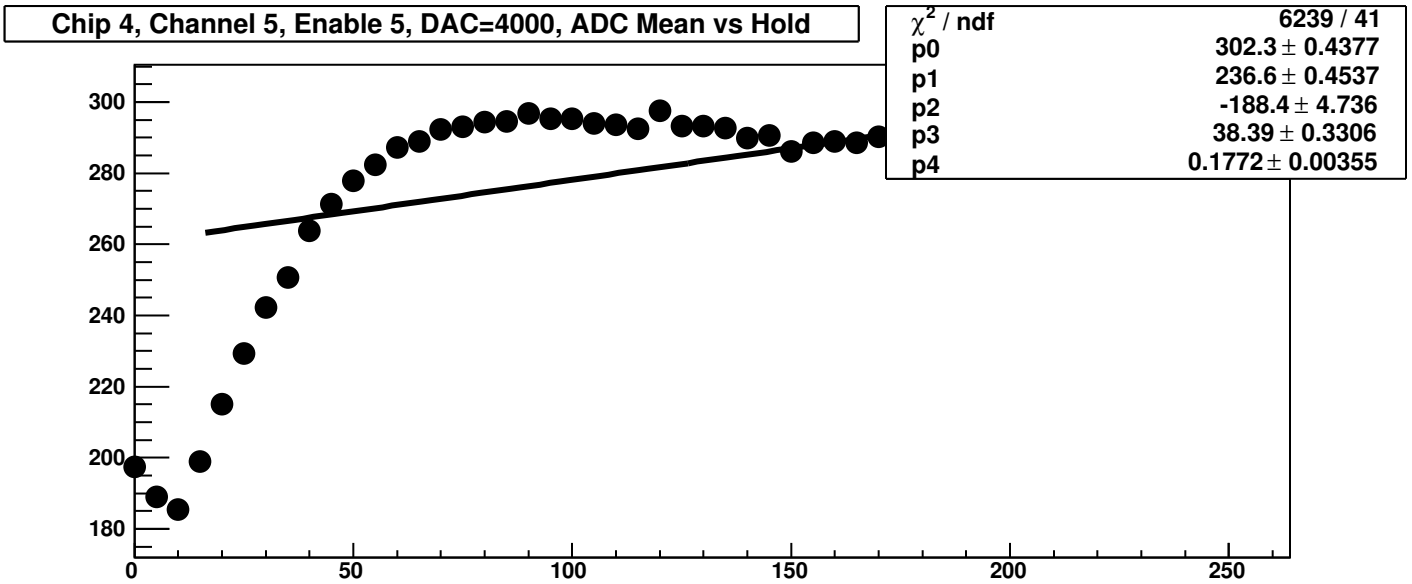


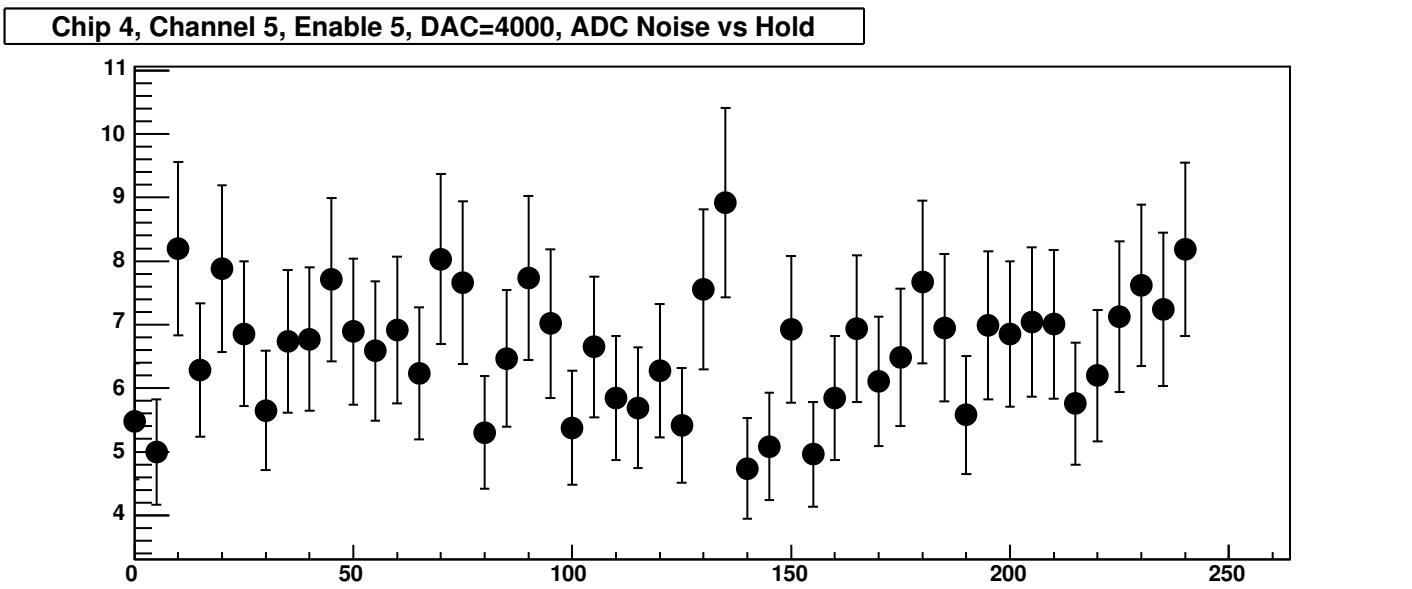
Chip 4, Channel 5, Enable 4, DAC=4000, ADC Noise vs Hold

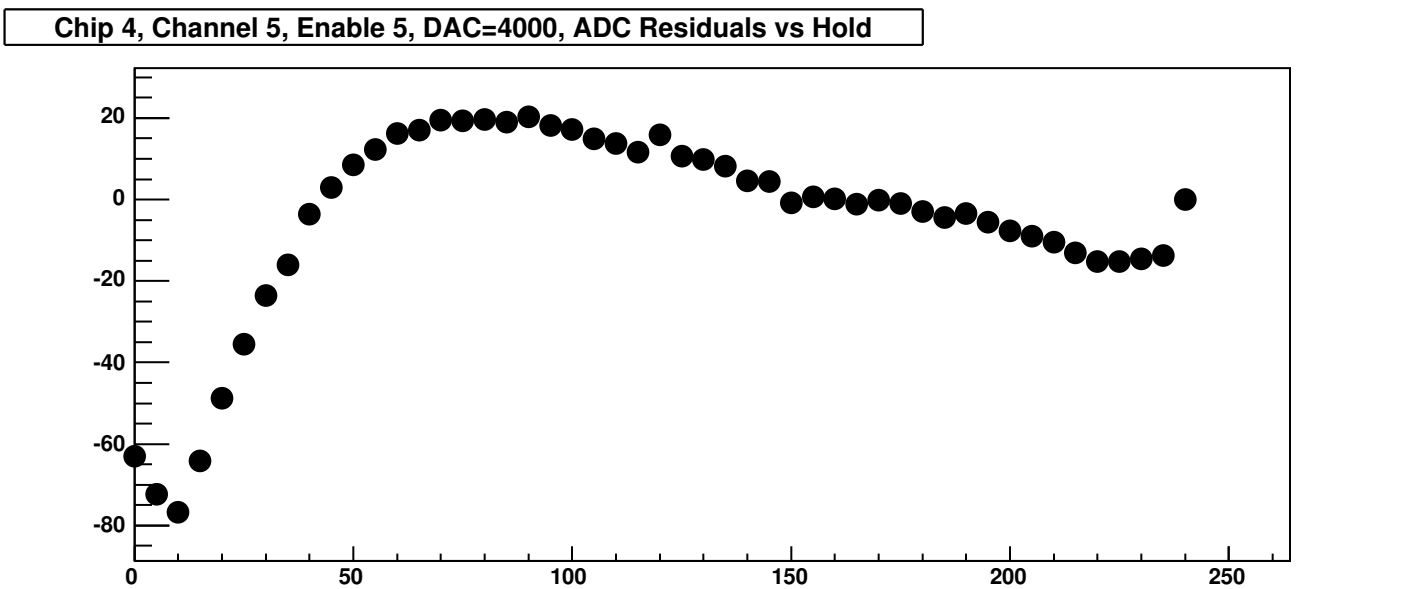


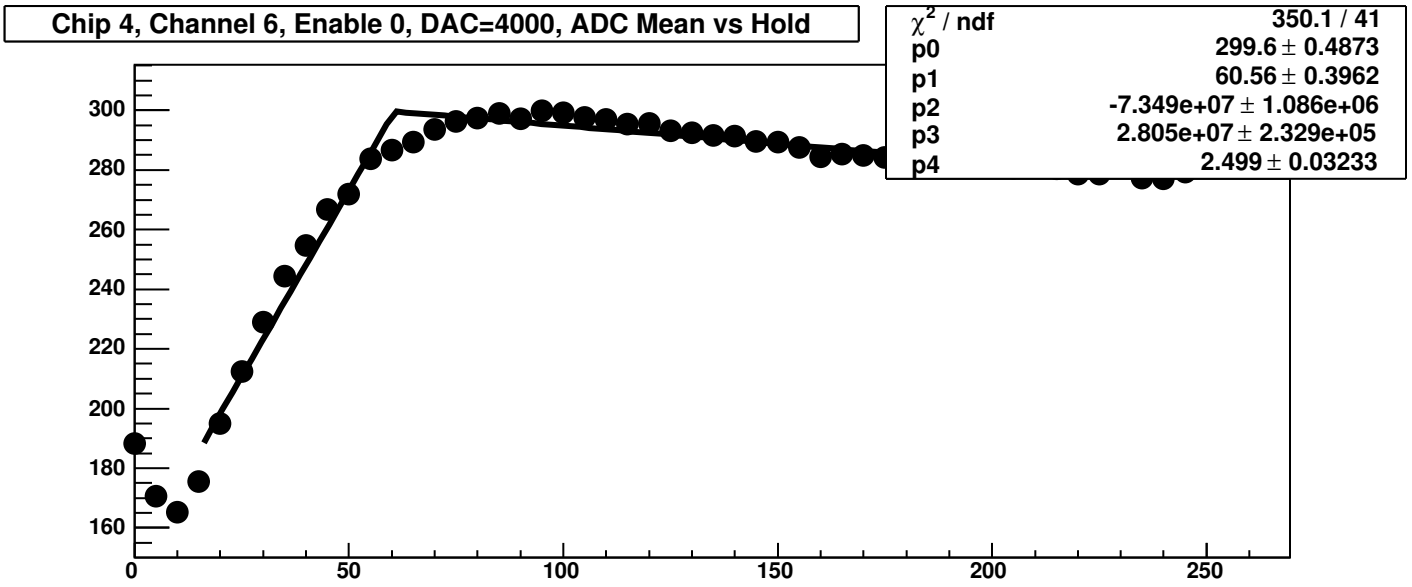
Chip 4, Channel 5, Enable 4, DAC=4000, ADC Residuals vs Hold

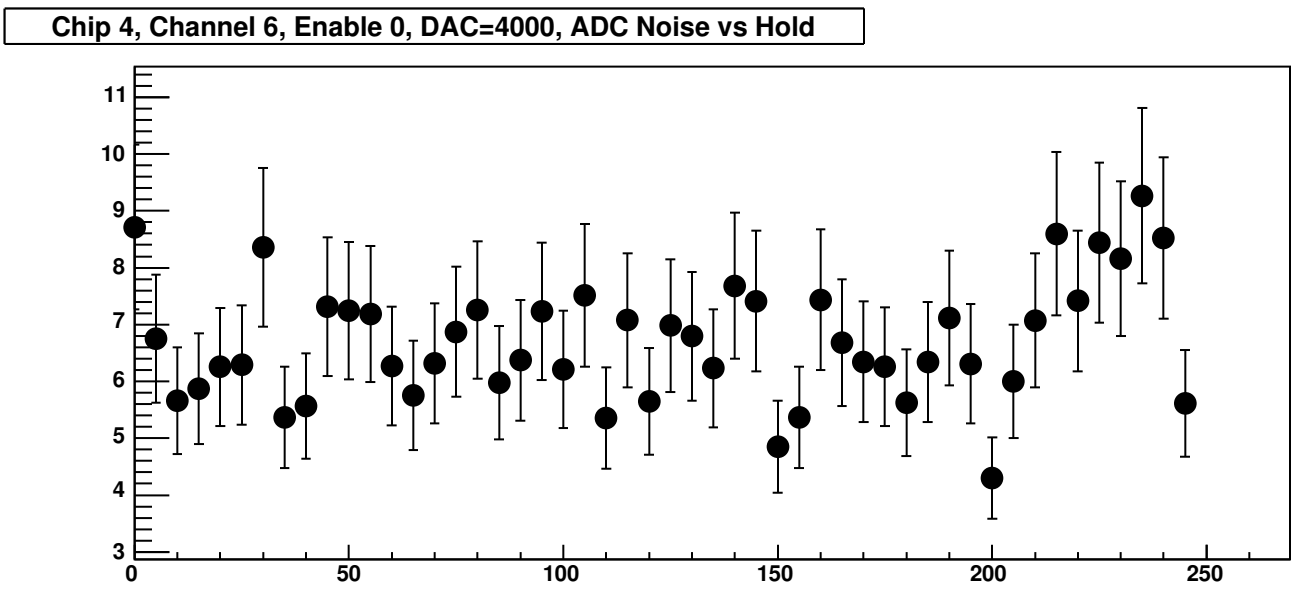


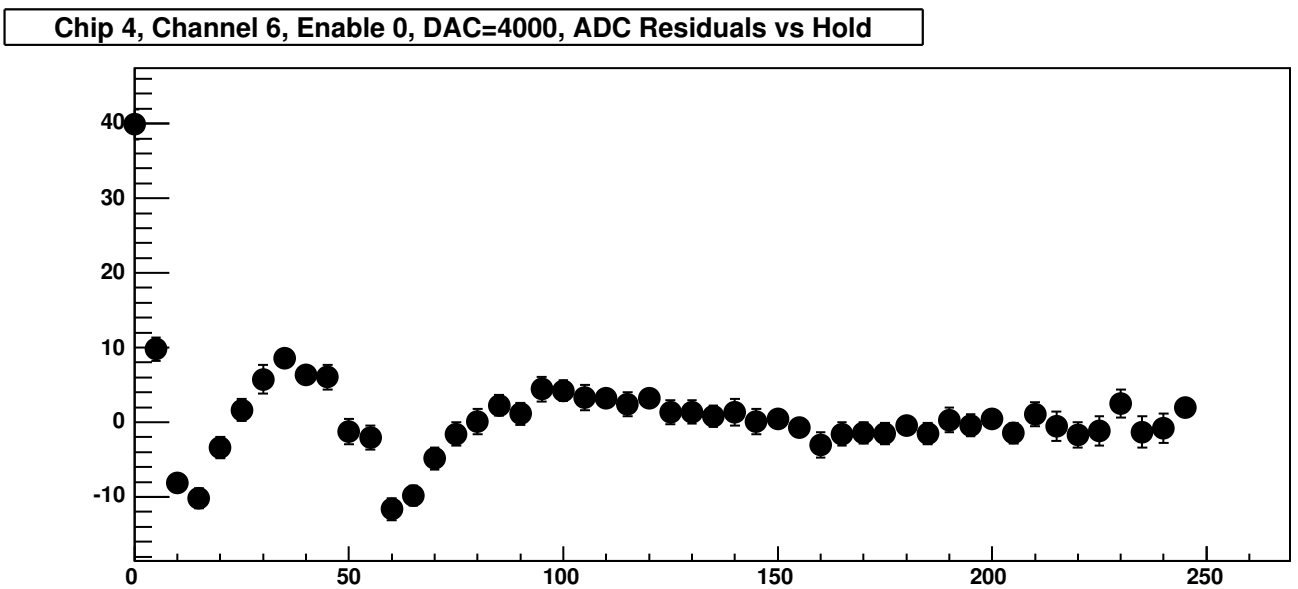

Chip 4, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 6239 / 41
p0 302.3 ± 0.4377
p1 236.6 ± 0.4537
p2 -188.4 ± 4.736
p3 38.39 ± 0.3306
p4 0.1772 ± 0.00355
300
280
260
240
220
200
180
0
50
100
150
200
250

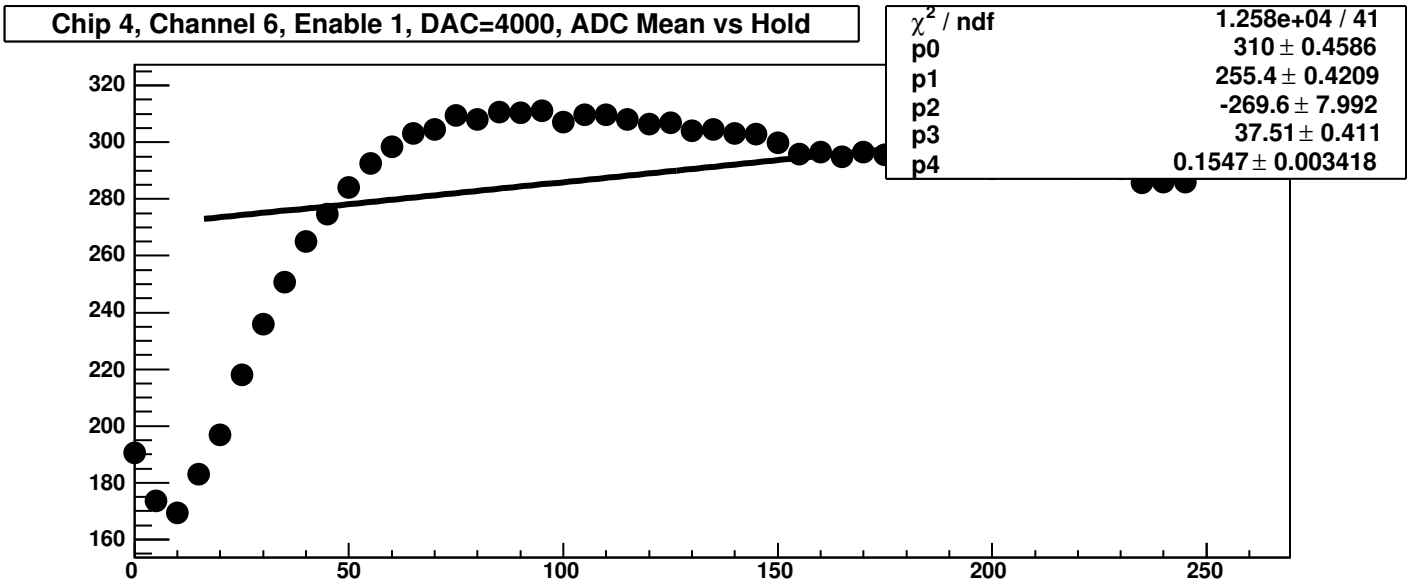

Chip 4, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250

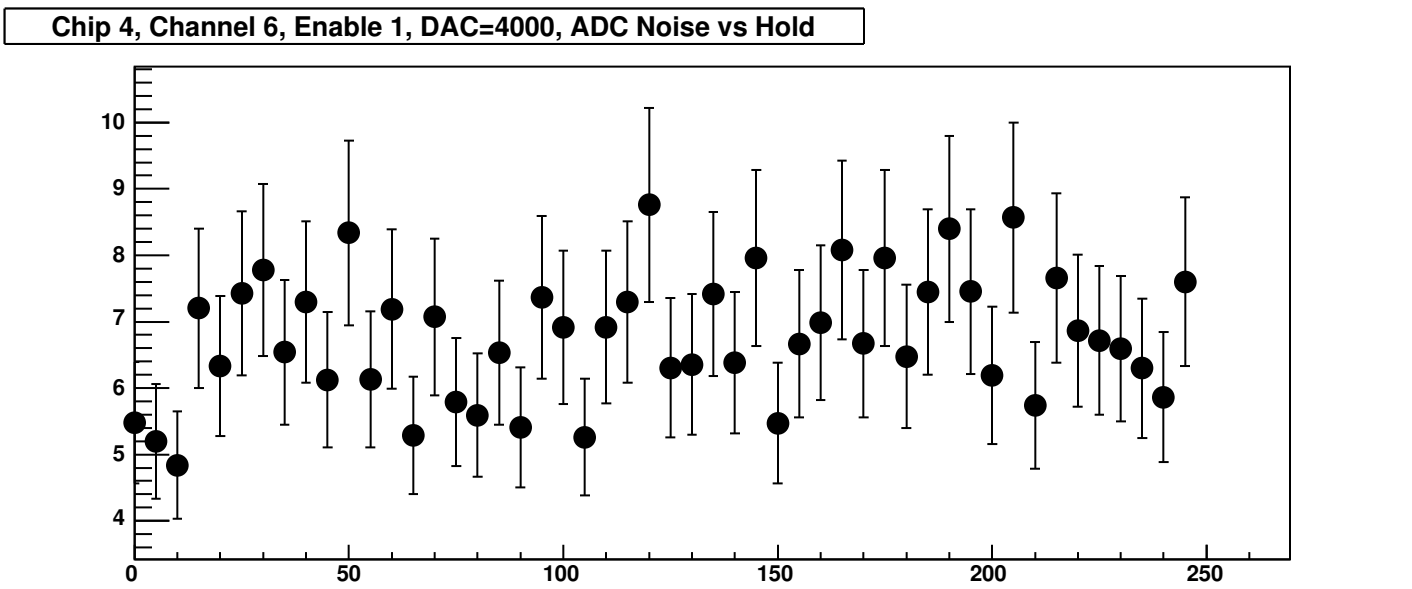

Chip 4, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
-80
0
50
100
150
200
250

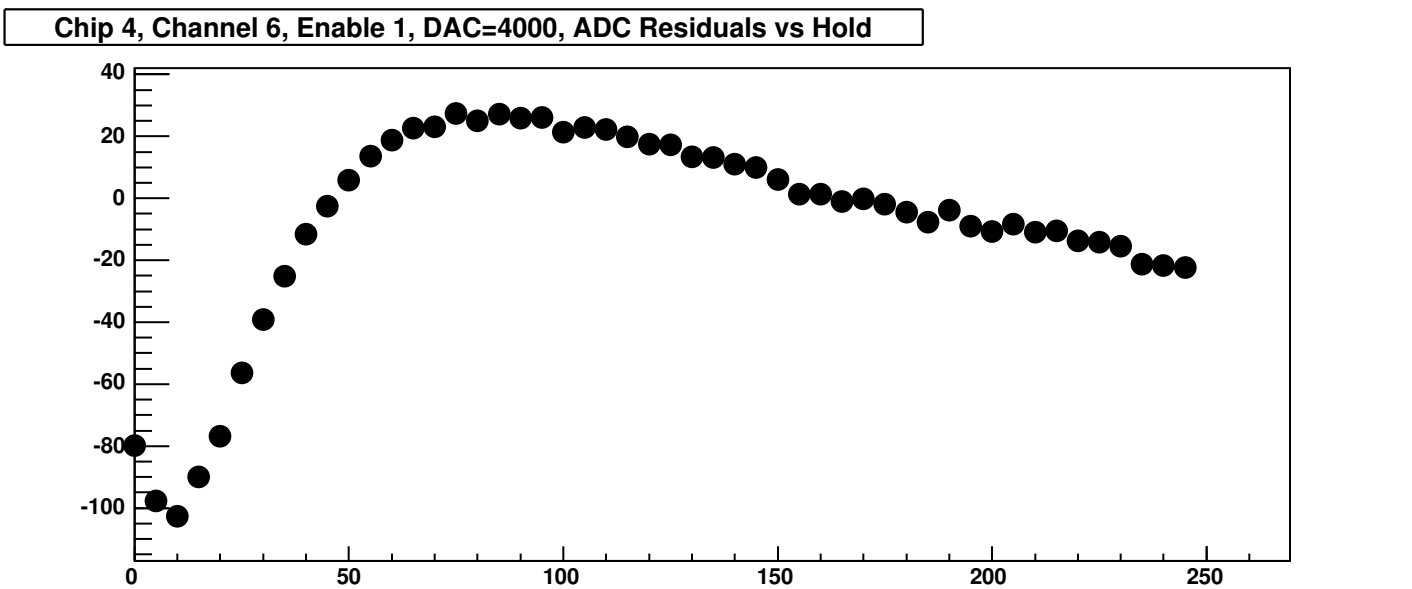

Chip 4, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 350.1 / 41
p0 299.6 ± 0.4873
p1 60.56 ± 0.3962
p2 -7.349e+07 ± 1.086e+06
p3 2.805e+07 ± 2.329e+05
p4 2.499 ± 0.03233
300
280
260
240
220
200
180
160
0
50
100
150
200
250


Chip 4, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 4, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

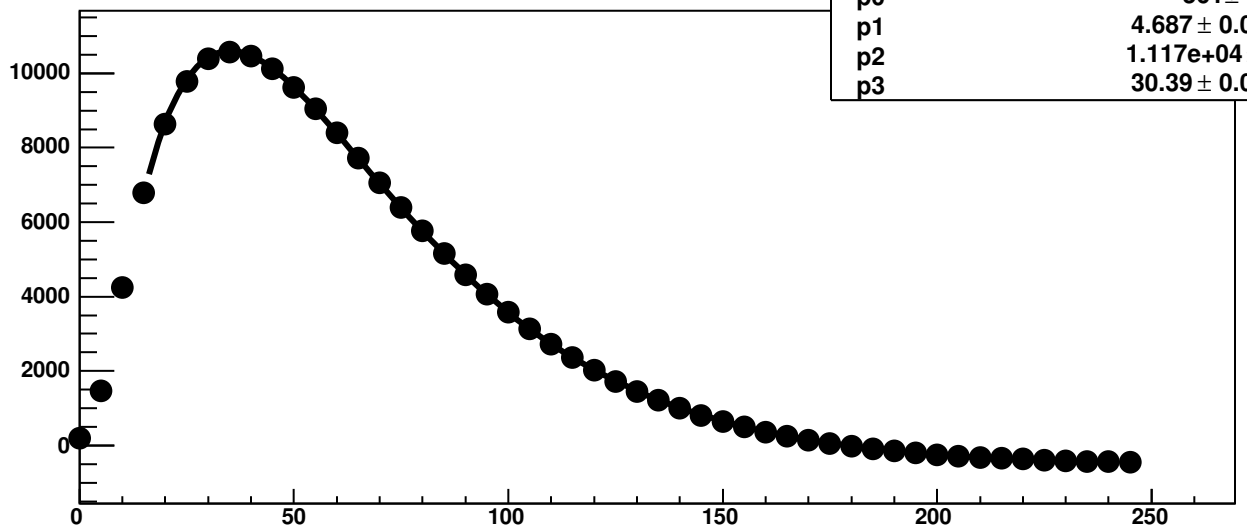

Chip 4, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.258e+04 / 41
p0 310 ± 0.4586
p1 255.4 ± 0.4209
p2 -269.6 ± 7.992
p3 37.51 ± 0.411
p4 0.1547 ± 0.003418
320
300
280
260
240
220
200
180
160
0
50
100
150
200
250


Chip 4, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold
10
9
8
7
6
5
4
0
50
100
150
200
250

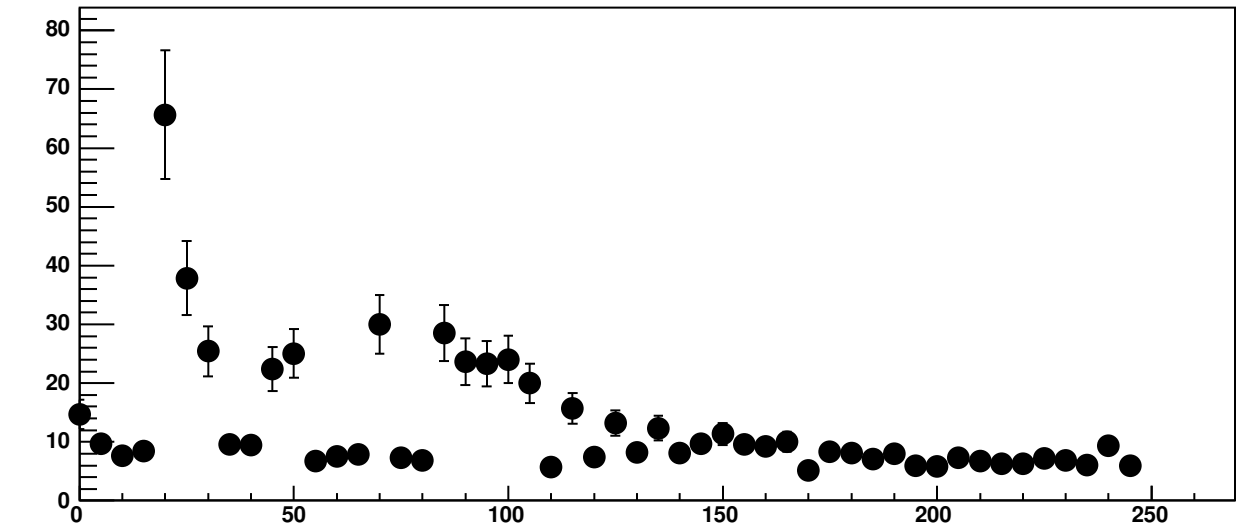

Chip 4, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
-100
0
50
100
150
200
250

Chip 4, Channel 6, Enable 2!, DAC=4000, ADC Mean vs Hold

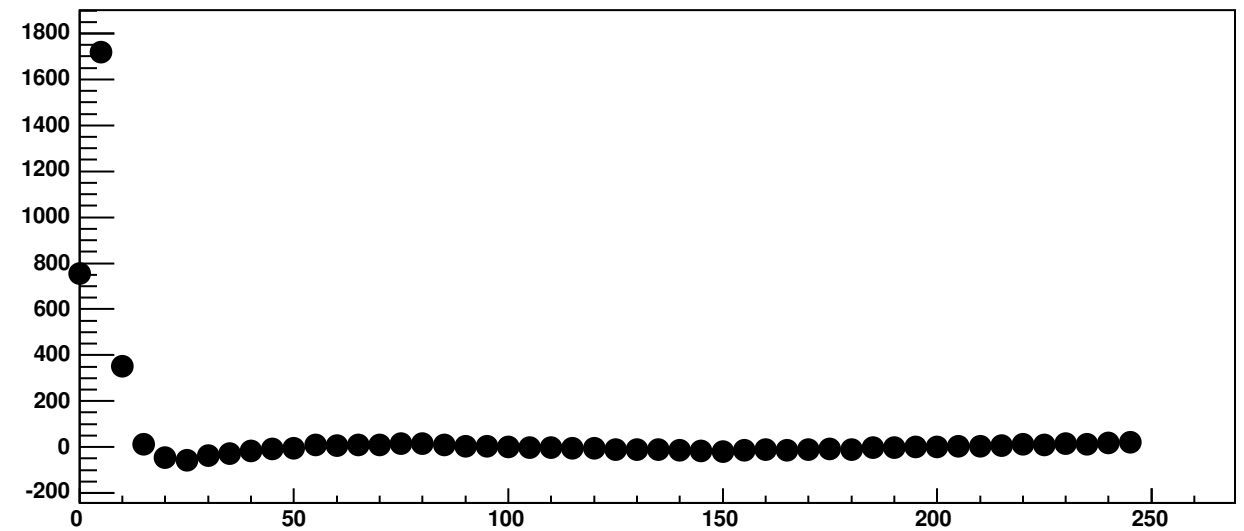
χ^2 / ndf	1508 / 42
p0	-561 ± 0.5176
p1	4.687 ± 0.005264
p2	1.117e+04 ± 1.05
p3	30.39 ± 0.004783



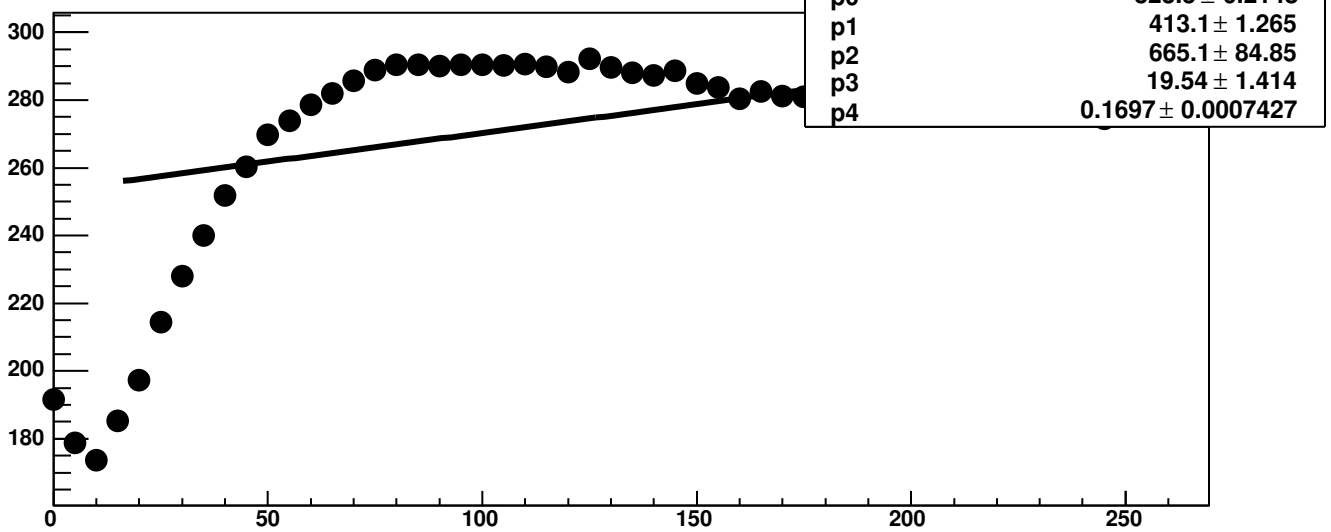
Chip 4, Channel 6, Enable 2!, DAC=4000, ADC Noise vs Hold



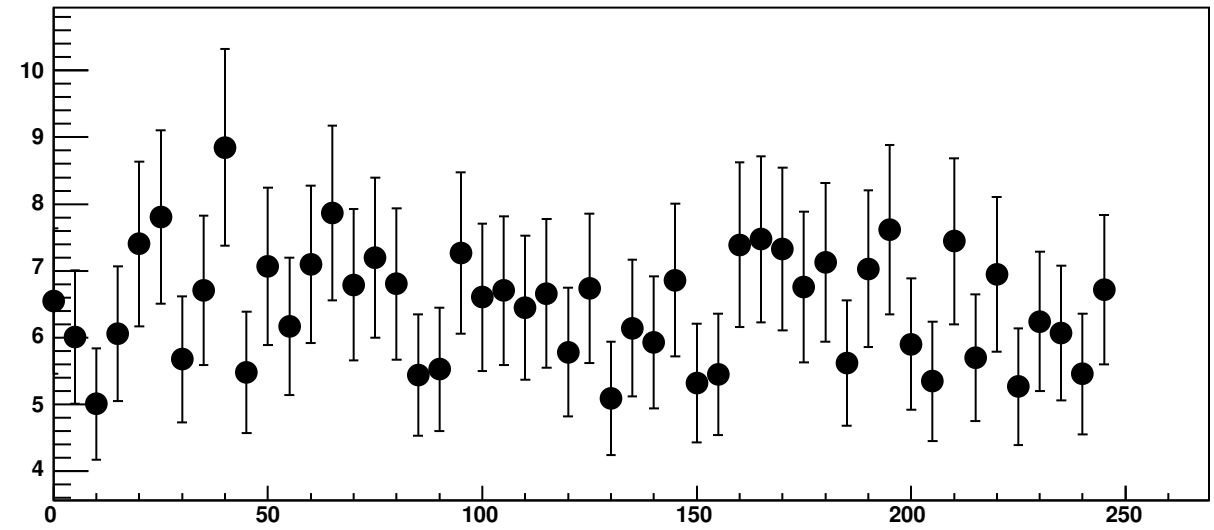
Chip 4, Channel 6, Enable 2!, DAC=4000, ADC Residuals vs Hold



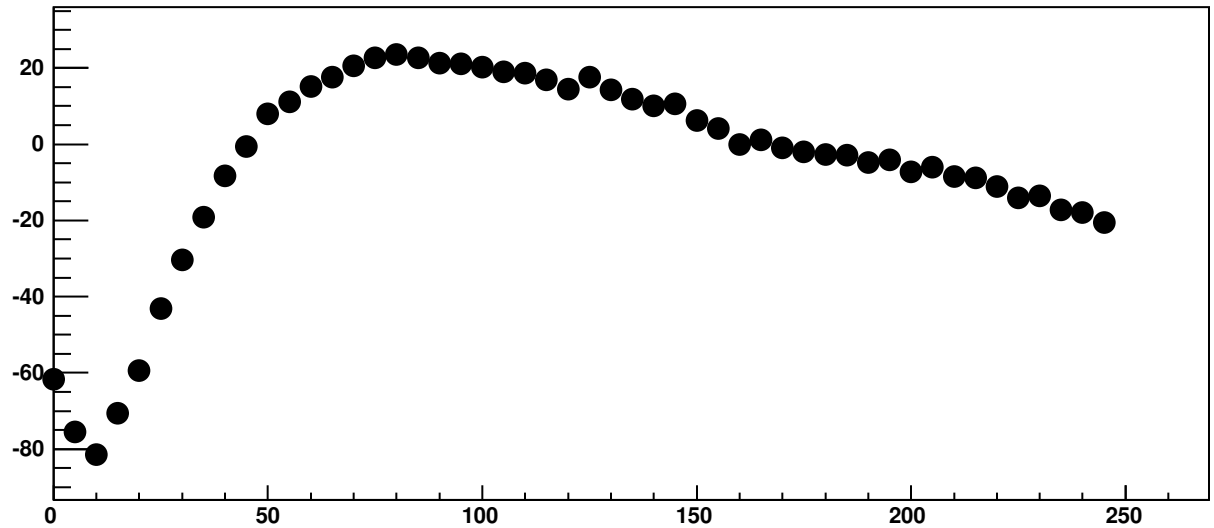
Chip 4, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold

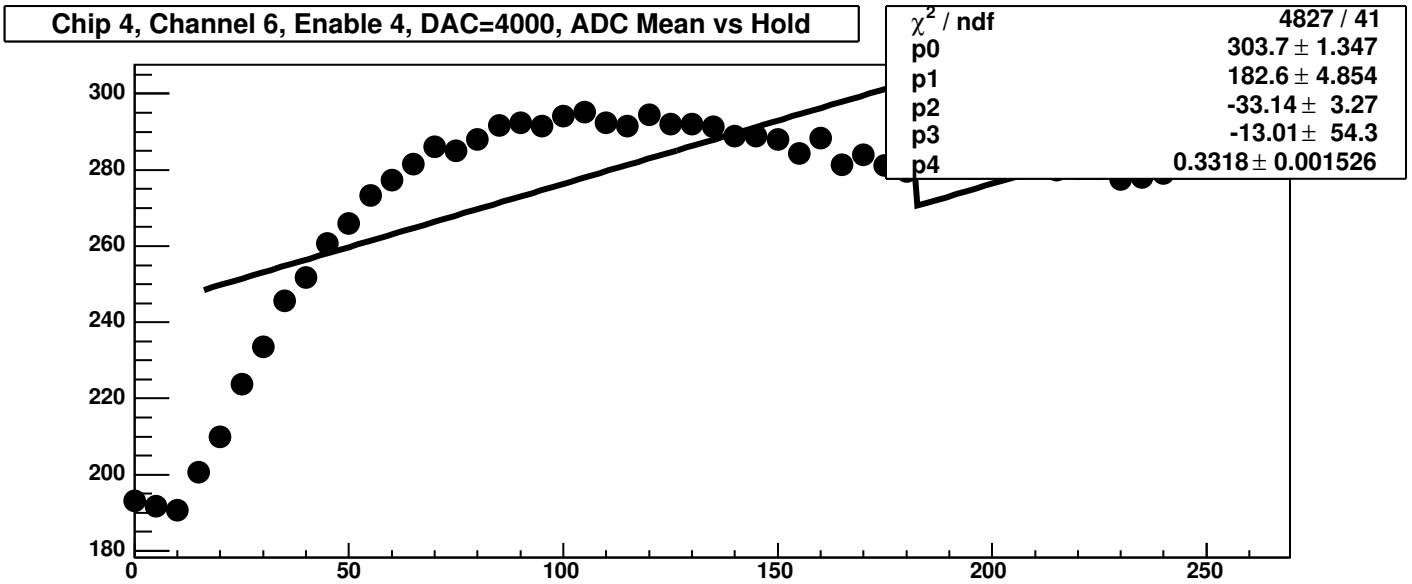


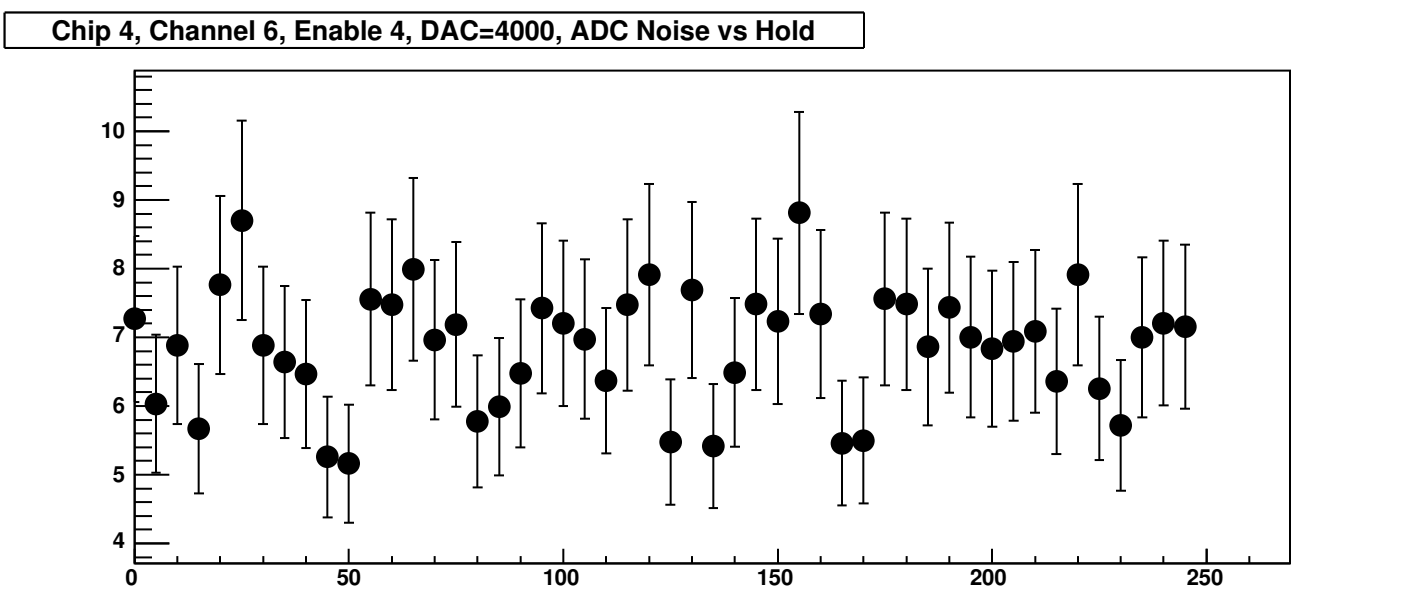
Chip 4, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold

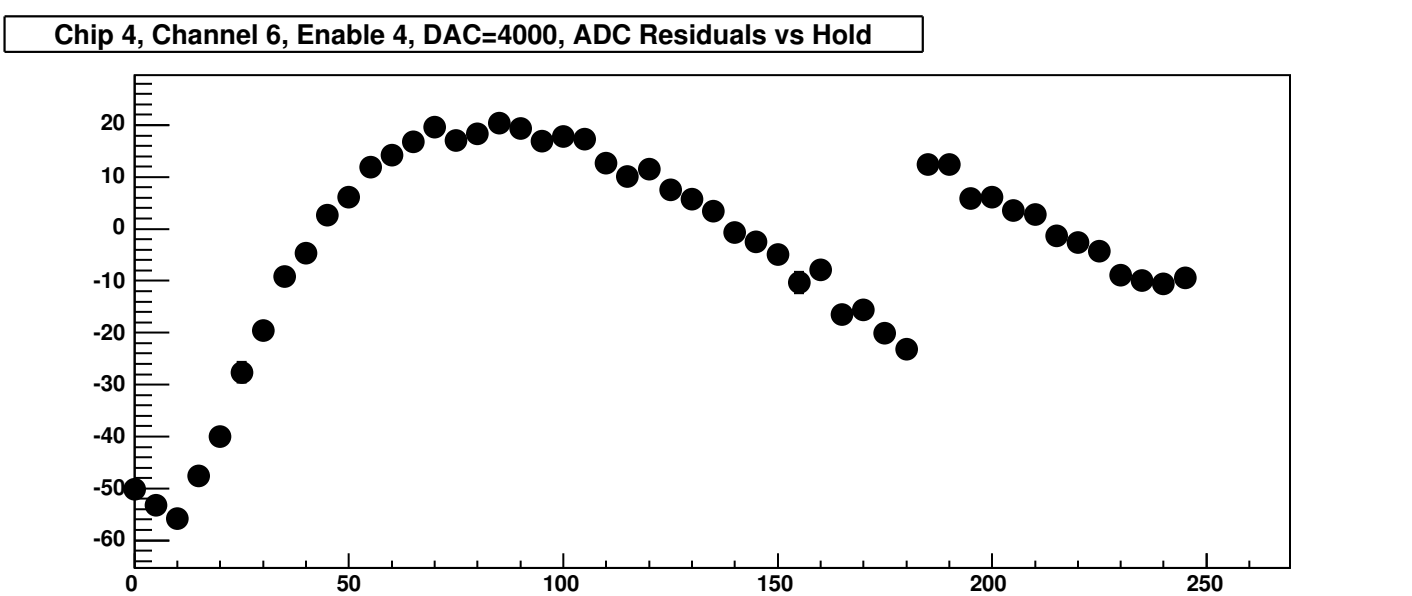


Chip 4, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold

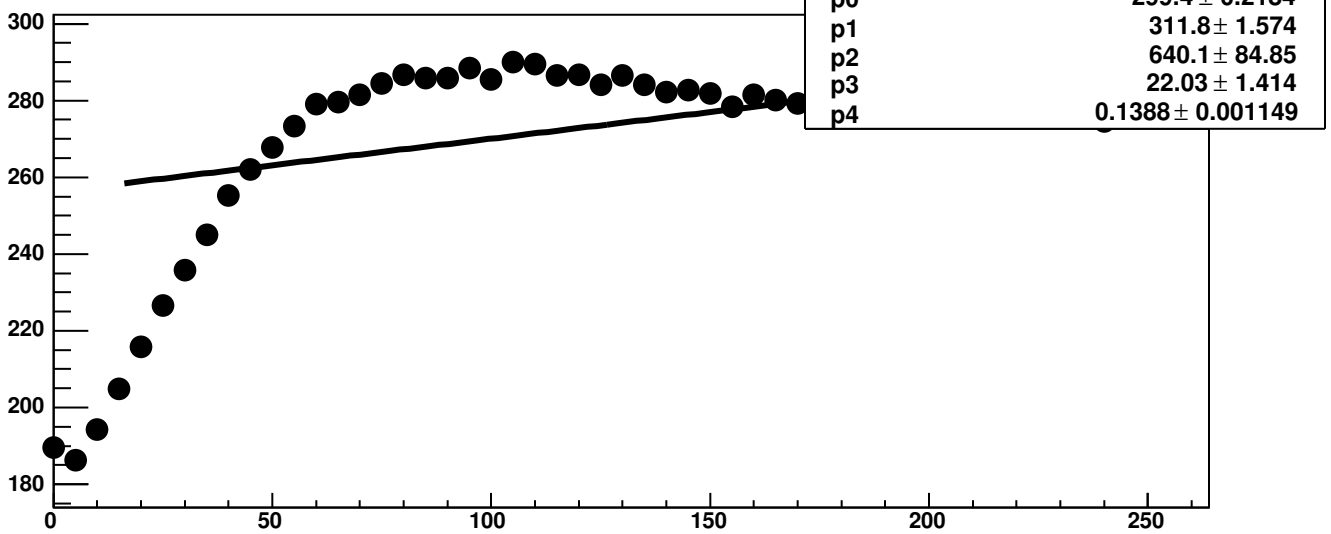



Chip 4, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 4827 / 41
p0 303.7 ± 1.347
p1 182.6 ± 4.854
p2 -33.14 ± 3.27
p3 -13.01 ± 54.3
p4 0.3318 ± 0.001526
300
280
260
240
220
200
180
0
50
100
150
200
250

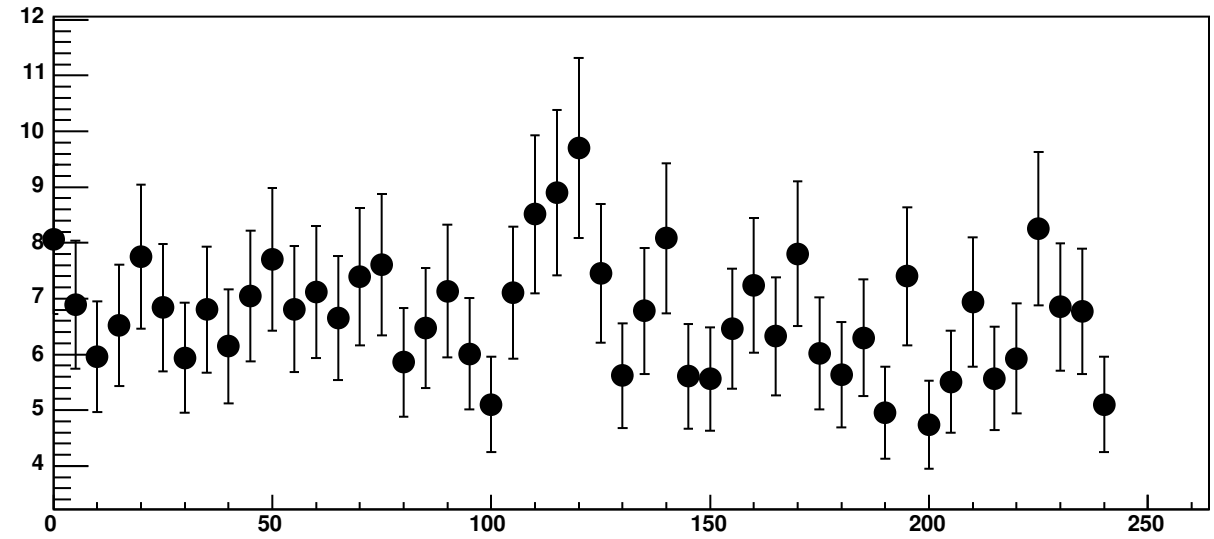

Chip 4, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 4, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
-50
-60
0
50
100
150
200
250

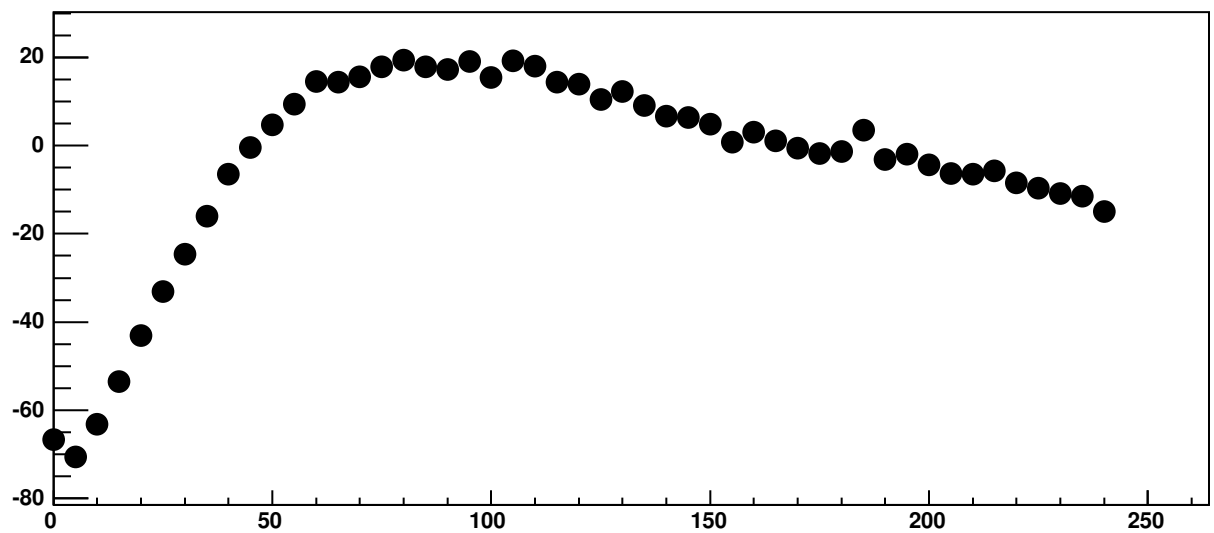
Chip 4, Channel 6, Enable 5, DAC=4000, ADC Mean vs Hold

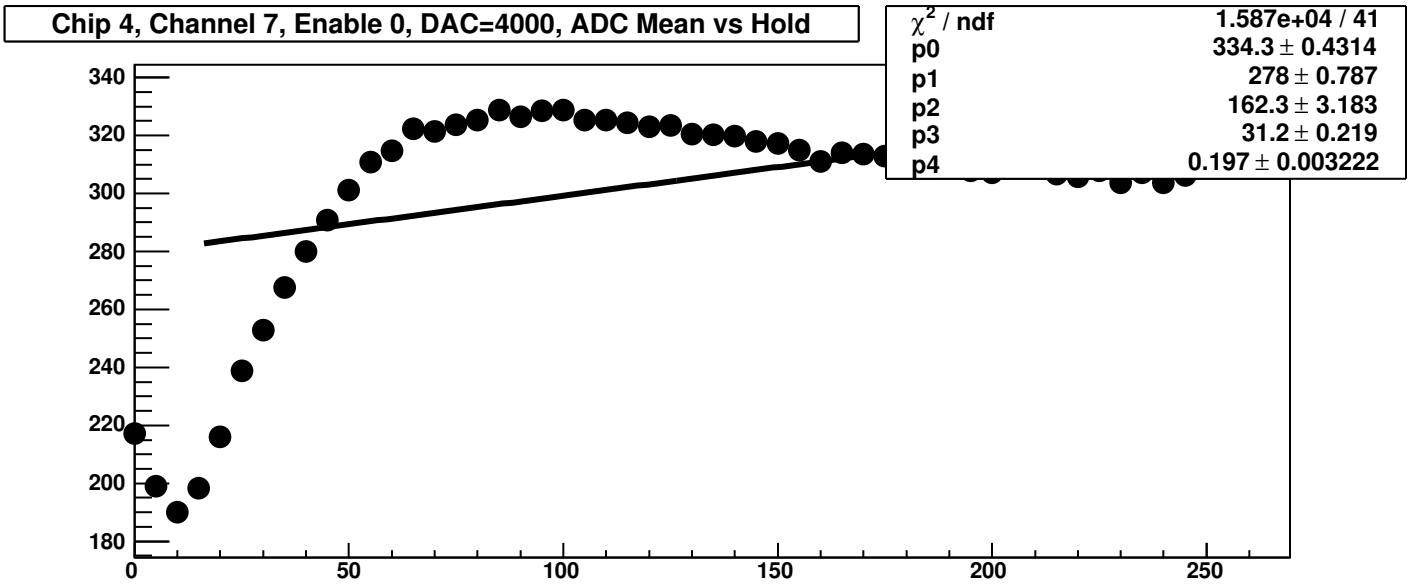


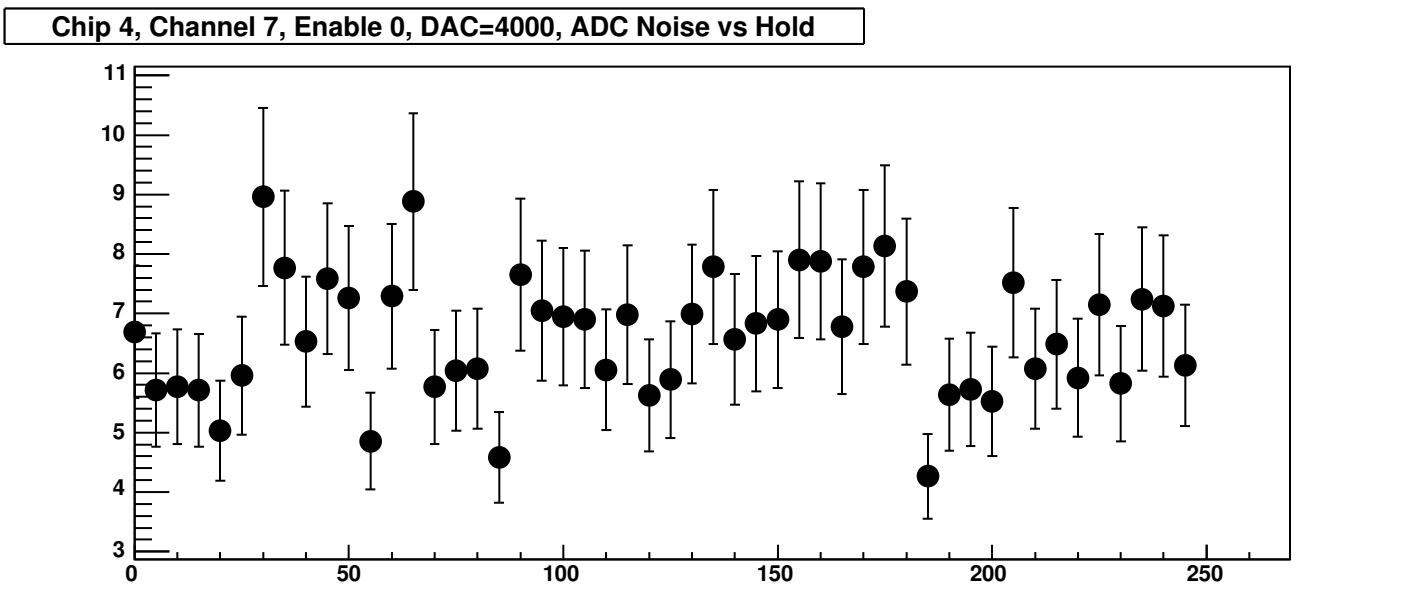
Chip 4, Channel 6, Enable 5, DAC=4000, ADC Noise vs Hold

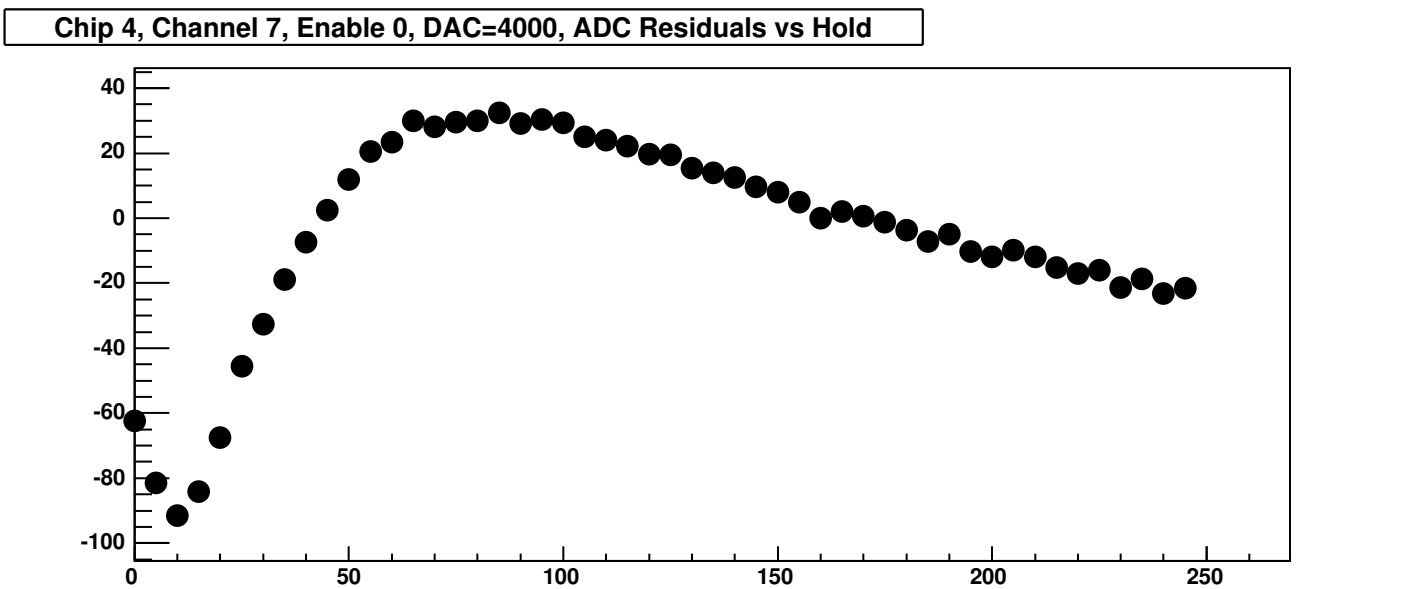


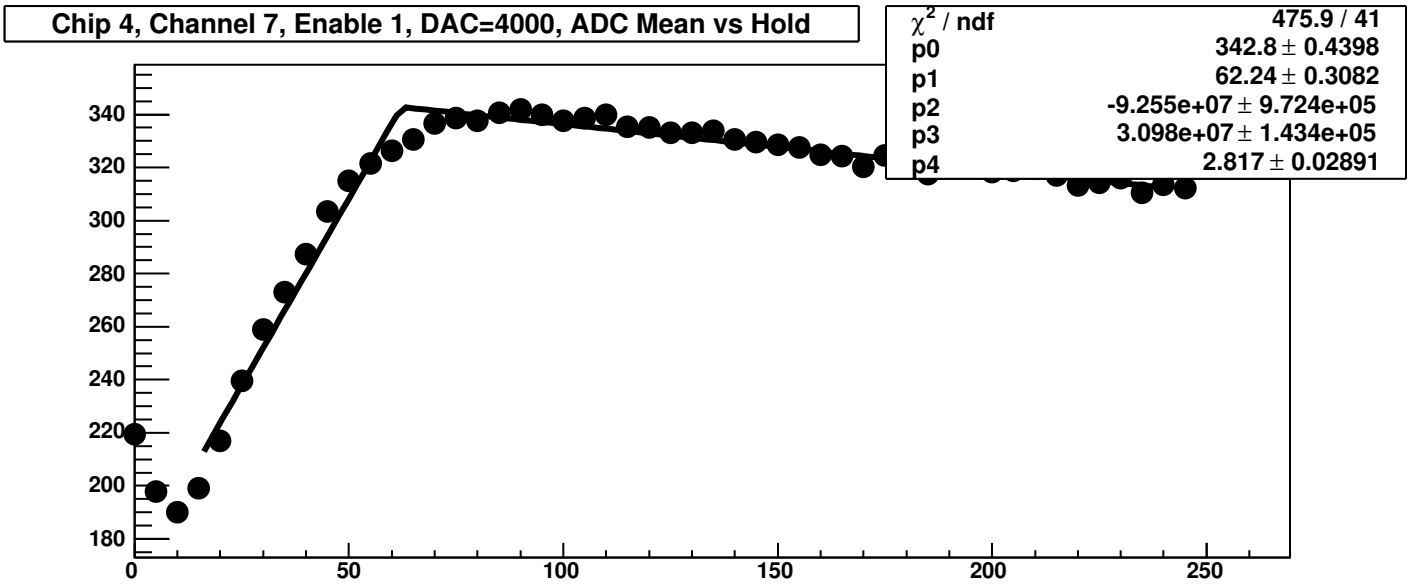
Chip 4, Channel 6, Enable 5, DAC=4000, ADC Residuals vs Hold

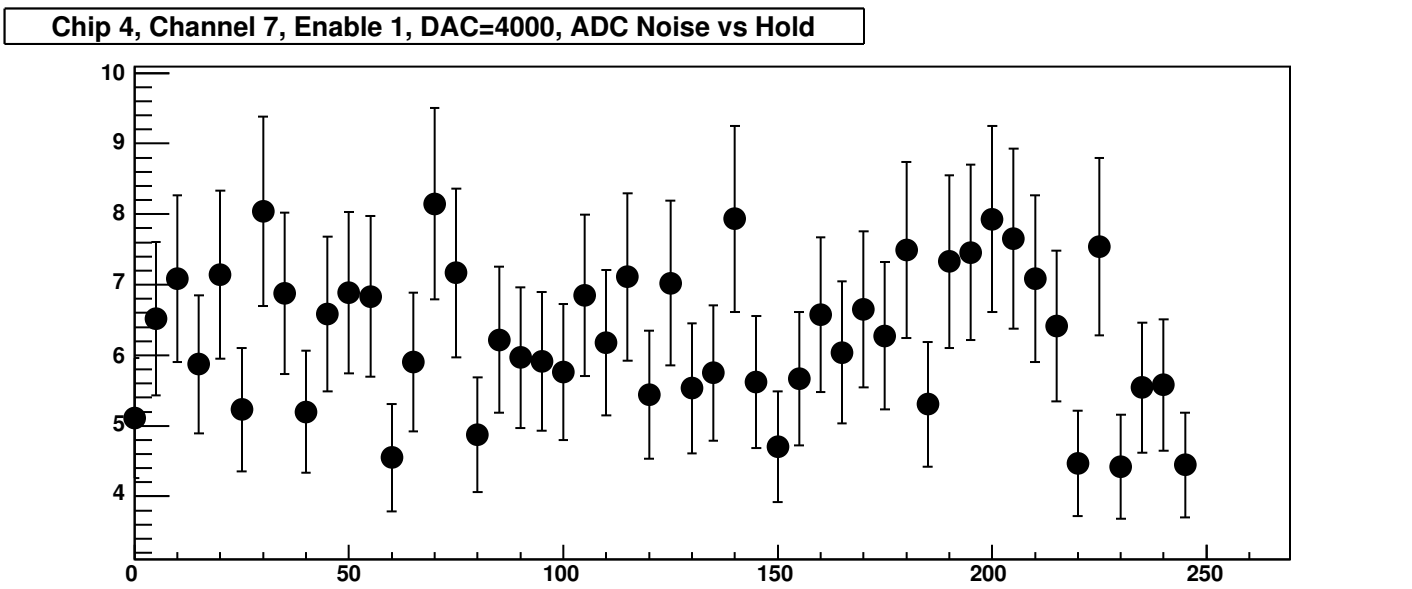


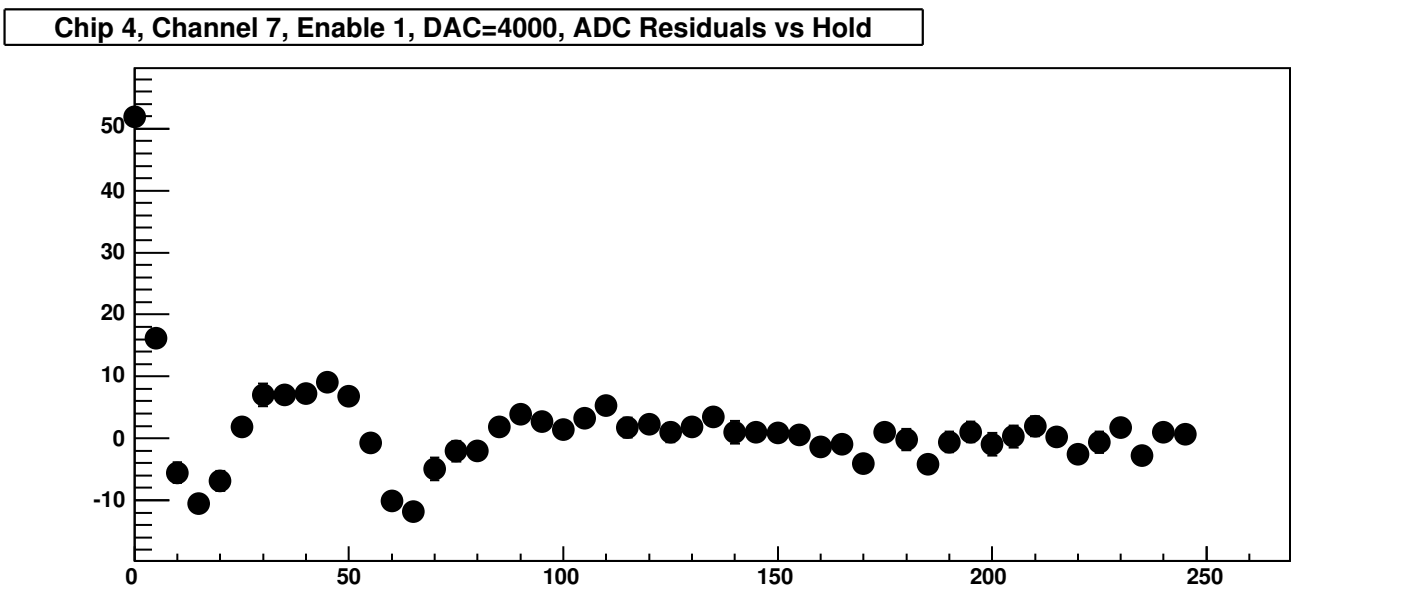

Chip 4, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.587e+04 / 41
p0 334.3 ± 0.4314
p1 278 ± 0.787
p2 162.3 ± 3.183
p3 31.2 ± 0.219
p4 0.197 ± 0.003222
340
320
300
280
260
240
220
200
180
0
50
100
150
200
250


Chip 4, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

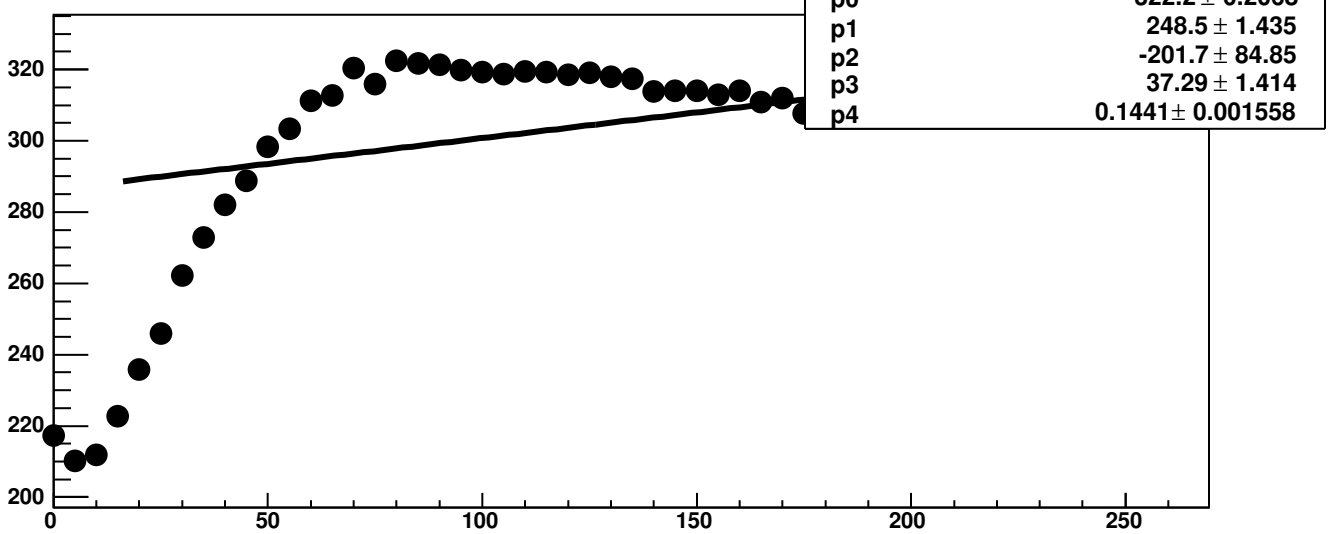

Chip 4, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
-100
0
50
100
150
200
250


Chip 4, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 475.9 / 41
p0 342.8 ± 0.4398
p1 62.24 ± 0.3082
p2 -9.255e+07 ± 9.724e+05
p3 3.098e+07 ± 1.434e+05
p4 2.817 ± 0.02891
340
320
300
280
260
240
220
200
180
0
50
100
150
200
250

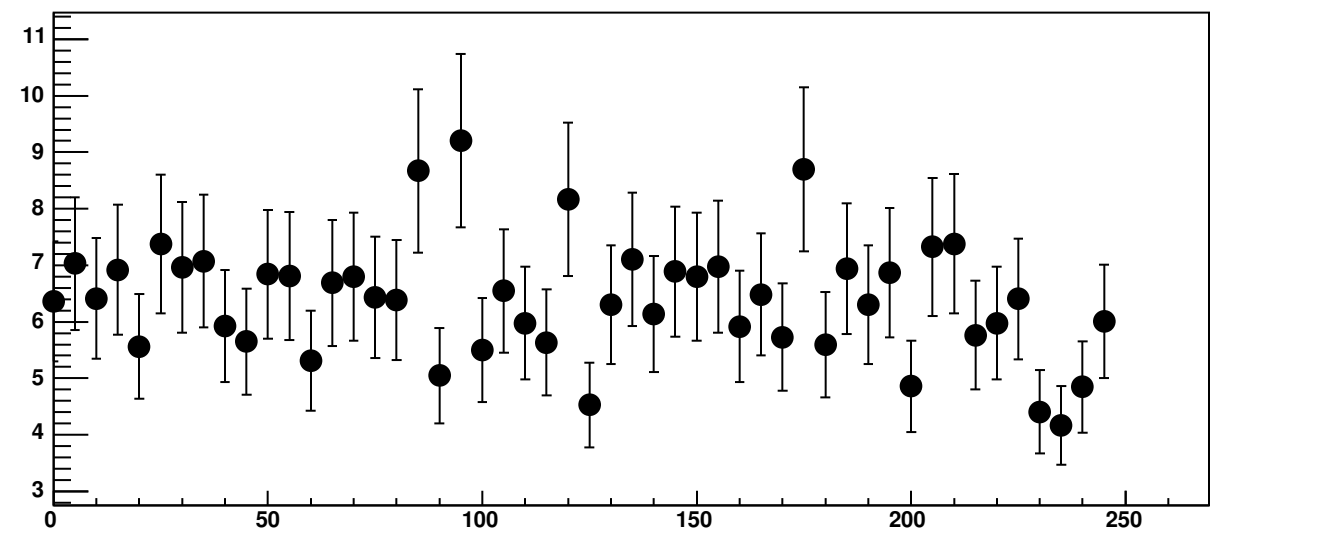

Chip 4, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 4, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold
50
40
30
20
10
0
-10
0
50
100
150
200
250

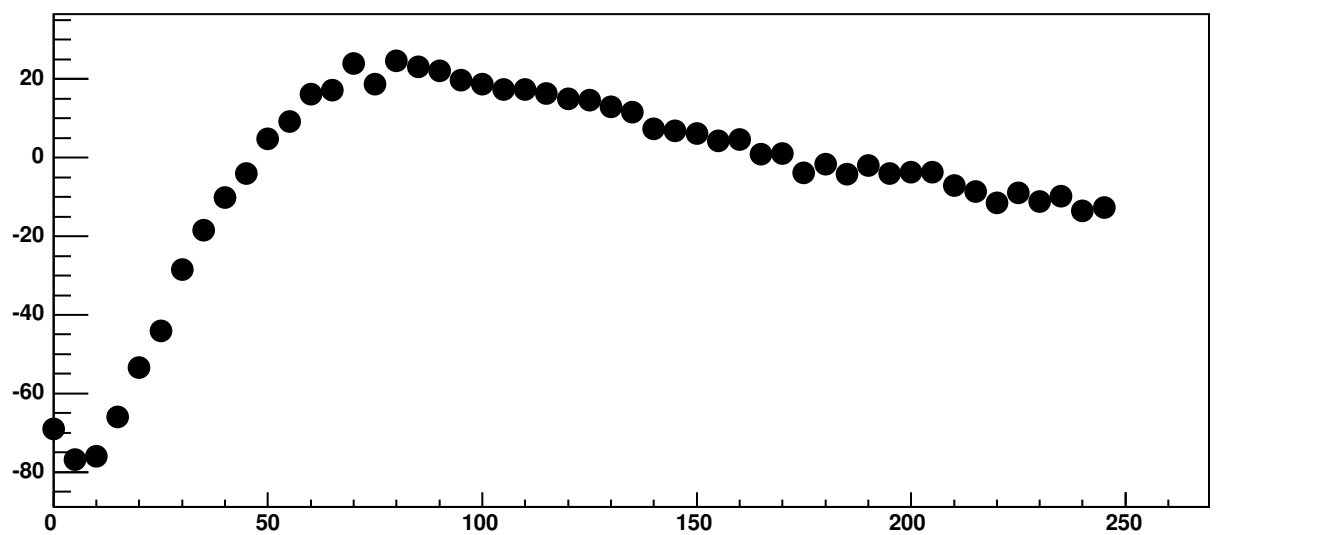
Chip 4, Channel 7, Enable 2, DAC=4000, ADC Mean vs Hold



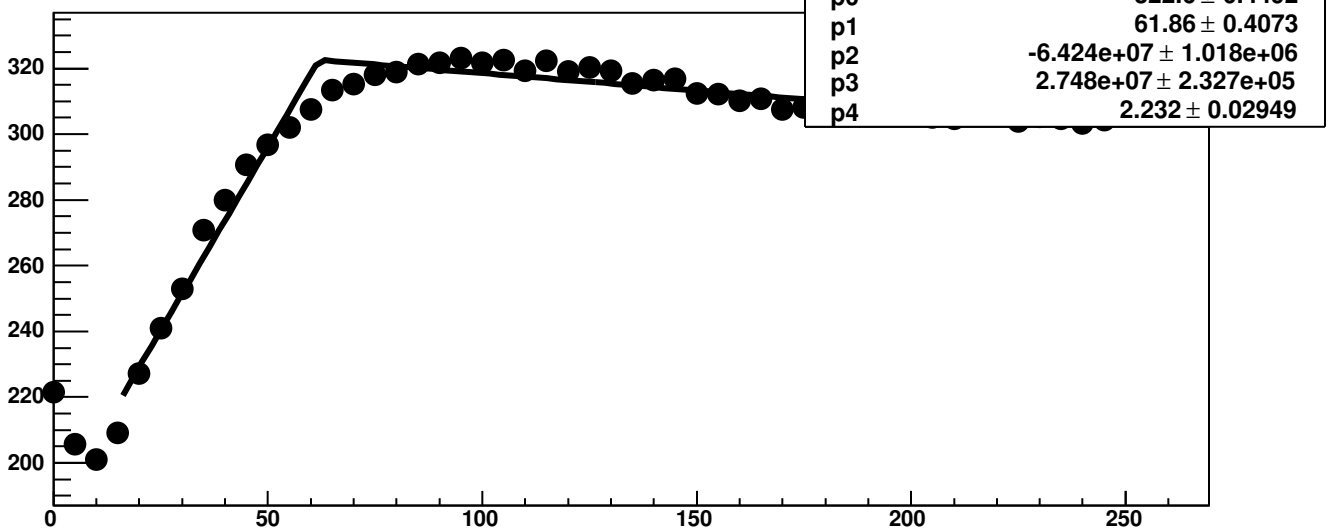
Chip 4, Channel 7, Enable 2, DAC=4000, ADC Noise vs Hold



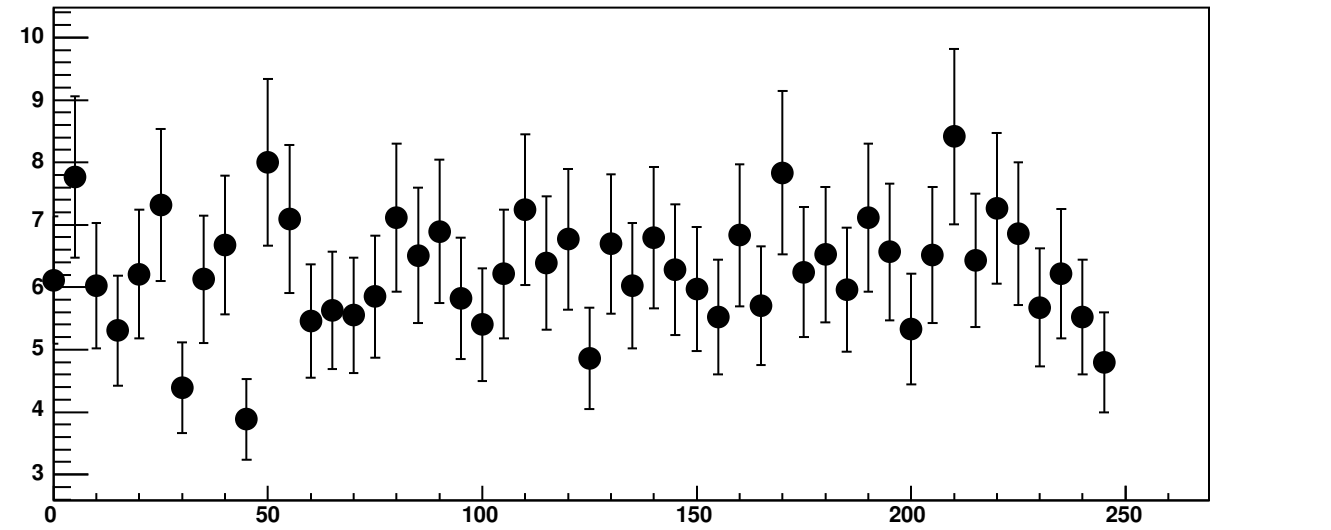
Chip 4, Channel 7, Enable 2, DAC=4000, ADC Residuals vs Hold



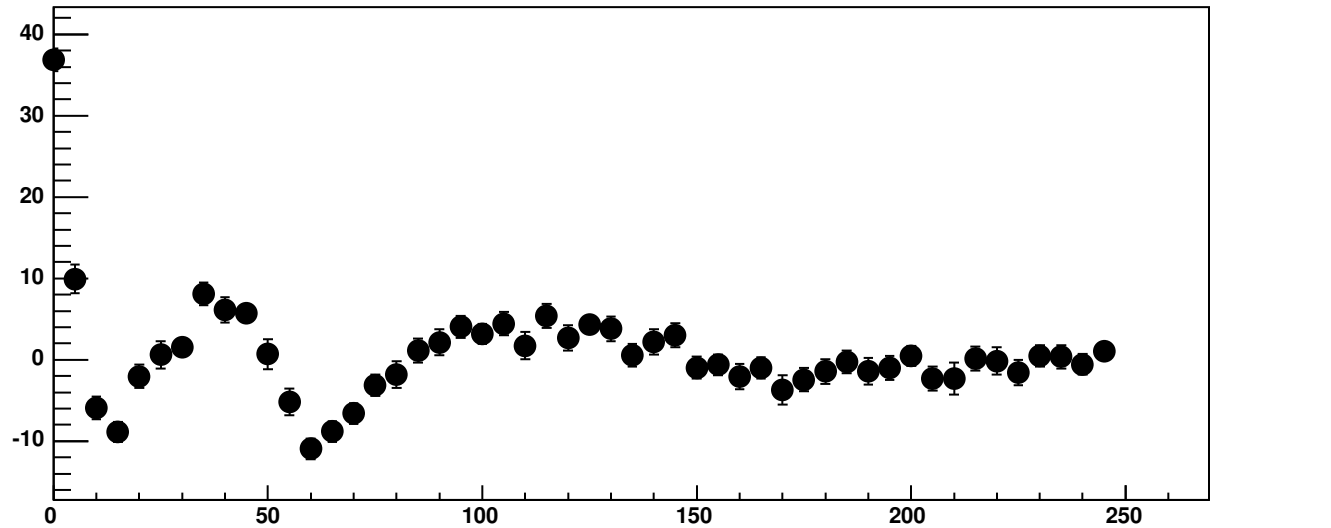
Chip 4, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold

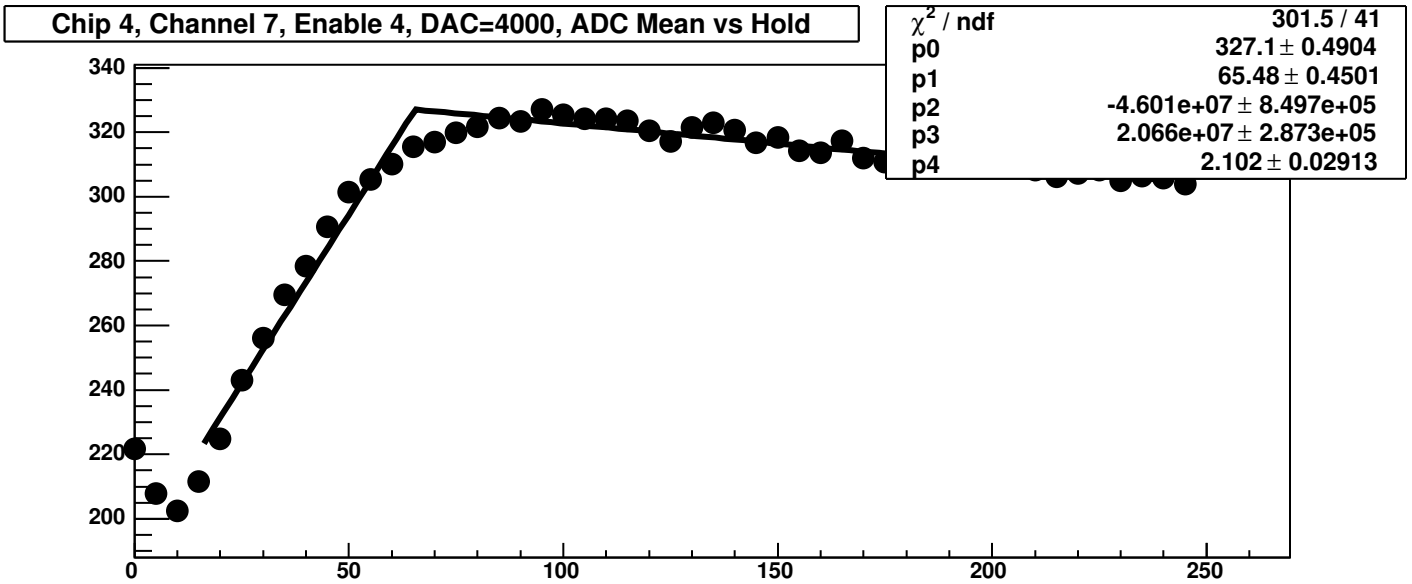


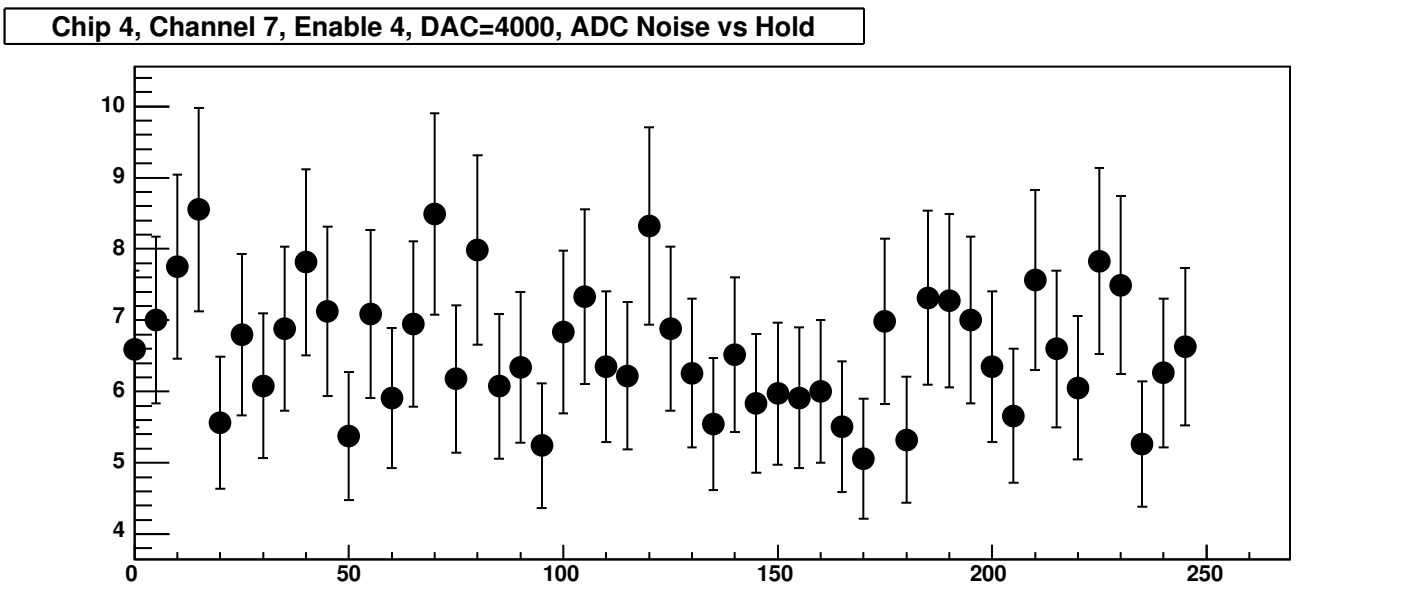
Chip 4, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold

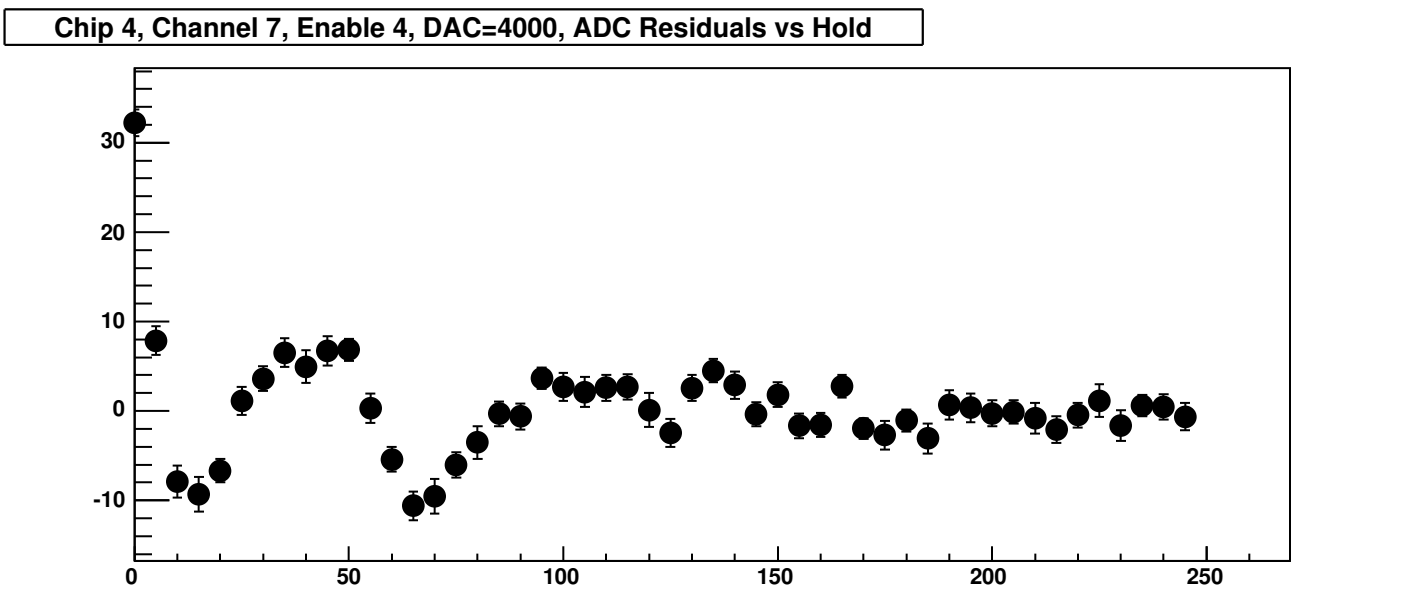


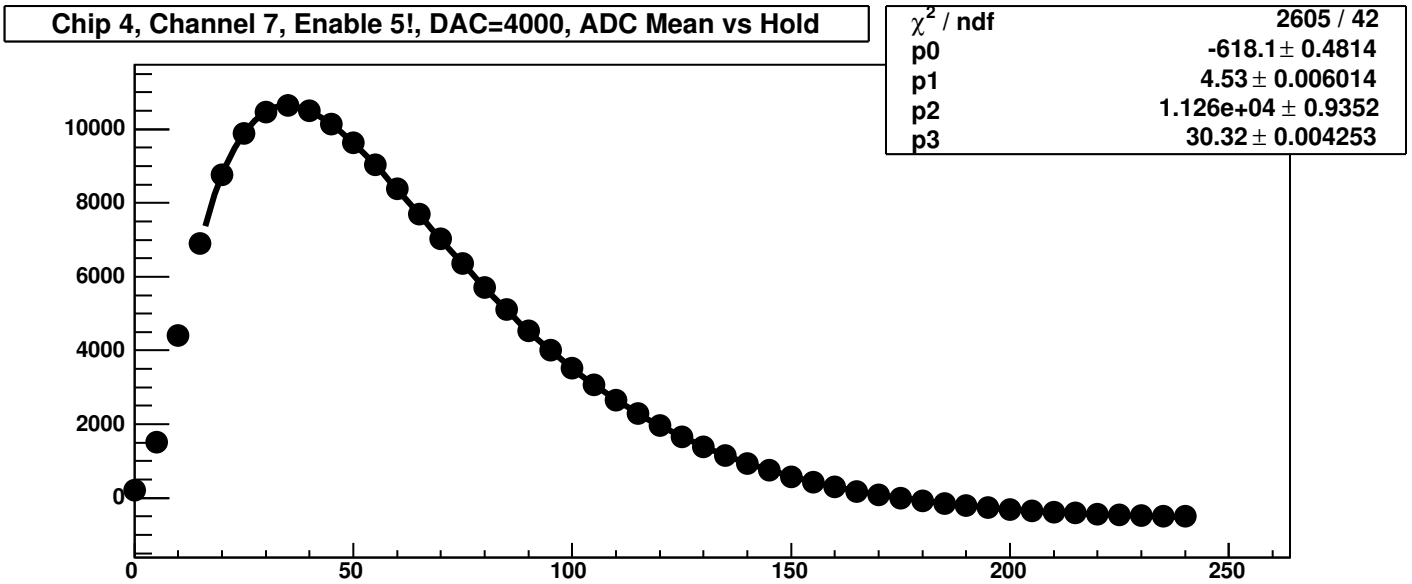
Chip 4, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold

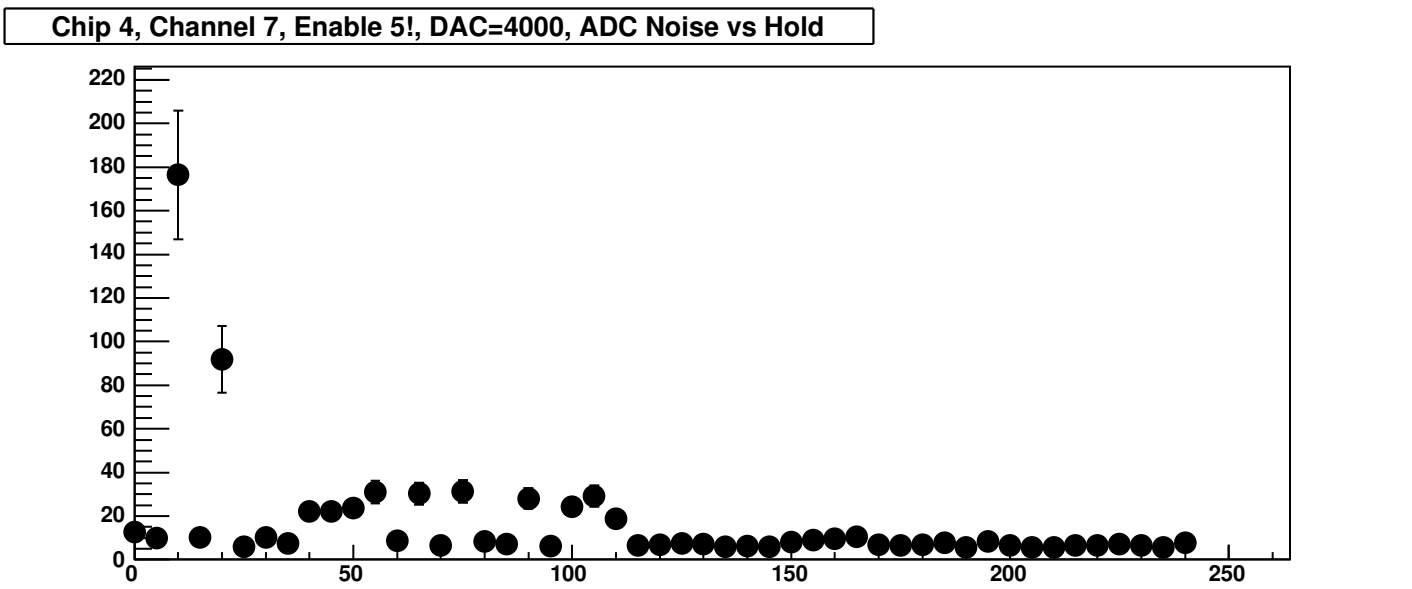


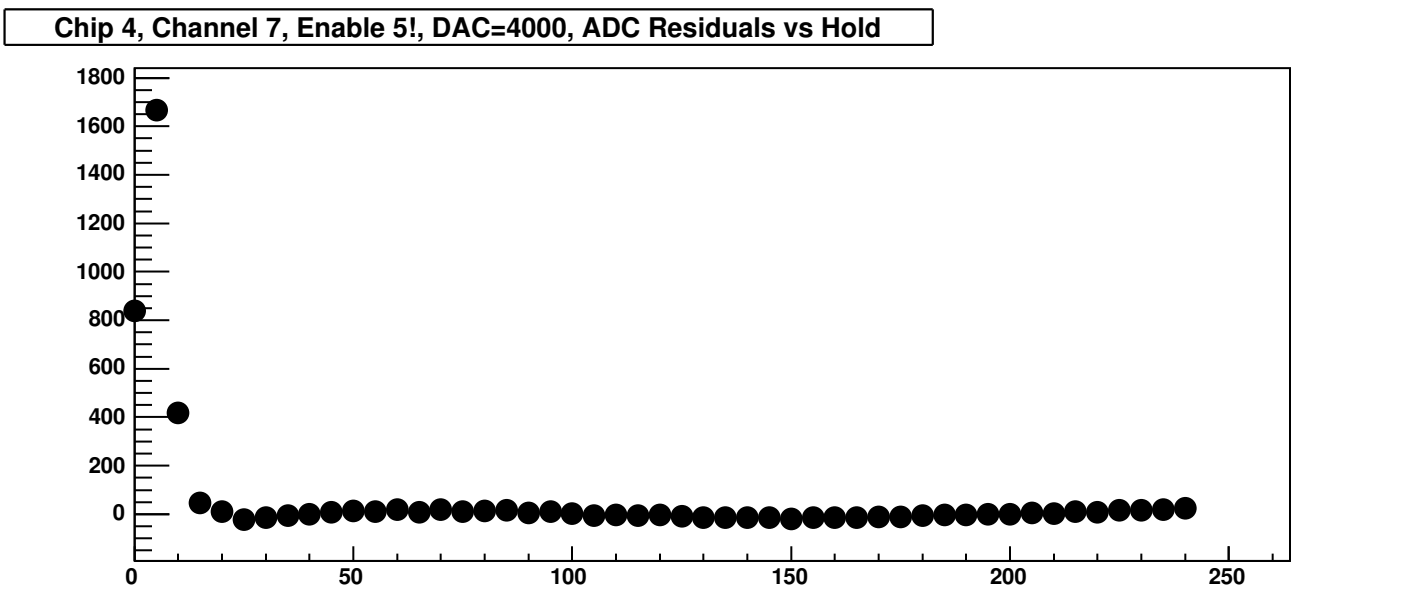

Chip 4, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 301.5 / 41
p0 327.1 ± 0.4904
p1 65.48 ± 0.4501
p2 -4.601e+07 ± 8.497e+05
p3 2.066e+07 ± 2.873e+05
p4 2.102 ± 0.02913
340
320
300
280
260
240
220
200
0
50
100
150
200
250


Chip 4, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold
10
9
8
7
6
5
4
0
50
100
150
200
250

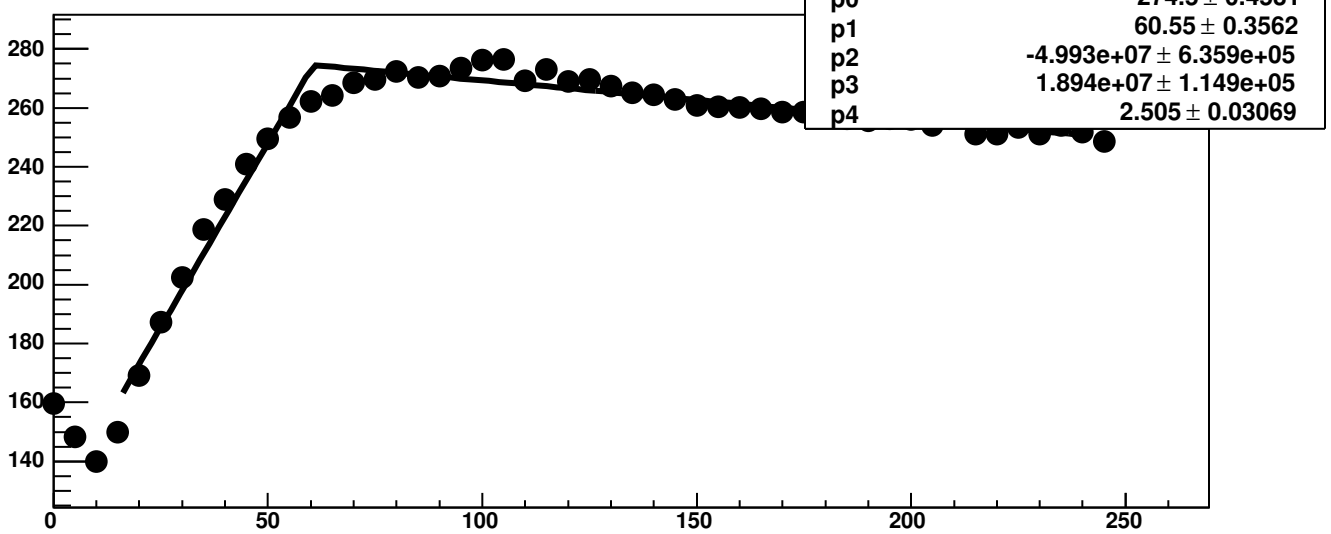

Chip 4, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
0
50
100
150
200
250


Chip 4, Channel 7, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 2605 / 42
p0 -618.1 ± 0.4814
p1 4.53 ± 0.006014
p2 1.126e+04 ± 0.9352
p3 30.32 ± 0.004253
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

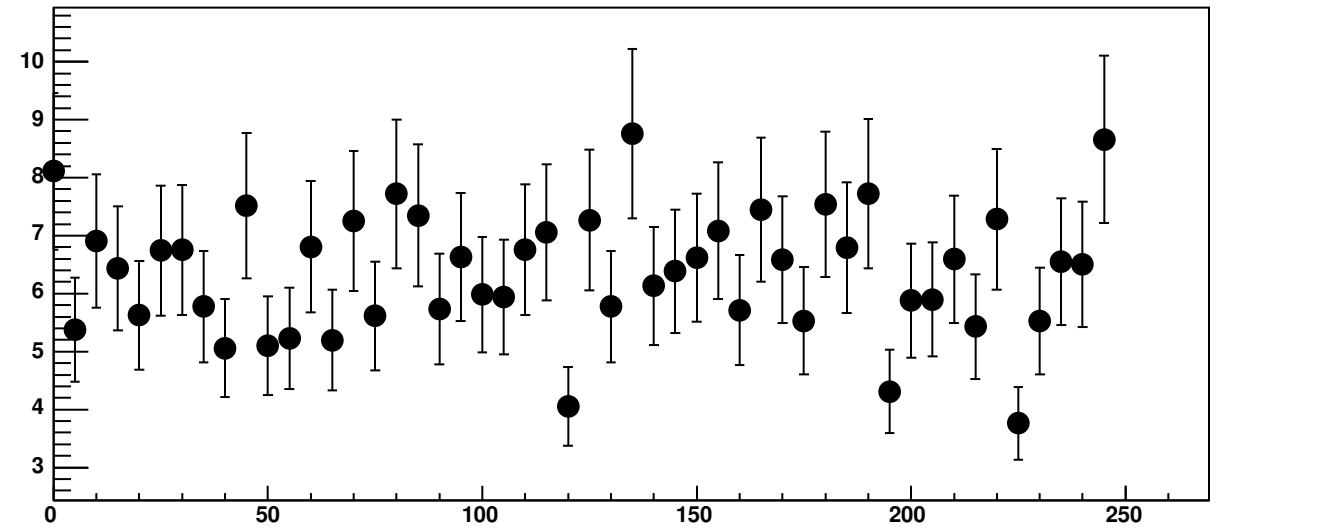

Chip 4, Channel 7, Enable 5!, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 4, Channel 7, Enable 5!, DAC=4000, ADC Residuals vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

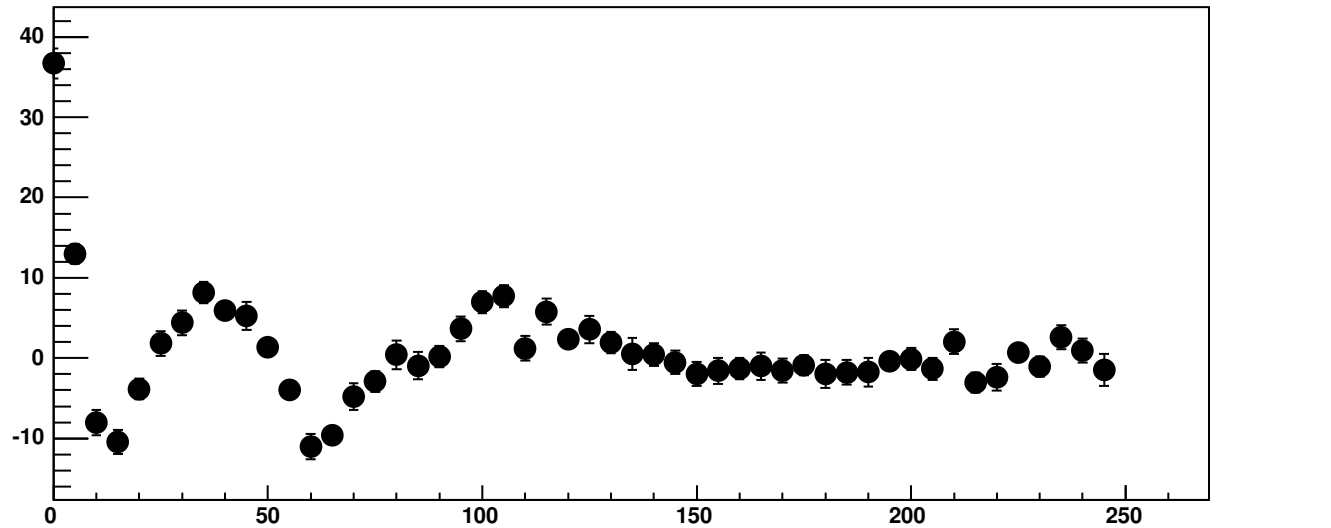
Chip 4, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold



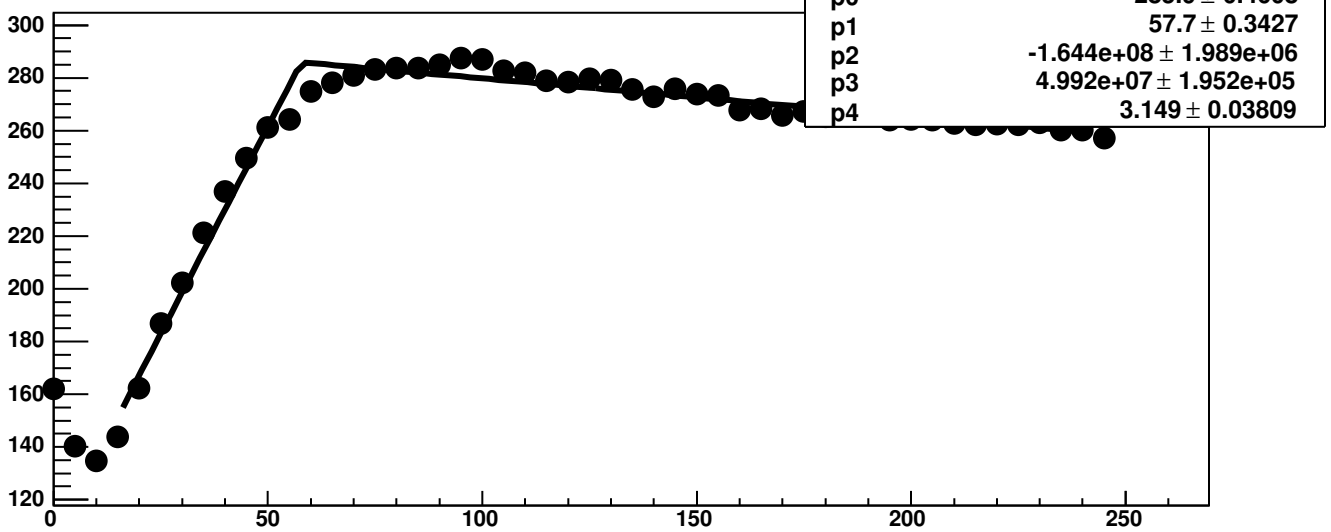
Chip 4, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



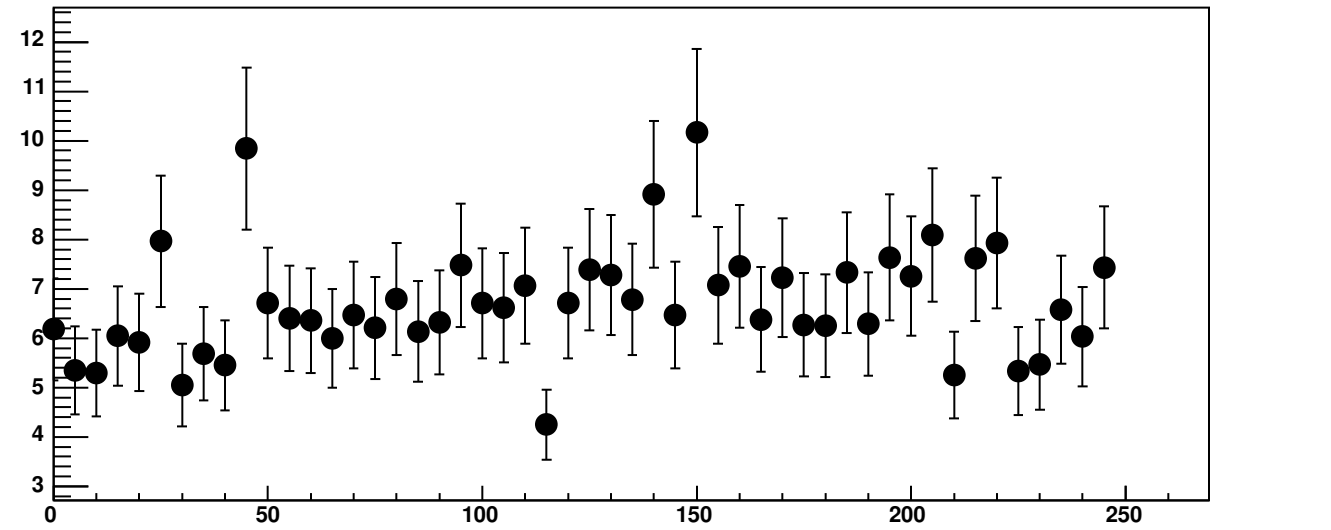
Chip 4, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold



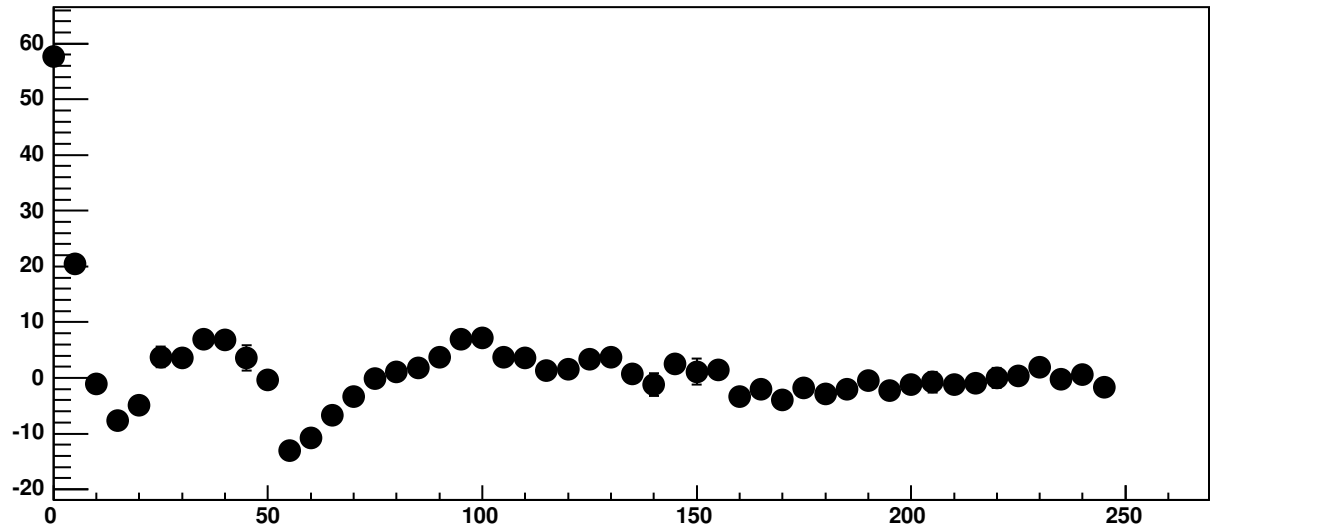
Chip 4, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 4, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold

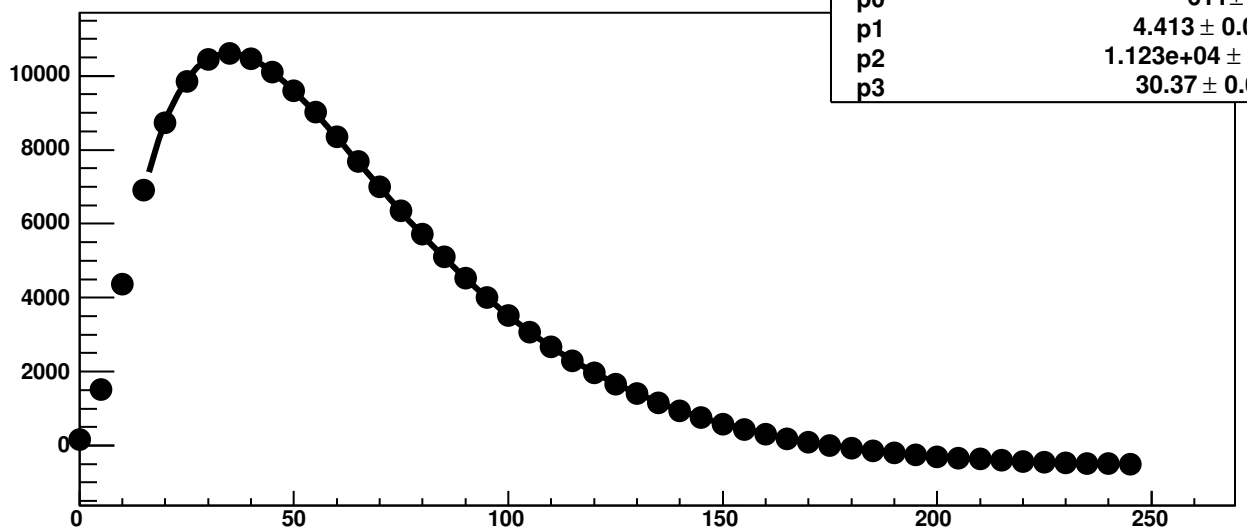


Chip 4, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

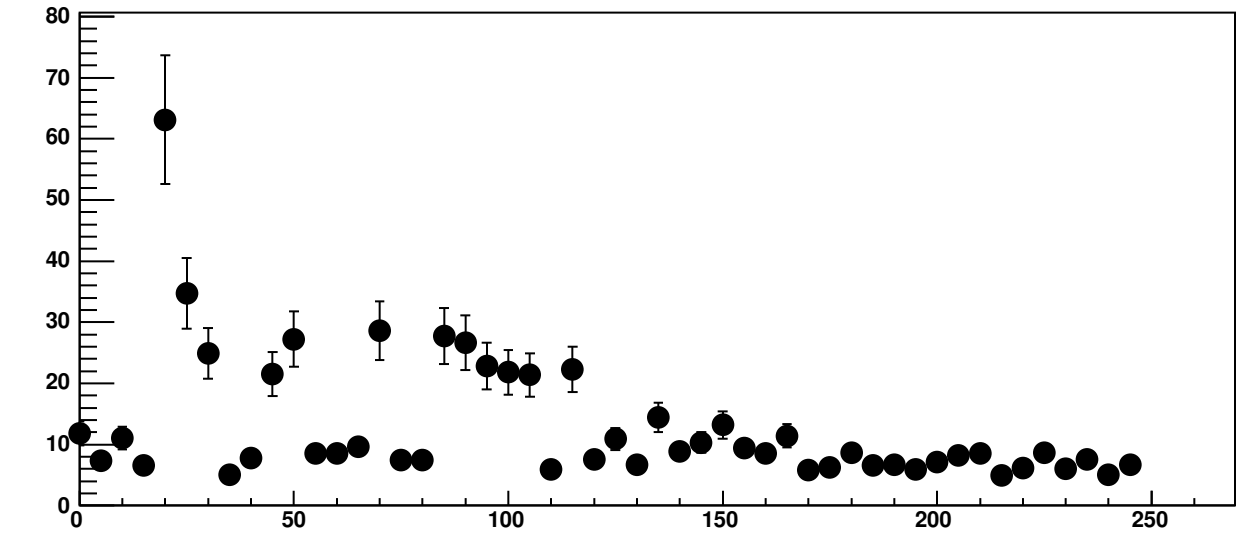


Chip 4, Channel 8, Enable 2!, DAC=4000, ADC Mean vs Hold

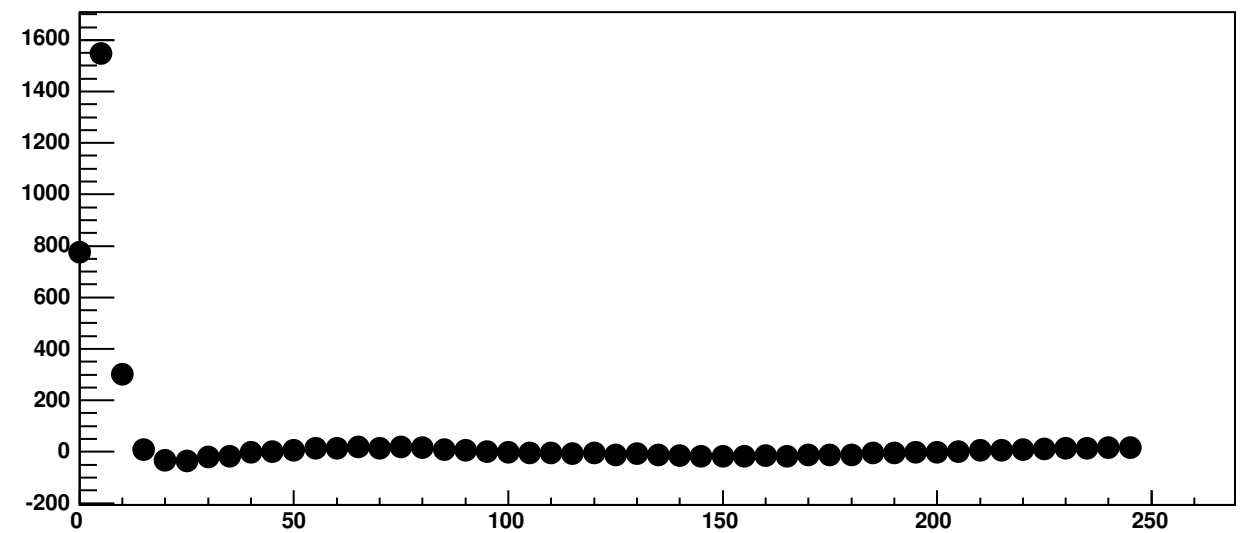
χ^2 / ndf	1781 / 42
p0	-611 ± 0.4831
p1	4.413 ± 0.004362
p2	1.123e+04 ± 0.8727
p3	30.37 ± 0.004151

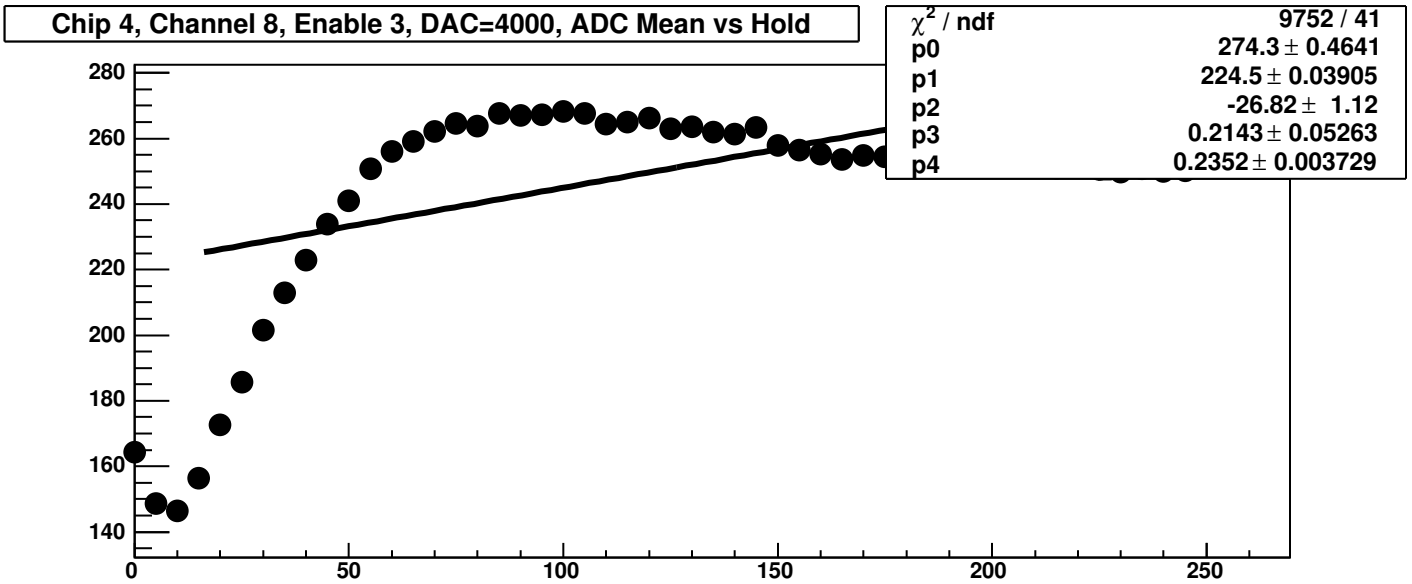


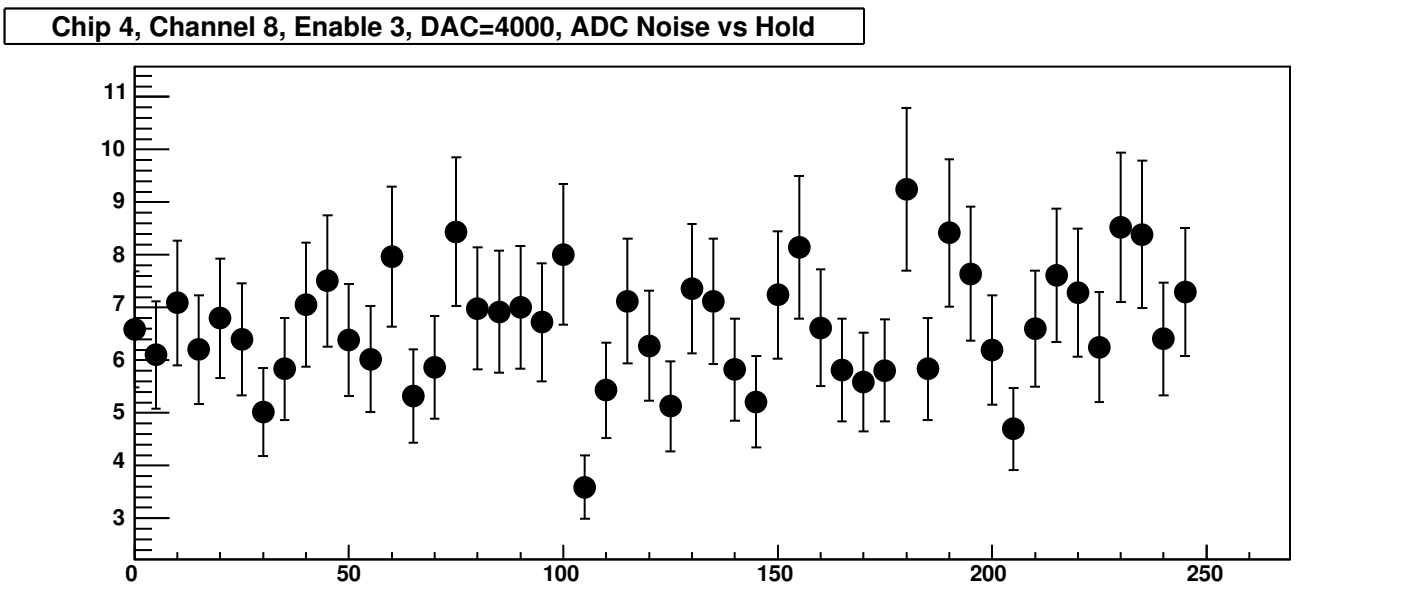
Chip 4, Channel 8, Enable 2!, DAC=4000, ADC Noise vs Hold

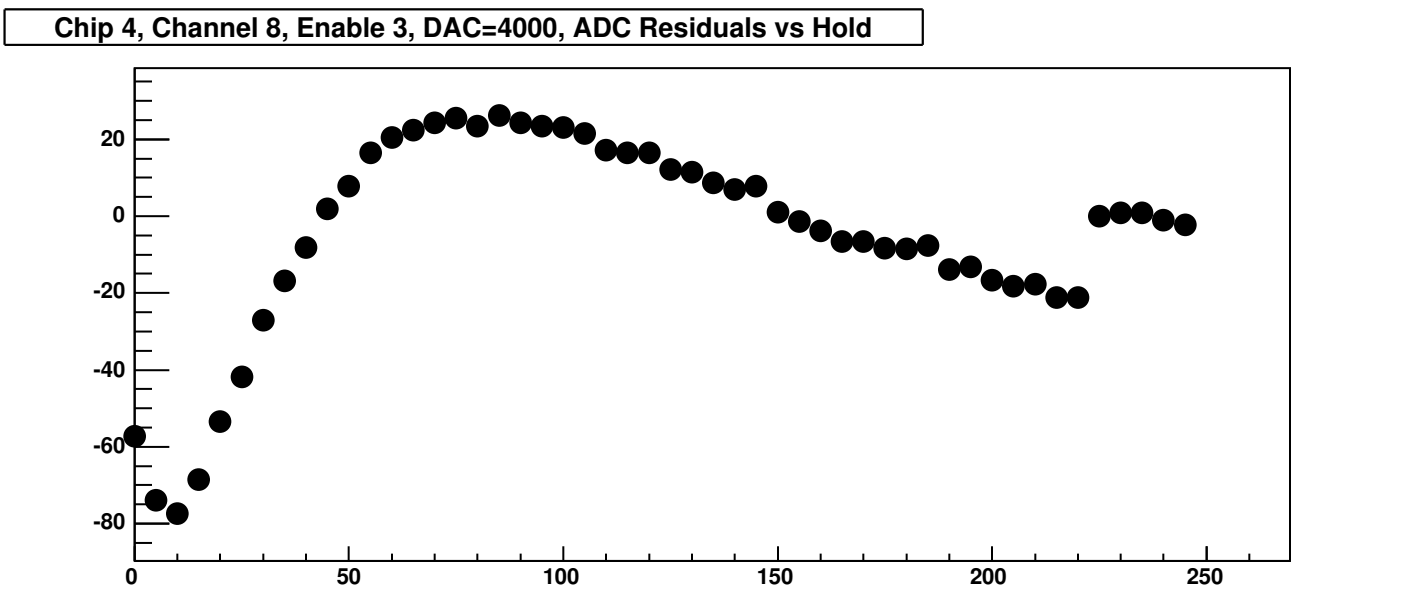


Chip 4, Channel 8, Enable 2!, DAC=4000, ADC Residuals vs Hold

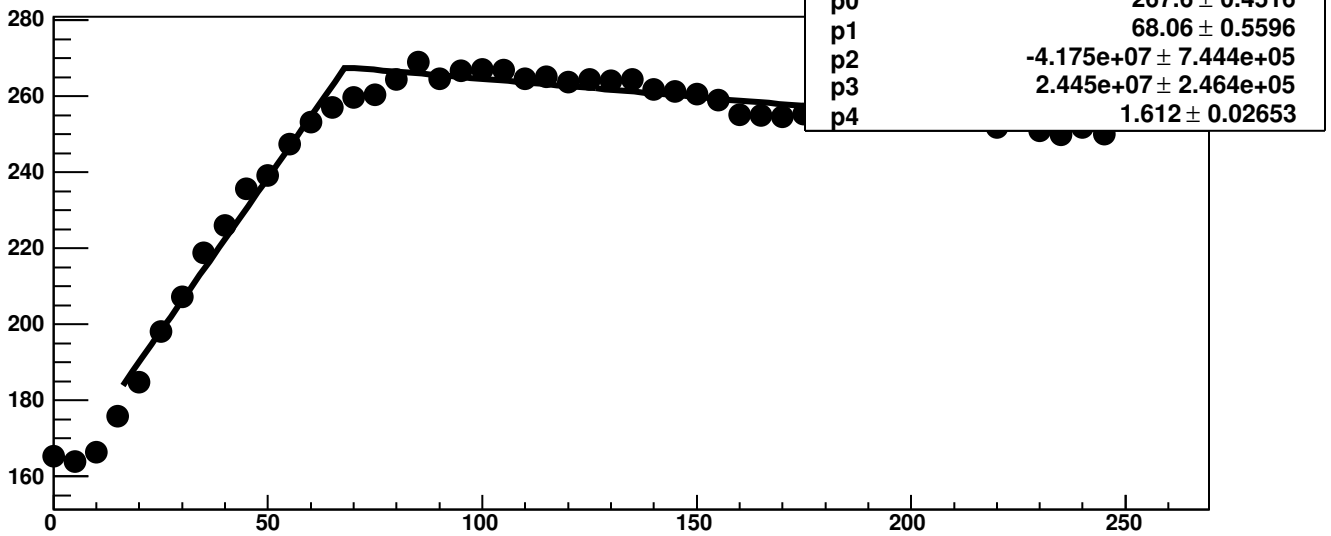



Chip 4, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 9752 / 41
p0 274.3 ± 0.4641
p1 224.5 ± 0.03905
p2 -26.82 ± 1.12
p3 0.2143 ± 0.05263
p4 0.2352 ± 0.003729
280
260
240
220
200
180
160
140
0
50
100
150
200
250

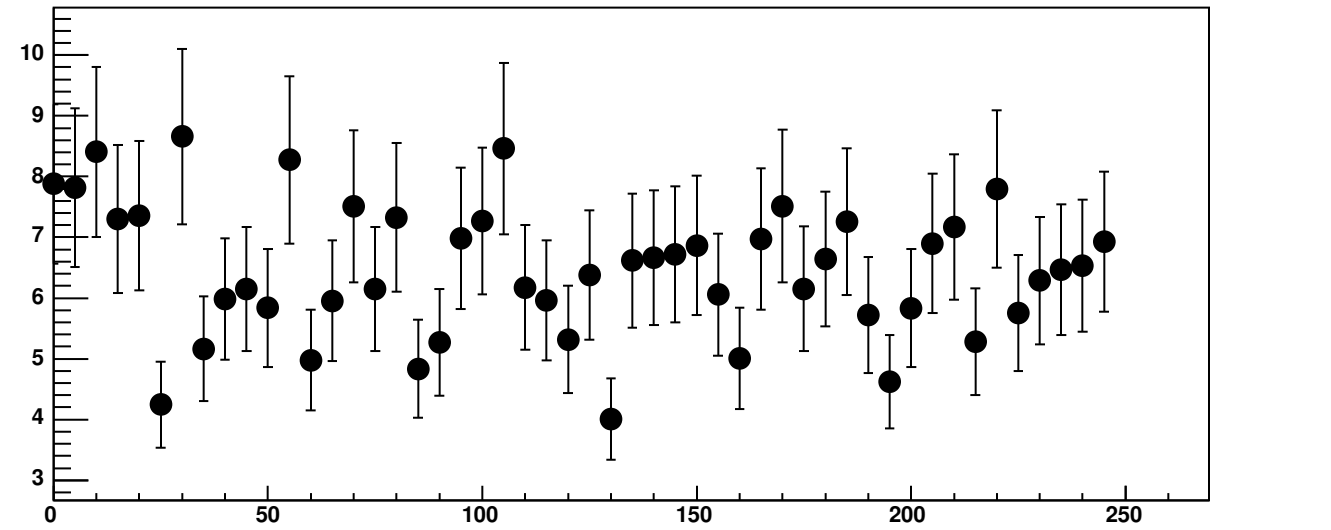

Chip 4, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 4, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
-80
0
50
100
150
200
250

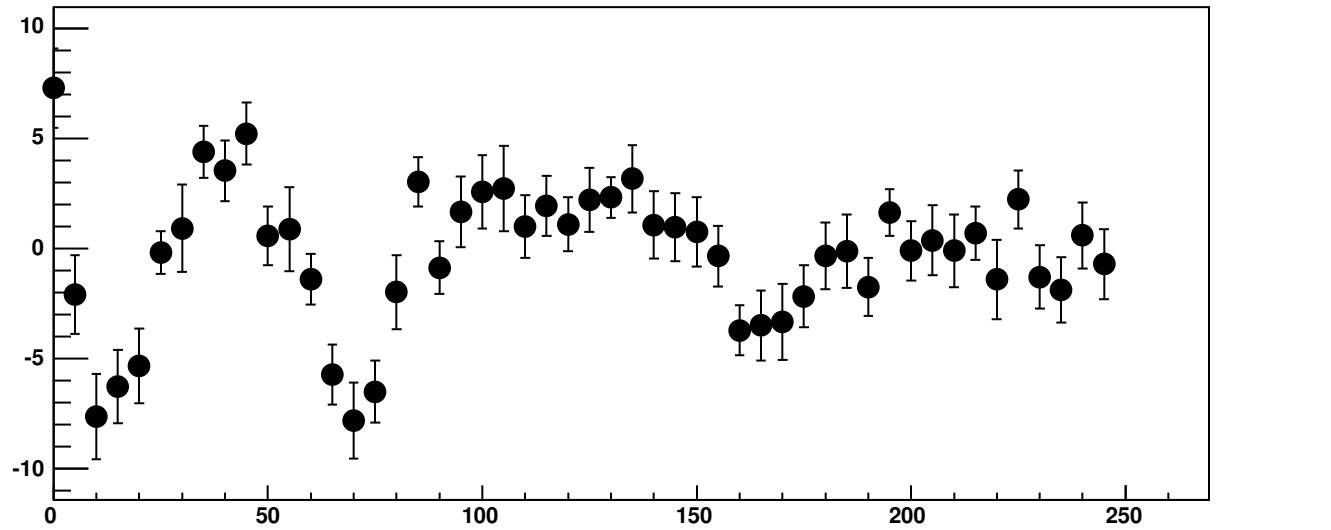
Chip 4, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold



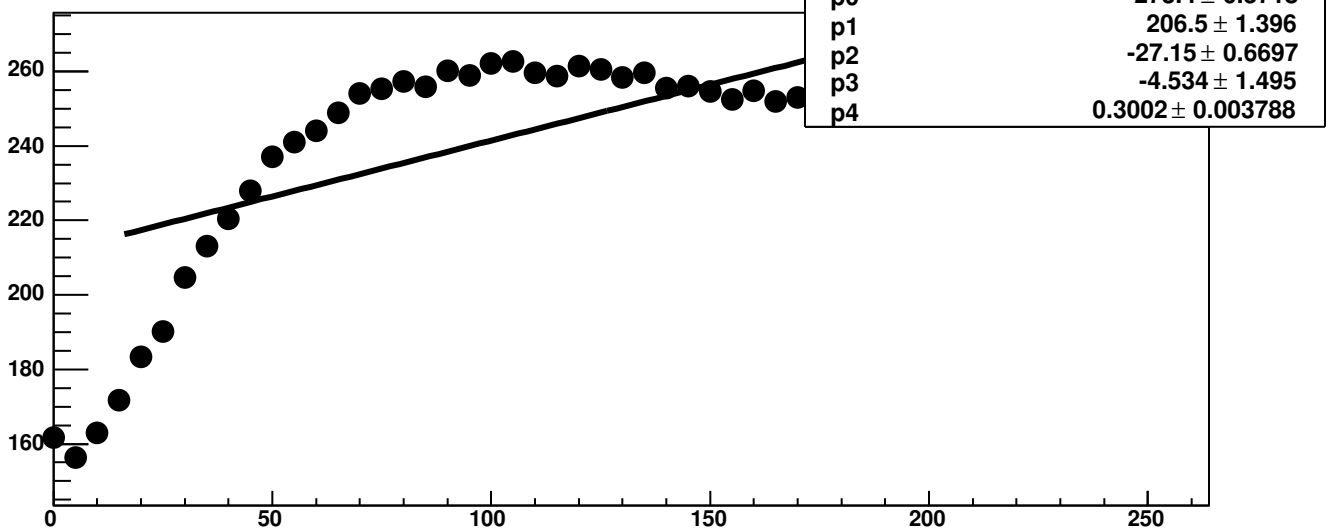
Chip 4, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold



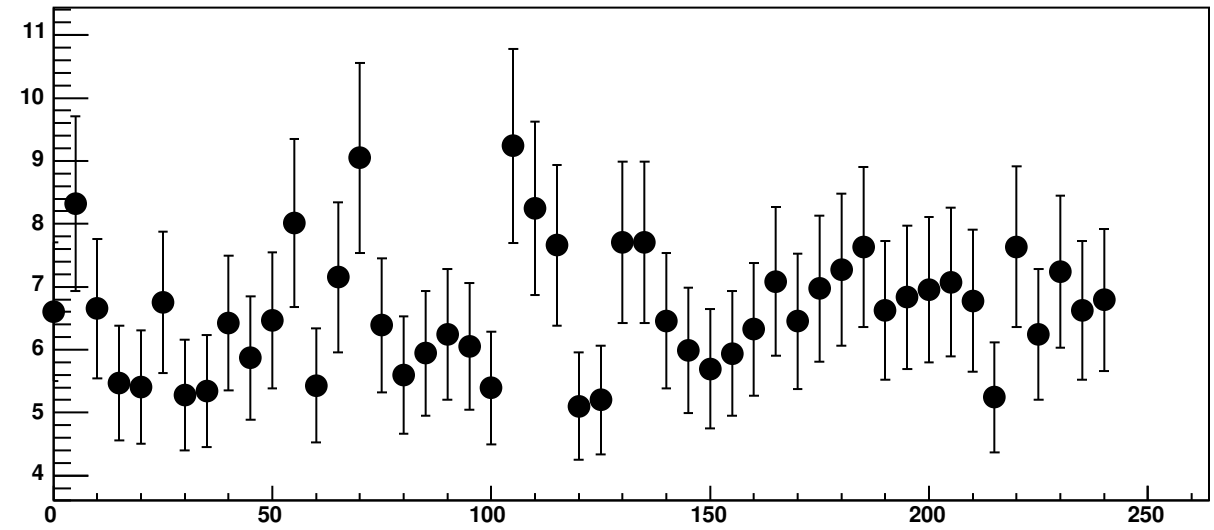
Chip 4, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold



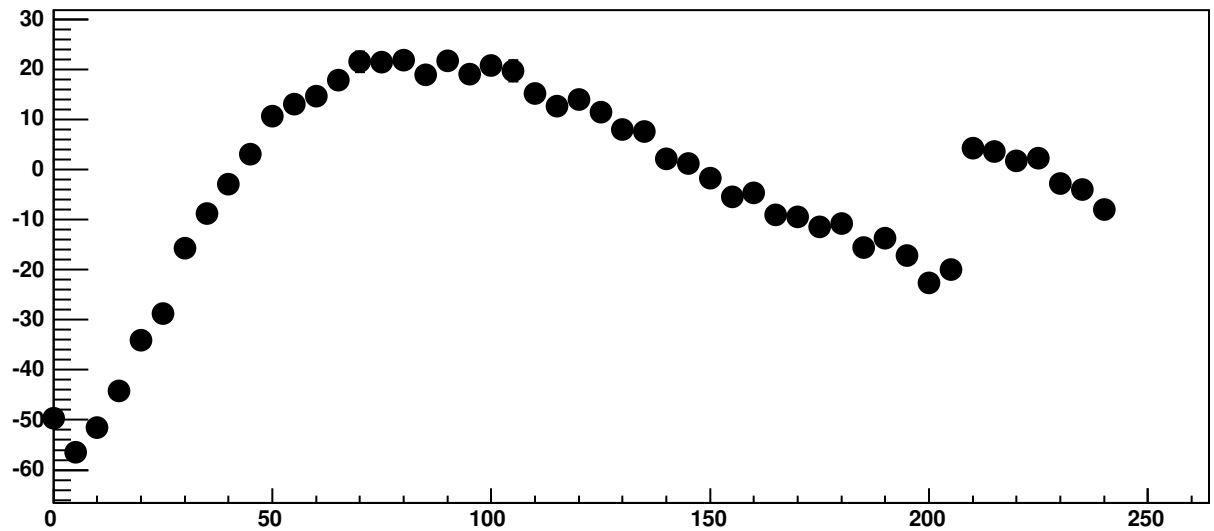
Chip 4, Channel 8, Enable 5, DAC=4000, ADC Mean vs Hold



Chip 4, Channel 8, Enable 5, DAC=4000, ADC Noise vs Hold

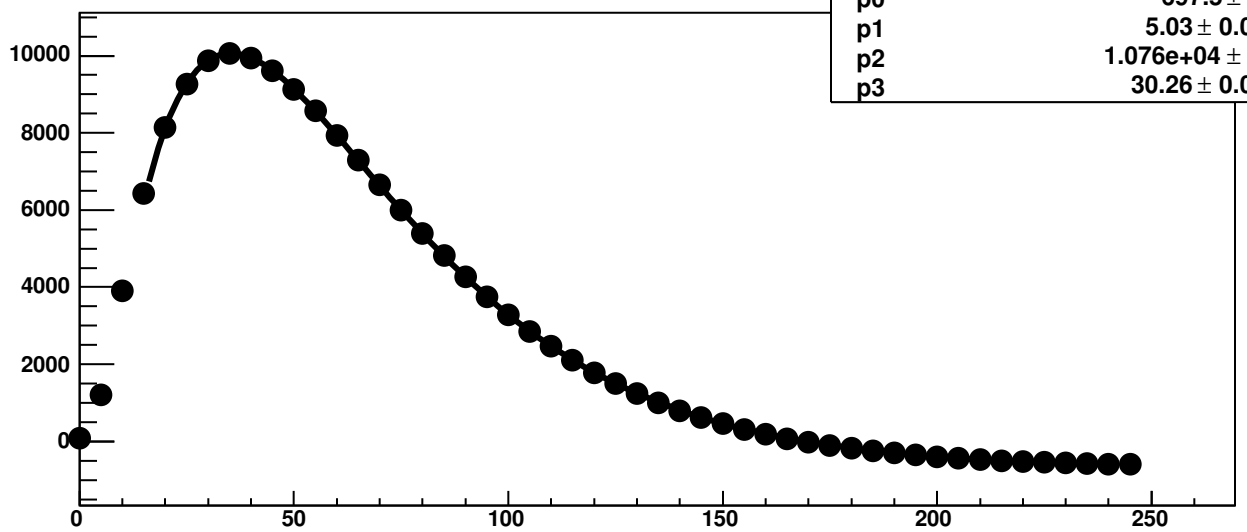


Chip 4, Channel 8, Enable 5, DAC=4000, ADC Residuals vs Hold

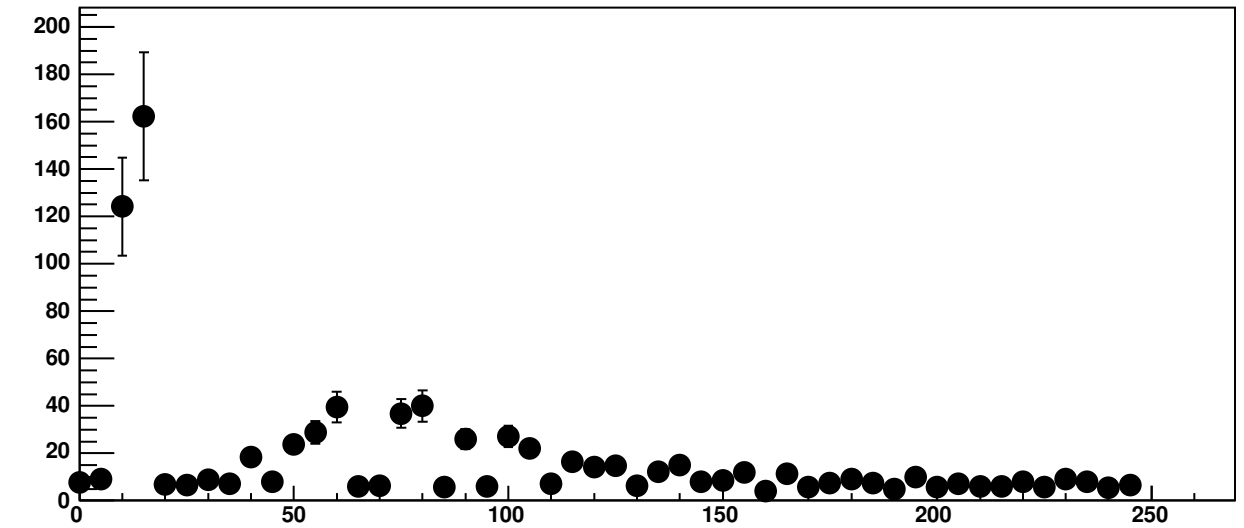


Chip 4, Channel 9, Enable 0!, DAC=4000, ADC Mean vs Hold

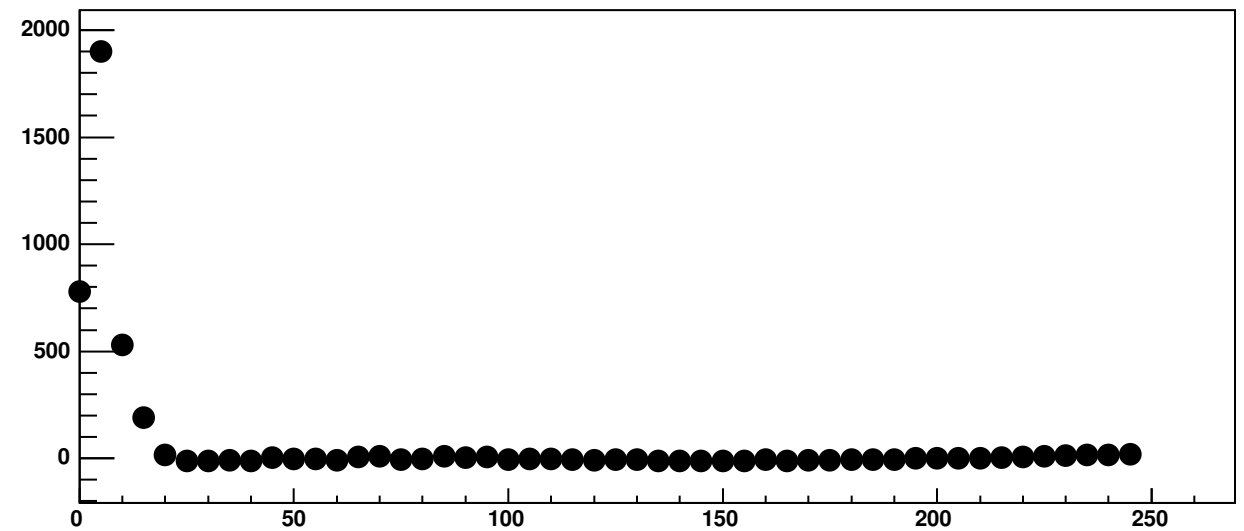
χ^2 / ndf	1091 / 42
p0	-697.5 ± 0.4982
p1	5.03 ± 0.007276
p2	1.076e+04 ± 0.8499
p3	30.26 ± 0.005108

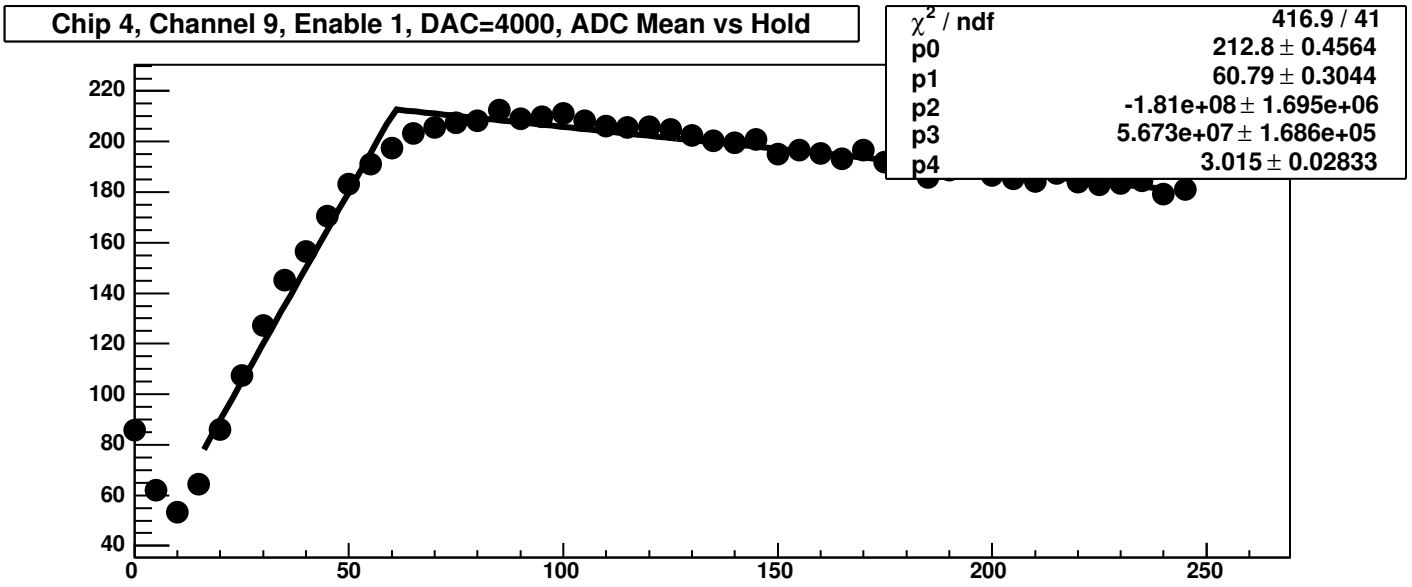


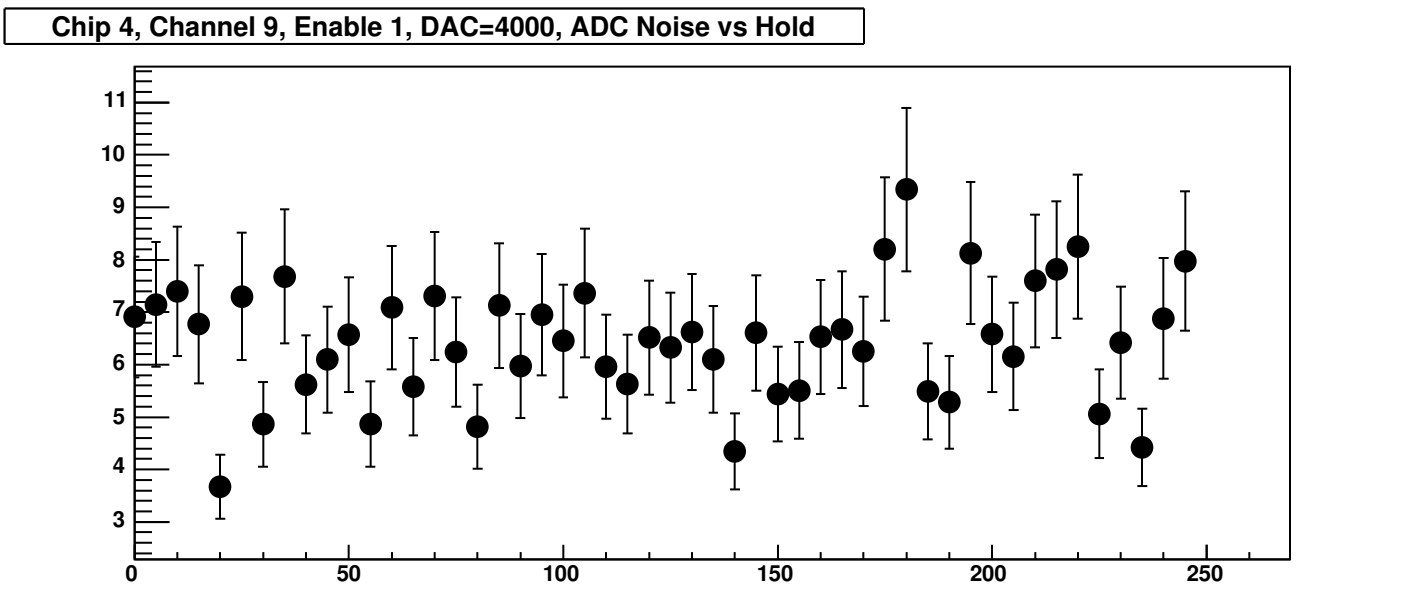
Chip 4, Channel 9, Enable 0!, DAC=4000, ADC Noise vs Hold

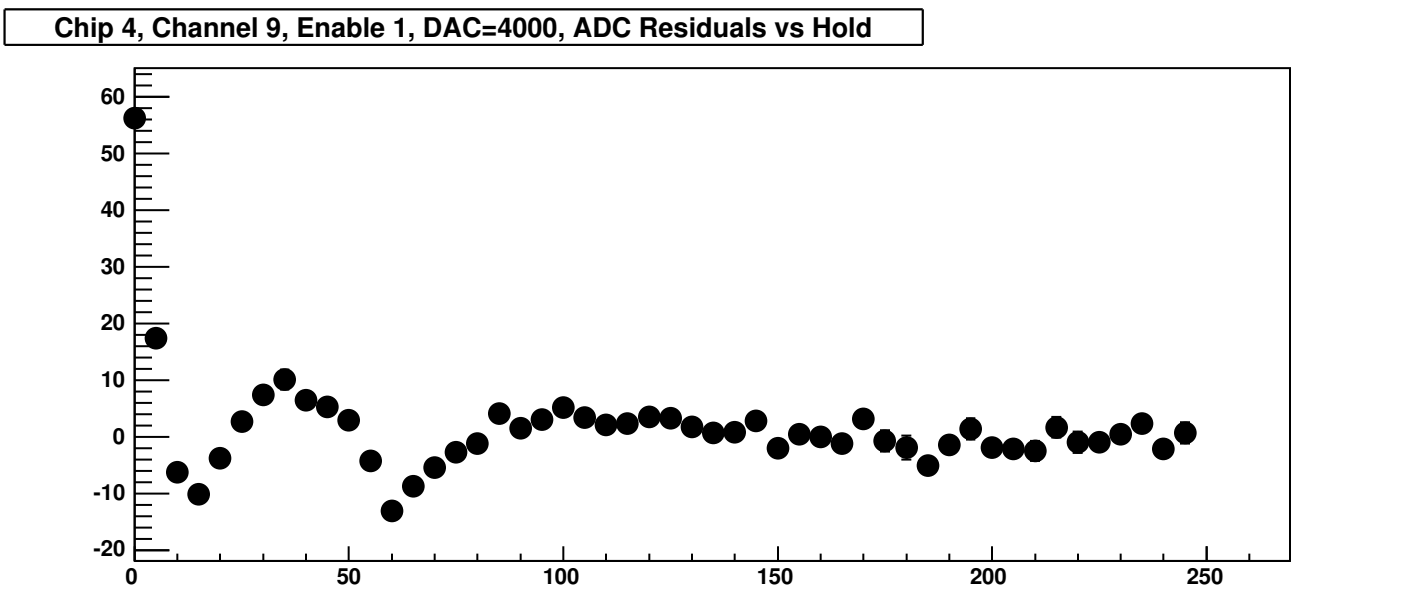


Chip 4, Channel 9, Enable 0!, DAC=4000, ADC Residuals vs Hold

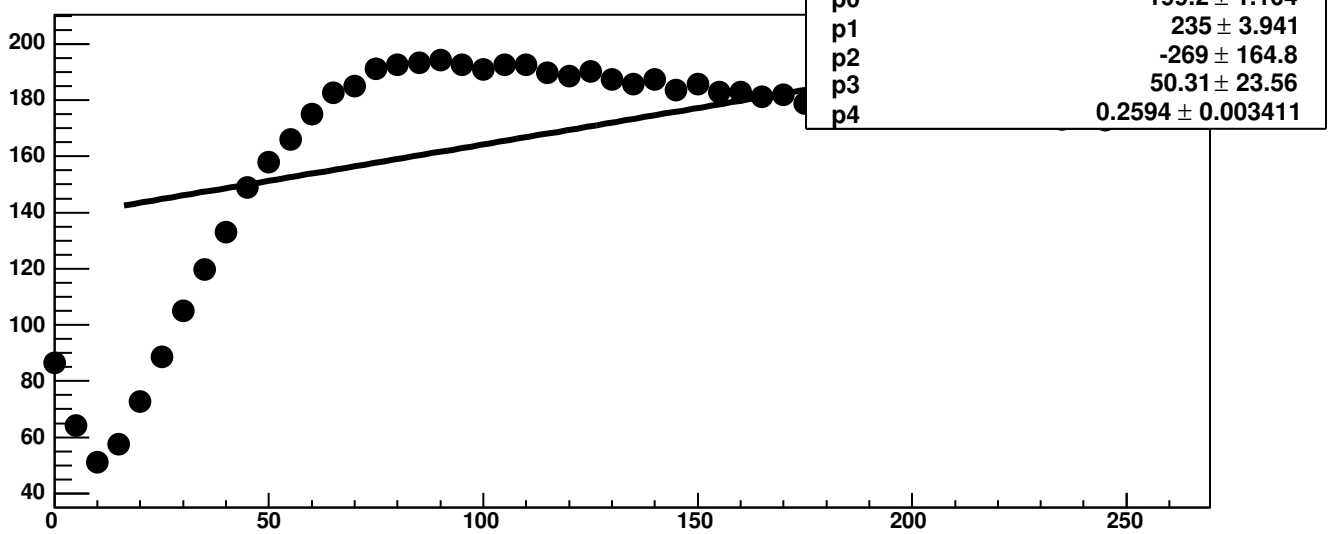



Chip 4, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 416.9 / 41
p0 212.8 ± 0.4564
p1 60.79 ± 0.3044
p2 -1.81e+08 ± 1.695e+06
p3 5.673e+07 ± 1.686e+05
p4 3.015 ± 0.02833

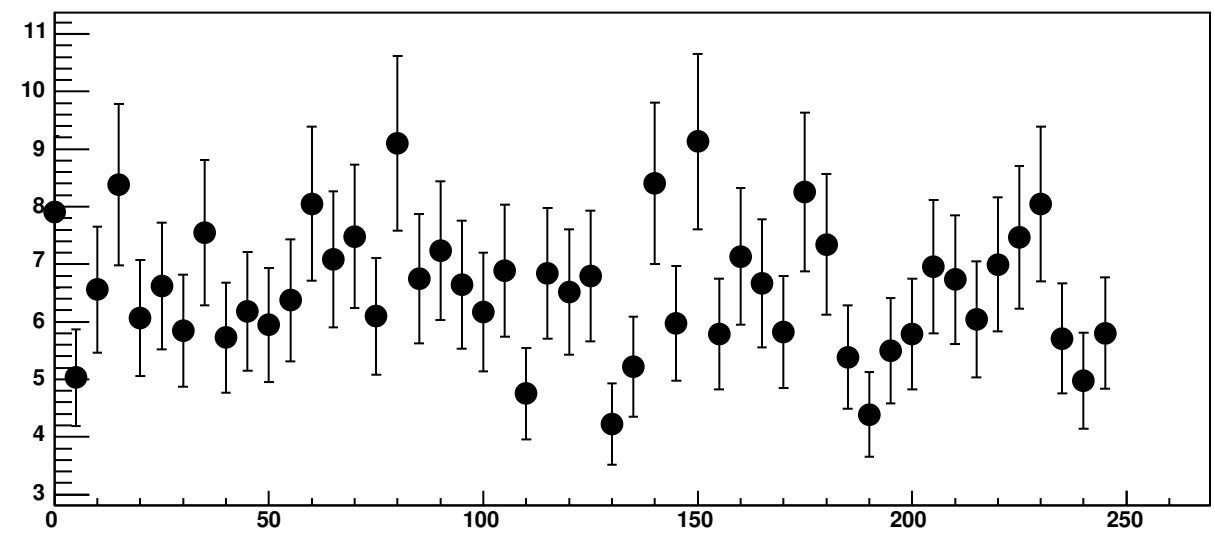

Chip 4, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold


Chip 4, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold

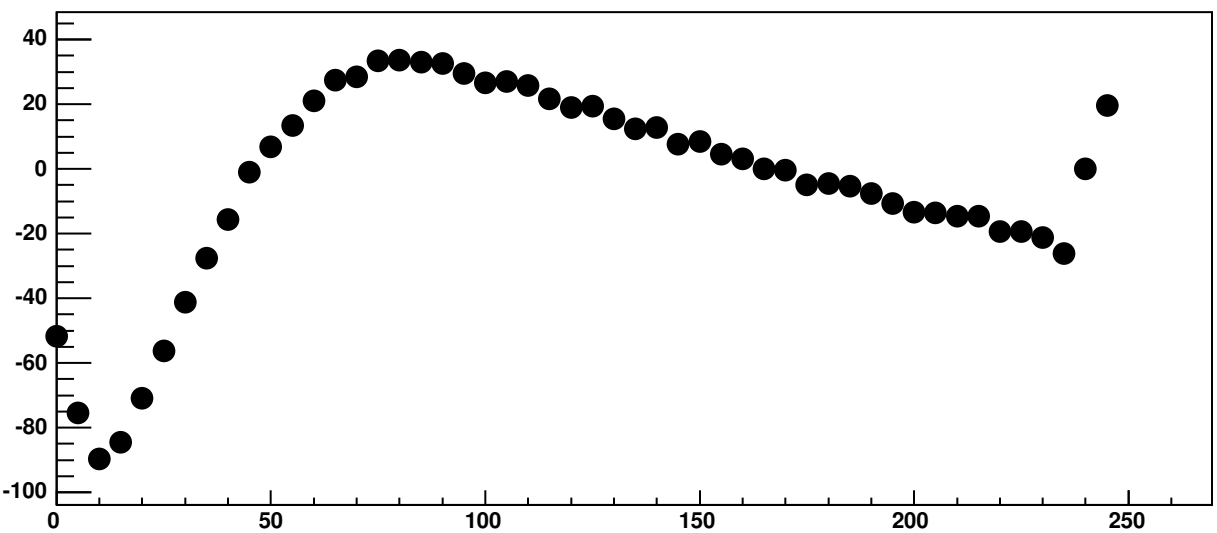
Chip 4, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold



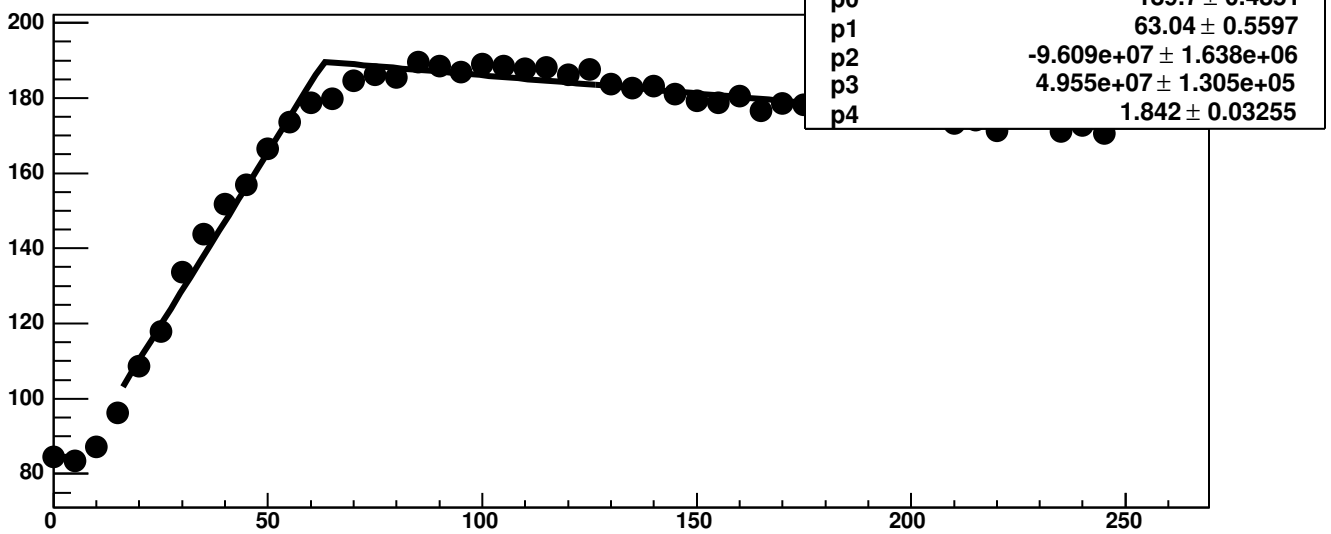
Chip 4, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



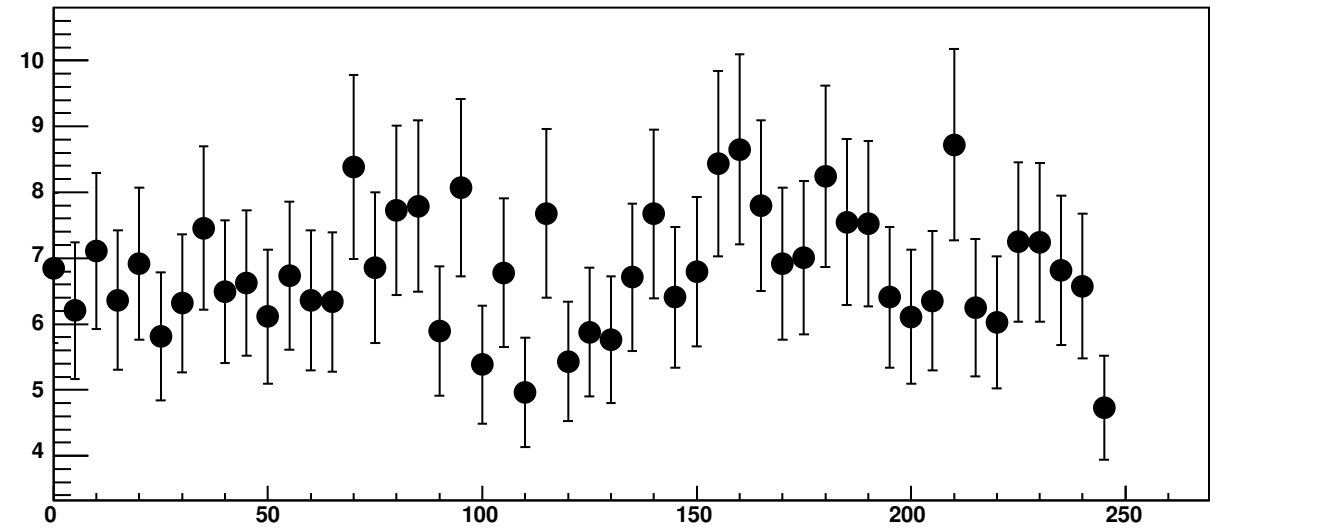
Chip 4, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold



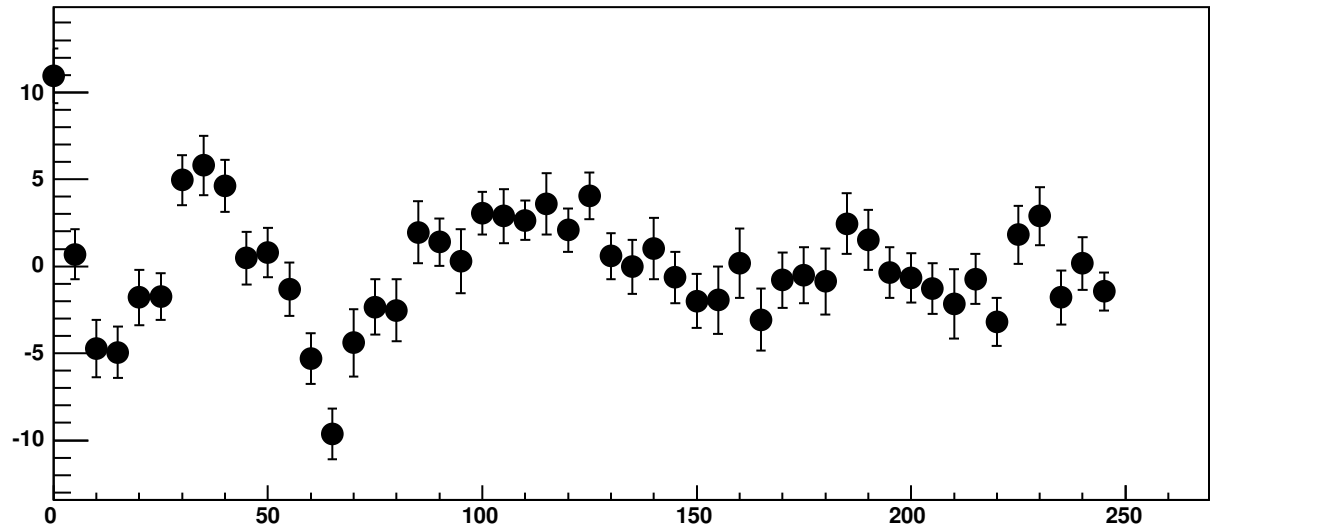
Chip 4, Channel 9, Enable 3, DAC=4000, ADC Mean vs Hold



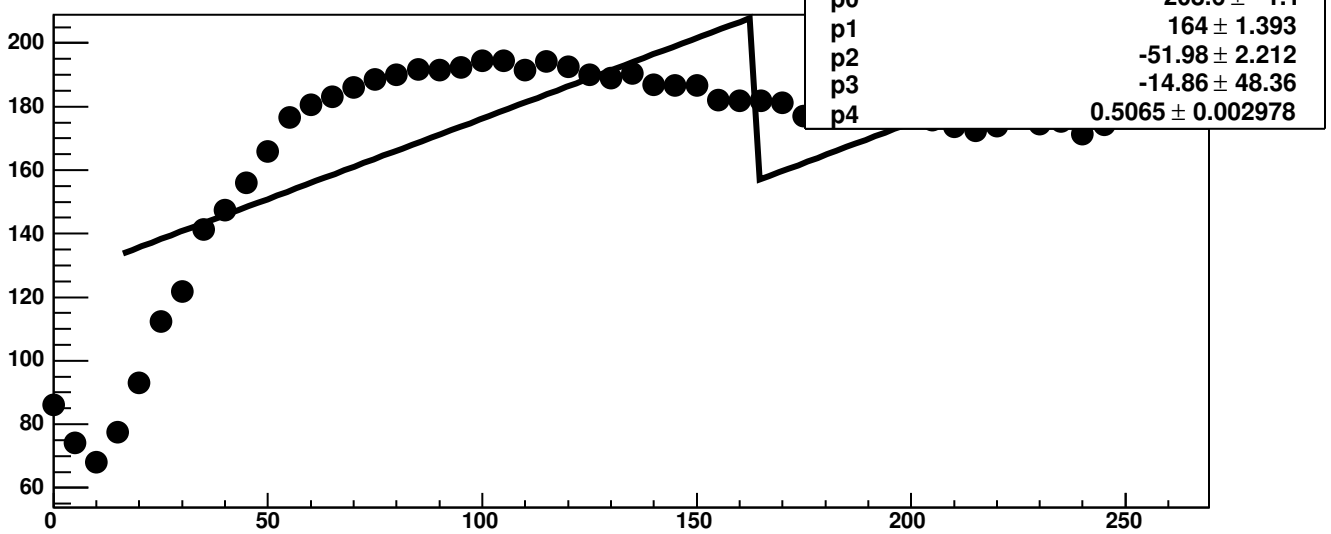
Chip 4, Channel 9, Enable 3, DAC=4000, ADC Noise vs Hold



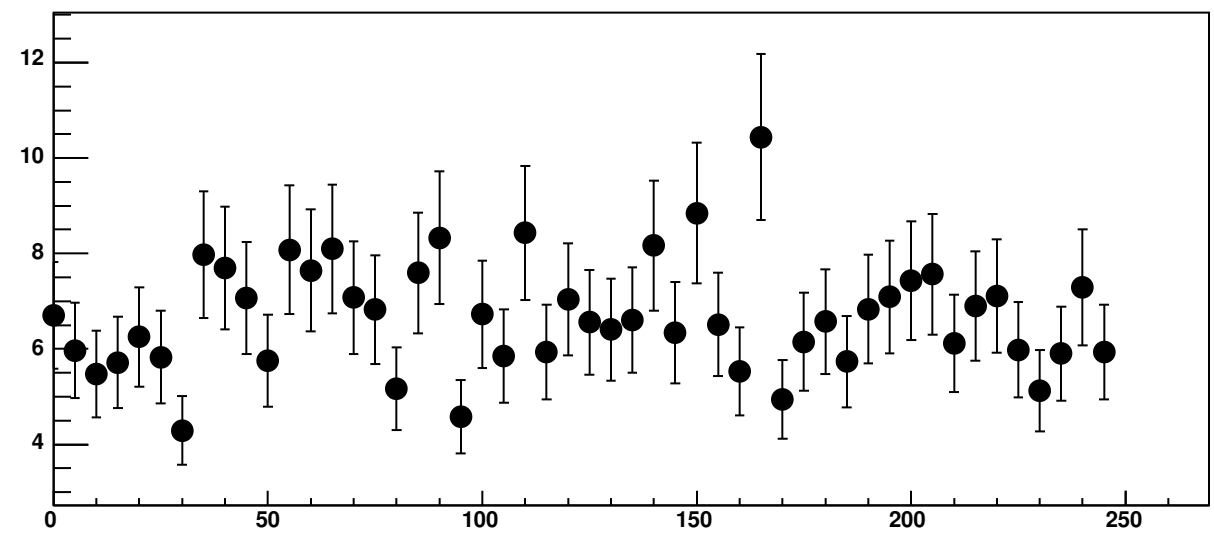
Chip 4, Channel 9, Enable 3, DAC=4000, ADC Residuals vs Hold



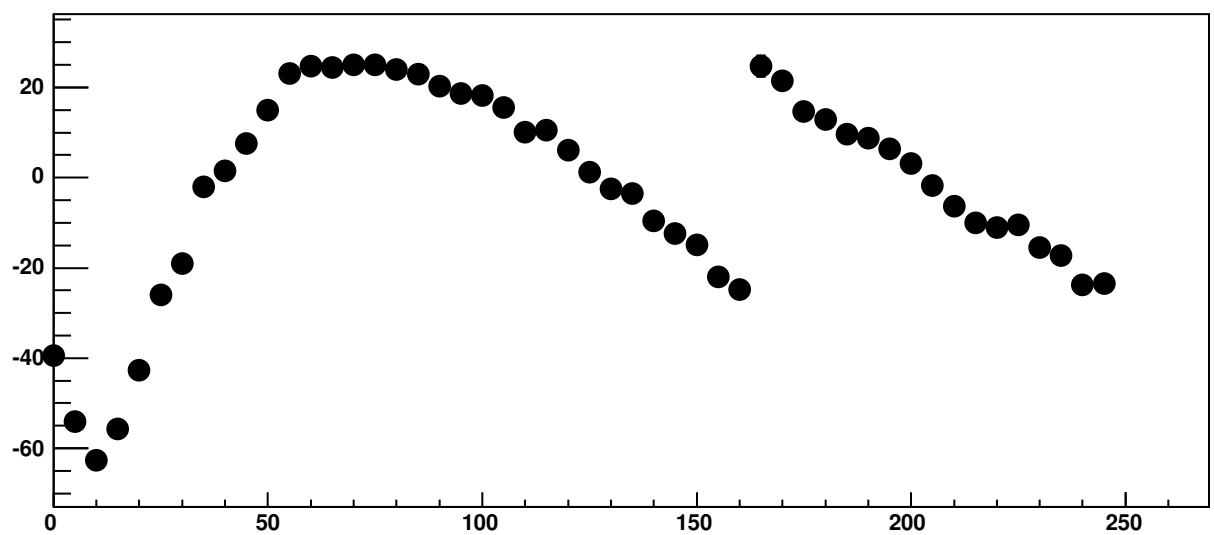
Chip 4, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold



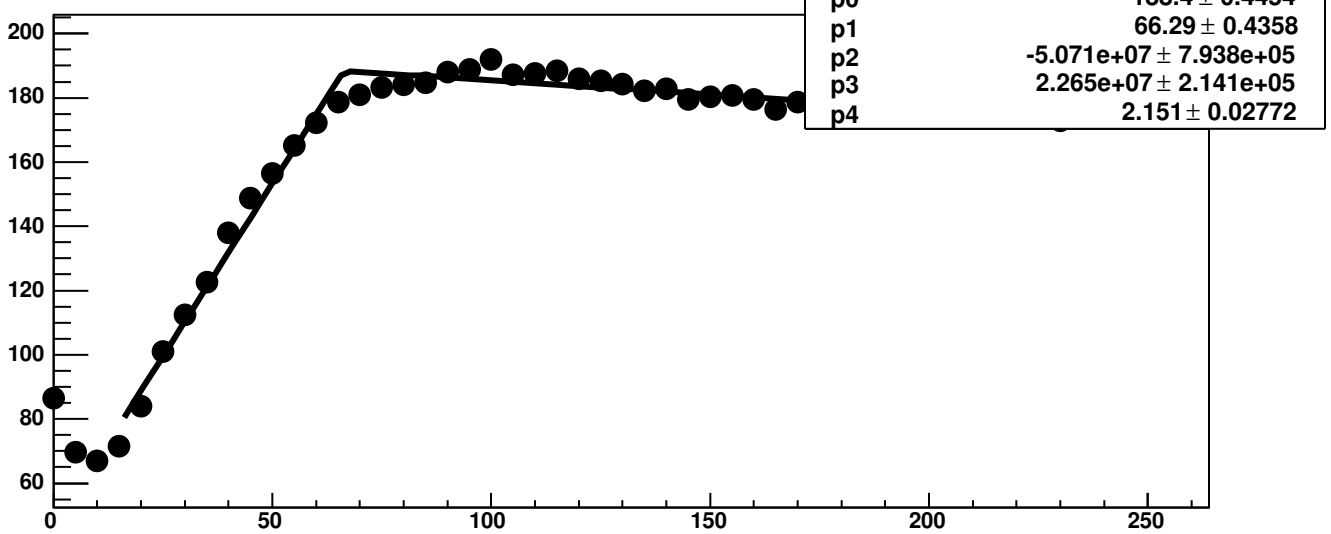
Chip 4, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



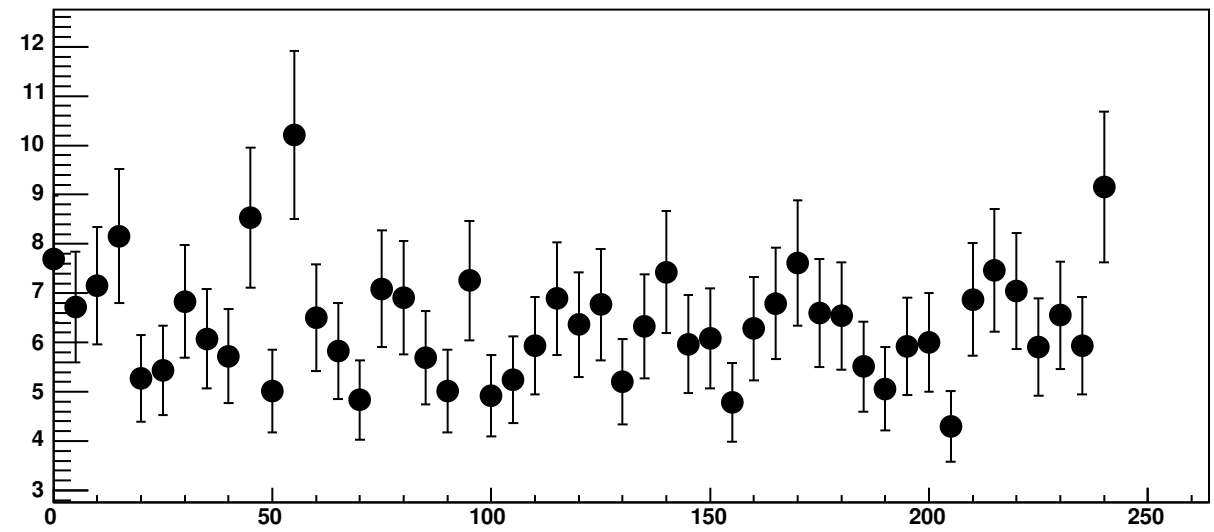
Chip 4, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold



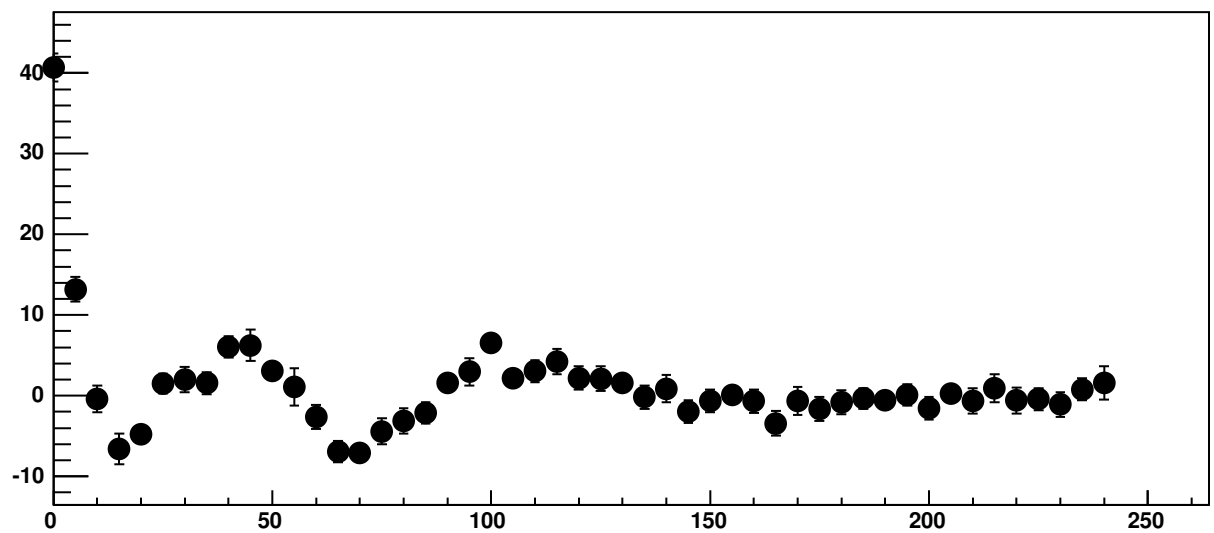
Chip 4, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold



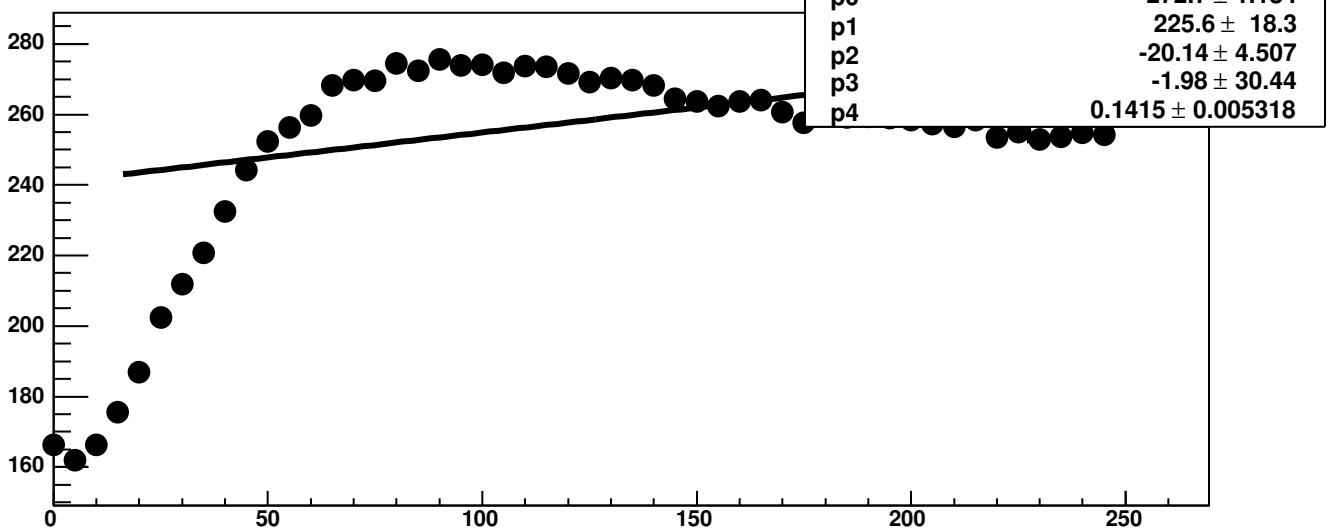
Chip 4, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold



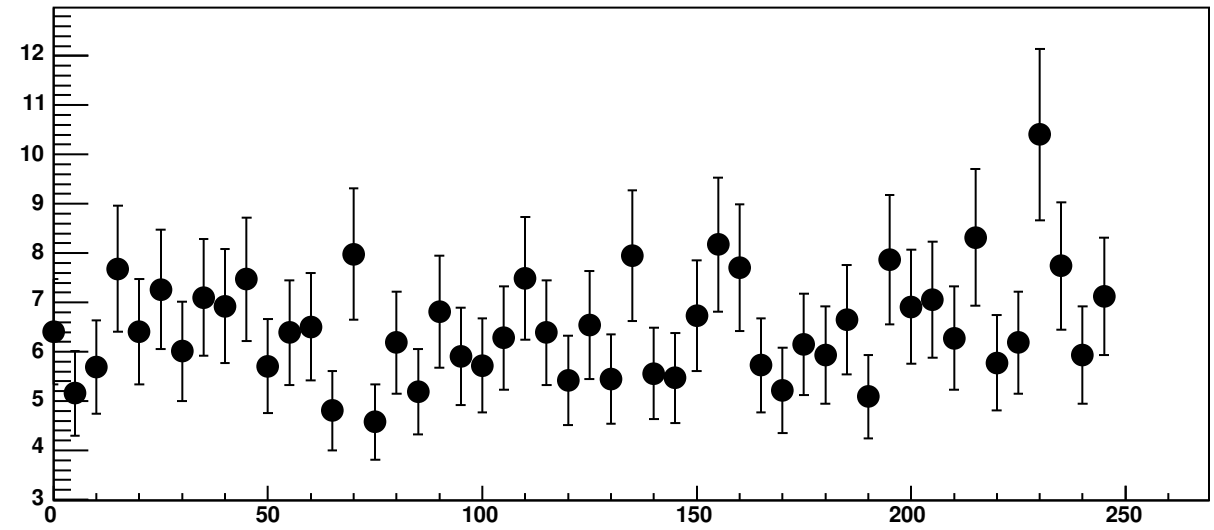
Chip 4, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold



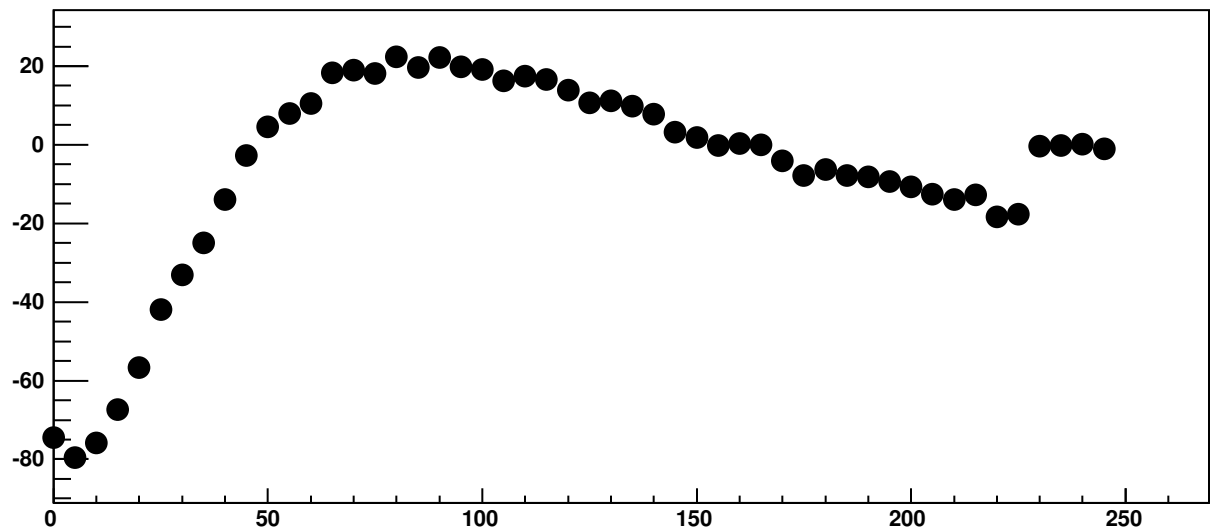
Chip 4, Channel 10, Enable 0, DAC=4000, ADC Mean vs Hold

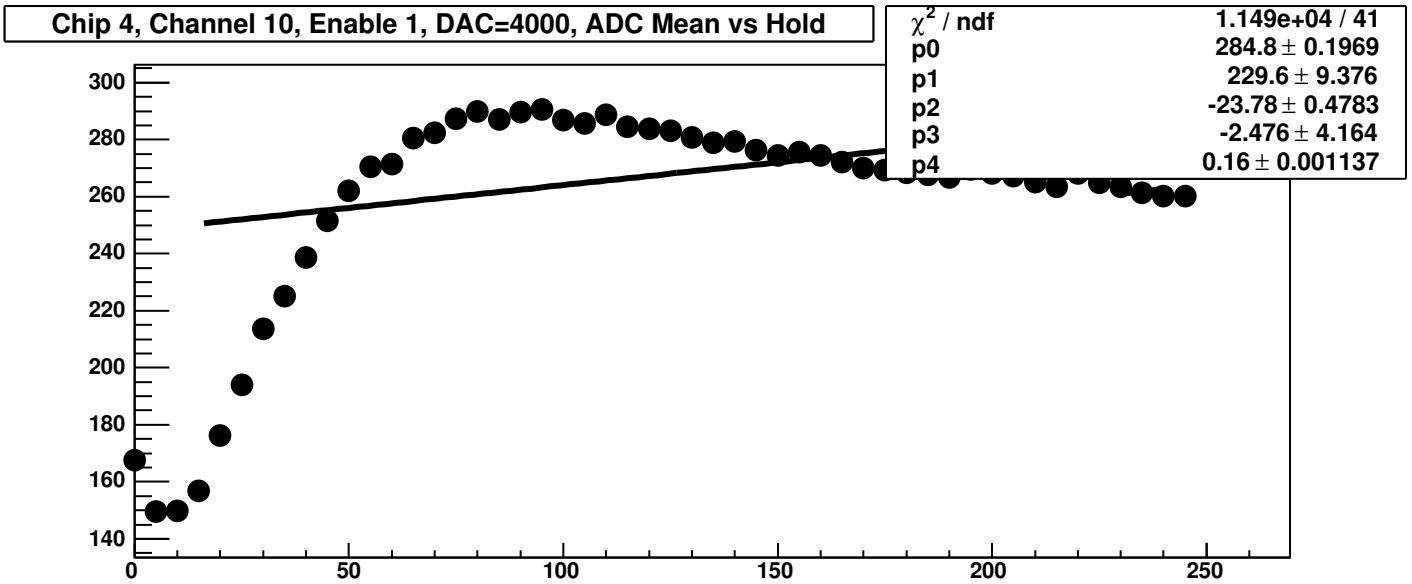


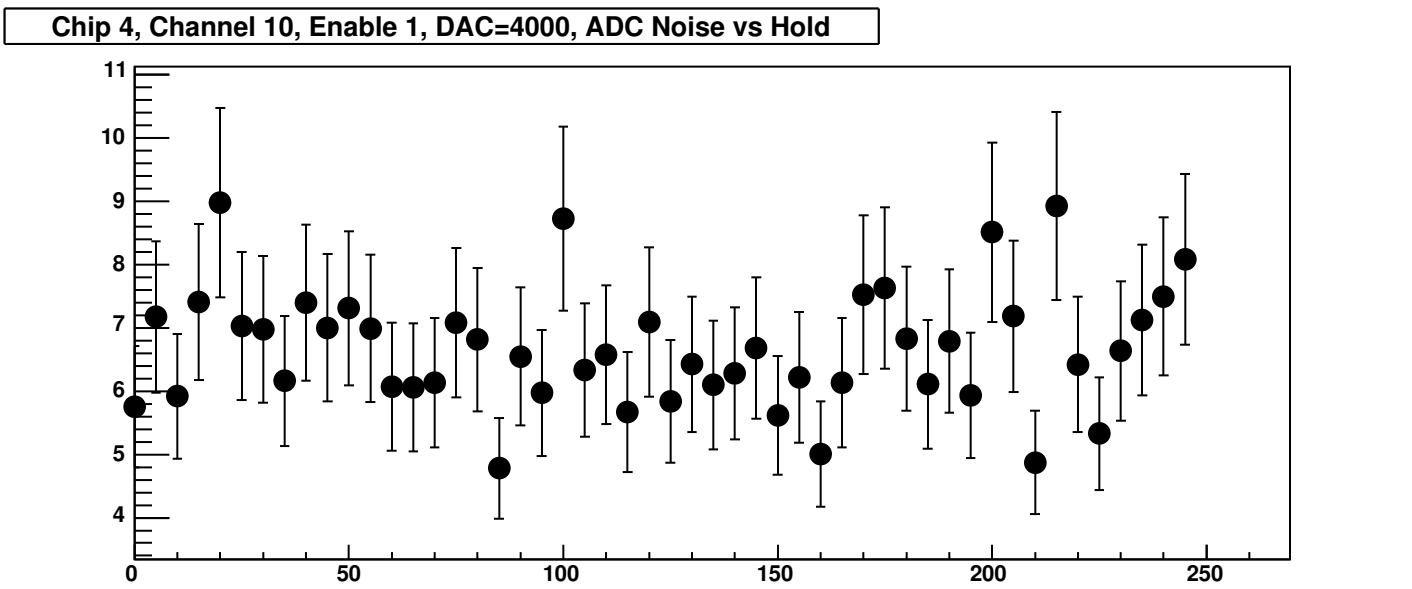
Chip 4, Channel 10, Enable 0, DAC=4000, ADC Noise vs Hold

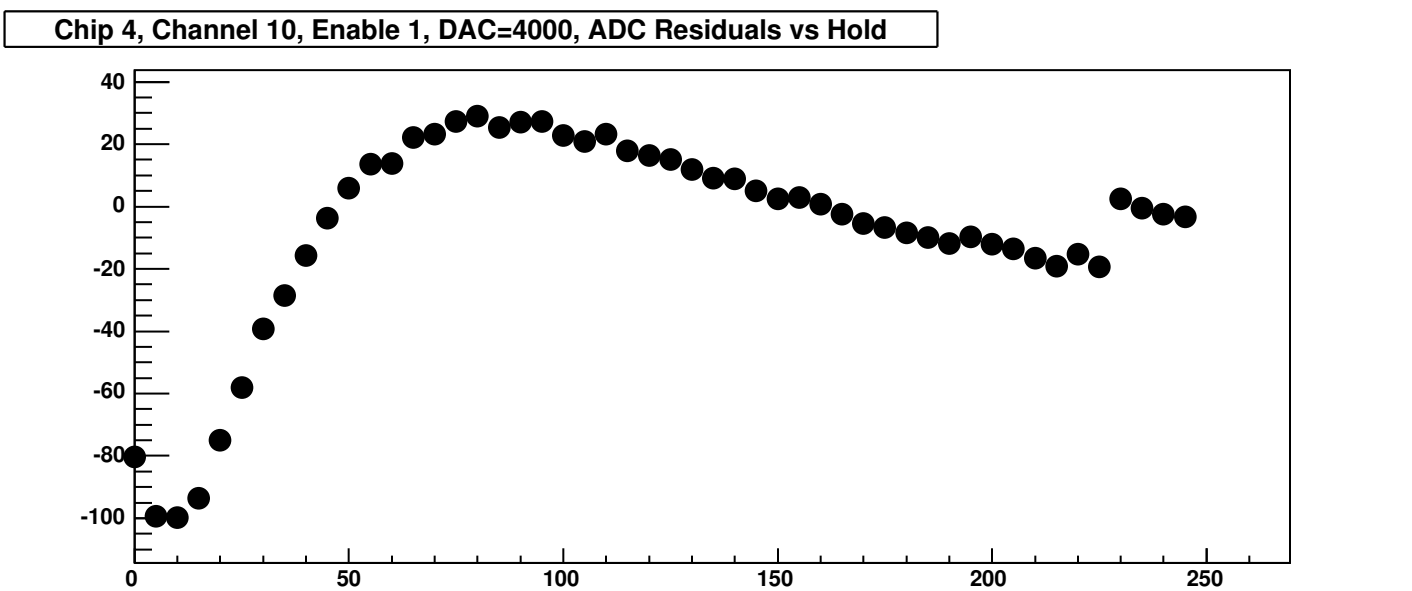


Chip 4, Channel 10, Enable 0, DAC=4000, ADC Residuals vs Hold

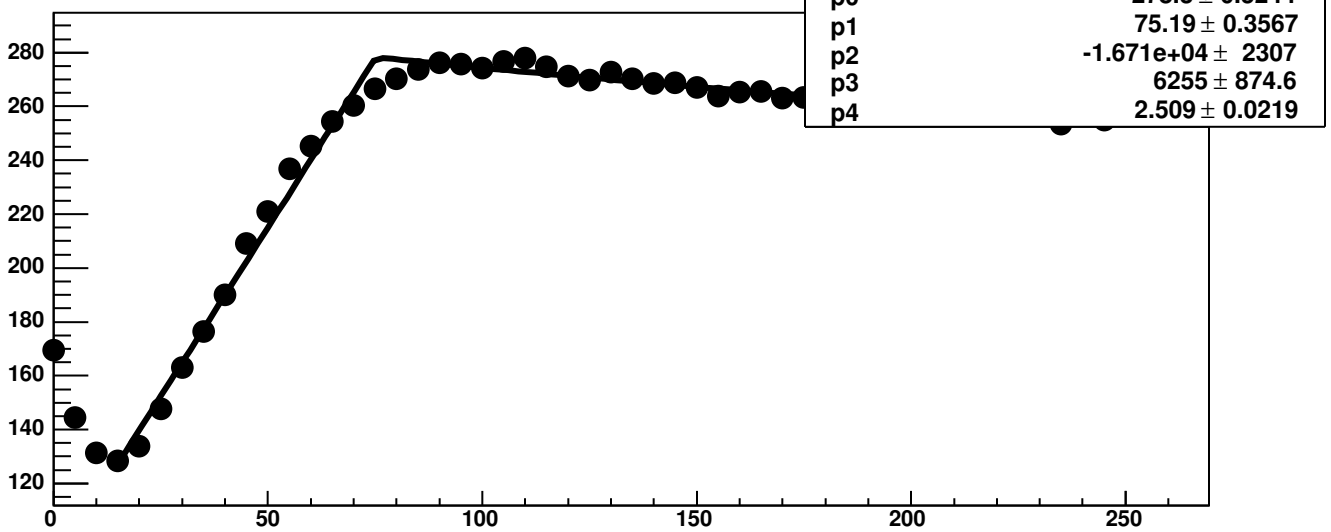



Chip 4, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.149e+04 / 41
p0 284.8 ± 0.1969
p1 229.6 ± 9.376
p2 -23.78 ± 0.4783
p3 -2.476 ± 4.164
p4 0.16 ± 0.001137
300
280
260
240
220
200
180
160
140
0
50
100
150
200
250

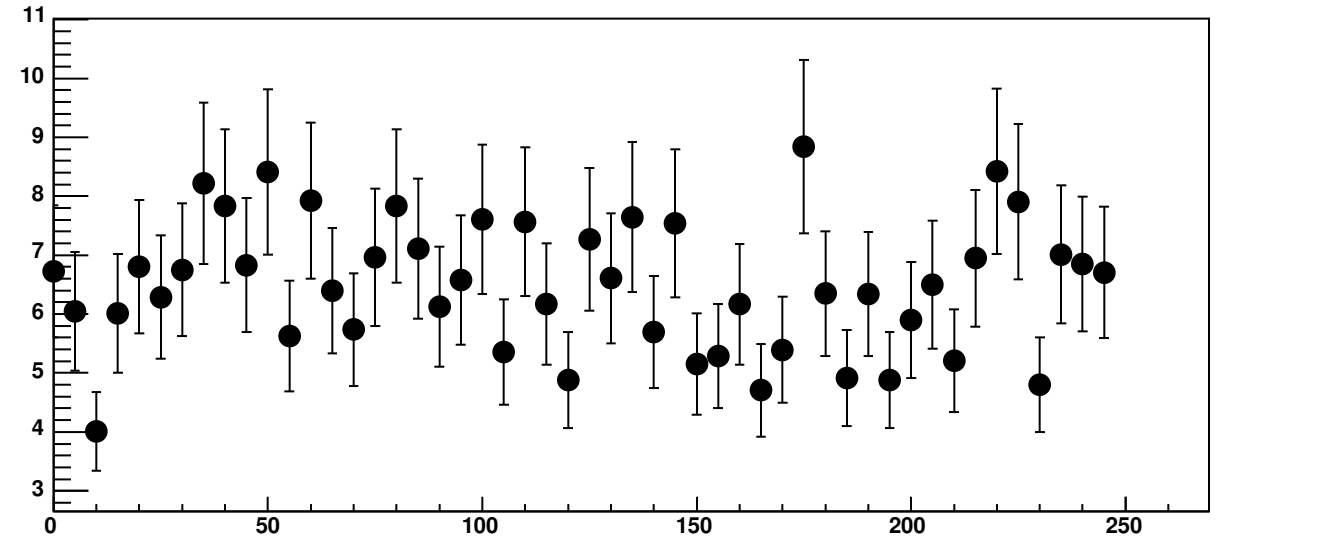

Chip 4, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 4, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
-100
0
50
100
150
200
250

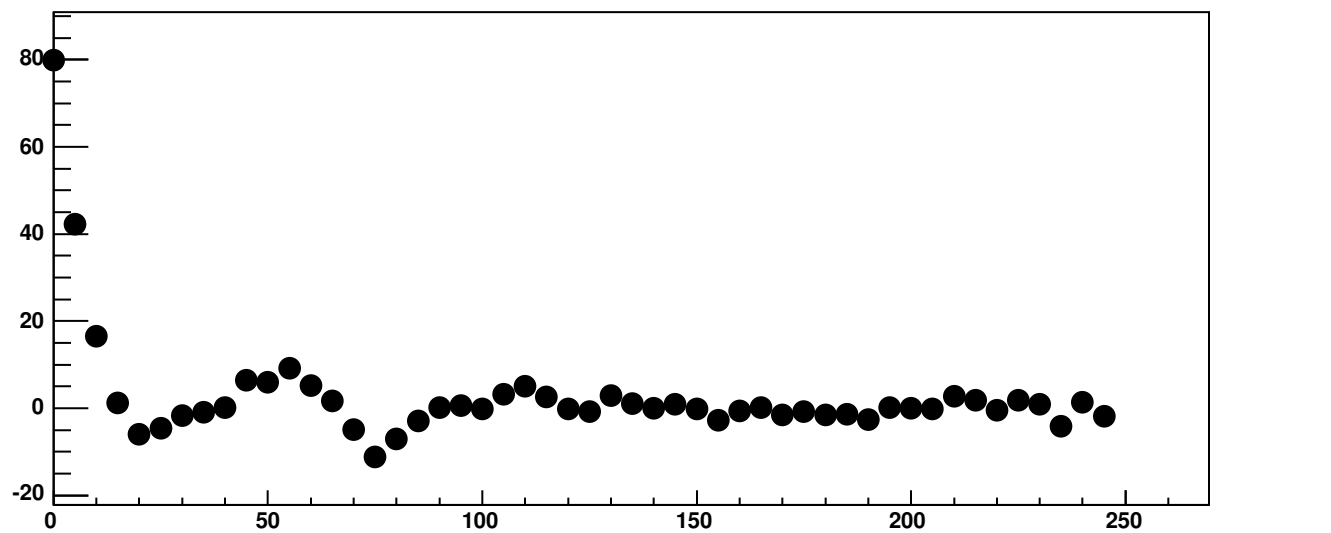
Chip 4, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold



Chip 4, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold

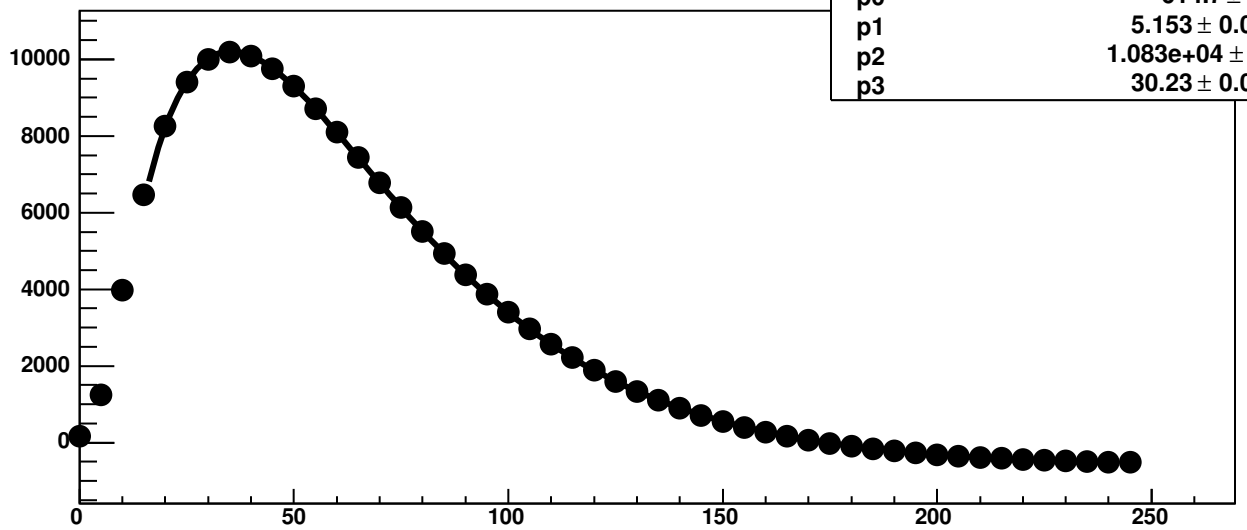


Chip 4, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold

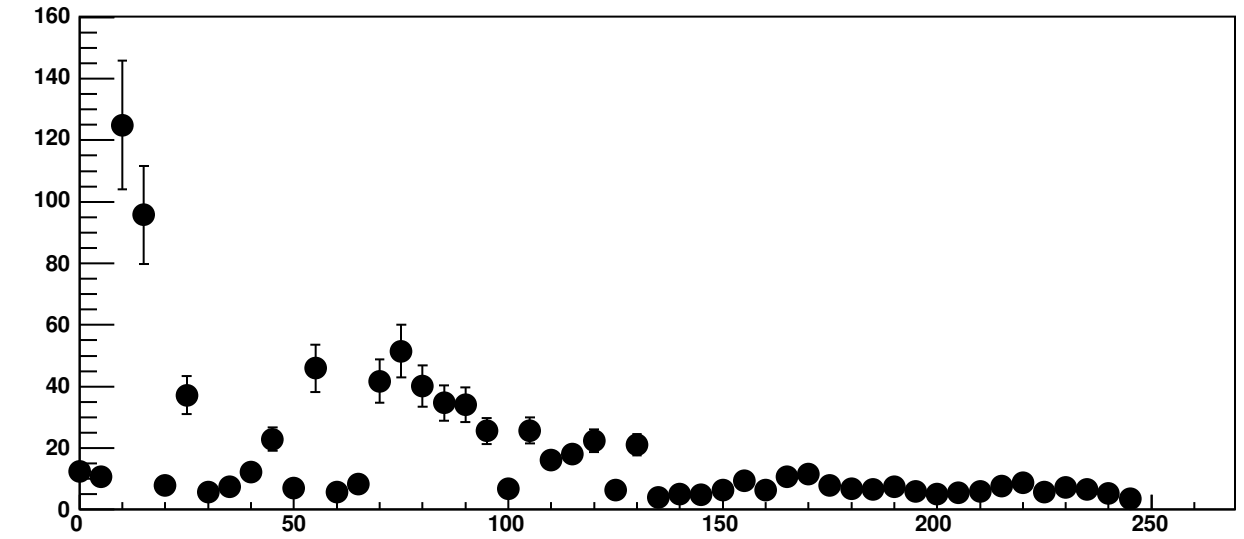


Chip 4, Channel 10, Enable 3!, DAC=4000, ADC Mean vs Hold

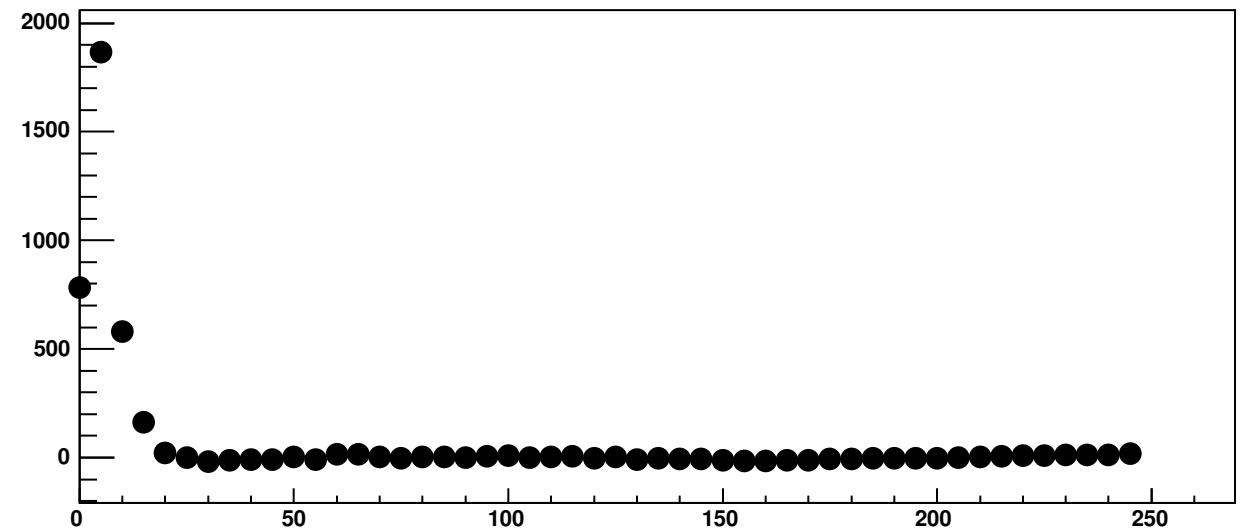
χ^2 / ndf	1364 / 42
p0	-614.7 ± 0.5062
p1	5.153 ± 0.007409
p2	1.083e+04 ± 0.8131
p3	30.23 ± 0.005058



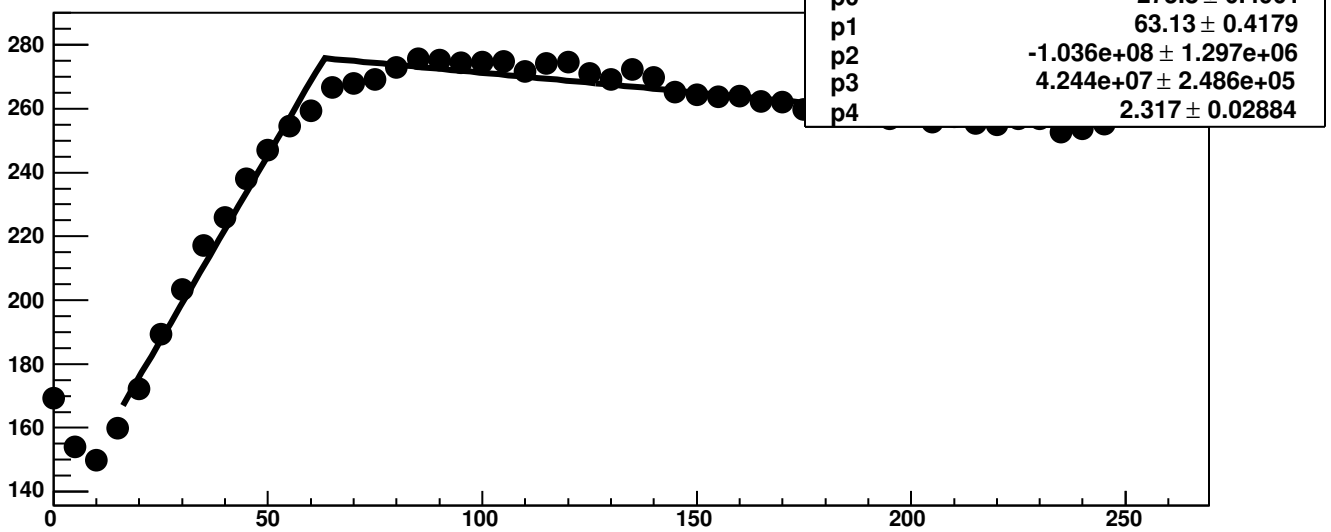
Chip 4, Channel 10, Enable 3!, DAC=4000, ADC Noise vs Hold



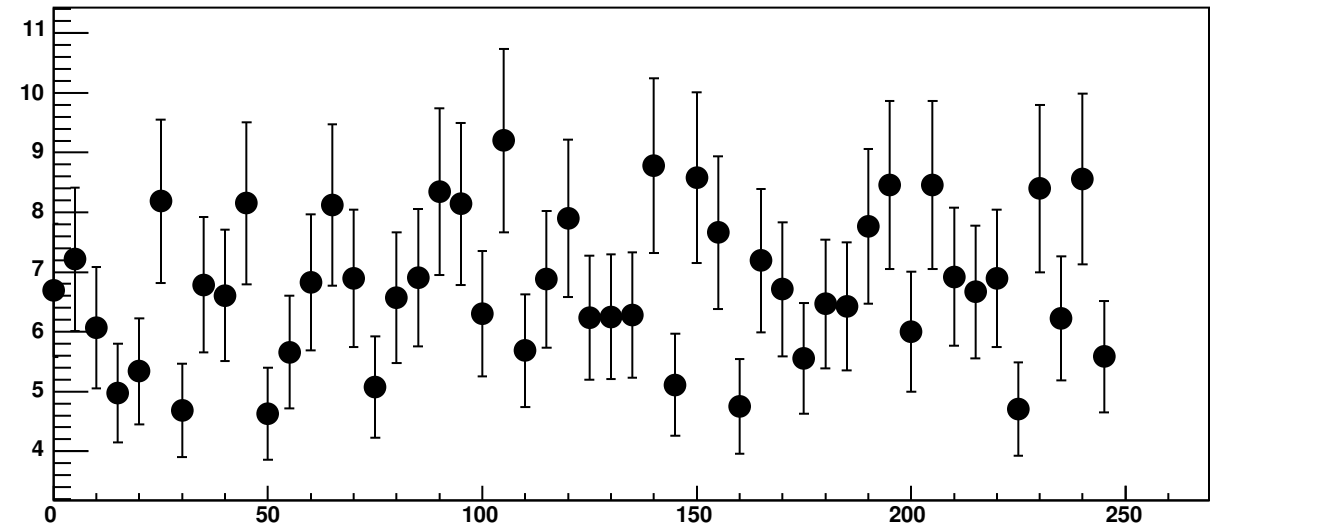
Chip 4, Channel 10, Enable 3!, DAC=4000, ADC Residuals vs Hold



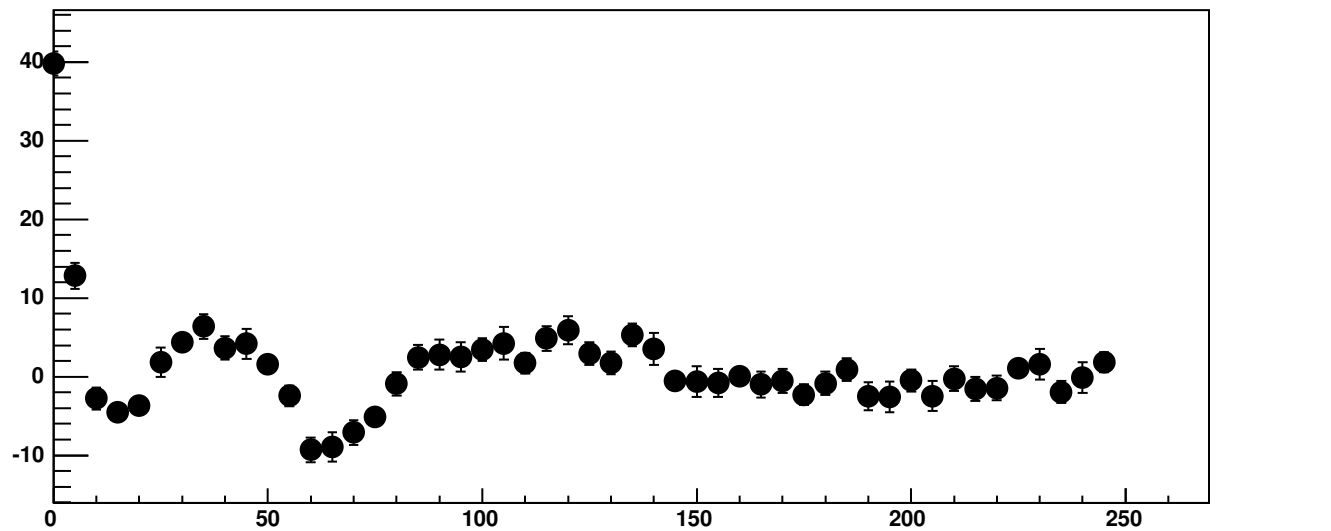
Chip 4, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold

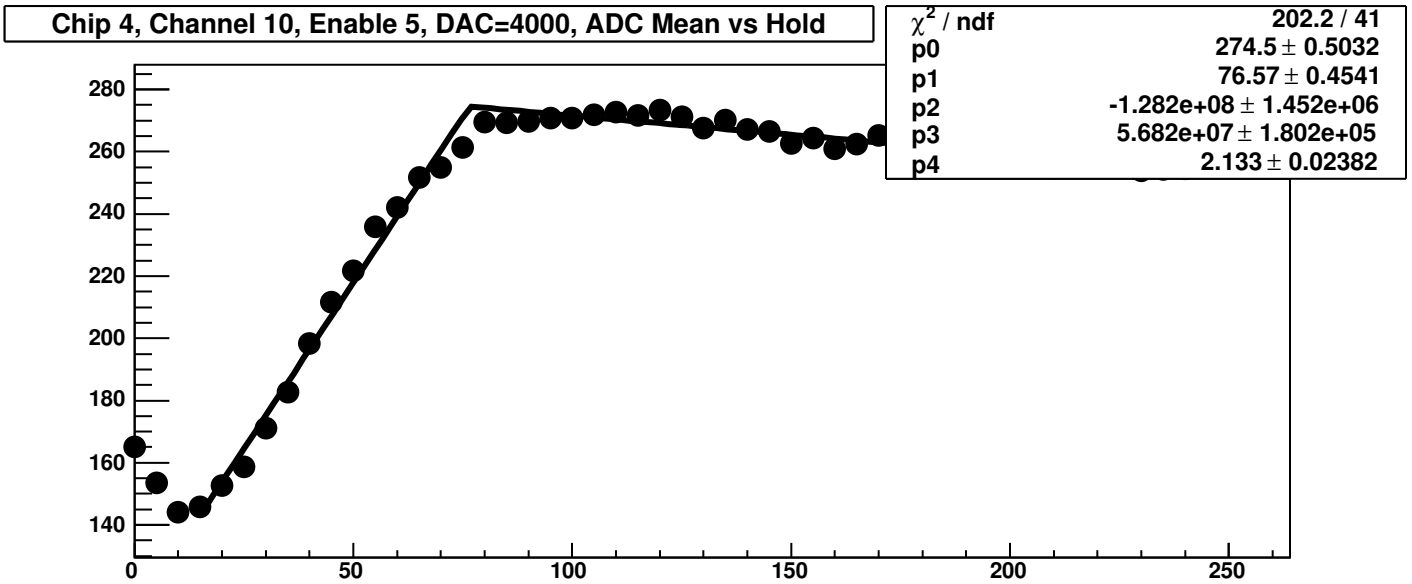


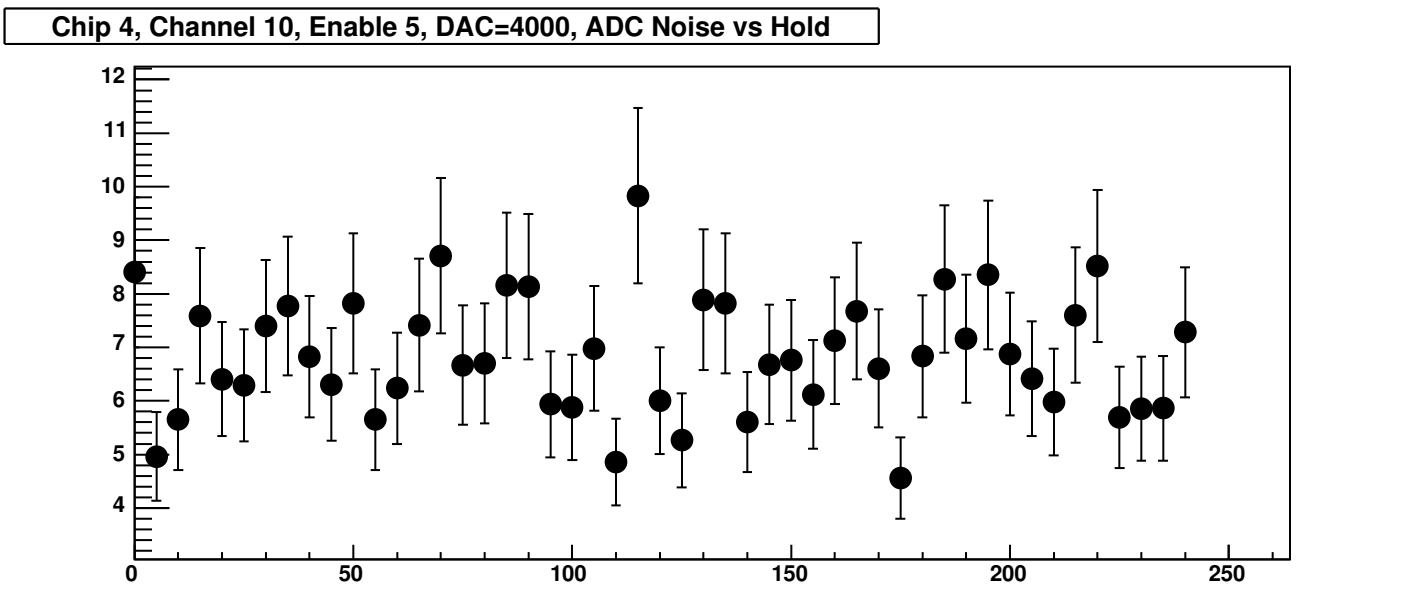
Chip 4, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold

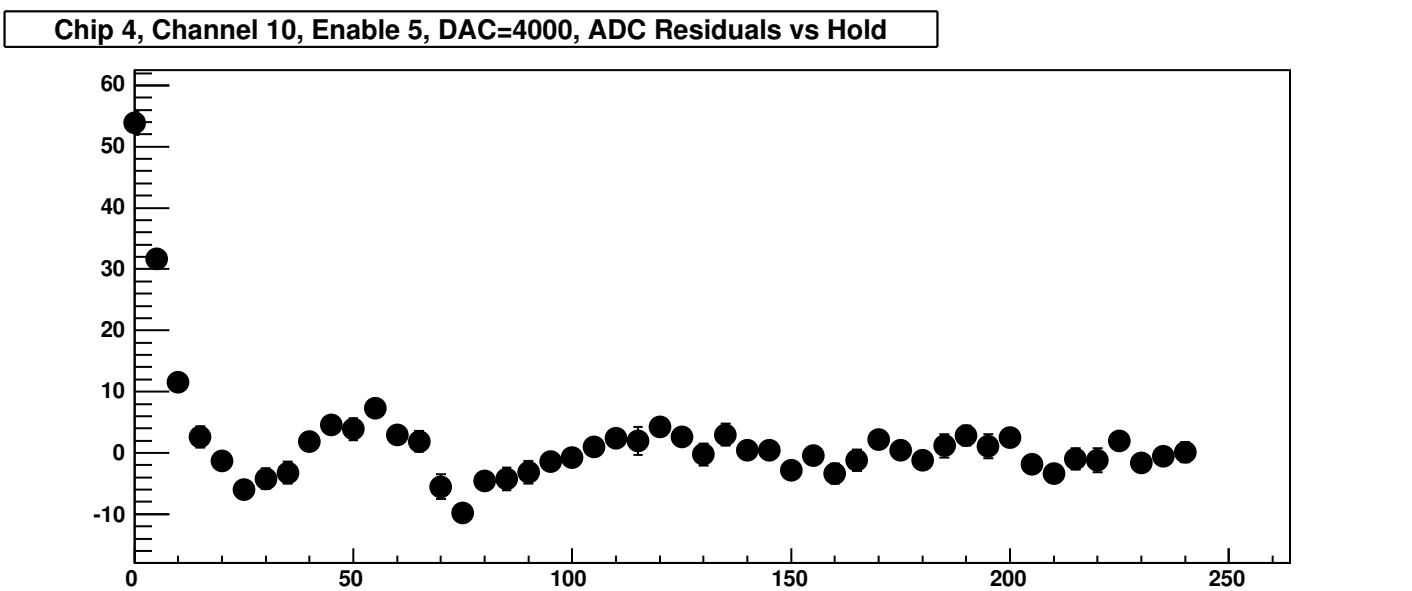


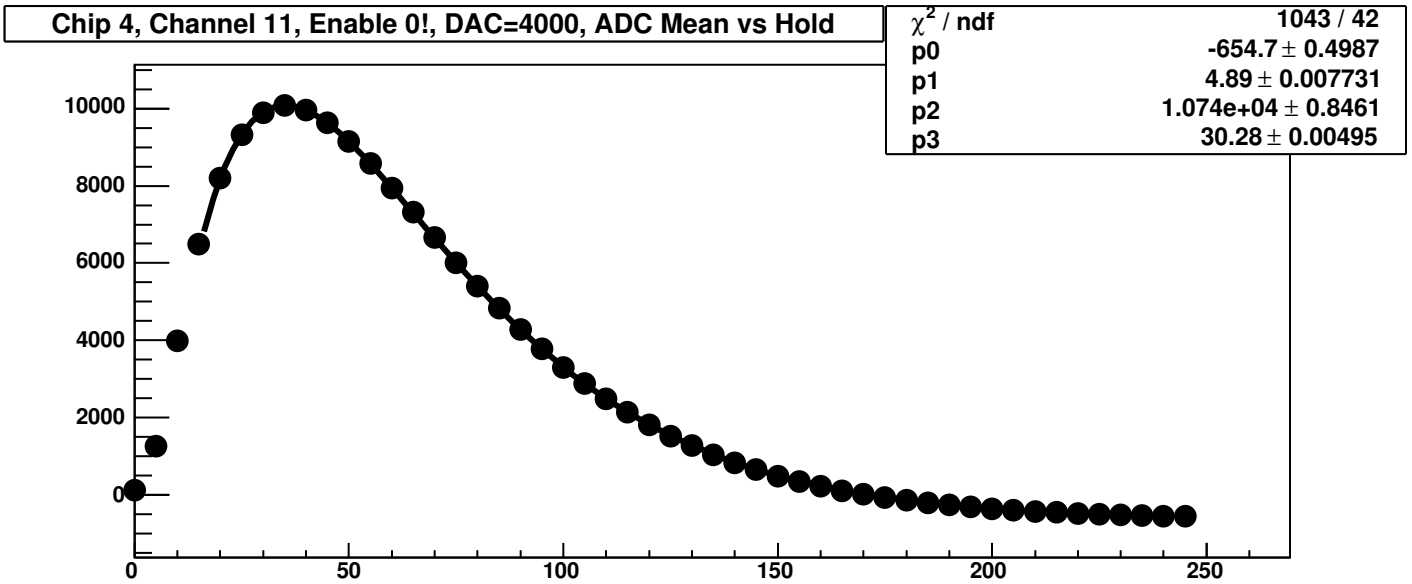
Chip 4, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold

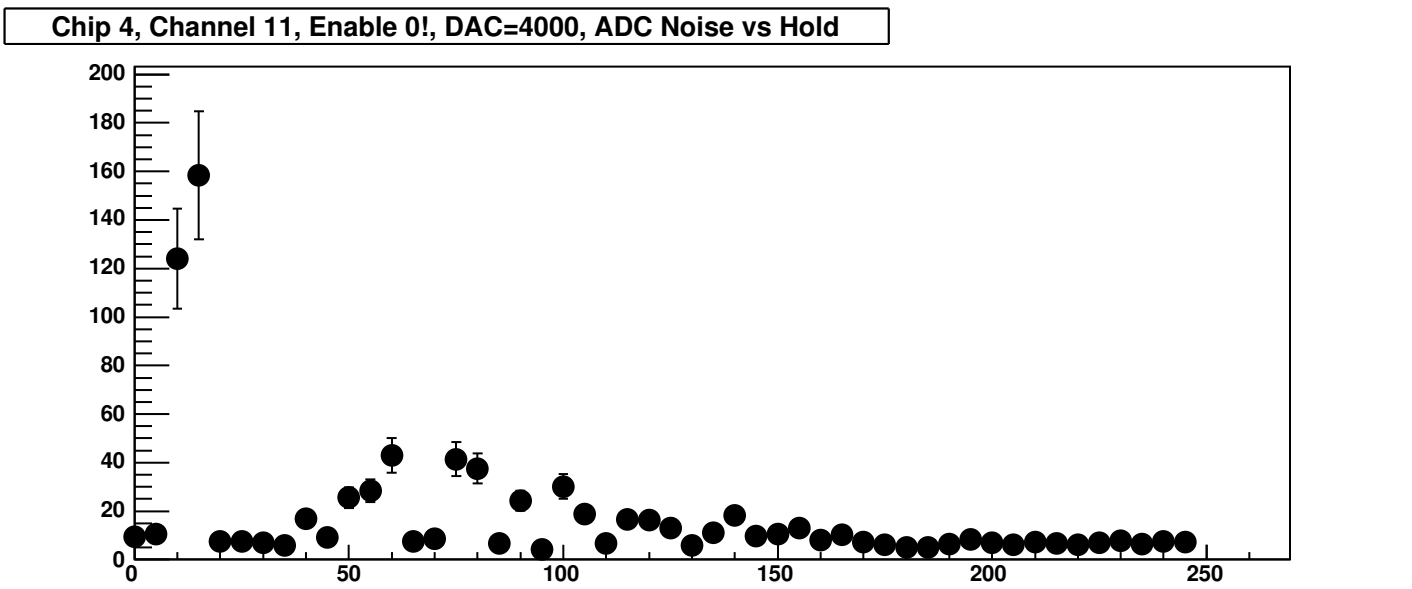


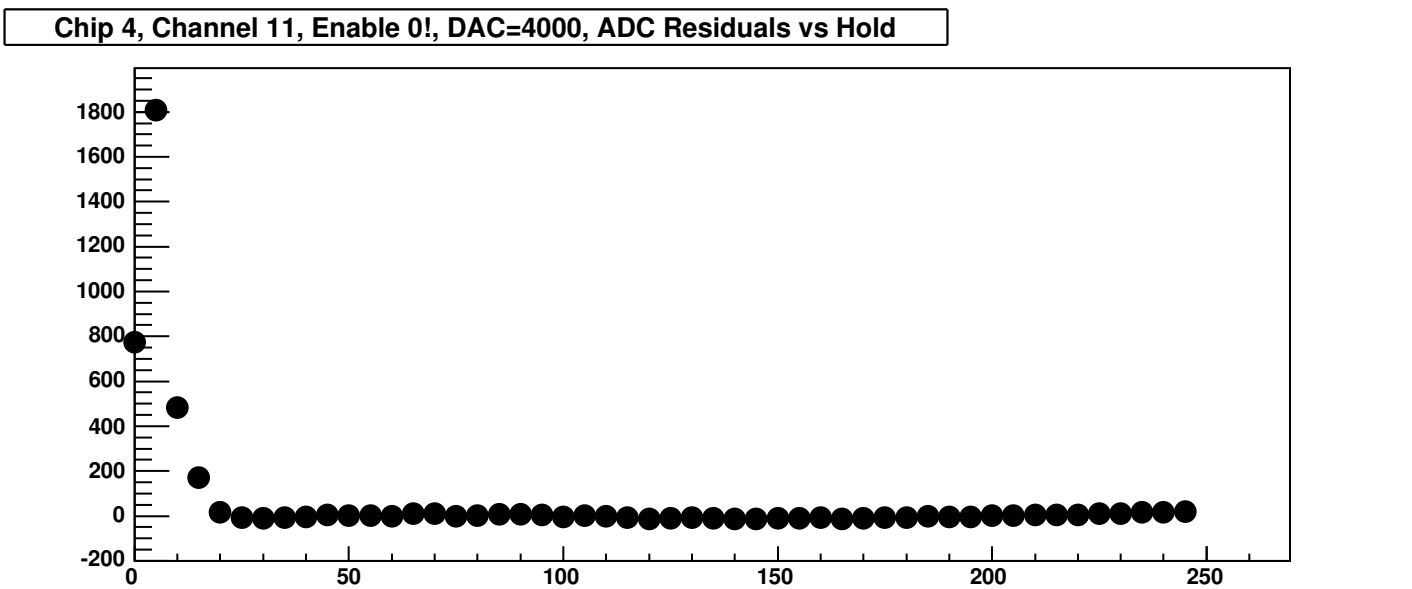

Chip 4, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 202.2 / 41
p0 274.5 ± 0.5032
p1 76.57 ± 0.4541
p2 -1.282e+08 ± 1.452e+06
p3 5.682e+07 ± 1.802e+05
p4 2.133 ± 0.02382
280
260
240
220
200
180
160
140
0
50
100
150
200
250


Chip 4, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

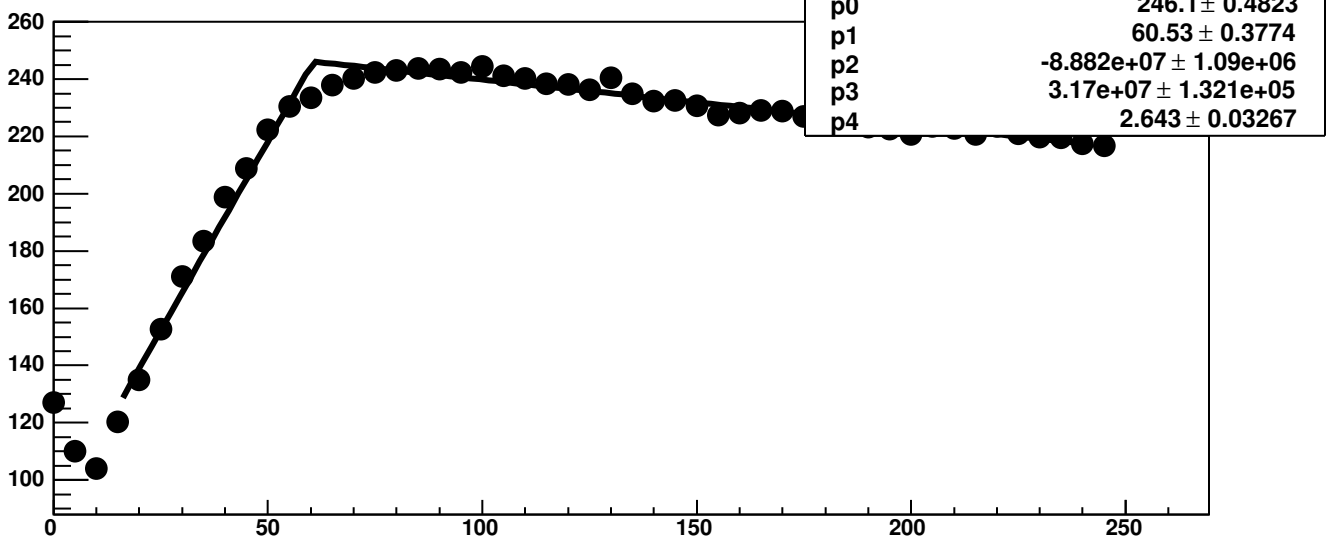

Chip 4, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold
60
50
40
30
20
10
0
-10
0
50
100
150
200
250


Chip 4, Channel 11, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1043 / 42
p0 -654.7 ± 0.4987
p1 4.89 ± 0.007731
p2 1.074e+04 ± 0.8461
p3 30.28 ± 0.00495
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

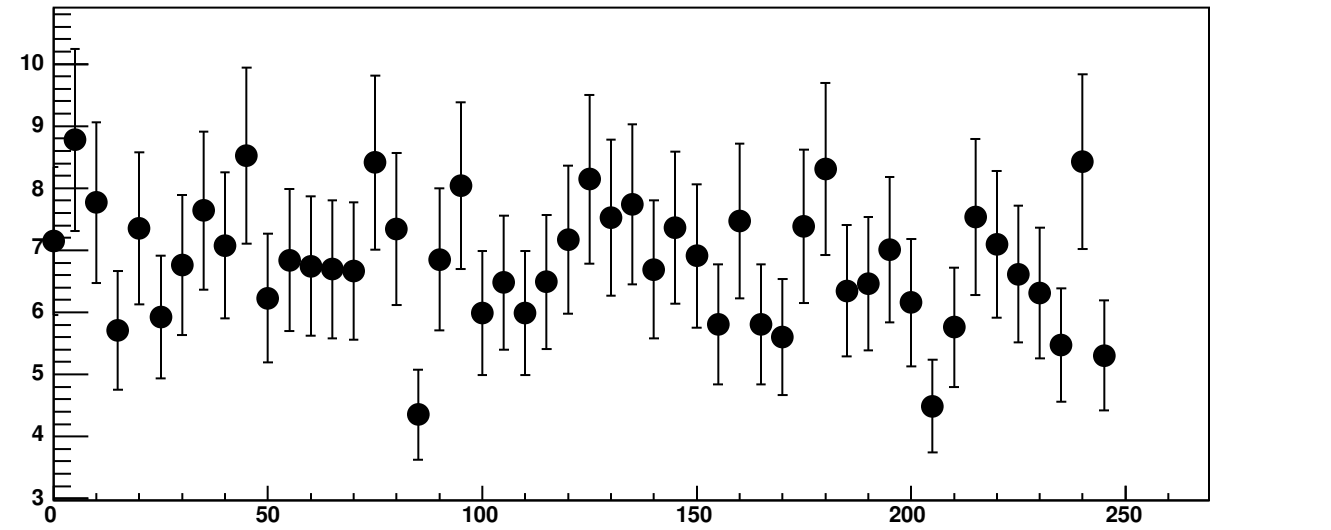

Chip 4, Channel 11, Enable 0!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 4, Channel 11, Enable 0!, DAC=4000, ADC Residuals vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

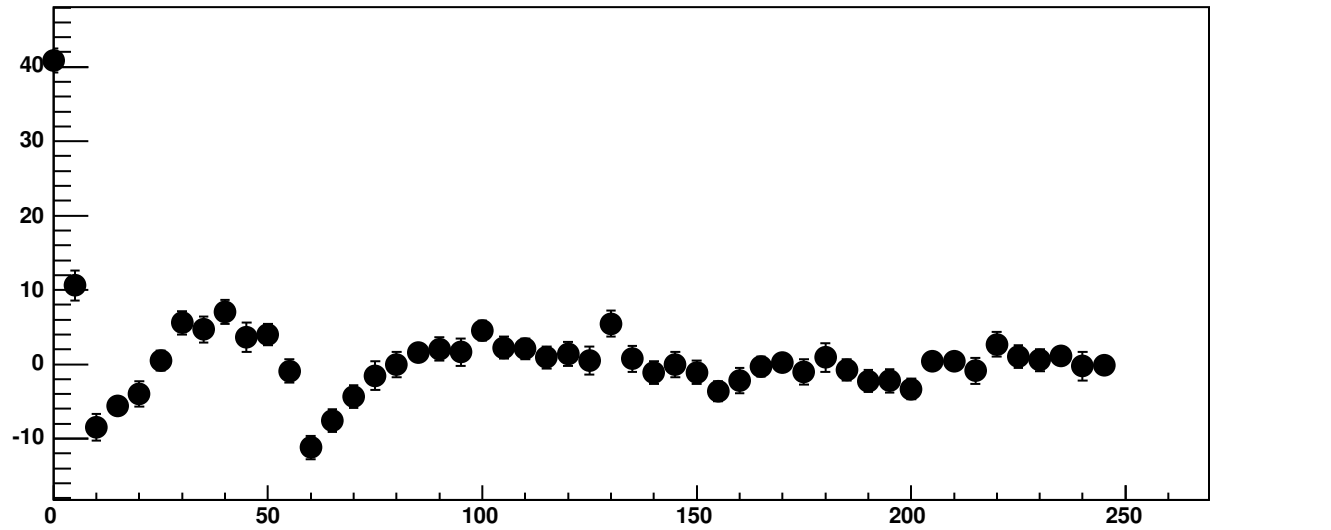
Chip 4, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold



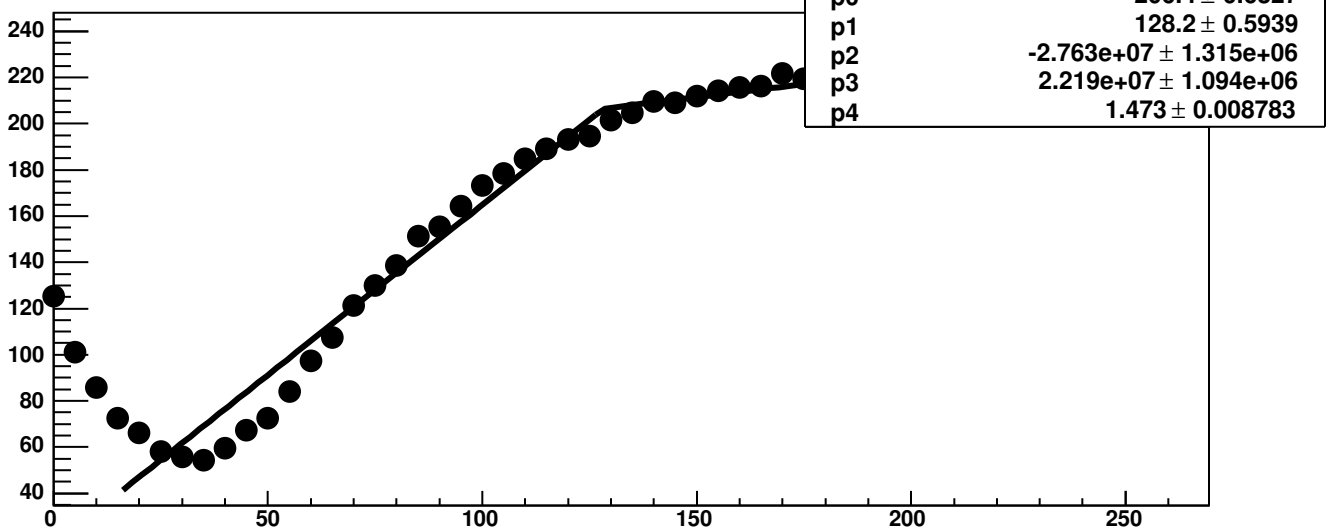
Chip 4, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



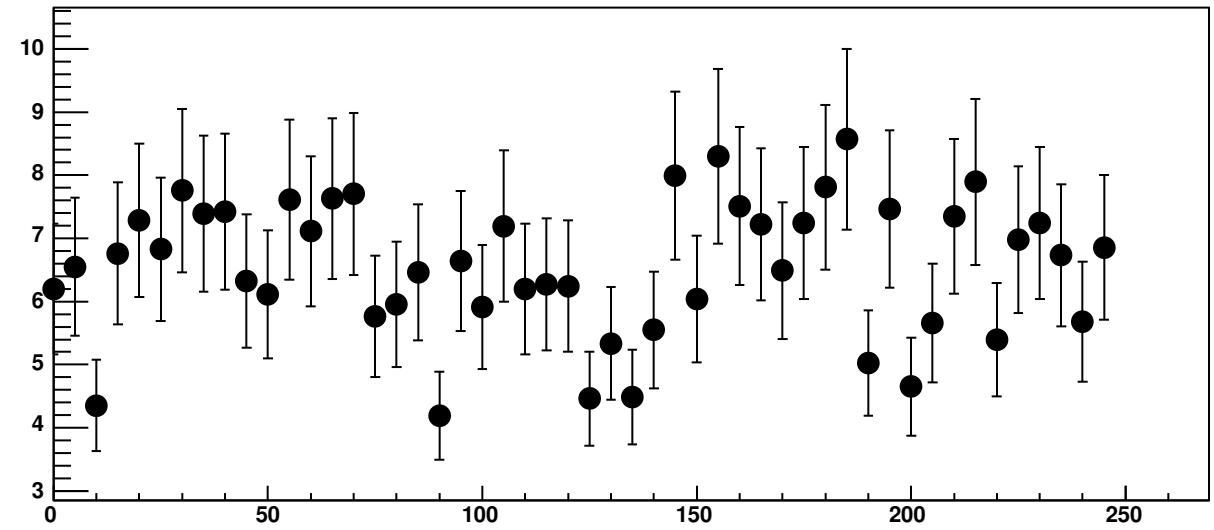
Chip 4, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold



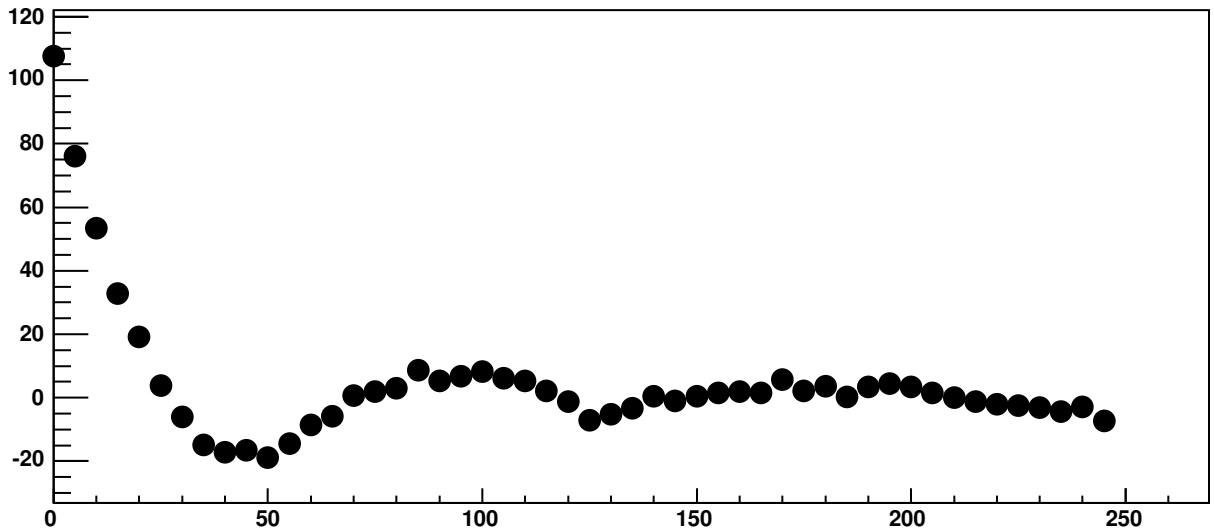
Chip 4, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

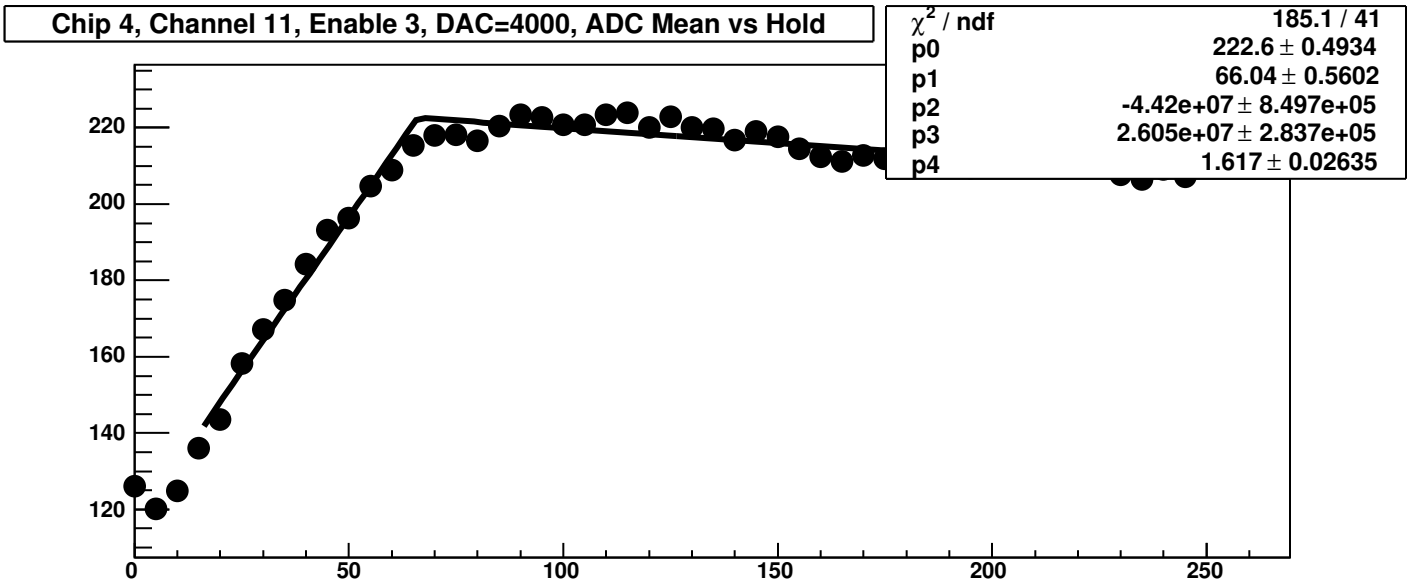


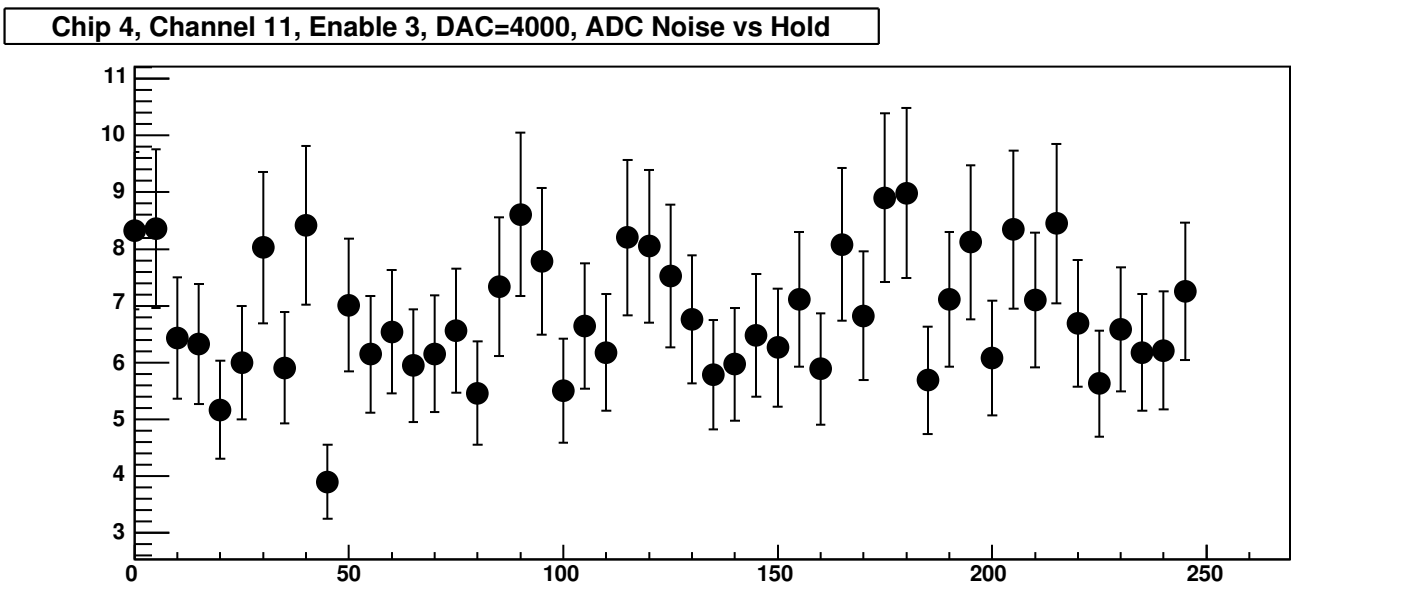
Chip 4, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold

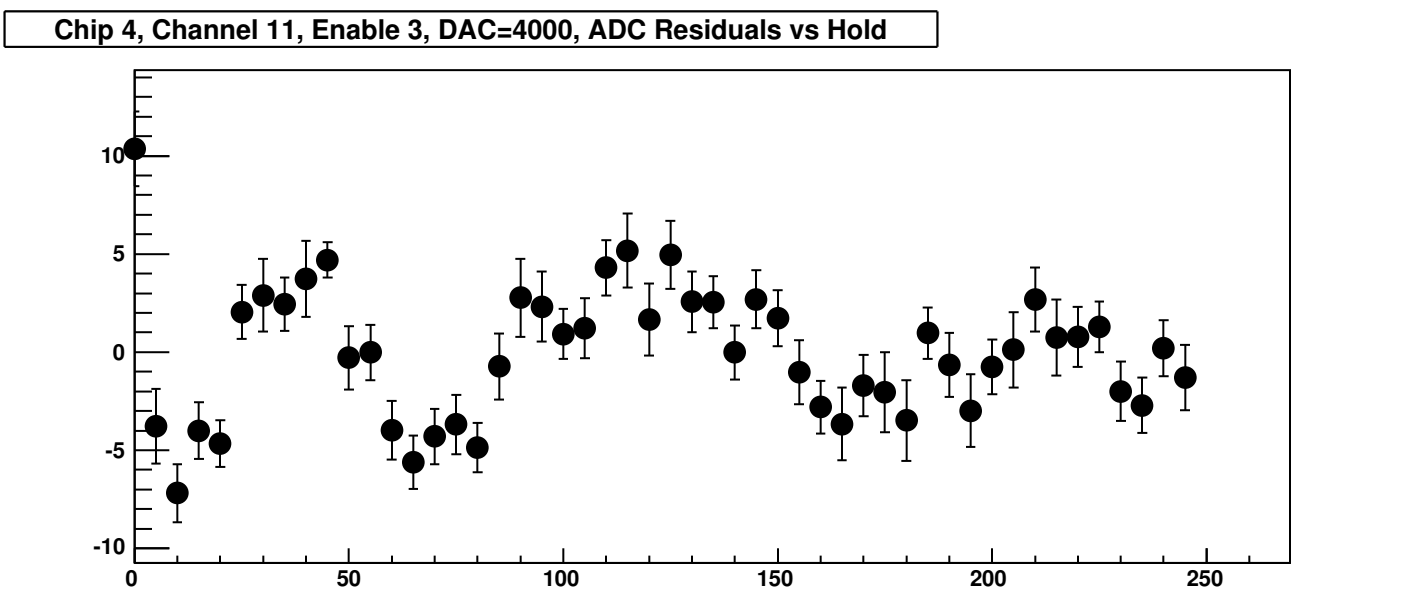


Chip 4, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

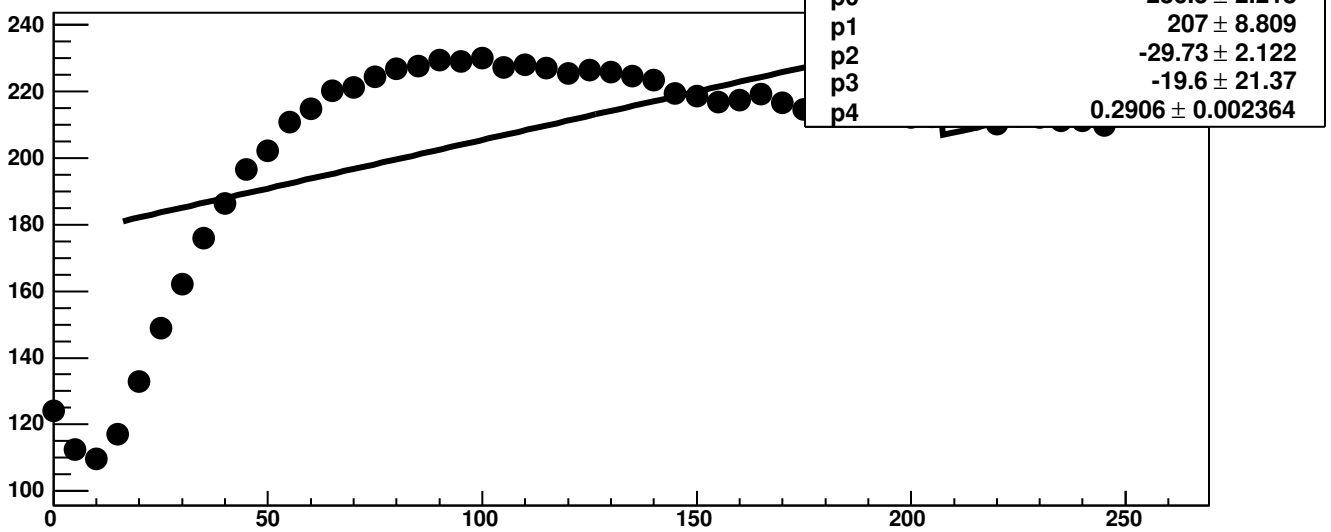



Chip 4, Channel 11, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 185.1 / 41
p0 222.6 ± 0.4934
p1 66.04 ± 0.5602
p2 -4.42e+07 ± 8.497e+05
p3 2.605e+07 ± 2.837e+05
p4 1.617 ± 0.02635
220
200
180
160
140
120
0
50
100
150
200
250

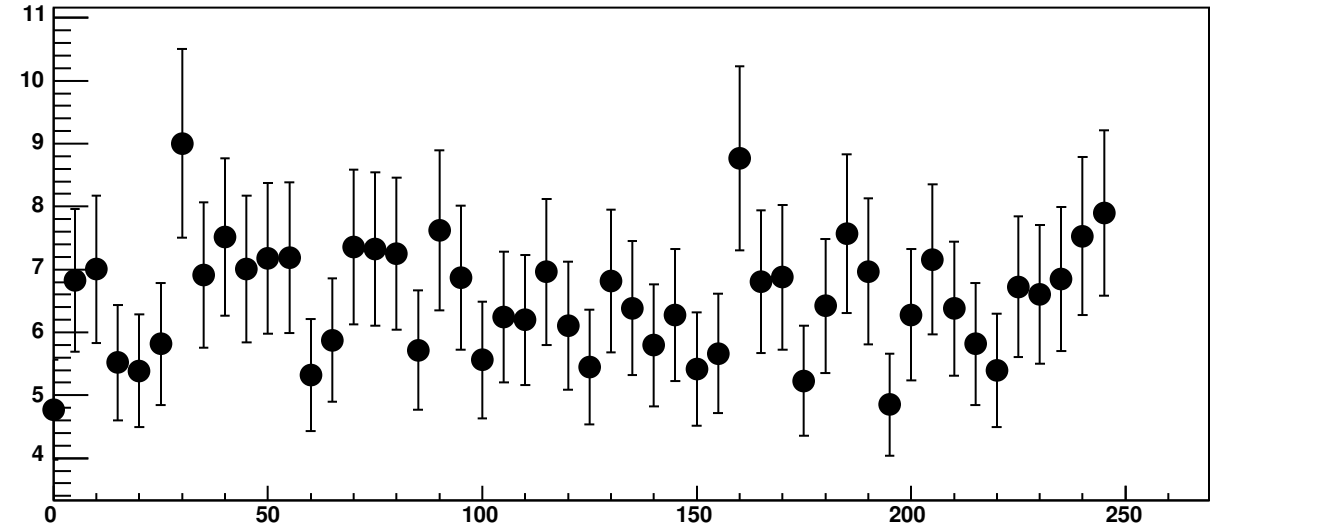

Chip 4, Channel 11, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 4, Channel 11, Enable 3, DAC=4000, ADC Residuals vs Hold
10
5
0
-5
-10
0
50
100
150
200
250

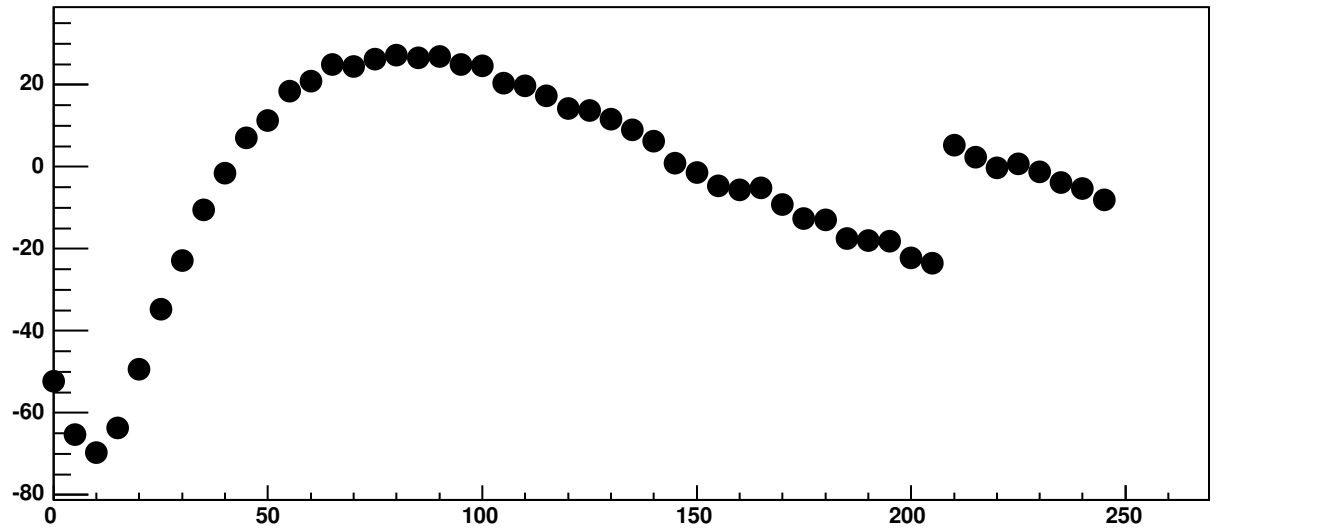
Chip 4, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

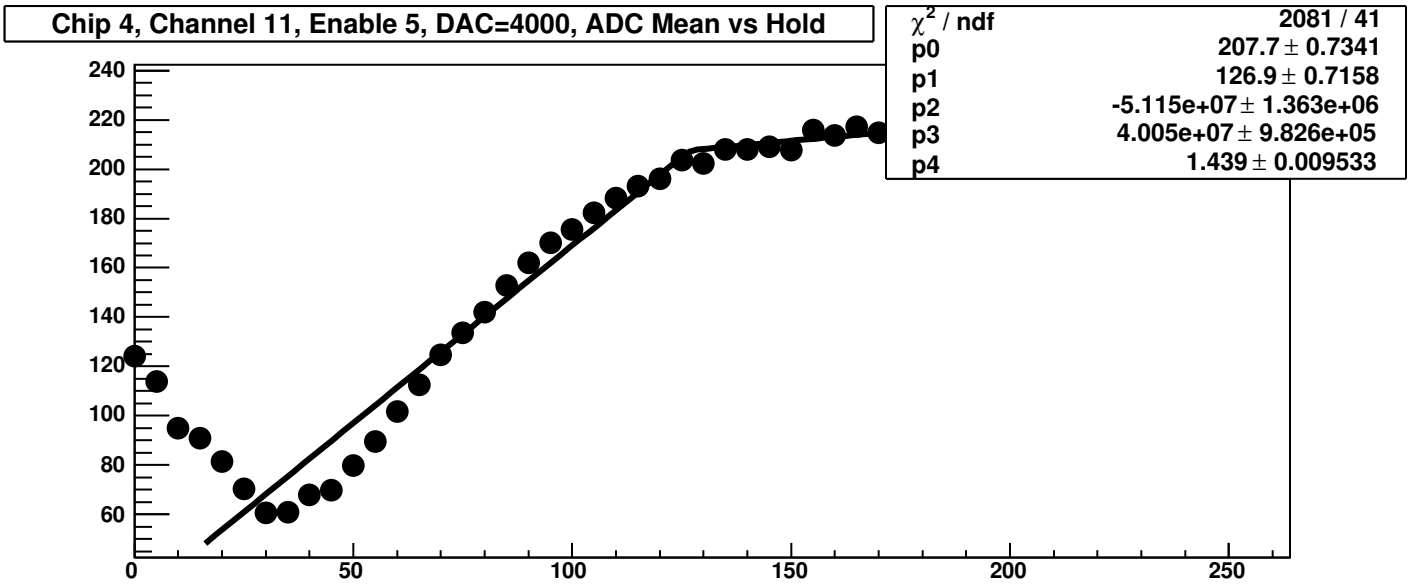


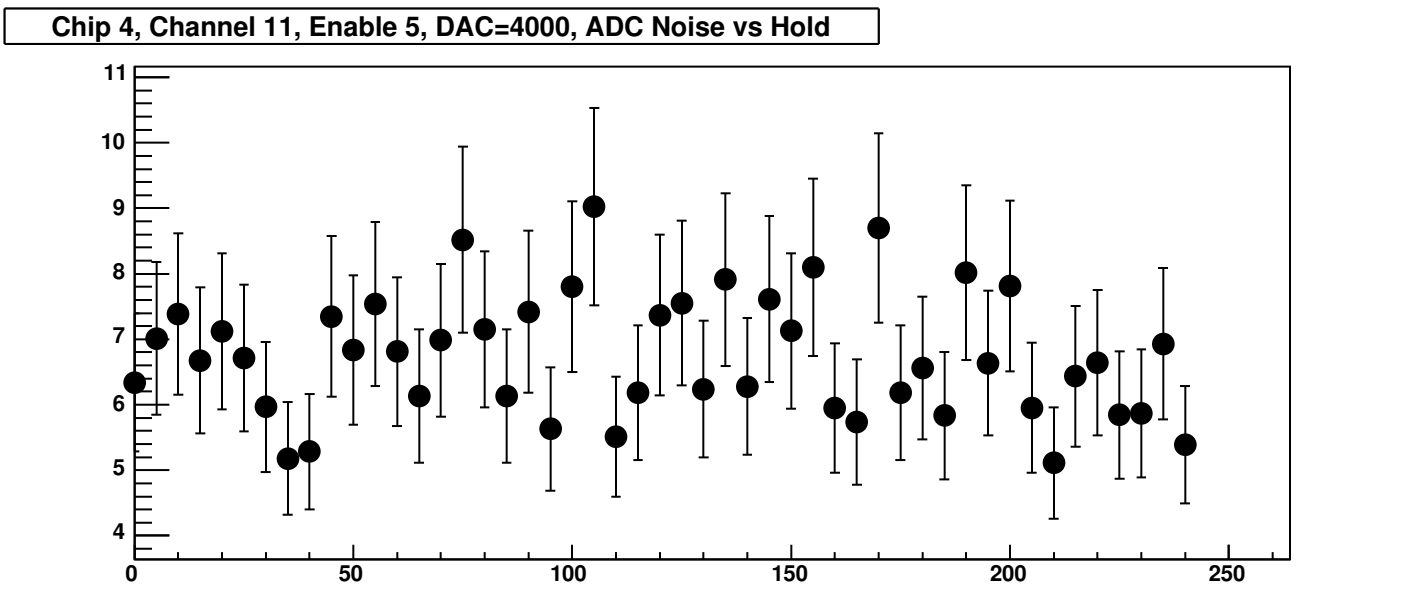
Chip 4, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold

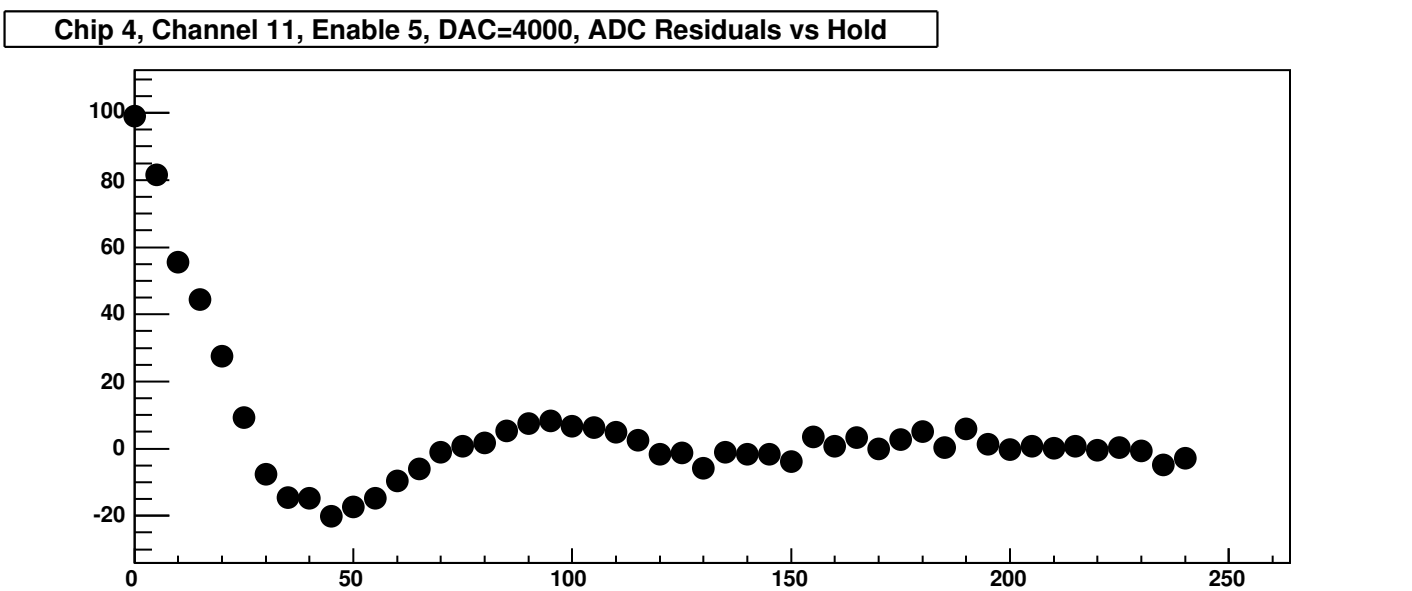


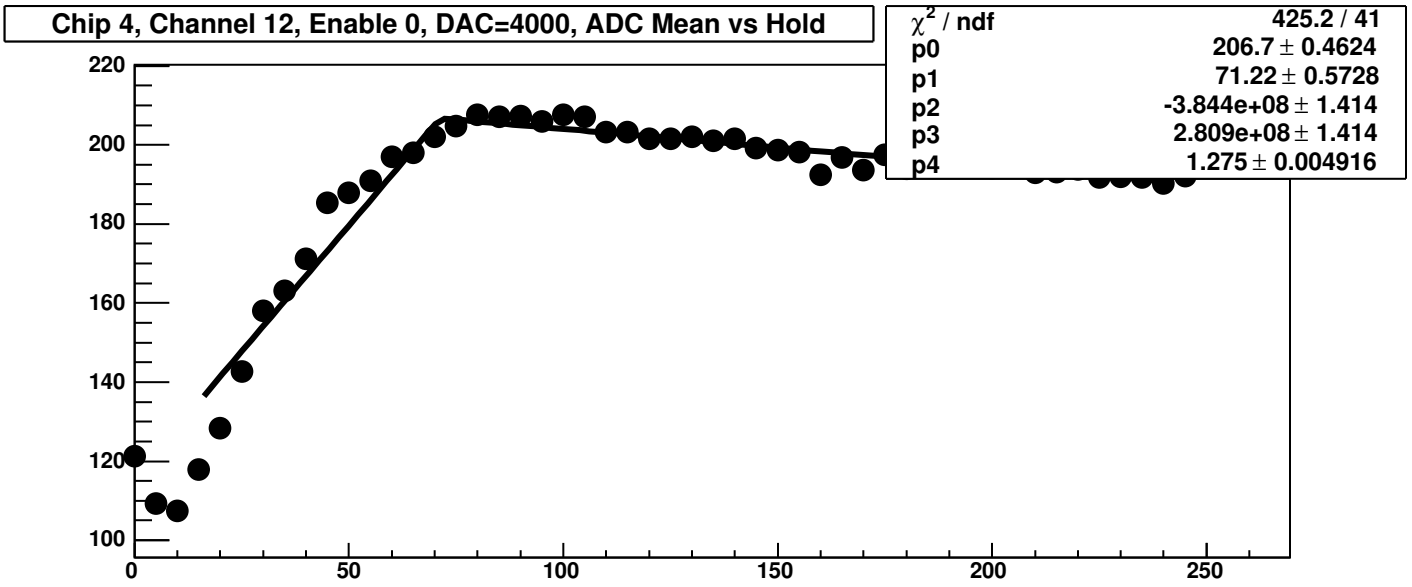
Chip 4, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

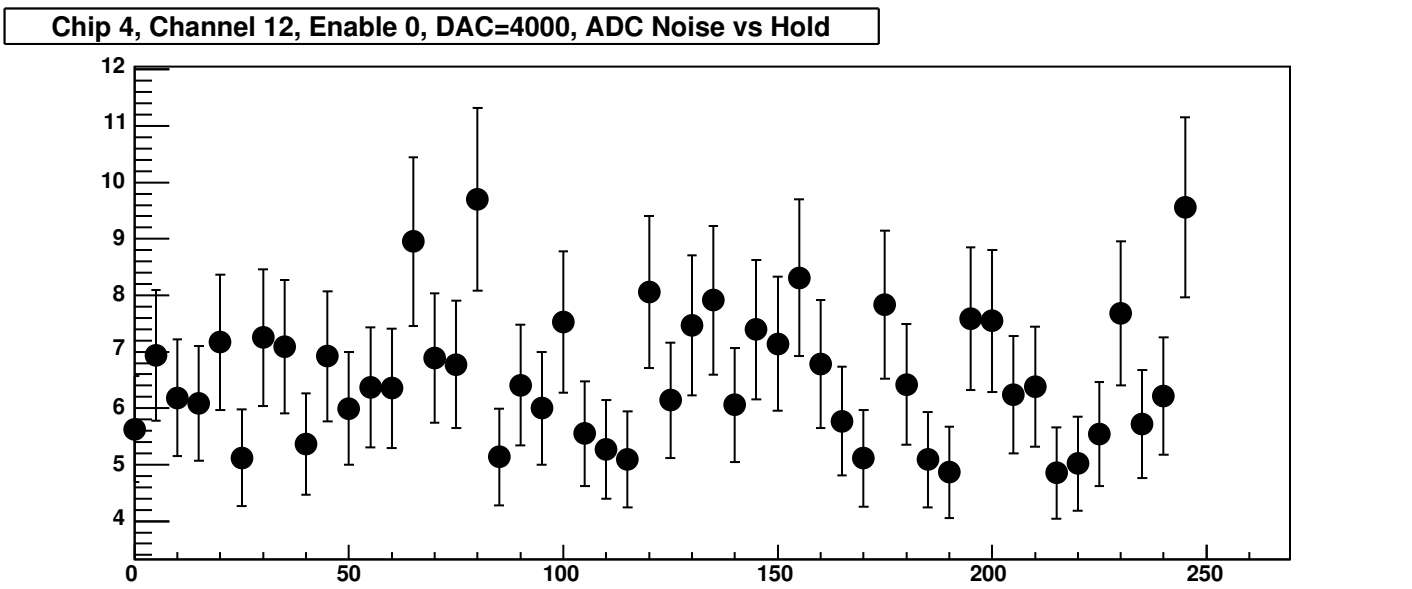


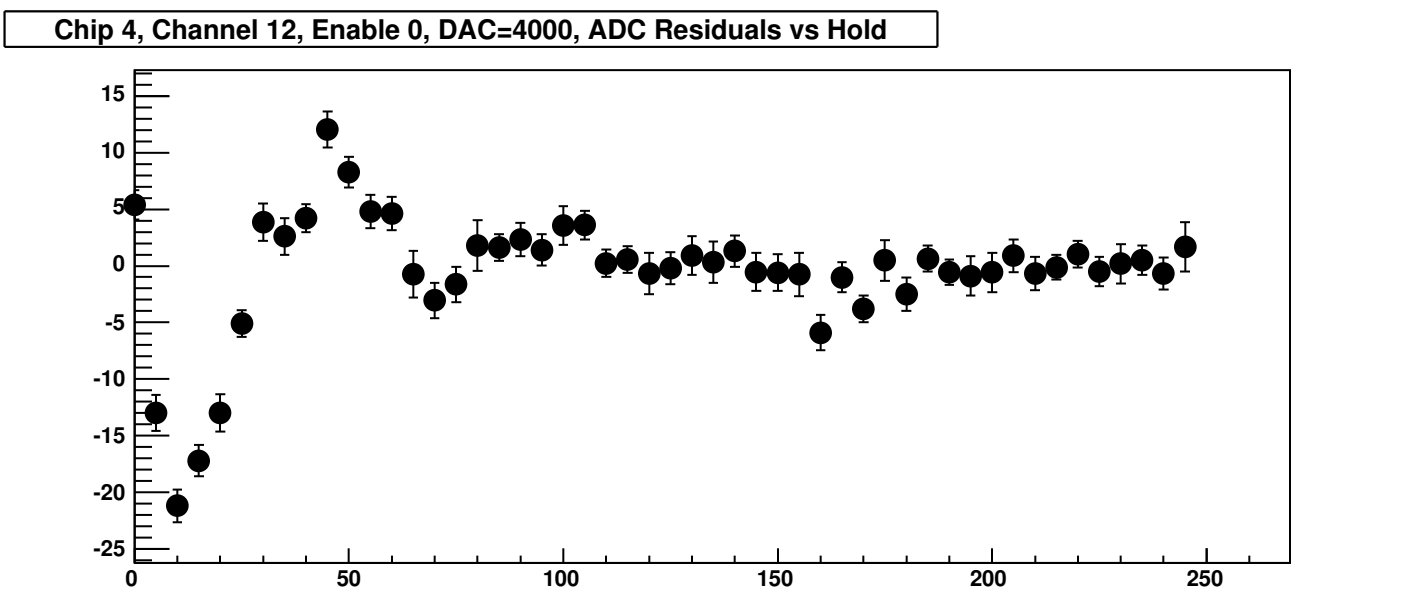

Chip 4, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 2081 / 41
p0 207.7 ± 0.7341
p1 126.9 ± 0.7158
p2 -5.115e+07 ± 1.363e+06
p3 4.005e+07 ± 9.826e+05
p4 1.439 ± 0.009533
240
220
200
180
160
140
120
100
80
60
0
50
100
150
200
250


Chip 4, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 4, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold
100
80
60
40
20
0
-20
0
50
100
150
200
250

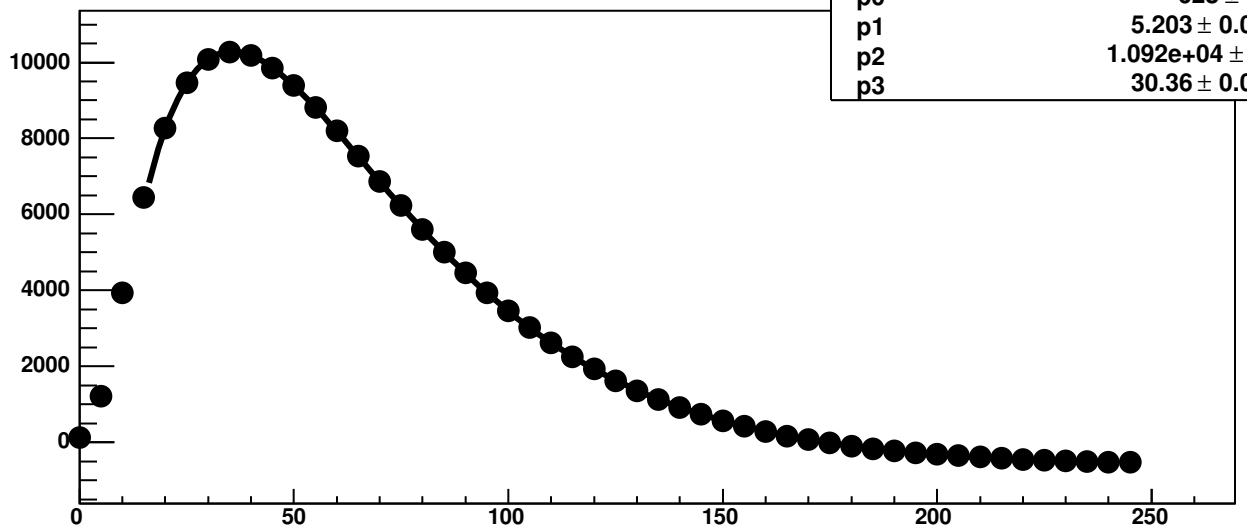

Chip 4, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 425.2 / 41
p0 206.7 ± 0.4624
p1 71.22 ± 0.5728
p2 -3.844e+08 ± 1.414
p3 2.809e+08 ± 1.414
p4 1.275 ± 0.004916
220
200
180
160
140
120
100
0
50
100
150
200
250


Chip 4, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250

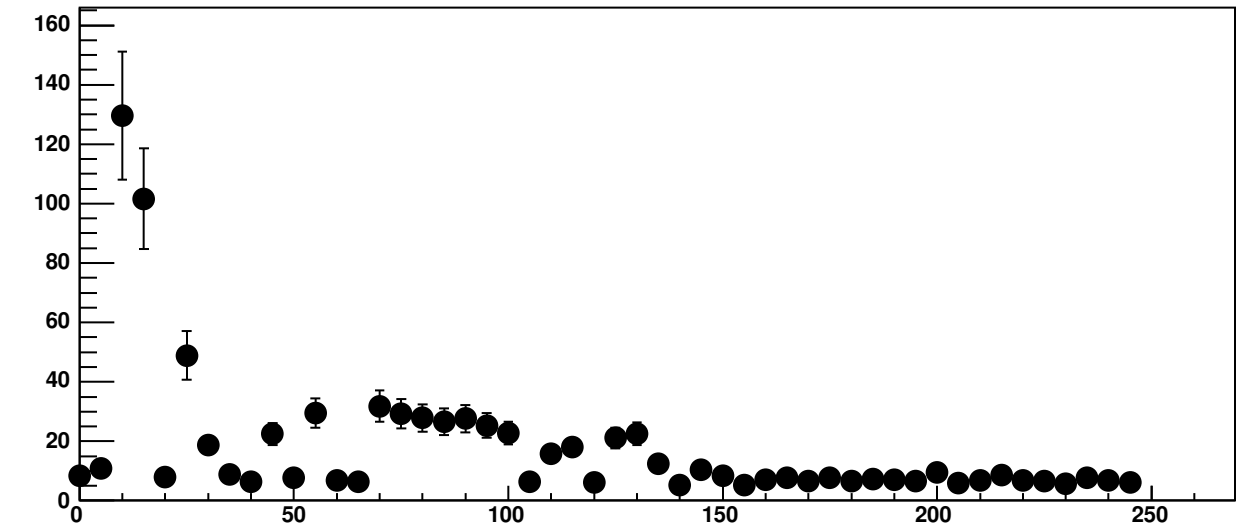

Chip 4, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
-15
-20
-25
0
50
100
150
200
250

Chip 4, Channel 12, Enable 1!, DAC=4000, ADC Mean vs Hold

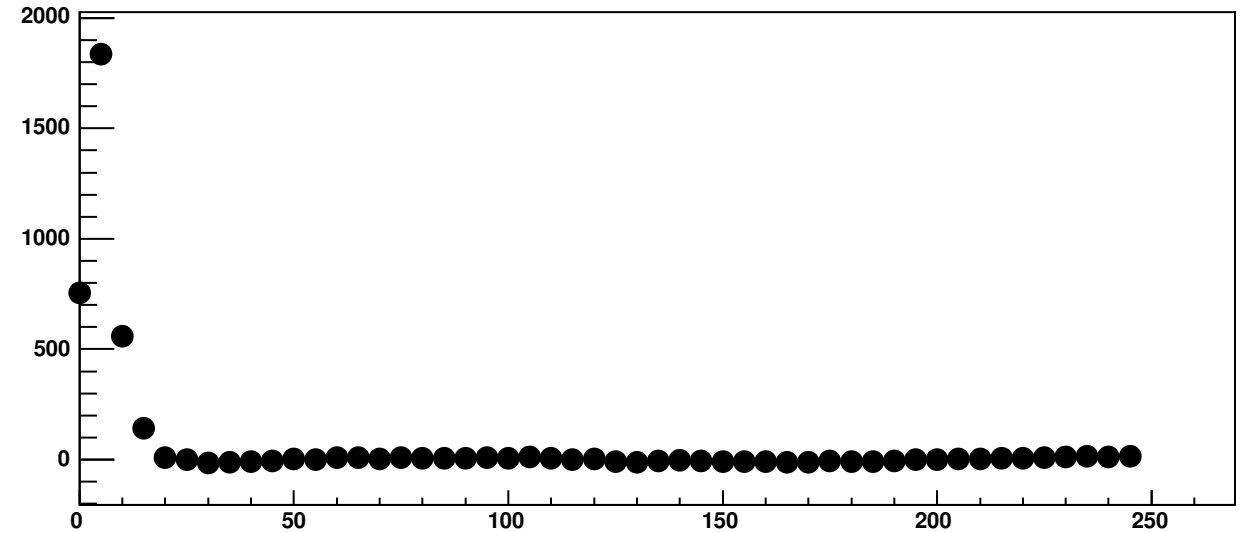
χ^2 / ndf	886.2 / 42
p0	-628 ± 0.5405
p1	5.203 ± 0.007394
p2	1.092e+04 ± 0.8881
p3	30.36 ± 0.005339



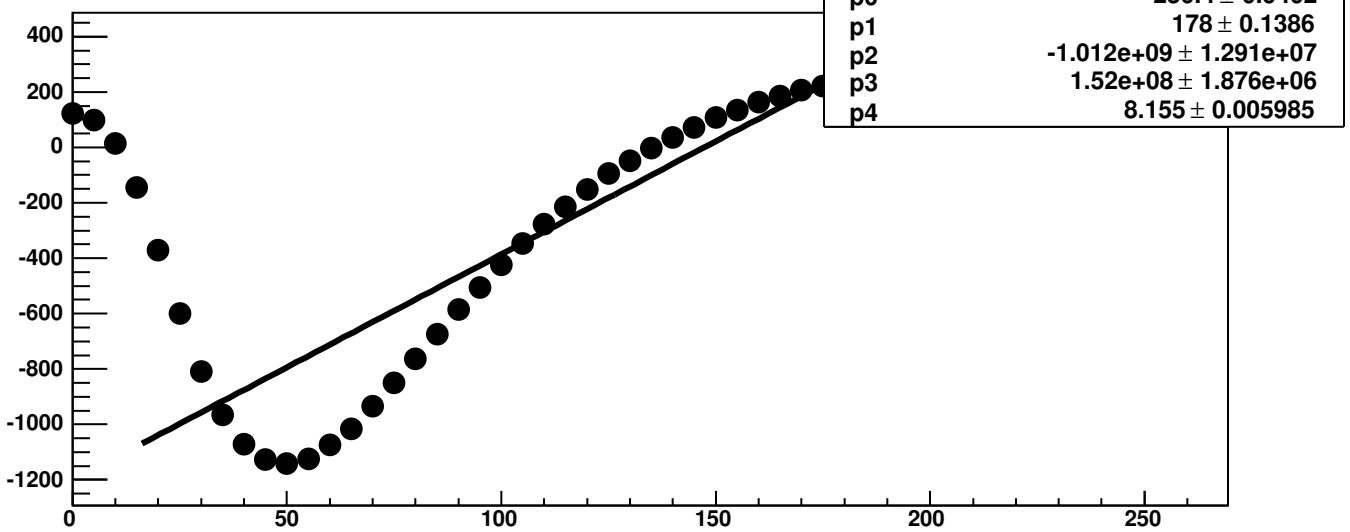
Chip 4, Channel 12, Enable 1!, DAC=4000, ADC Noise vs Hold



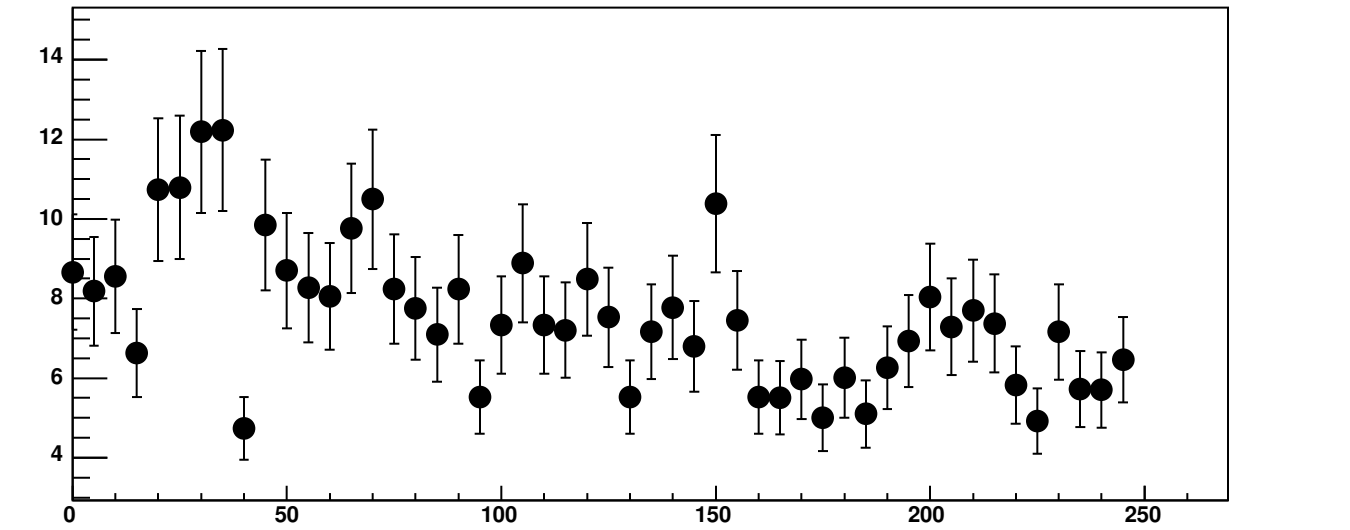
Chip 4, Channel 12, Enable 1!, DAC=4000, ADC Residuals vs Hold



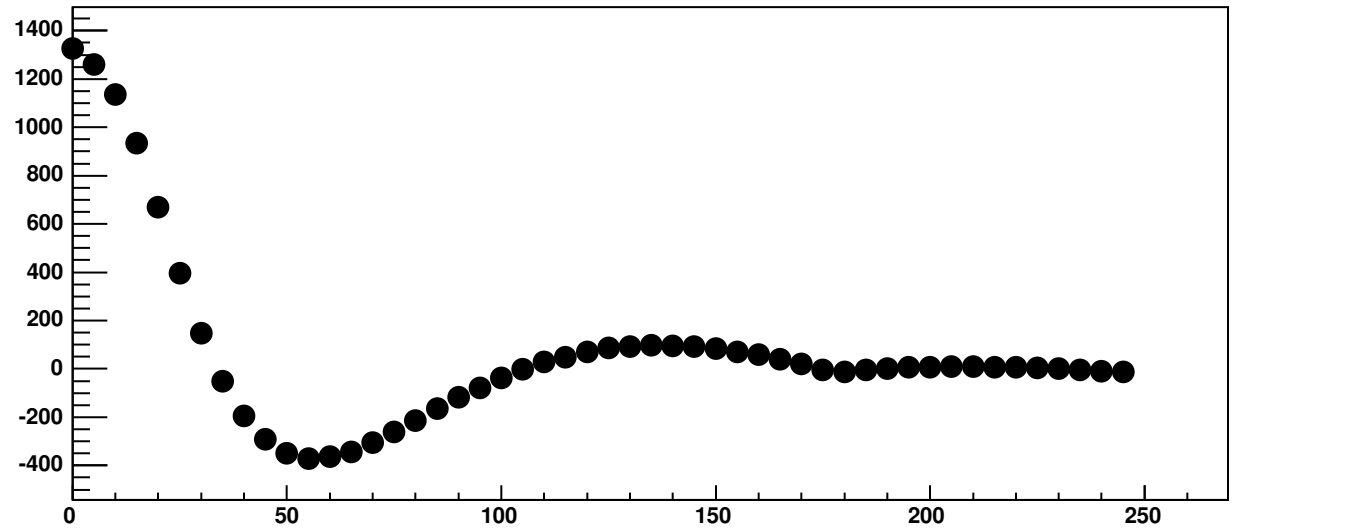
Chip 4, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold



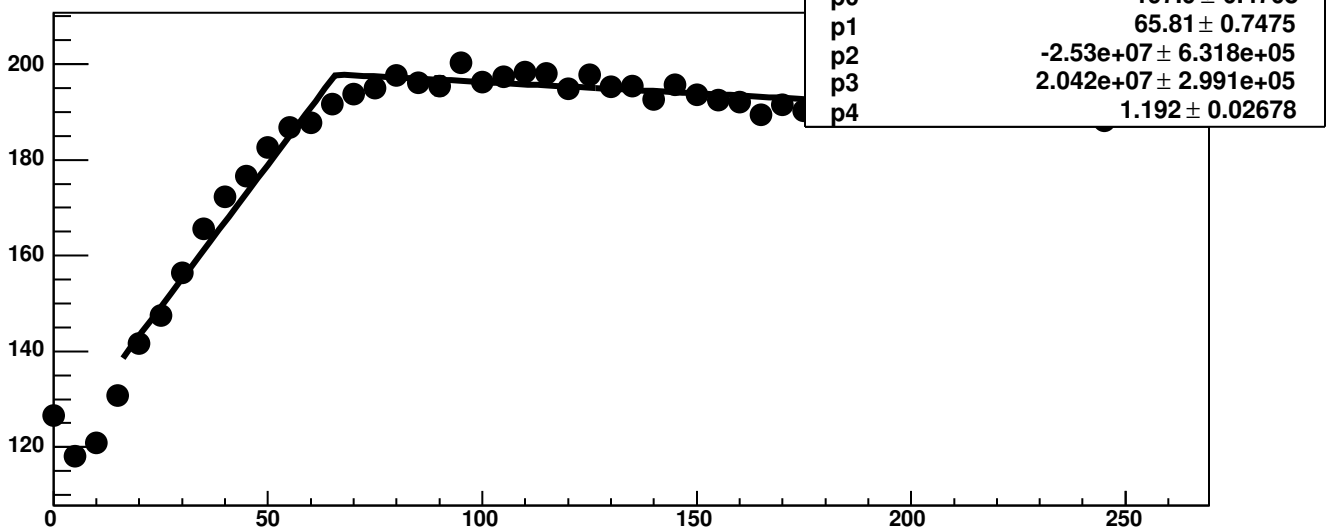
Chip 4, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



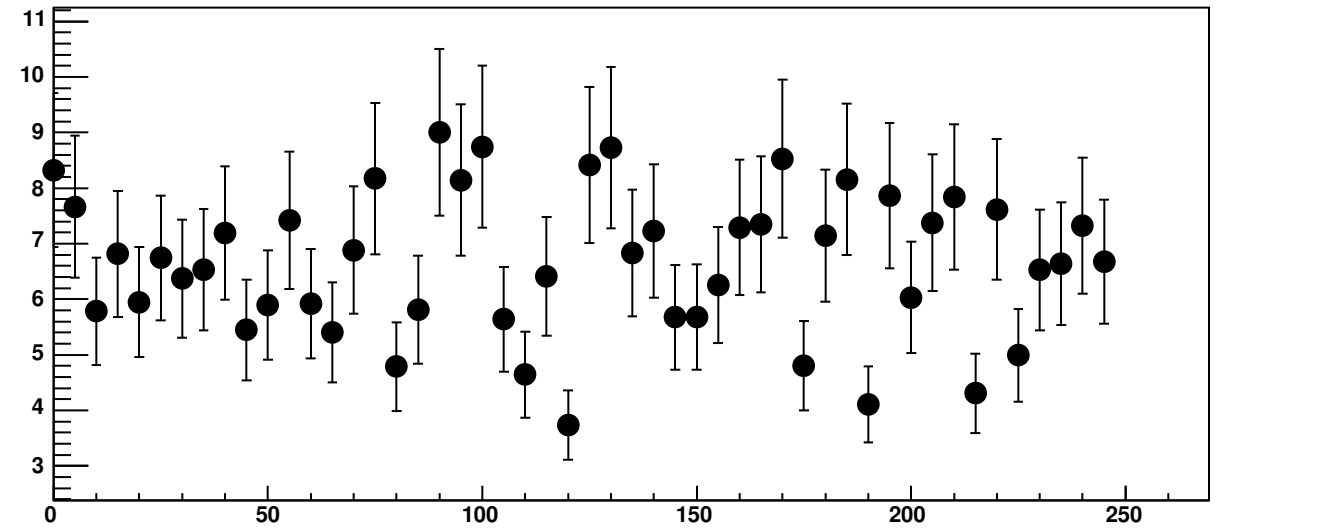
Chip 4, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold



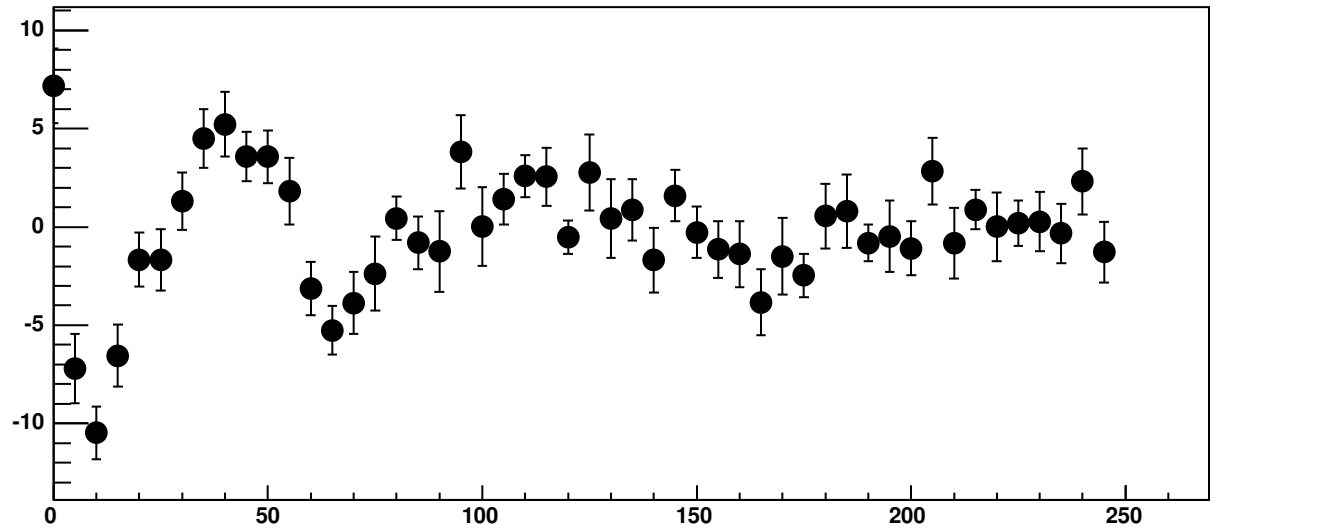
Chip 4, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold

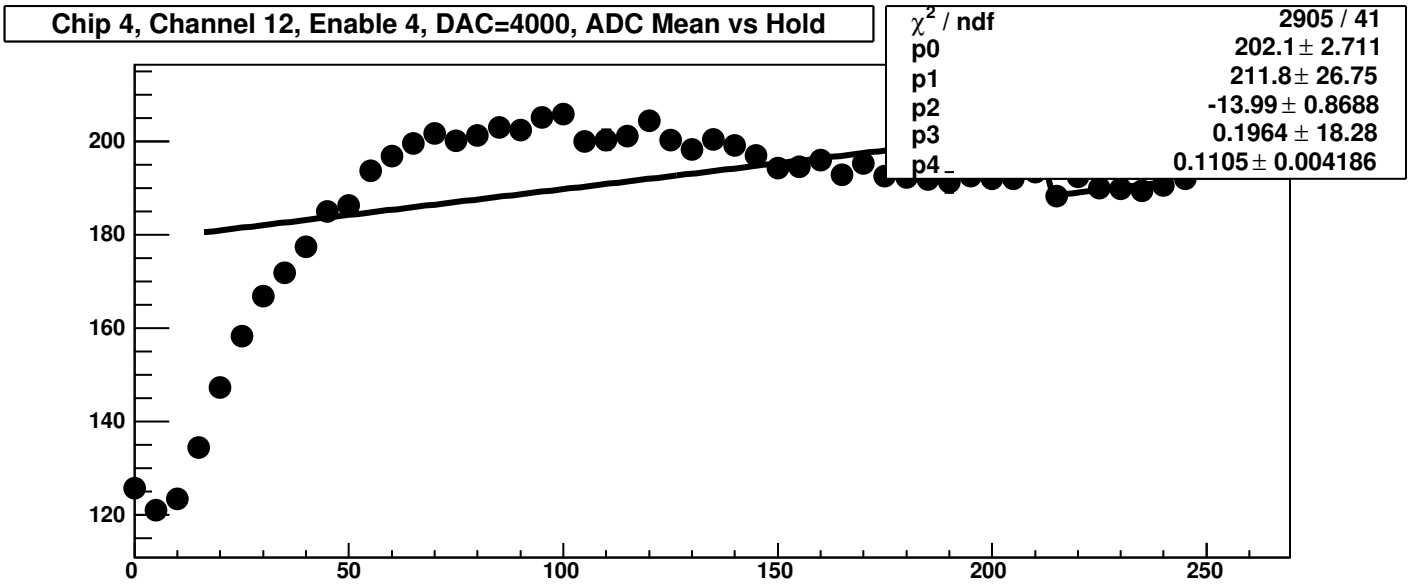


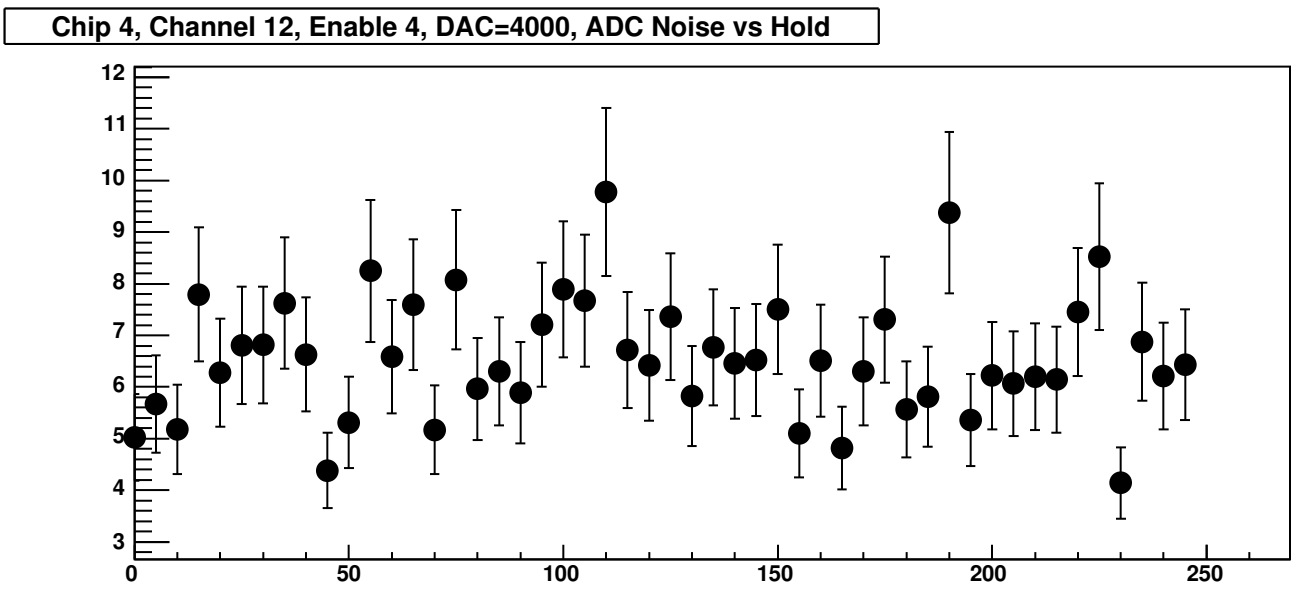
Chip 4, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold

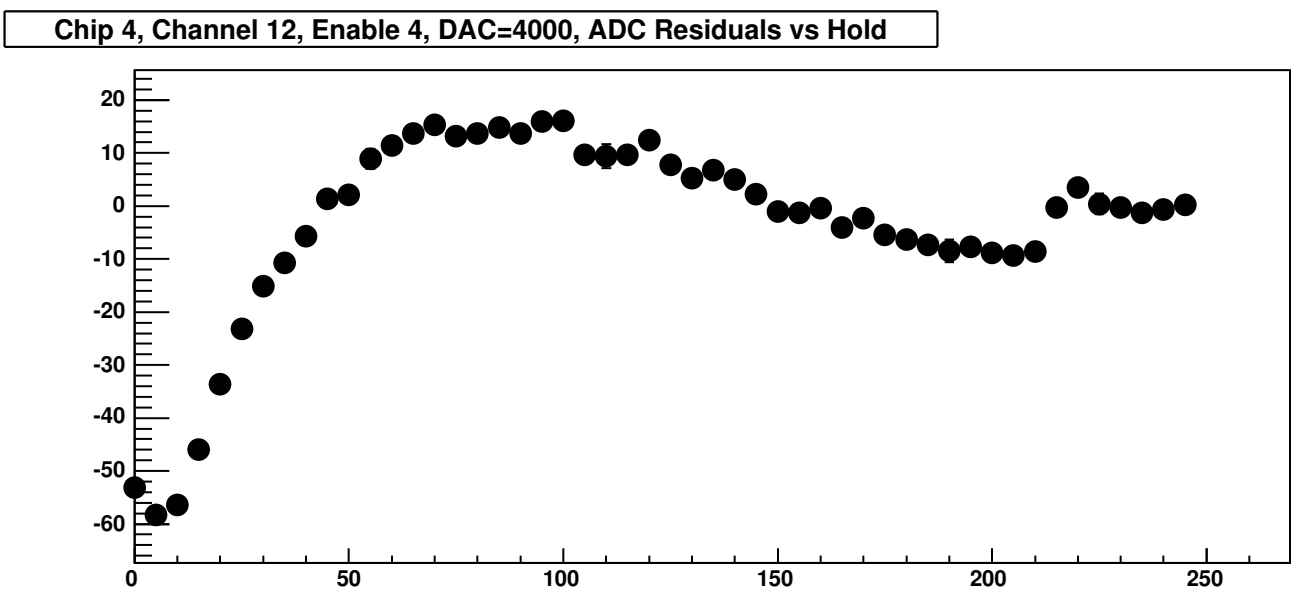


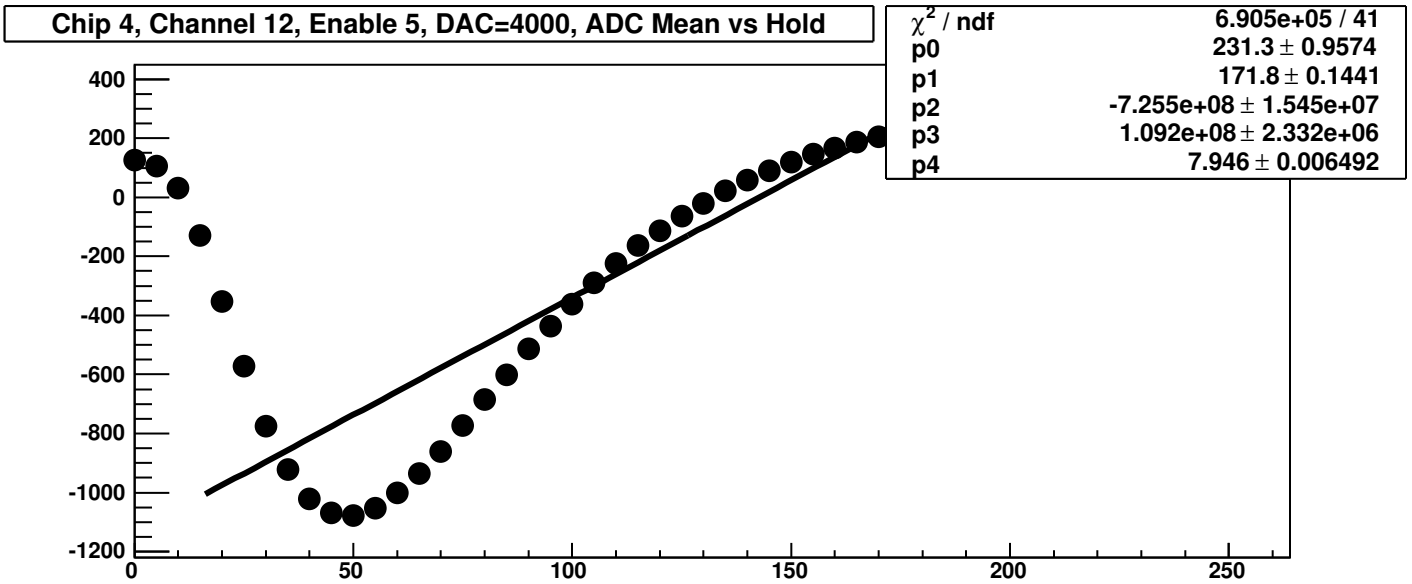
Chip 4, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

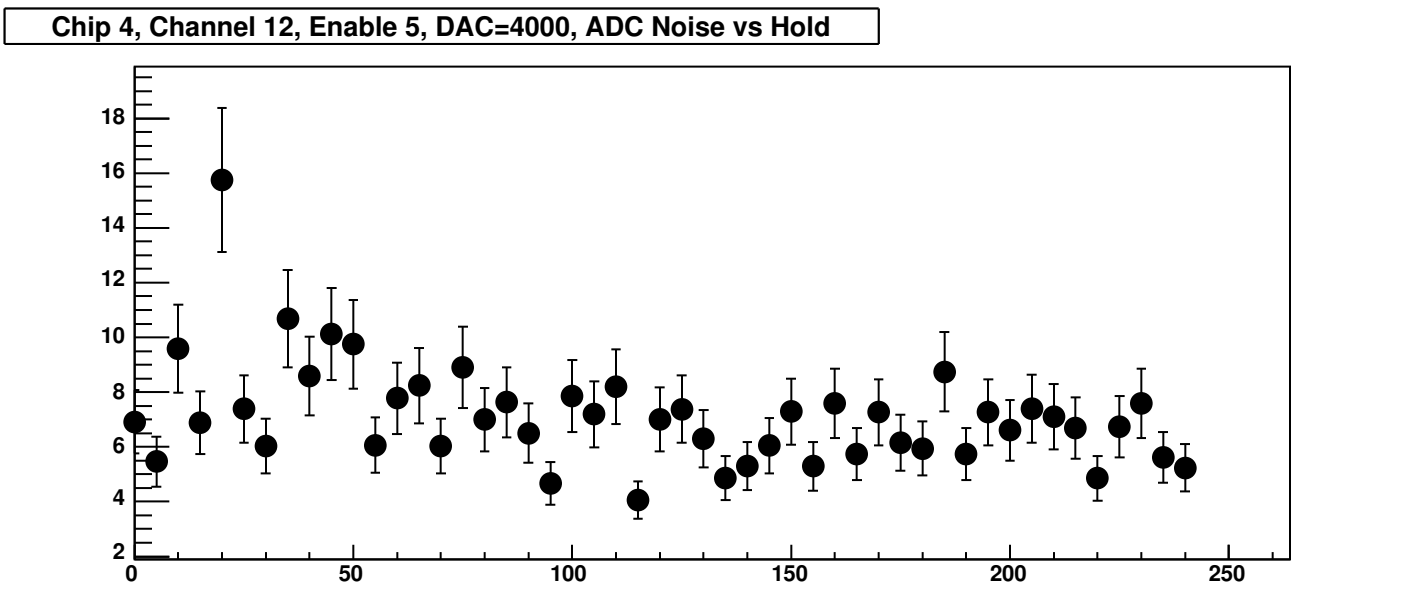


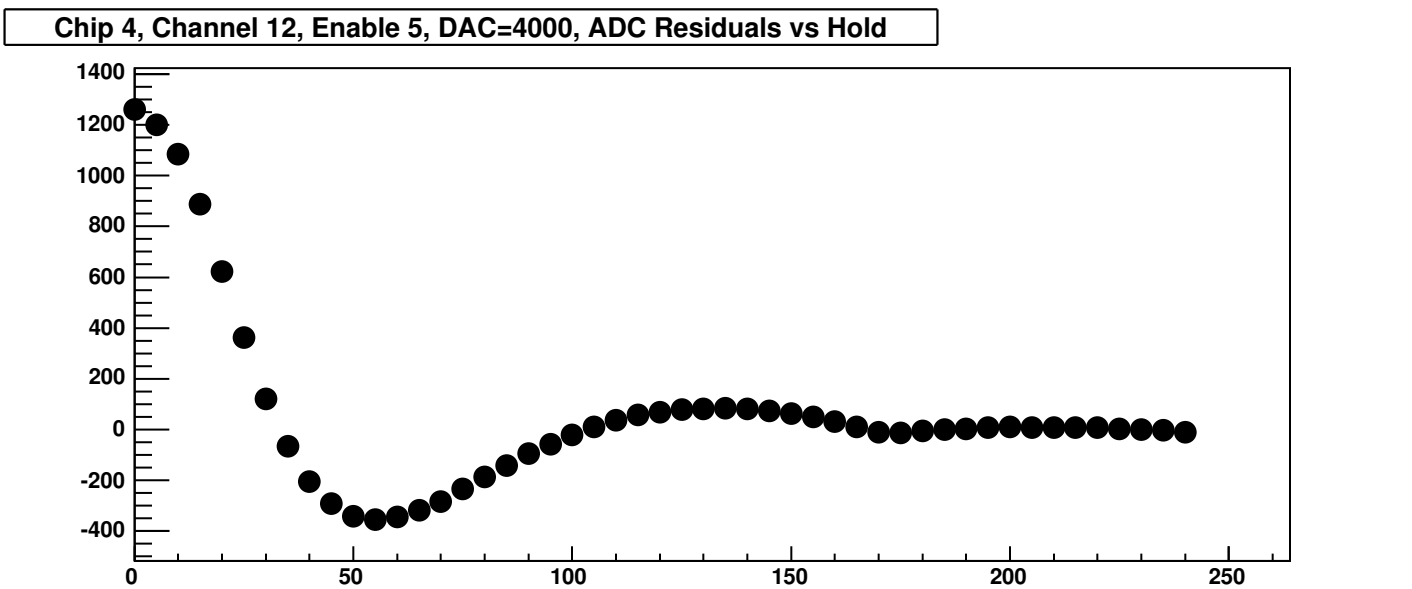

Chip 4, Channel 12, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 2905 / 41
p0 202.1 ± 2.711
p1 211.8 ± 26.75
p2 -13.99 ± 0.8688
p3 0.1964 ± 18.28
p4 0.1105 ± 0.004186
200
180
160
140
120
0
50
100
150
200
250

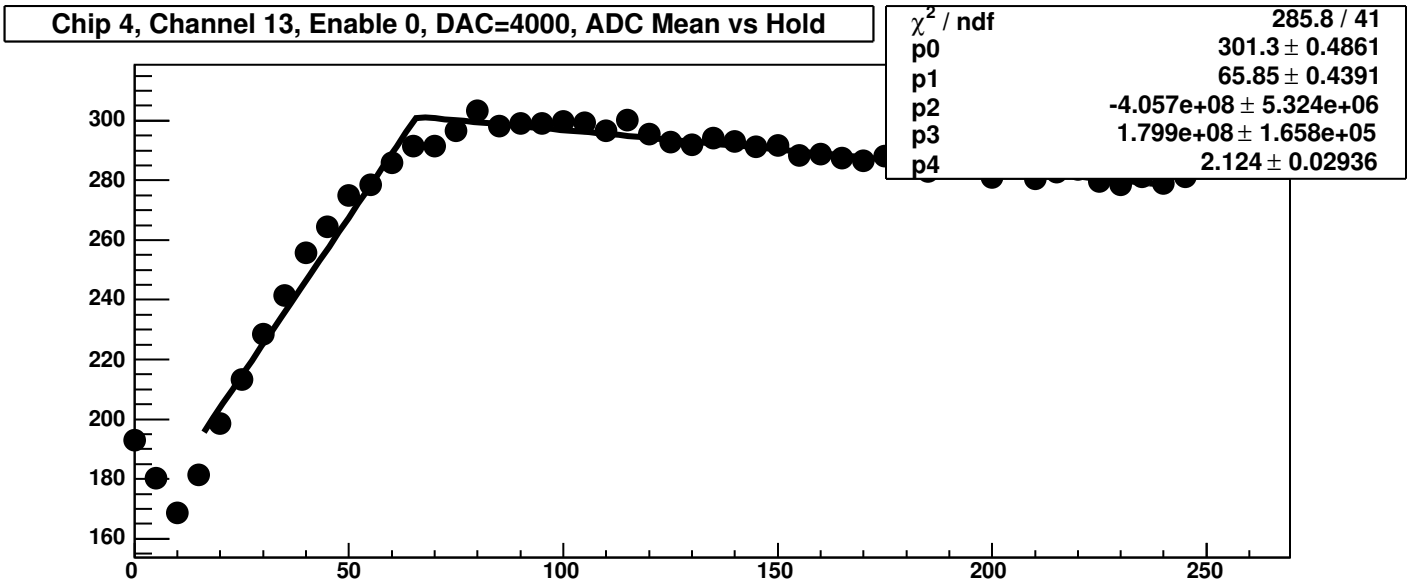

Chip 4, Channel 12, Enable 4, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

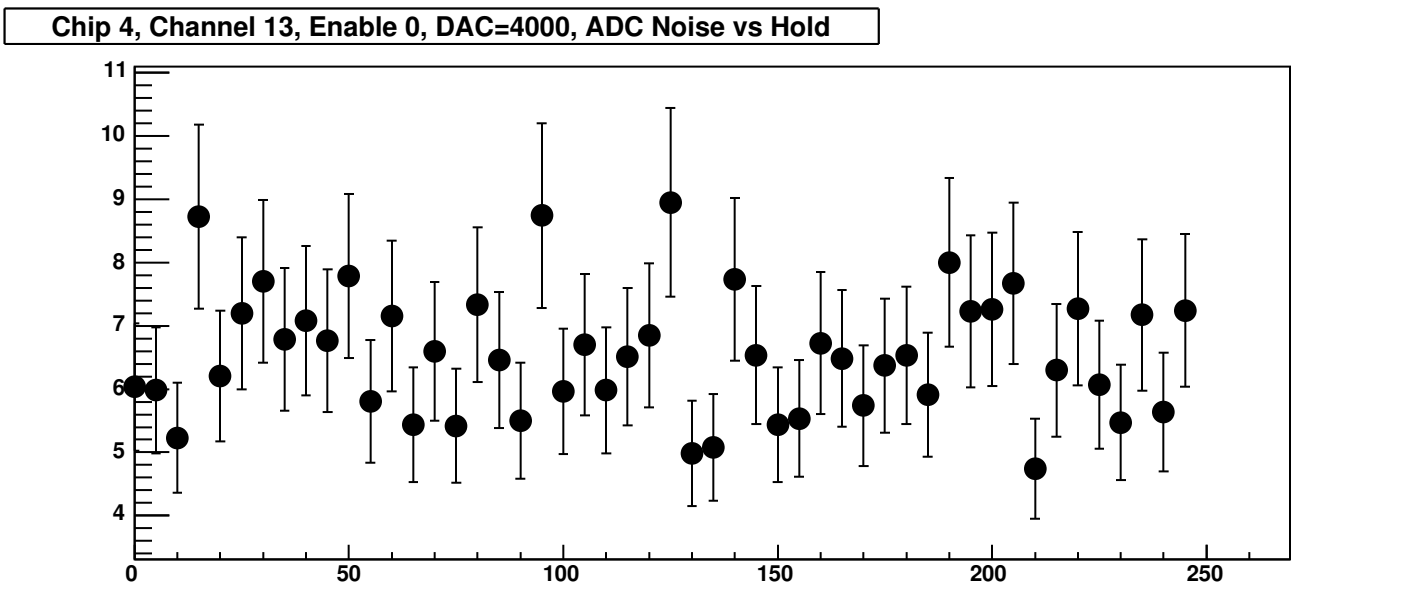

Chip 4, Channel 12, Enable 4, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
-30
-40
-50
-60
0
50
100
150
200
250

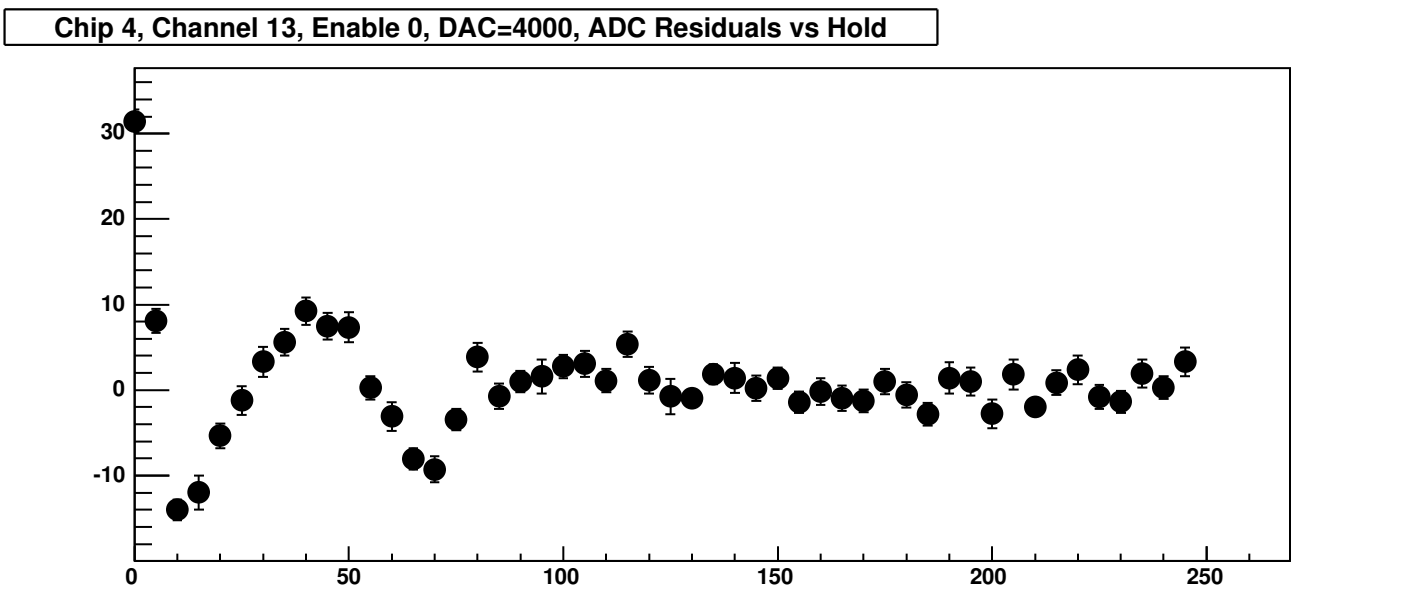

Chip 4, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 6.905e+05 / 41
p0 231.3 ± 0.9574
p1 171.8 ± 0.1441
p2 -7.255e+08 ± 1.545e+07
p3 1.092e+08 ± 2.332e+06
p4 7.946 ± 0.006492
400
200
0
-200
-400
-600
-800
-1000
-1200
0
50
100
150
200
250


Chip 4, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold
18
16
14
12
10
8
6
4
2
0
50
100
150
200
250

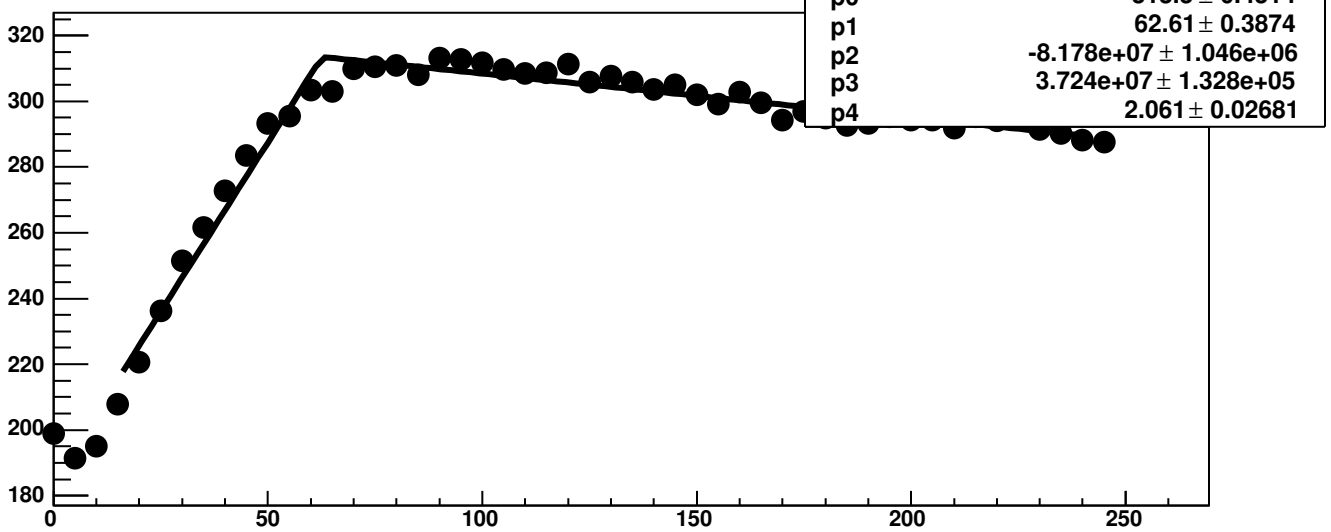

Chip 4, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
-200
-400
0
50
100
150
200
250


Chip 4, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 285.8 / 41
p0 301.3 ± 0.4861
p1 65.85 ± 0.4391
p2 -4.057e+08 ± 5.324e+06
p3 1.799e+08 ± 1.658e+05
p4 2.124 ± 0.02936
300
280
260
240
220
200
180
160
0
50
100
150
200
250

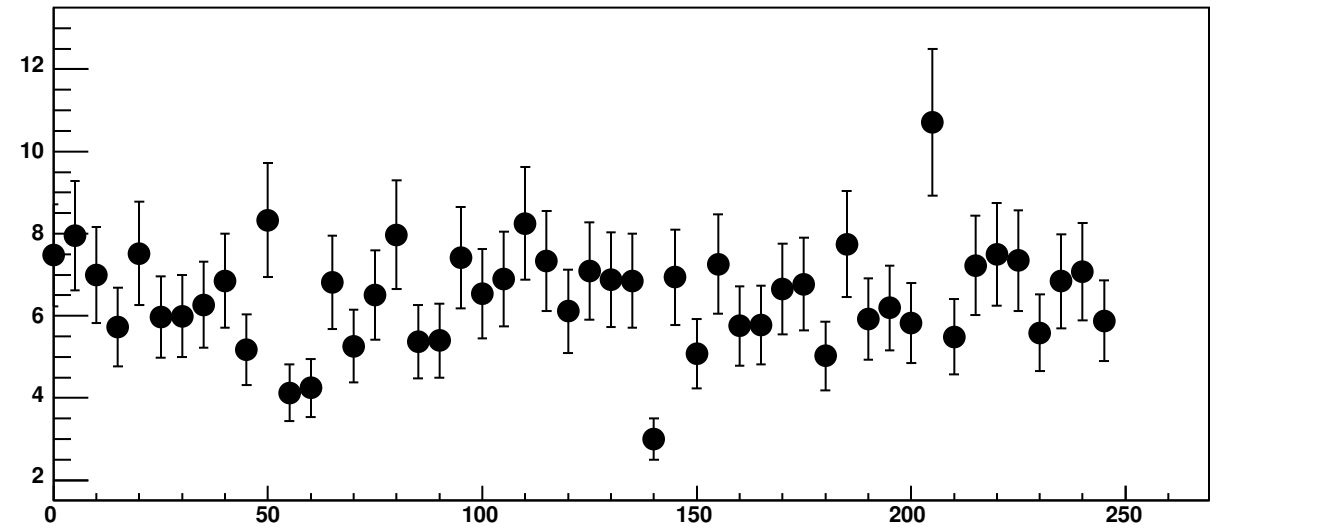

Chip 4, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 4, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold
30
20
10
0
-10
0
50
100
150
200
250

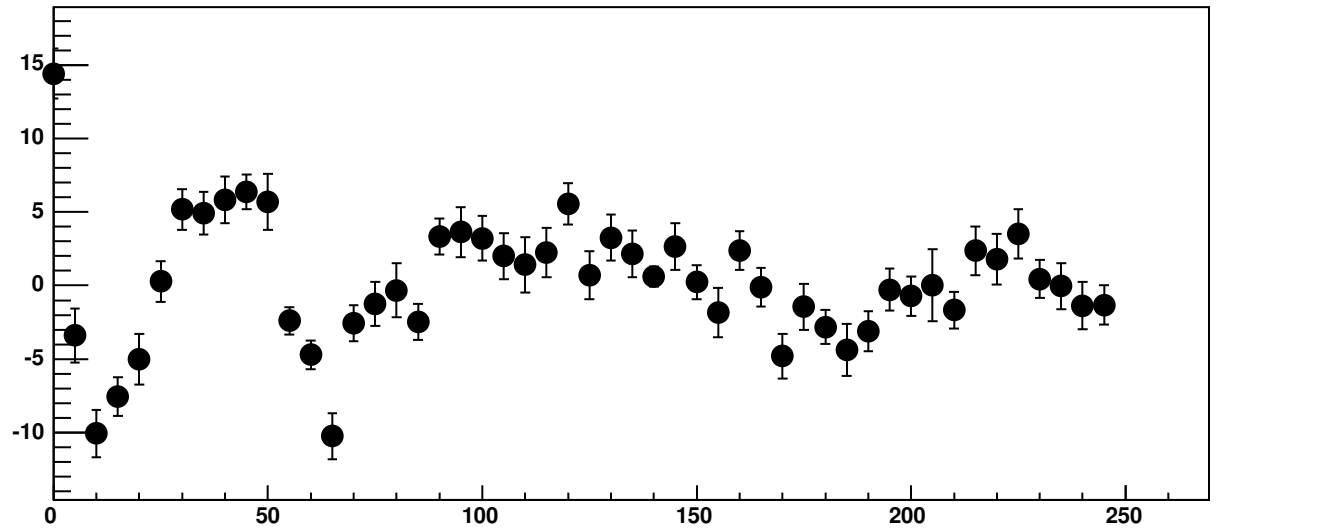
Chip 4, Channel 13, Enable 1, DAC=4000, ADC Mean vs Hold



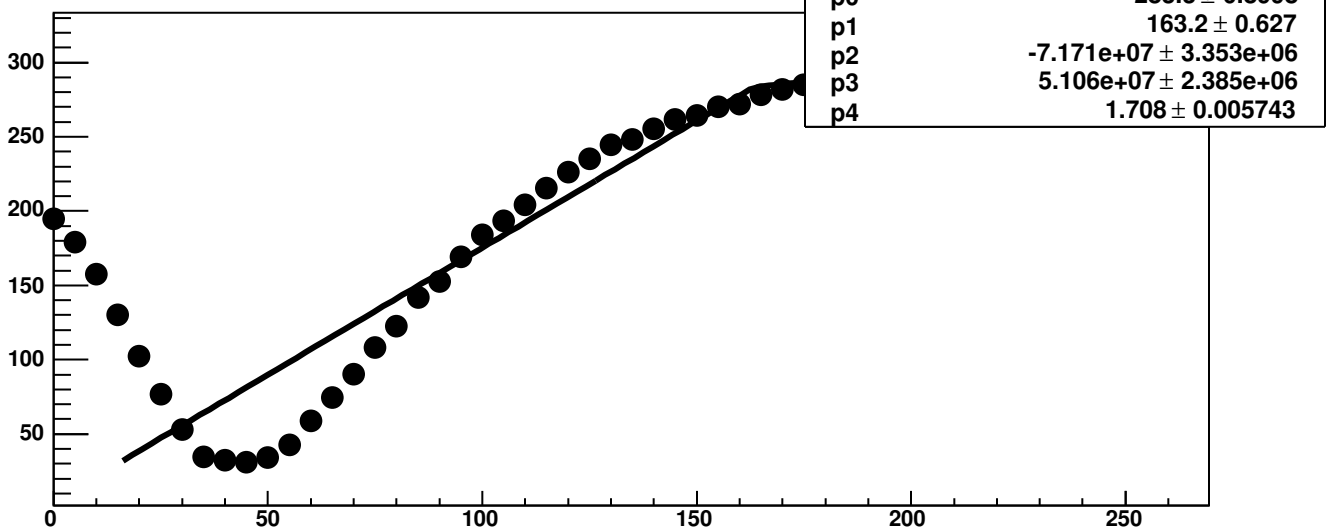
Chip 4, Channel 13, Enable 1, DAC=4000, ADC Noise vs Hold



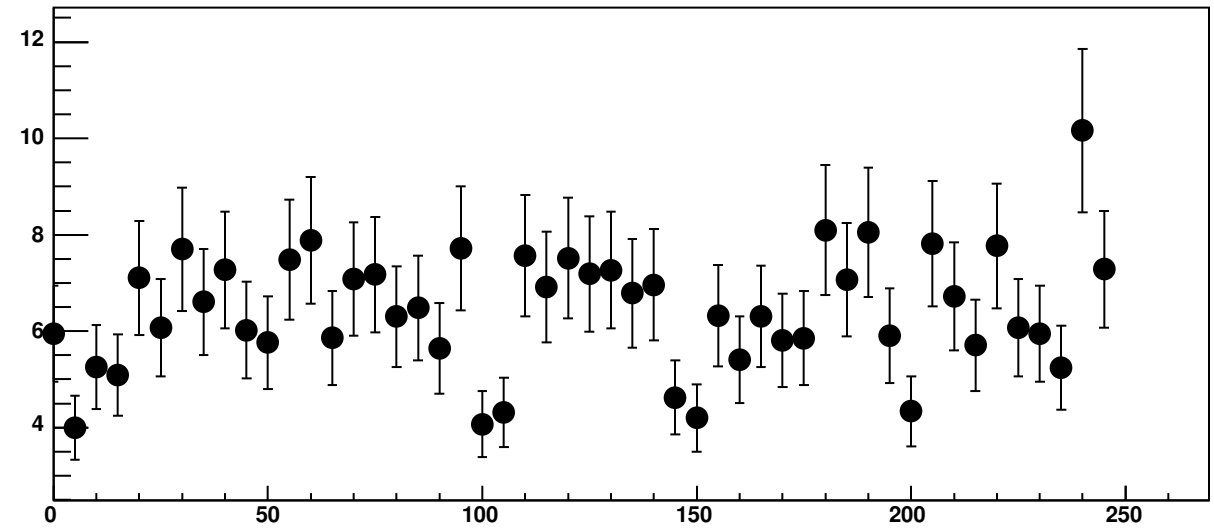
Chip 4, Channel 13, Enable 1, DAC=4000, ADC Residuals vs Hold



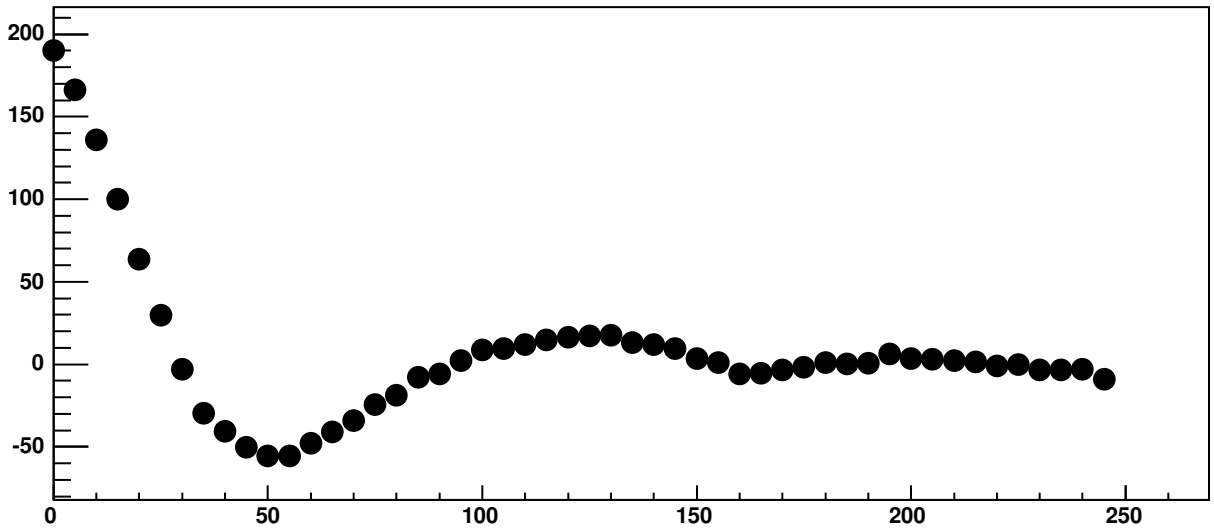
Chip 4, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold



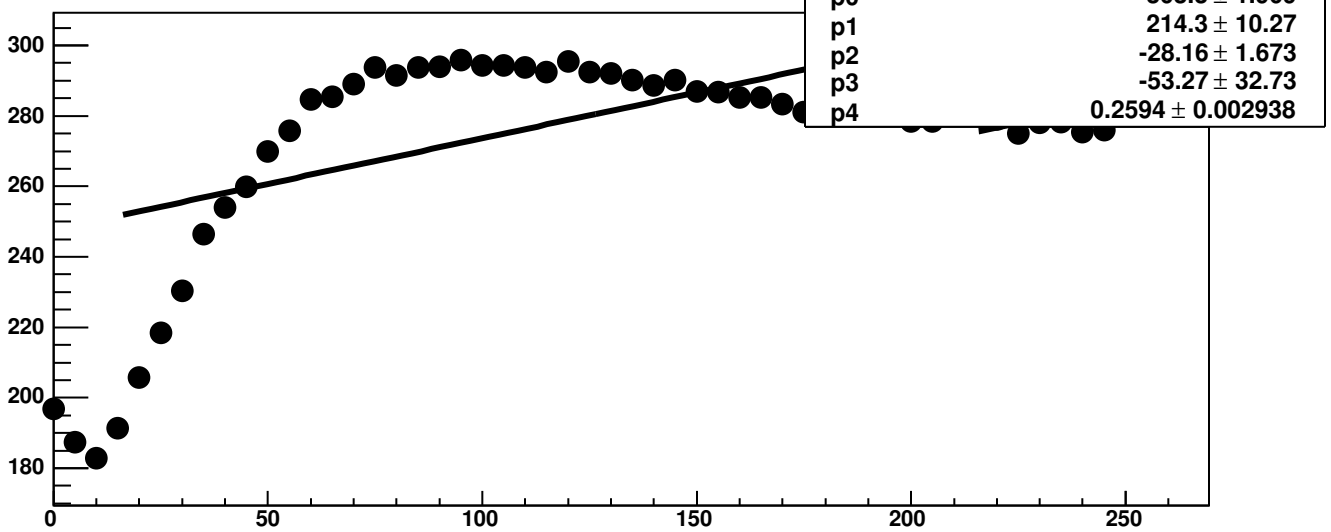
Chip 4, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



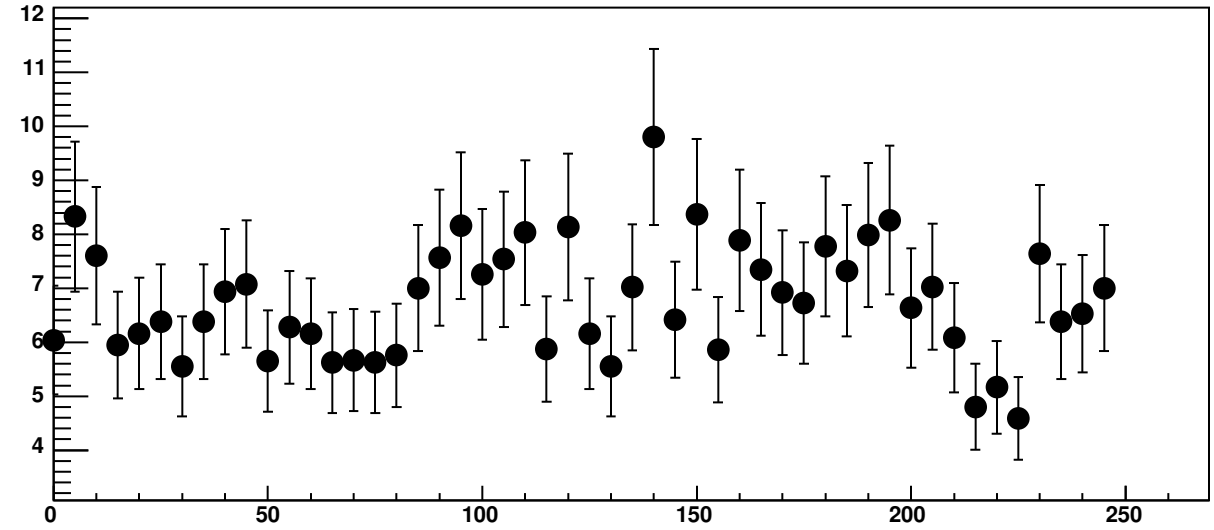
Chip 4, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold



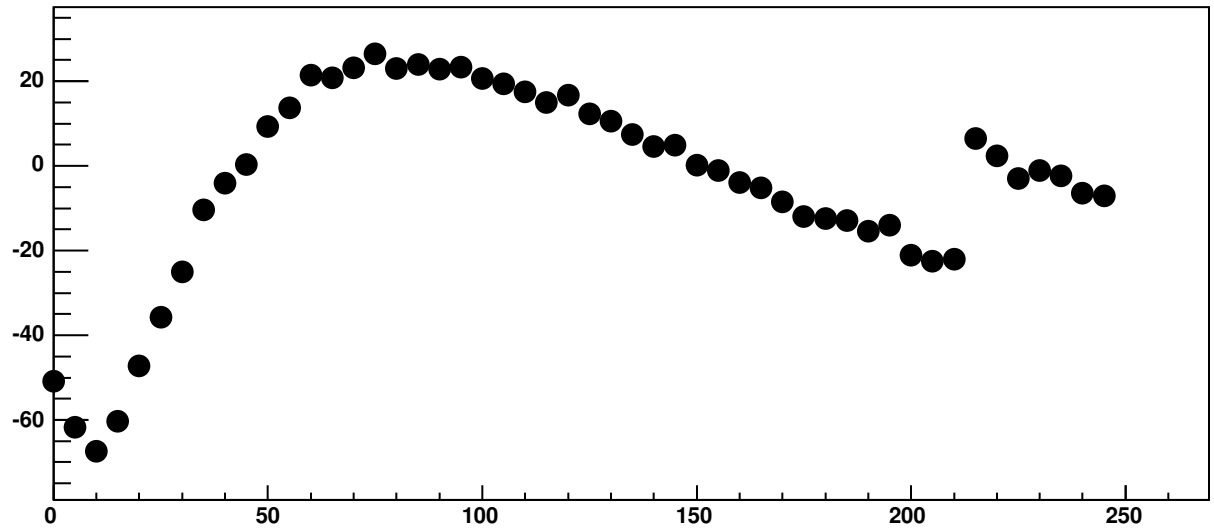
Chip 4, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold

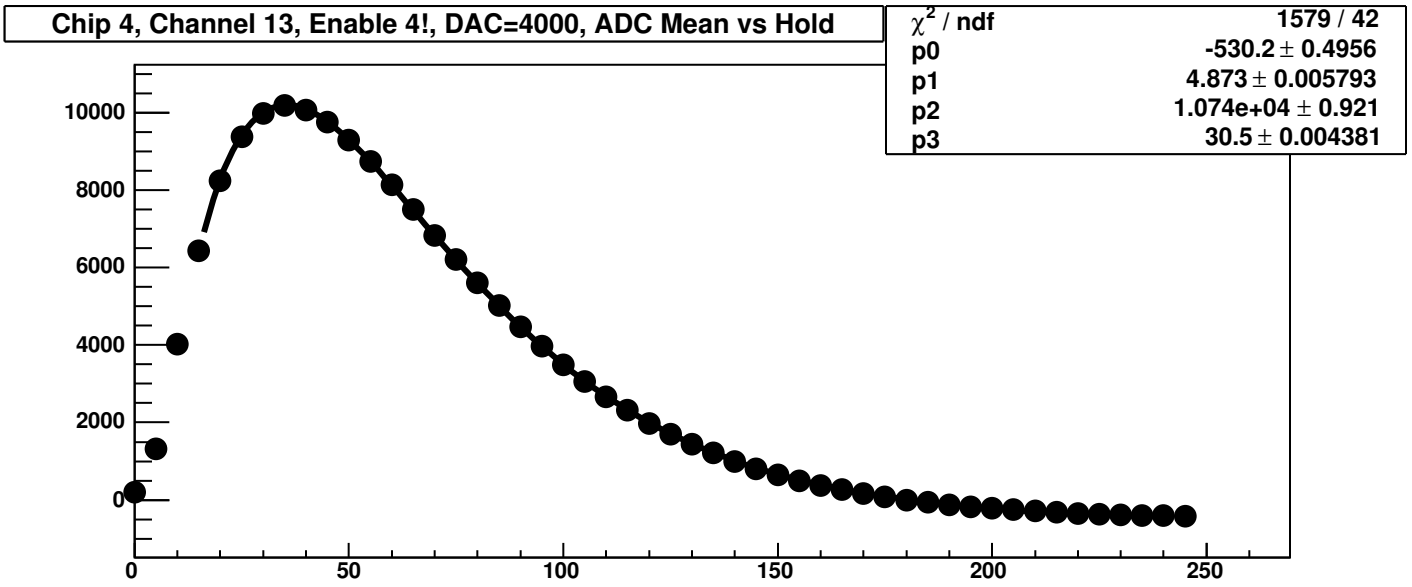


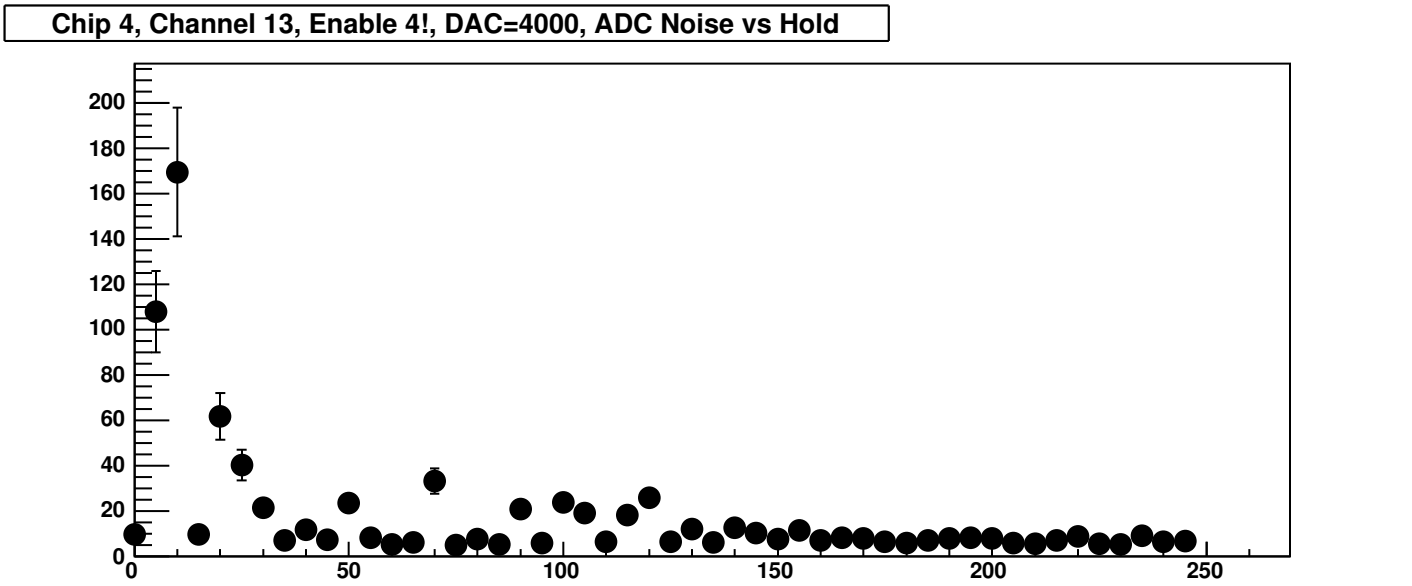
Chip 4, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold

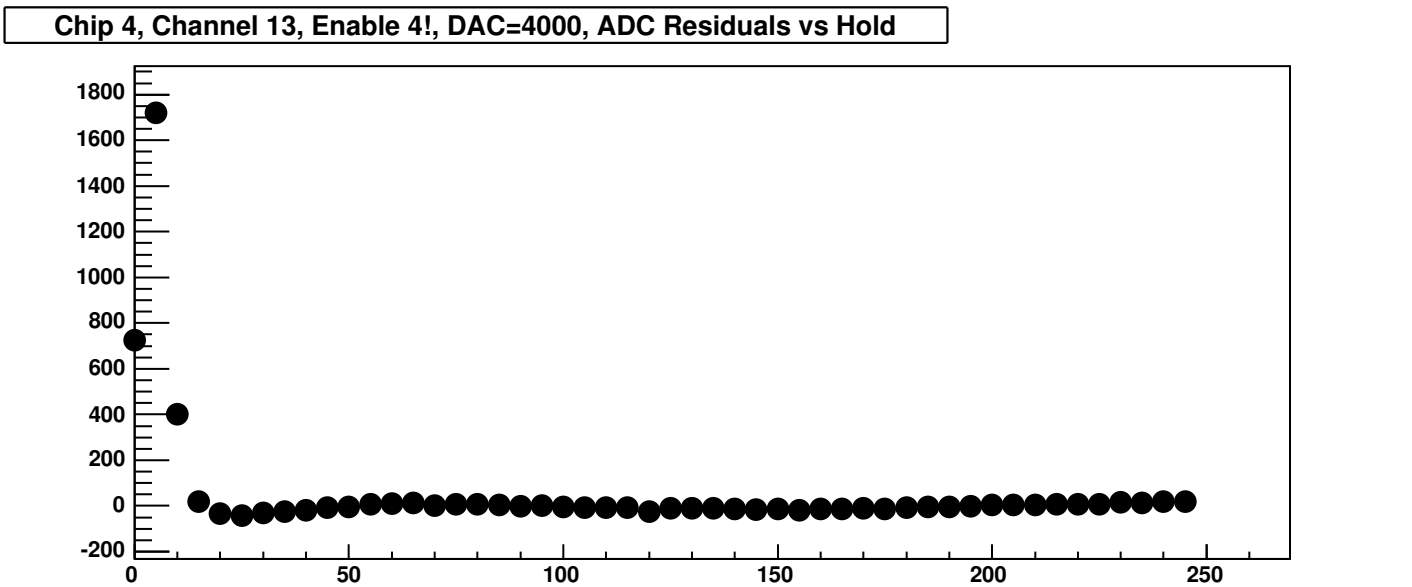


Chip 4, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold

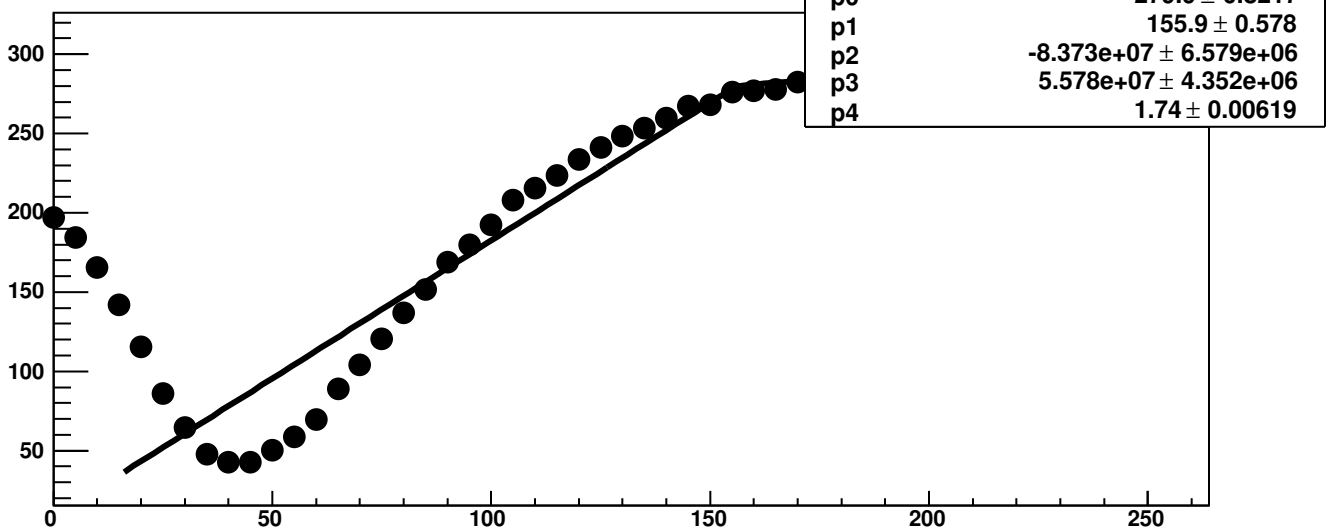



Chip 4, Channel 13, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1579 / 42
p0 -530.2 ± 0.4956
p1 4.873 ± 0.005793
p2 1.074e+04 ± 0.921
p3 30.5 ± 0.004381
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

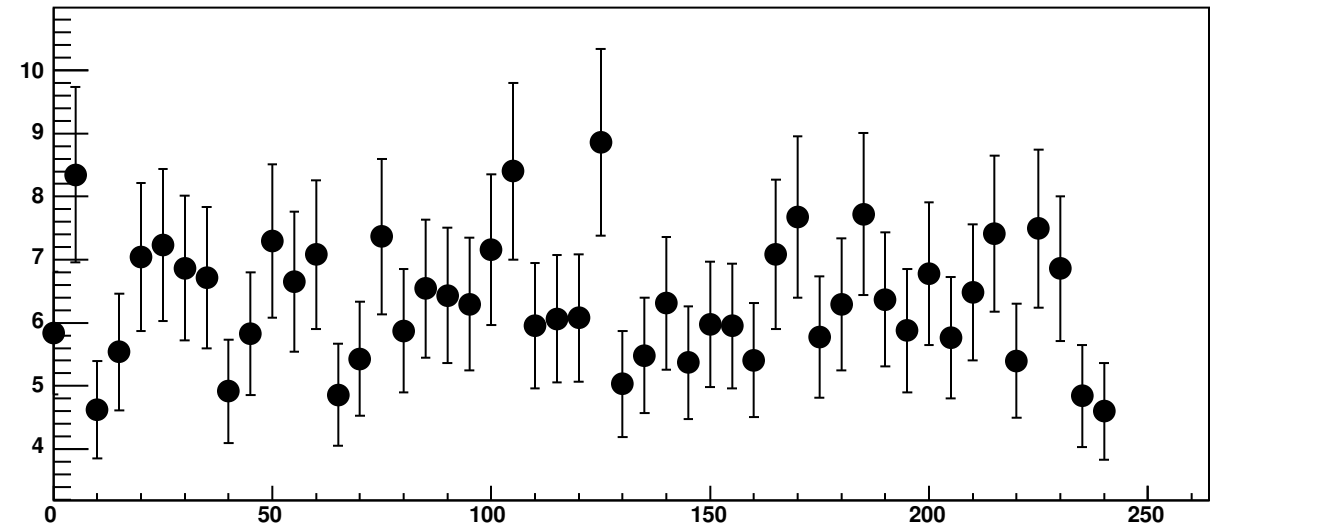

Chip 4, Channel 13, Enable 4!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 4, Channel 13, Enable 4!, DAC=4000, ADC Residuals vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

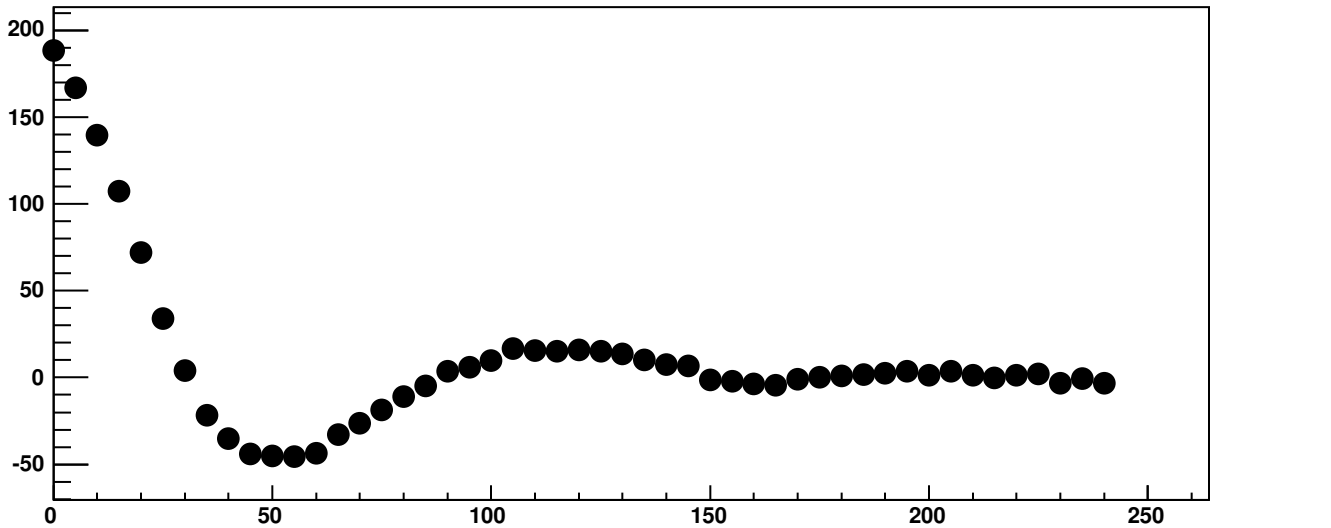
Chip 4, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold

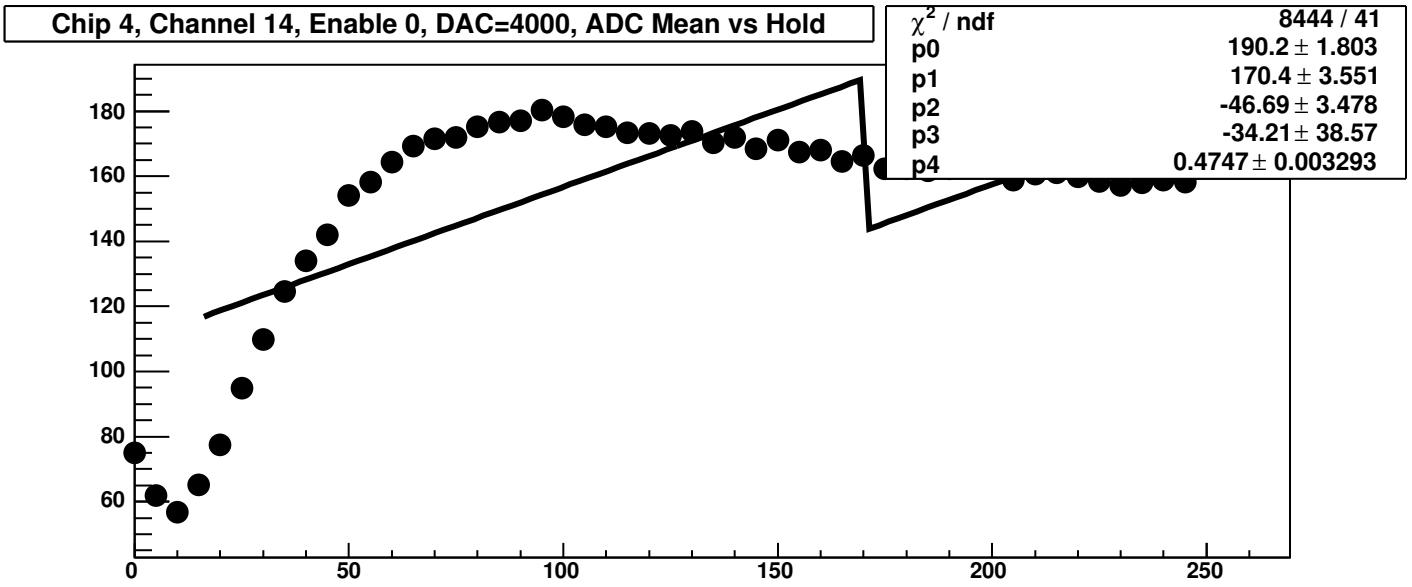


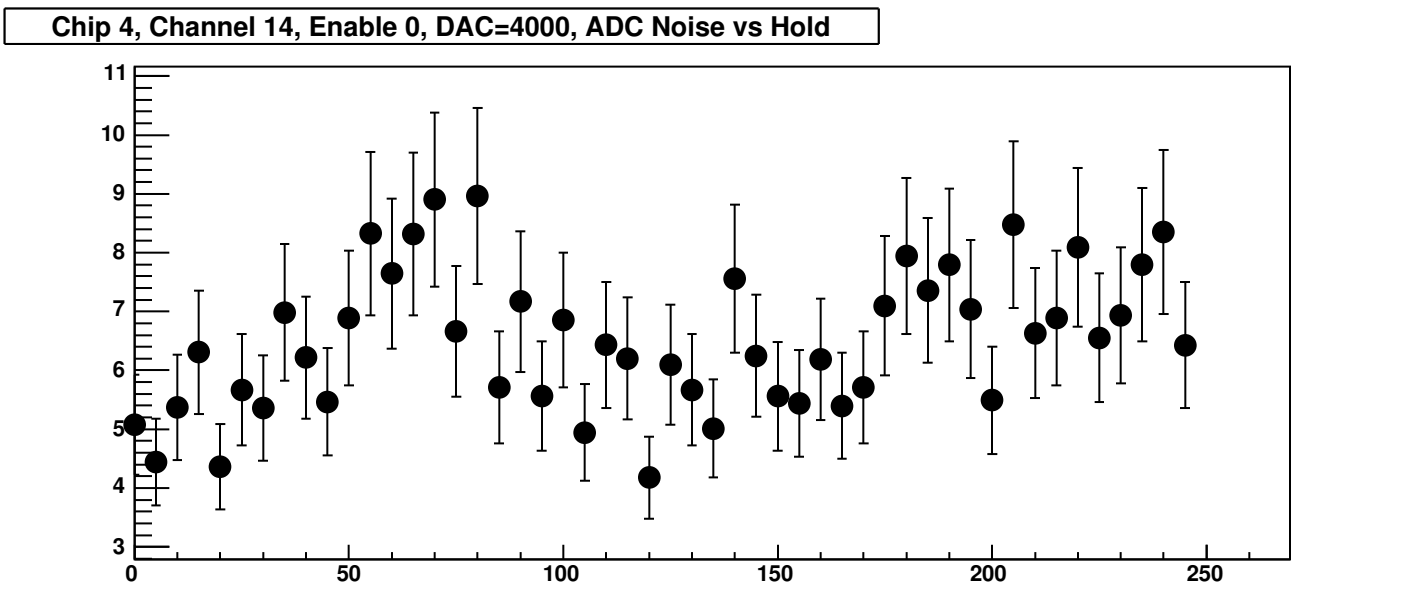
Chip 4, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold

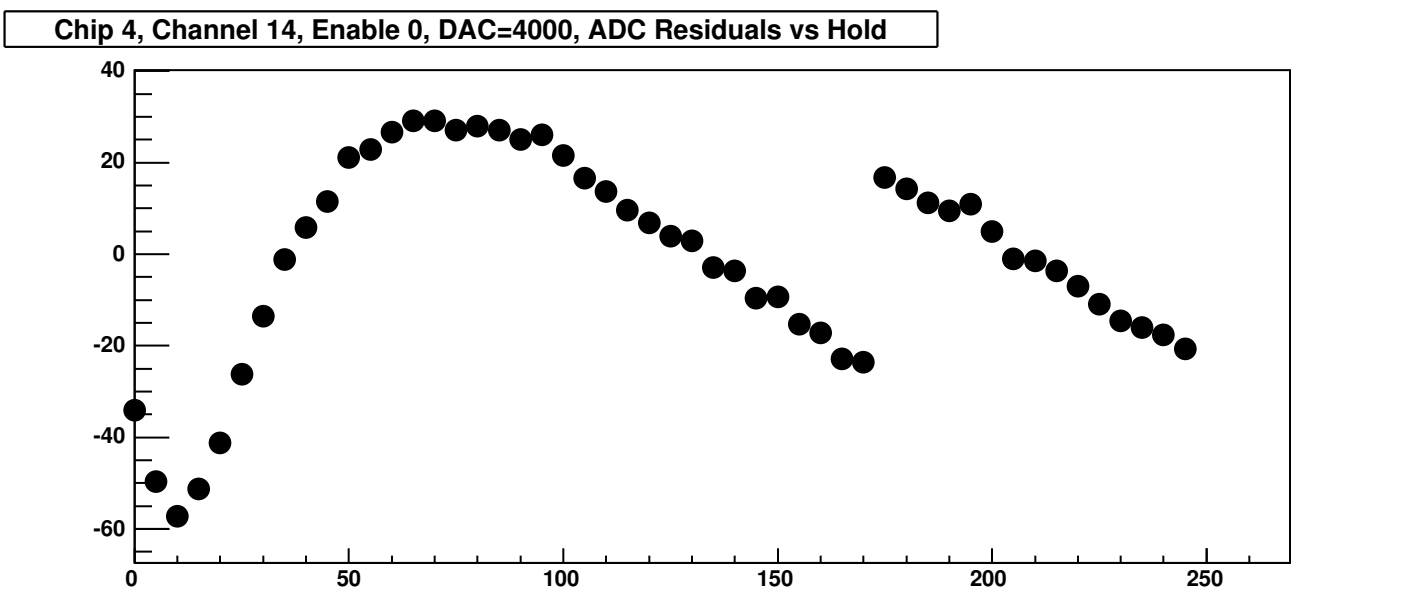


Chip 4, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold

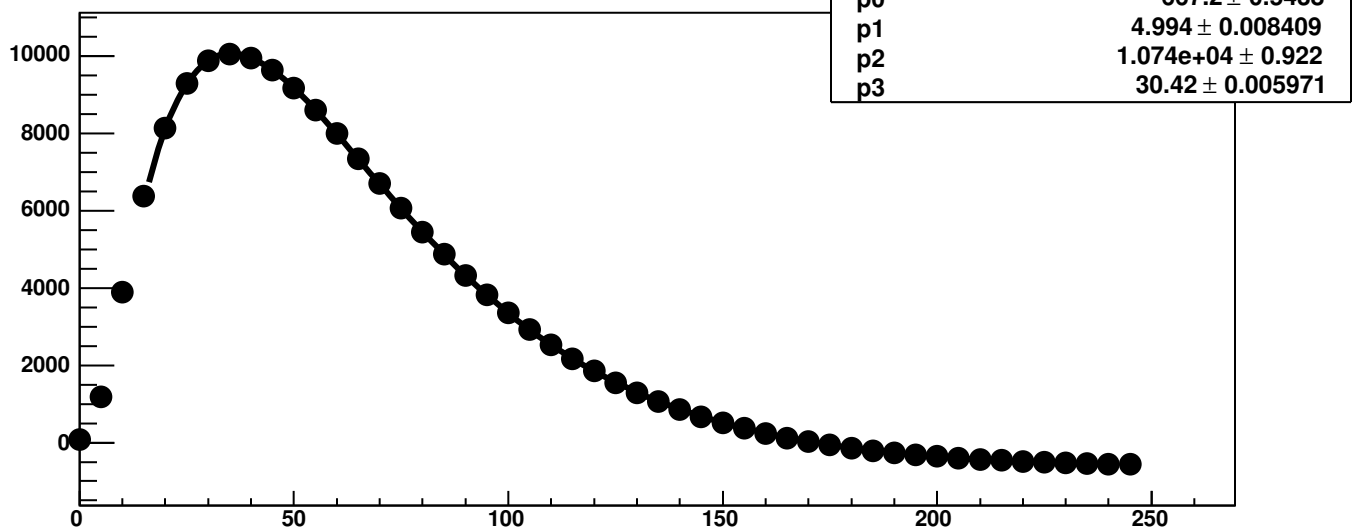



Chip 4, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 8444 / 41
p0 190.2 ± 1.803
p1 170.4 ± 3.551
p2 -46.69 ± 3.478
p3 -34.21 ± 38.57
p4 0.4747 ± 0.003293
180
160
140
120
100
80
60
0
50
100
150
200
250

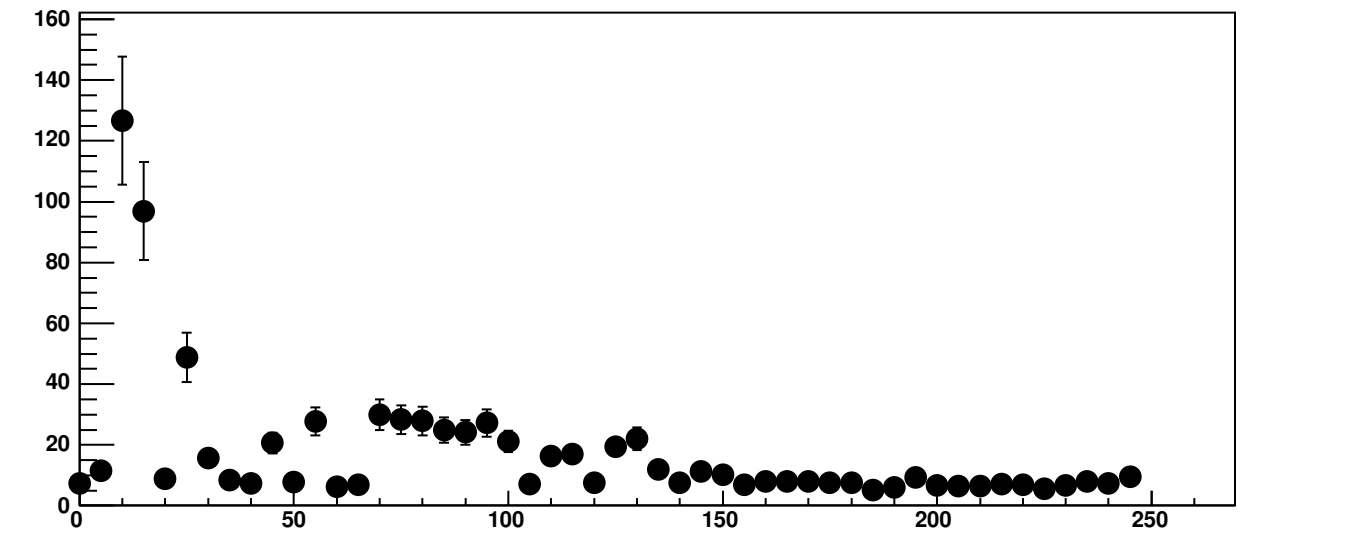

Chip 4, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 4, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
0
50
100
150
200
250

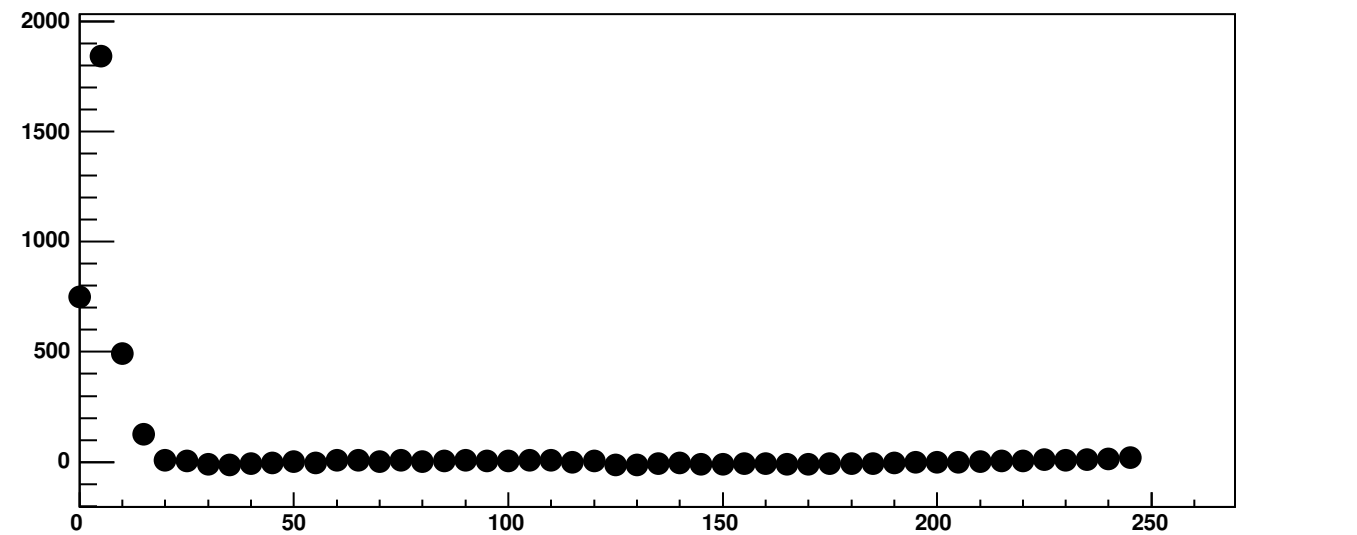
Chip 4, Channel 14, Enable 1!, DAC=4000, ADC Mean vs Hold



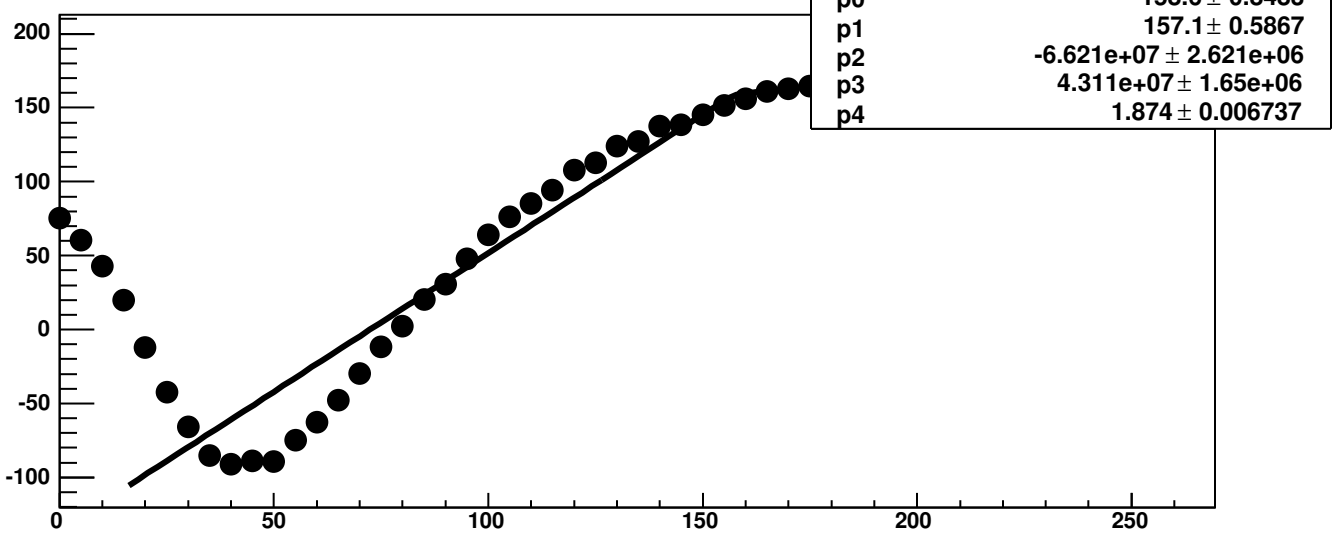
Chip 4, Channel 14, Enable 1!, DAC=4000, ADC Noise vs Hold



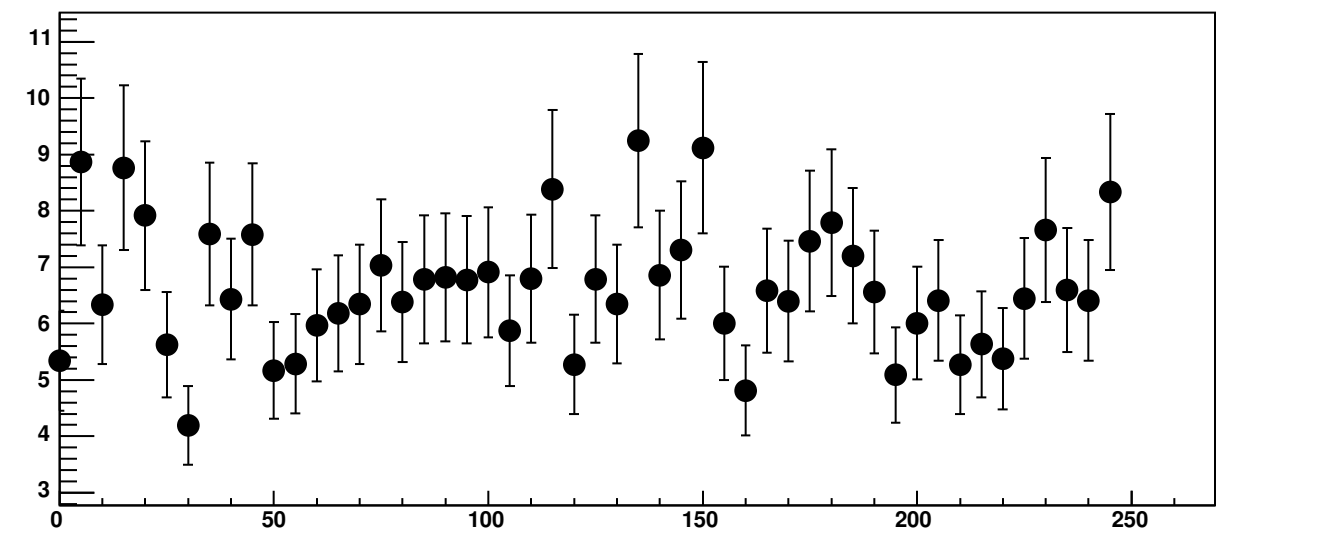
Chip 4, Channel 14, Enable 1!, DAC=4000, ADC Residuals vs Hold



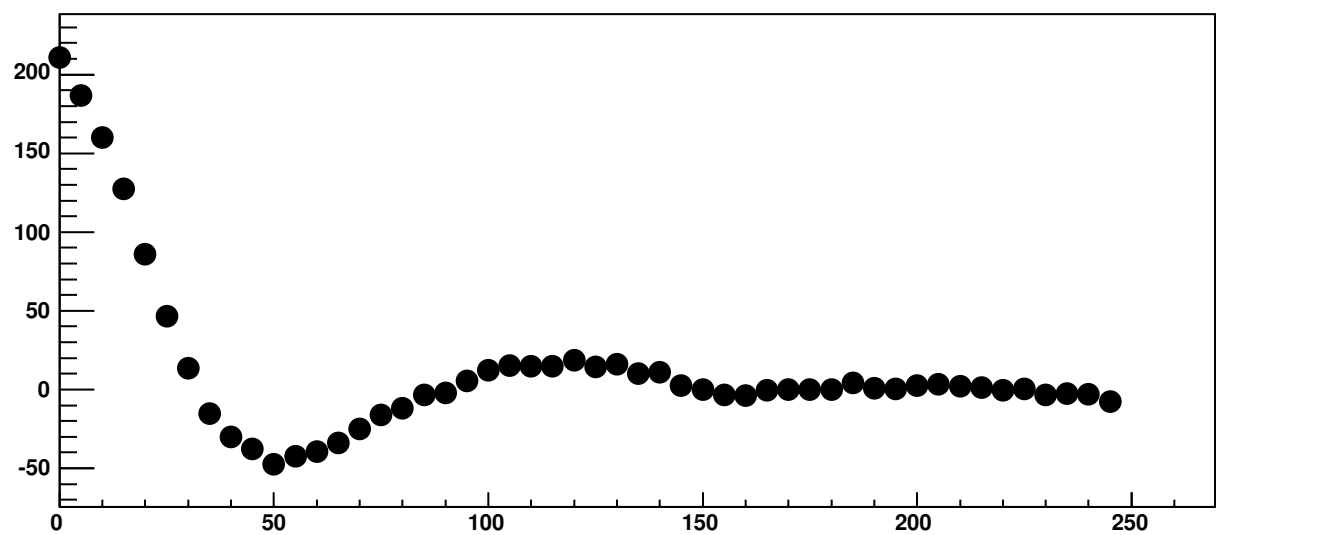
Chip 4, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold



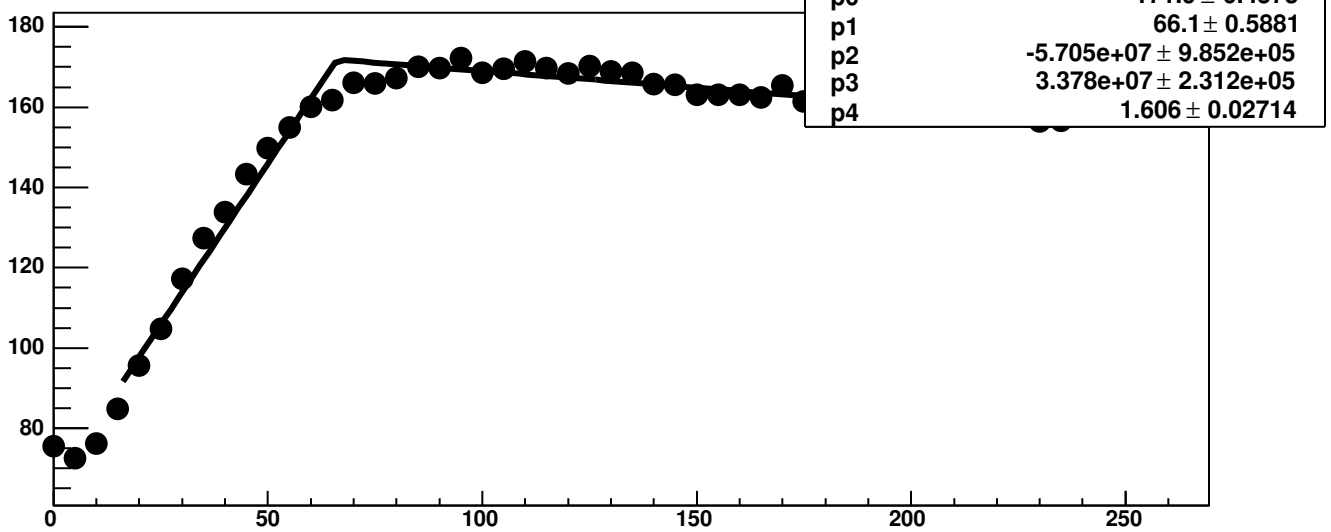
Chip 4, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold



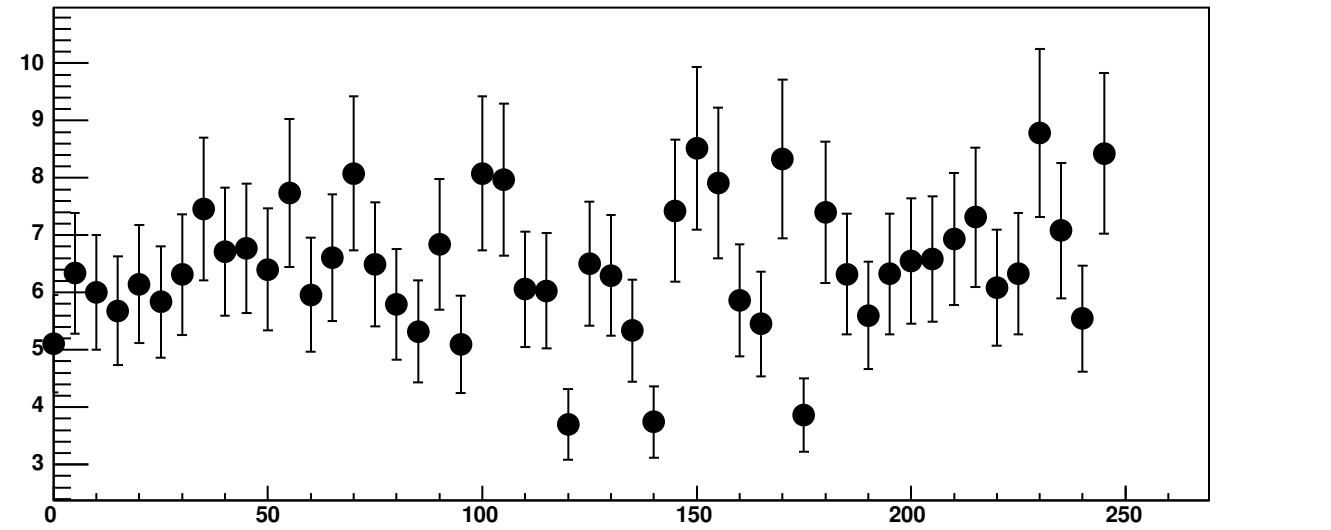
Chip 4, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold



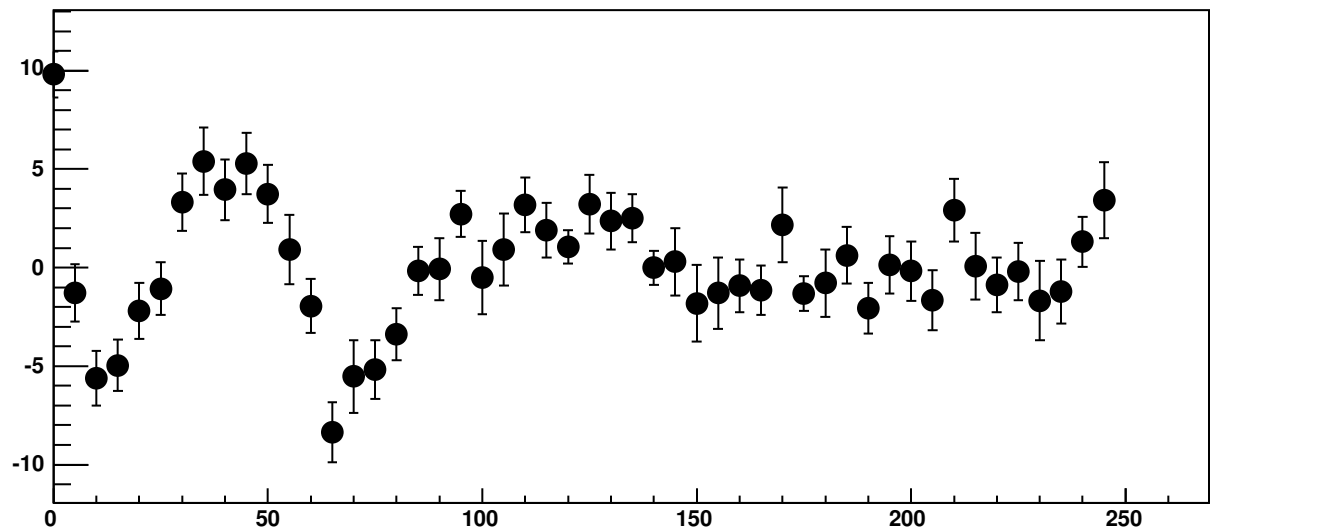
Chip 4, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold



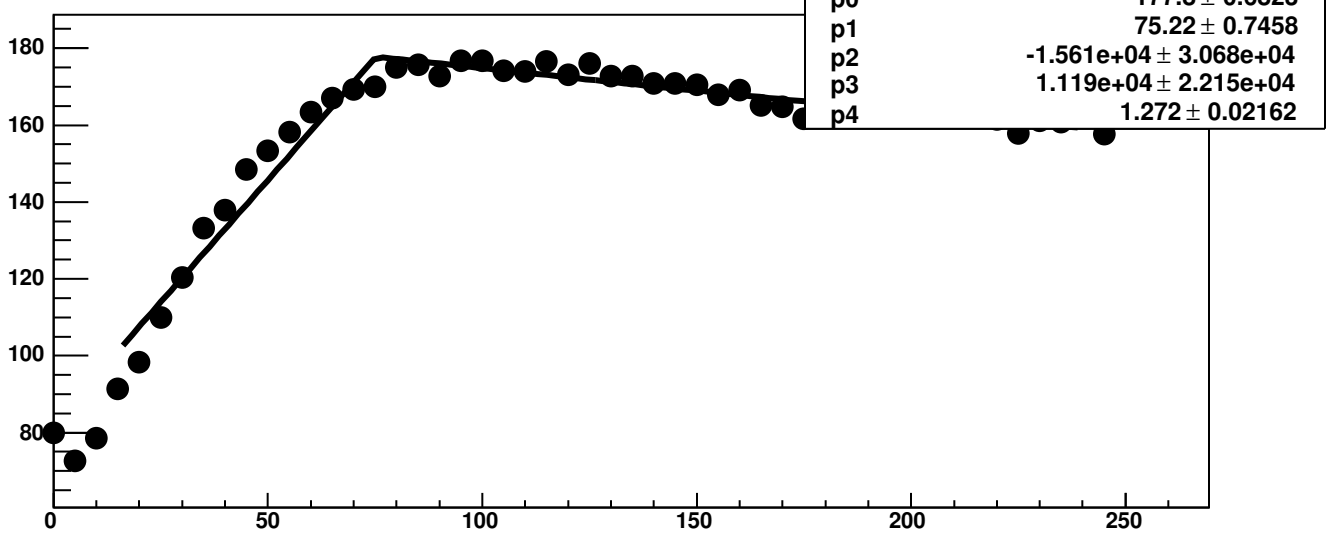
Chip 4, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold



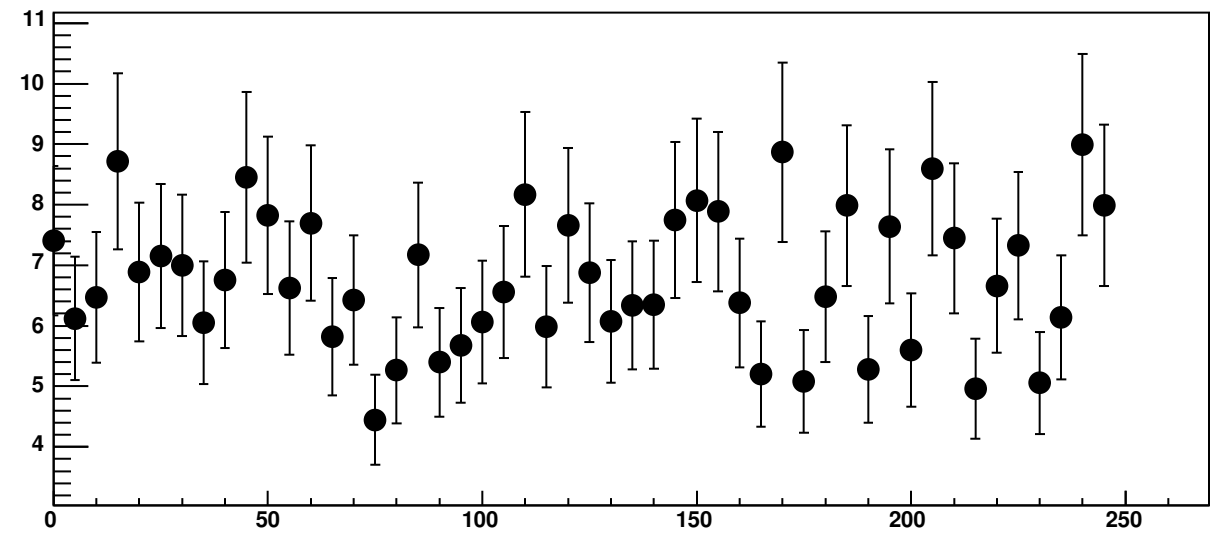
Chip 4, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold



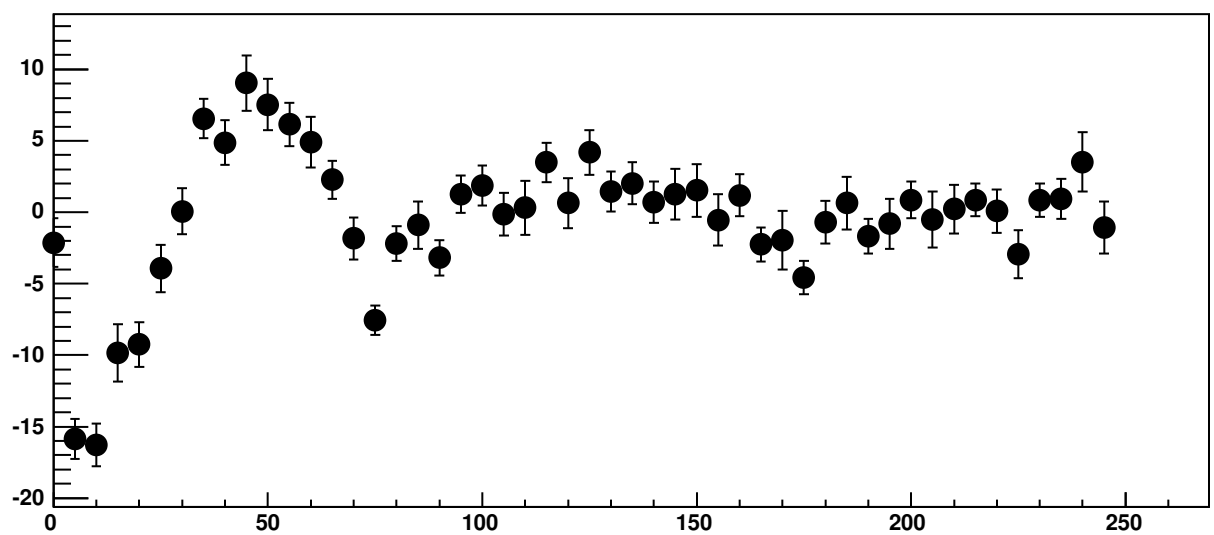
Chip 4, Channel 14, Enable 4, DAC=4000, ADC Mean vs Hold



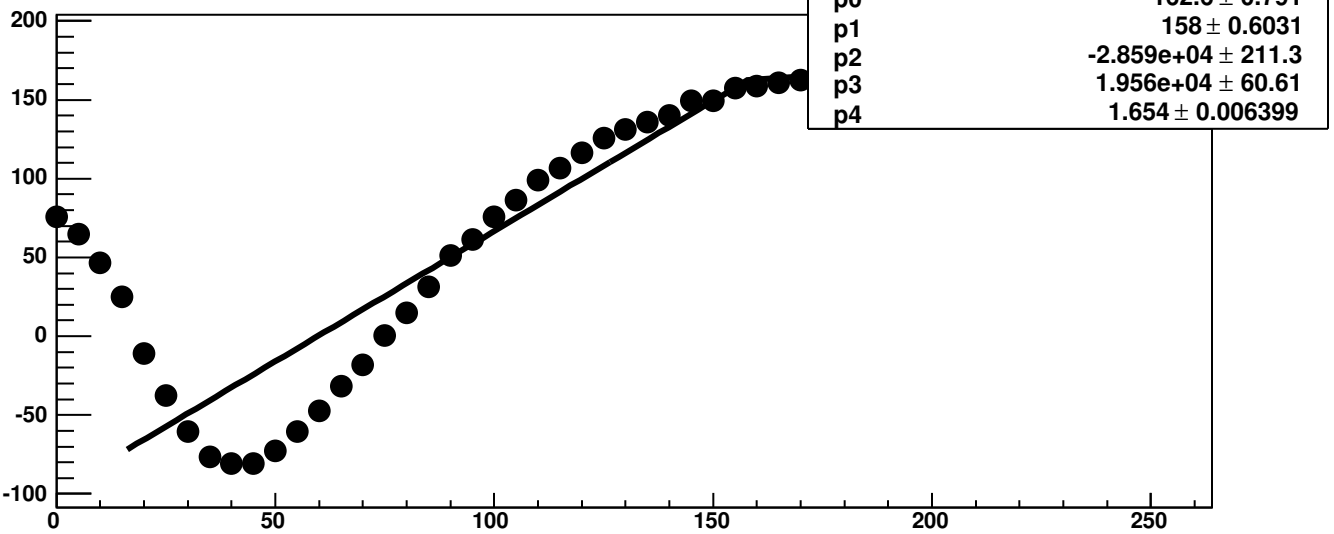
Chip 4, Channel 14, Enable 4, DAC=4000, ADC Noise vs Hold



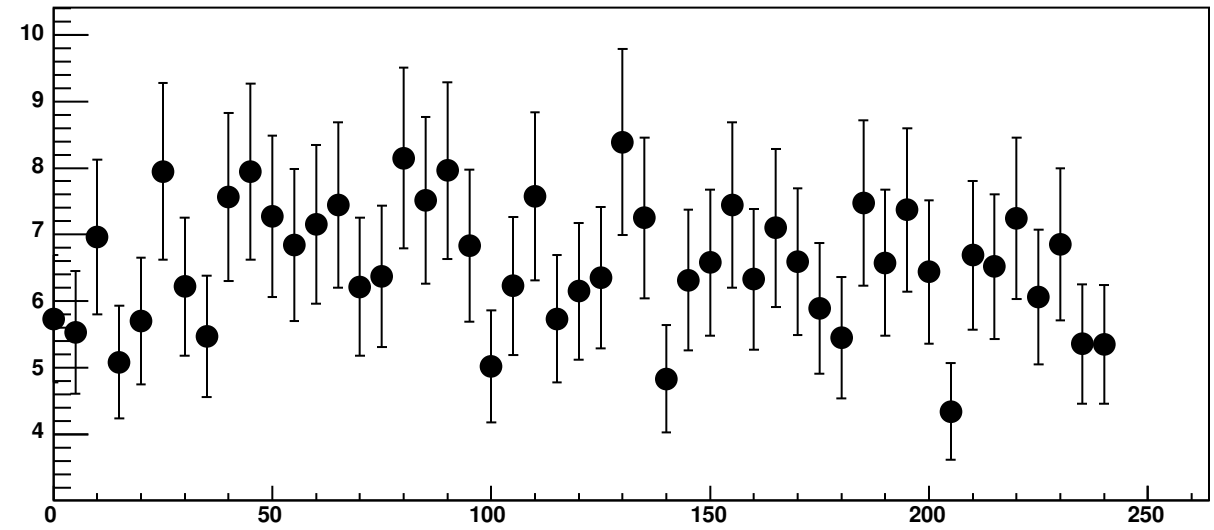
Chip 4, Channel 14, Enable 4, DAC=4000, ADC Residuals vs Hold



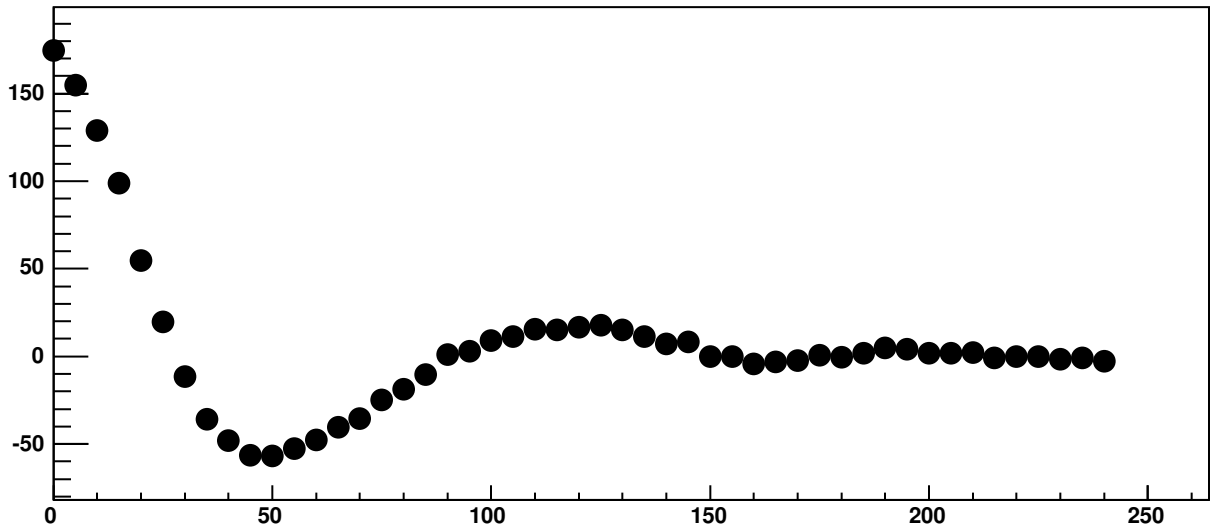
Chip 4, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold

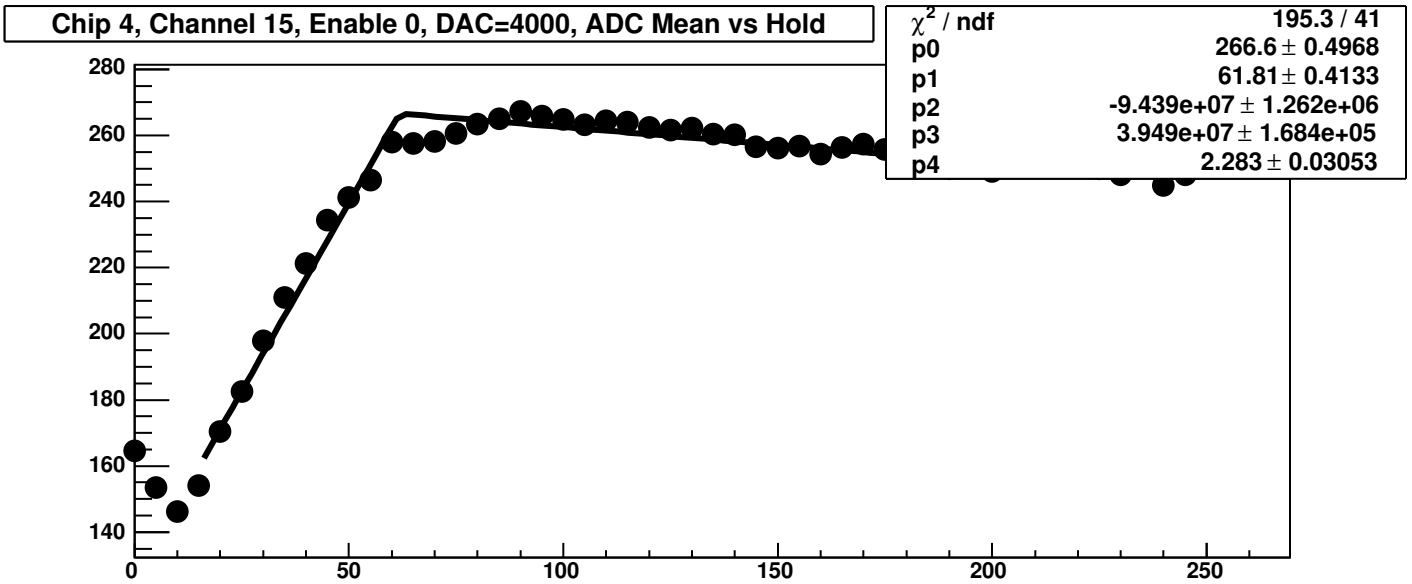


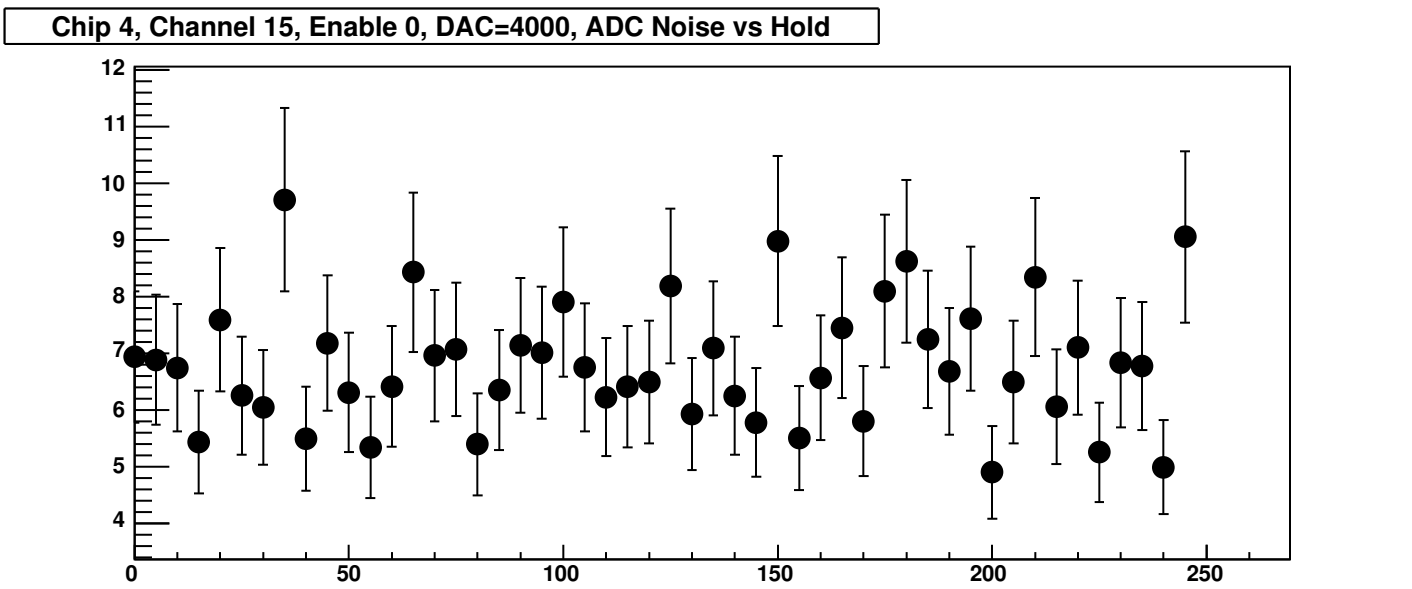
Chip 4, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold

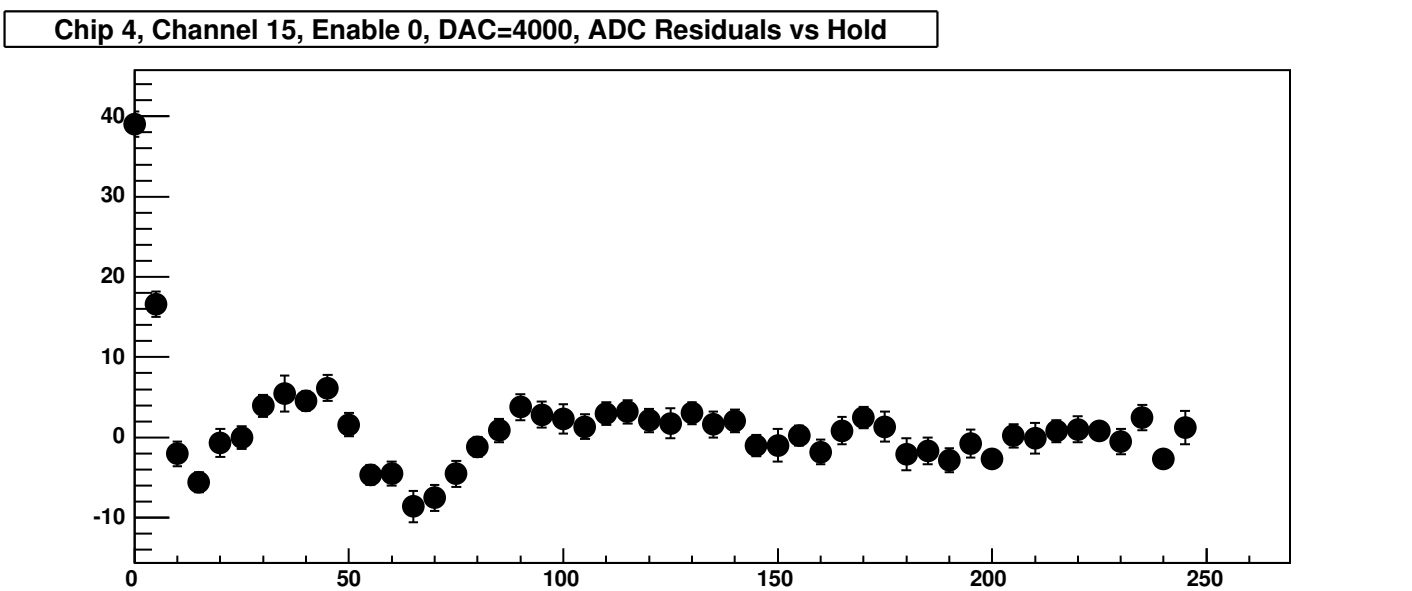


Chip 4, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold

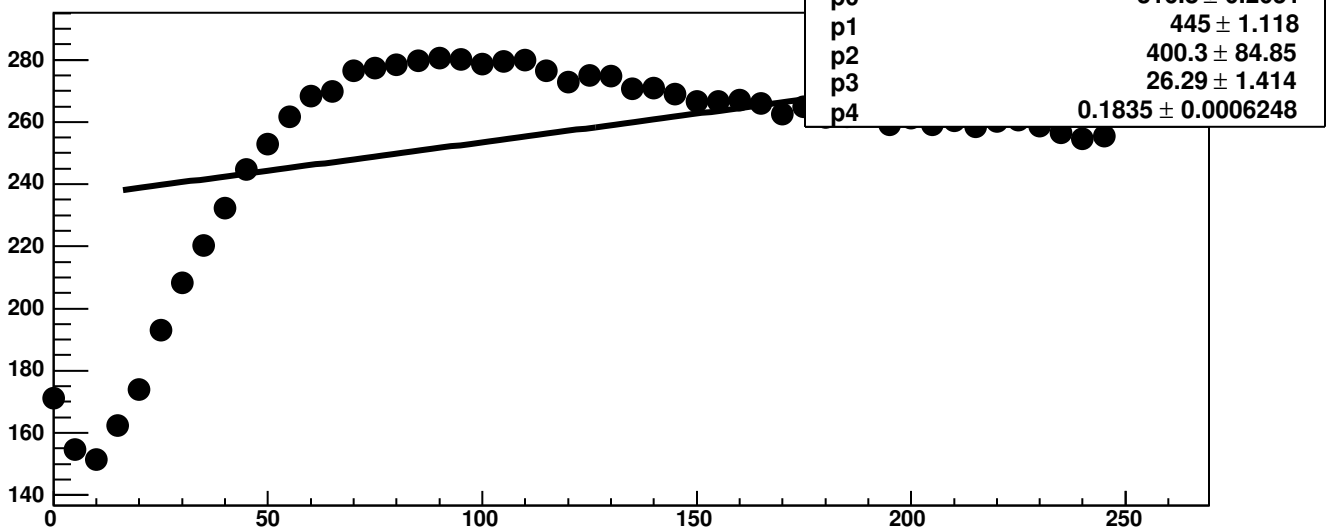



Chip 4, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 195.3 / 41
p0 266.6 ± 0.4968
p1 61.81 ± 0.4133
p2 -9.439e+07 ± 1.262e+06
p3 3.949e+07 ± 1.684e+05
p4 2.283 ± 0.03053
280
260
240
220
200
180
160
140
0
50
100
150
200
250

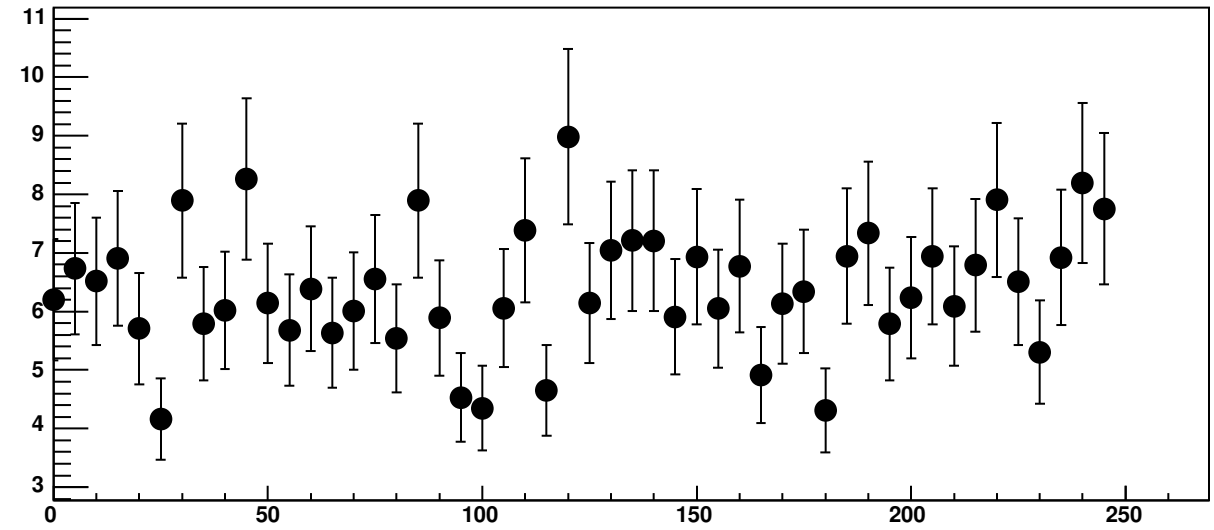

Chip 4, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 4, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
0
50
100
150
200
250

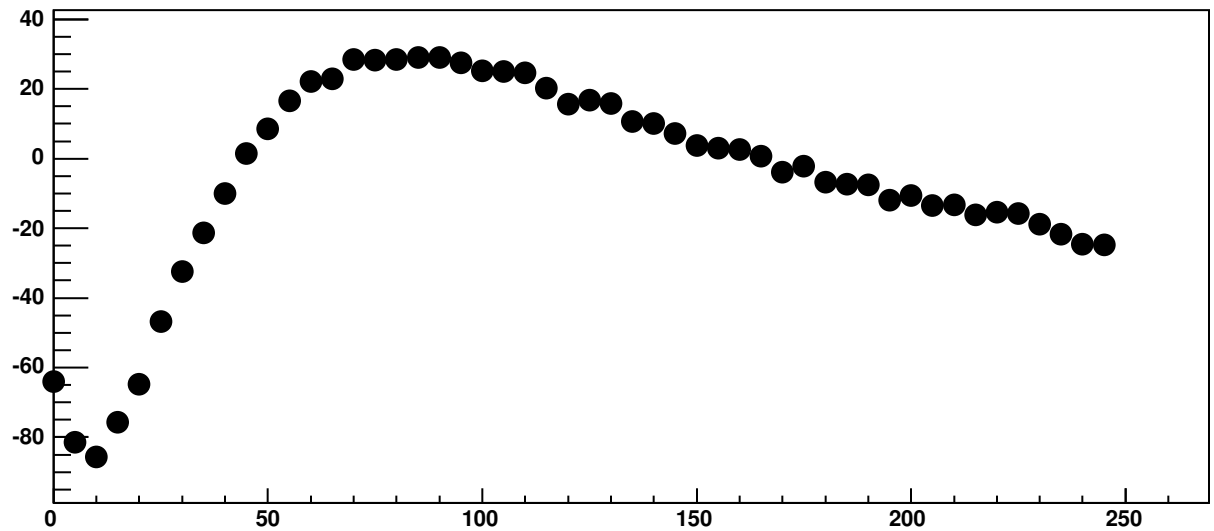
Chip 4, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 4, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold

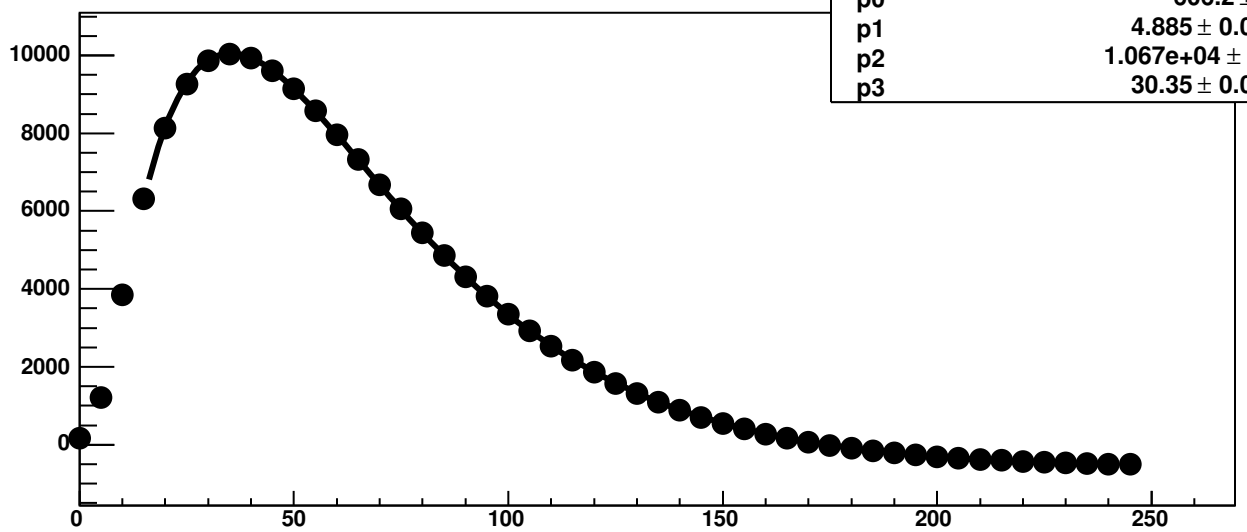


Chip 4, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold

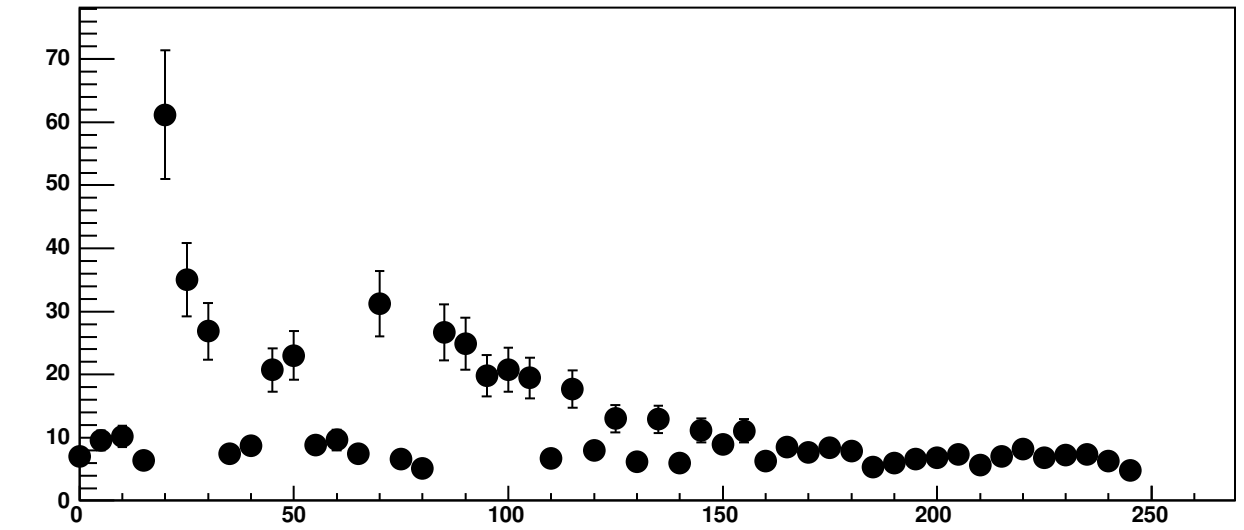


Chip 4, Channel 15, Enable 2!, DAC=4000, ADC Mean vs Hold

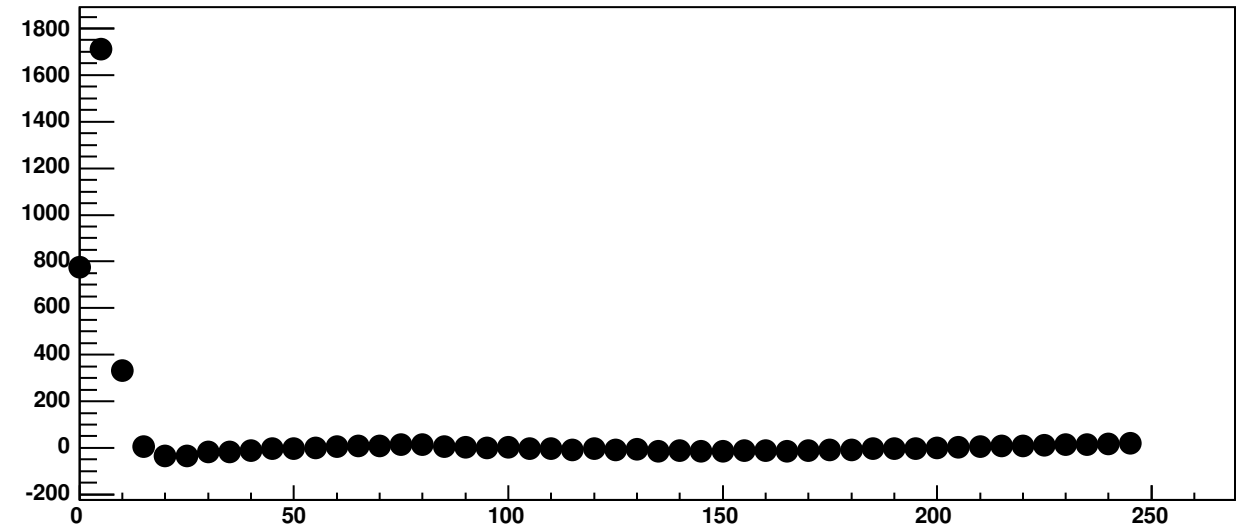
χ^2 / ndf	1238 / 42
p0	-606.2 ± 0.502
p1	4.885 ± 0.004554
p2	1.067e+04 ± 0.9902
p3	30.35 ± 0.004553

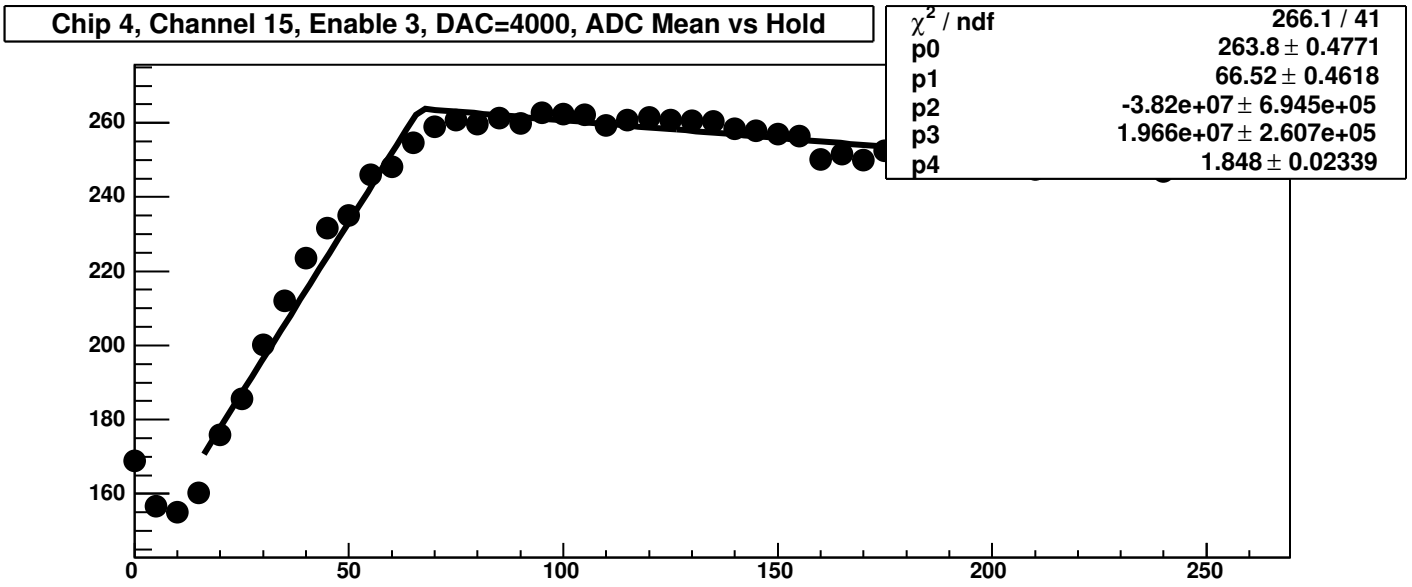


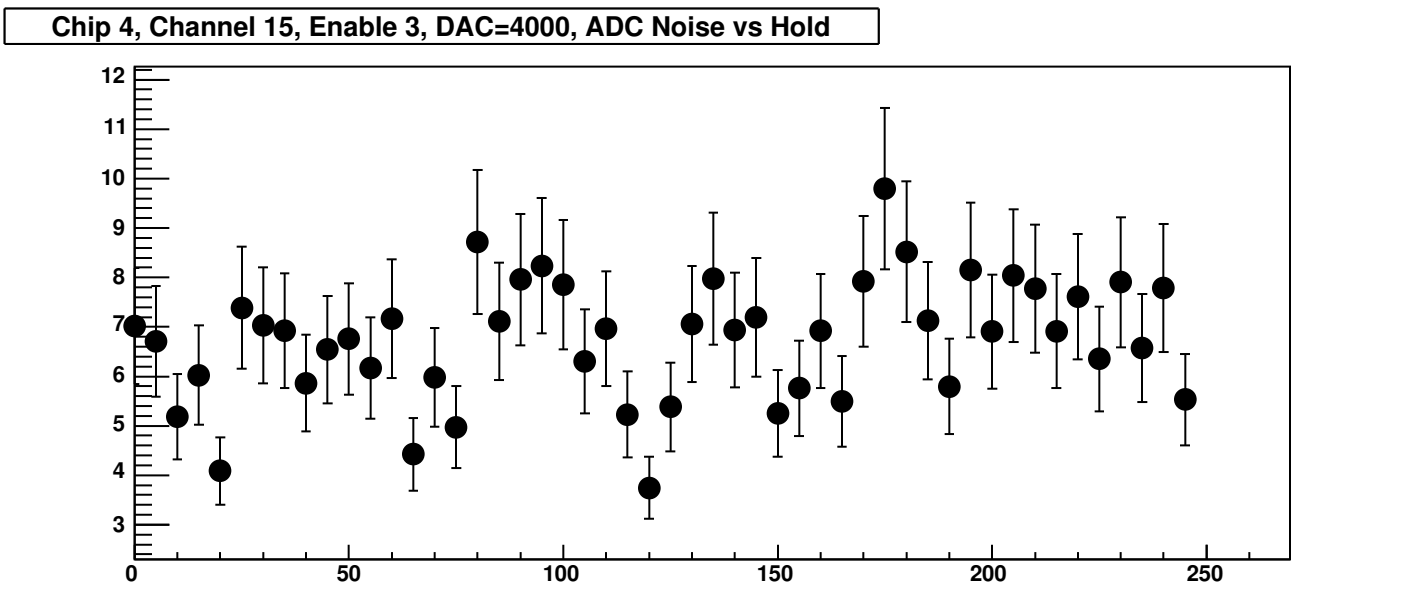
Chip 4, Channel 15, Enable 2!, DAC=4000, ADC Noise vs Hold

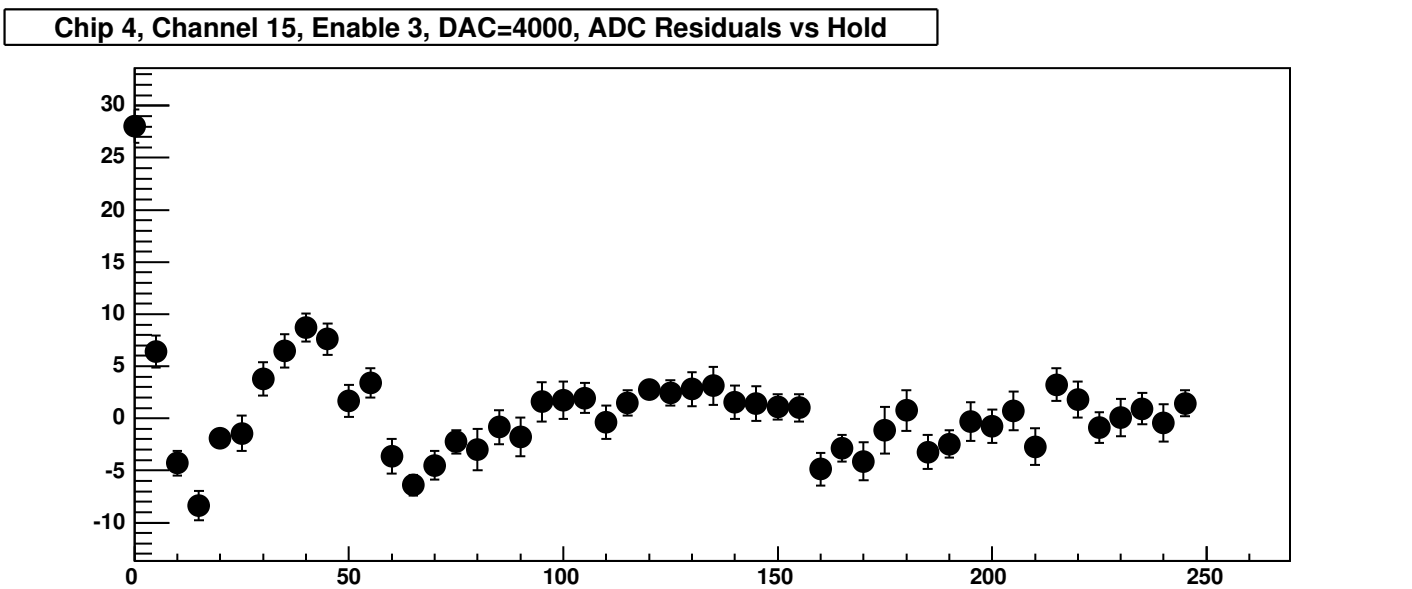


Chip 4, Channel 15, Enable 2!, DAC=4000, ADC Residuals vs Hold

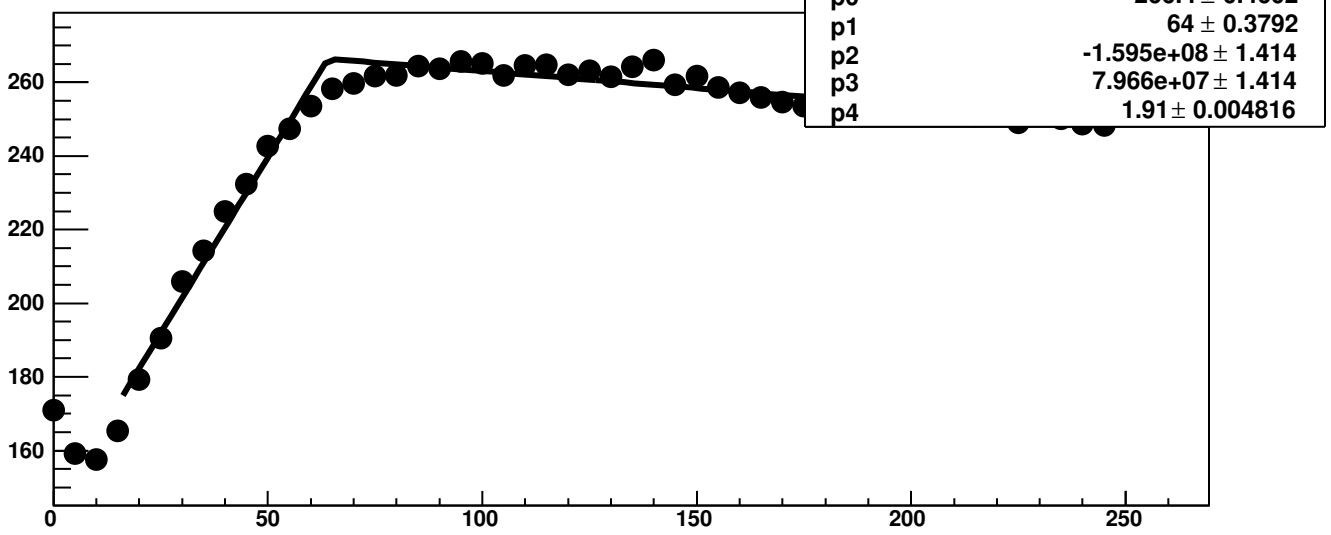



Chip 4, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 266.1 / 41
p0 263.8 ± 0.4771
p1 66.52 ± 0.4618
p2 -3.82e+07 ± 6.945e+05
p3 1.966e+07 ± 2.607e+05
p4 1.848 ± 0.02339
260
240
220
200
180
160
0
50
100
150
200
250

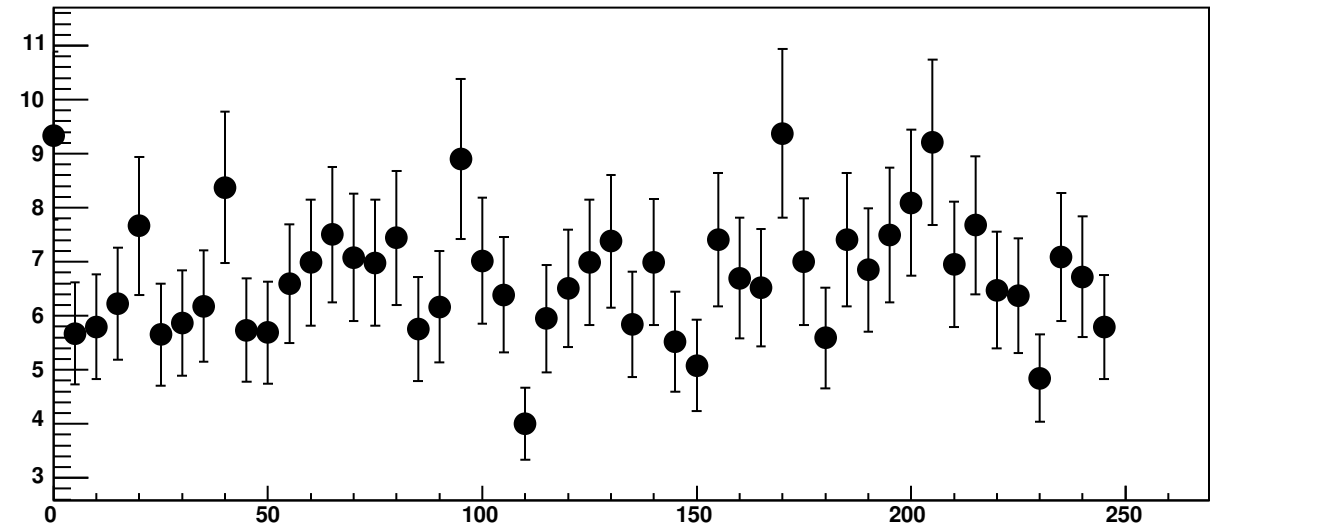

Chip 4, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 4, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold
30
25
20
15
10
5
0
-5
-10
0
50
100
150
200
250

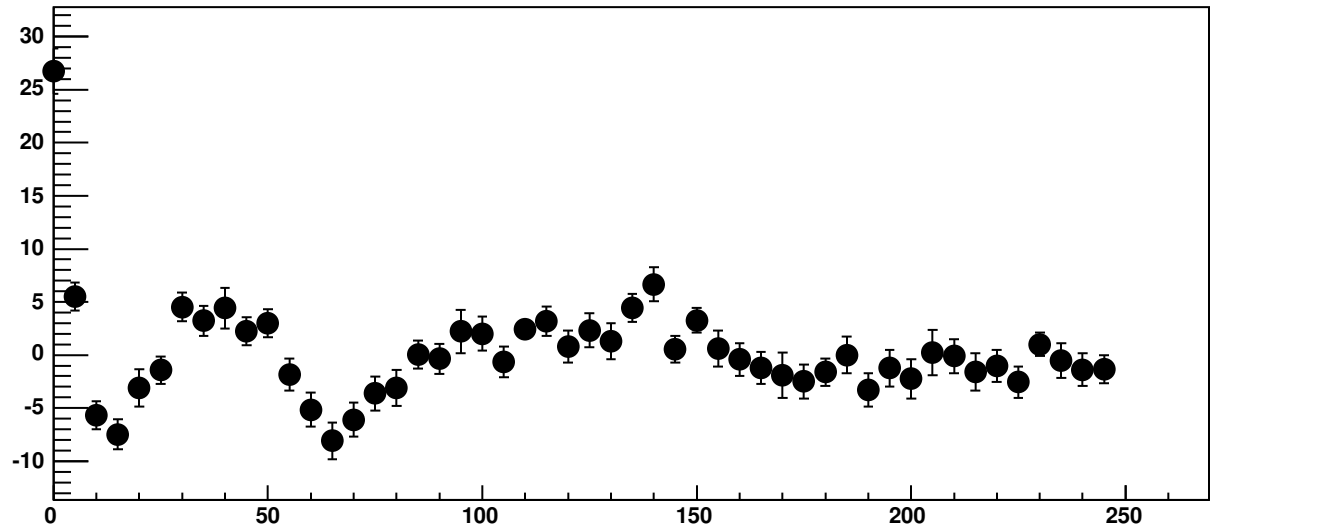
Chip 4, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold



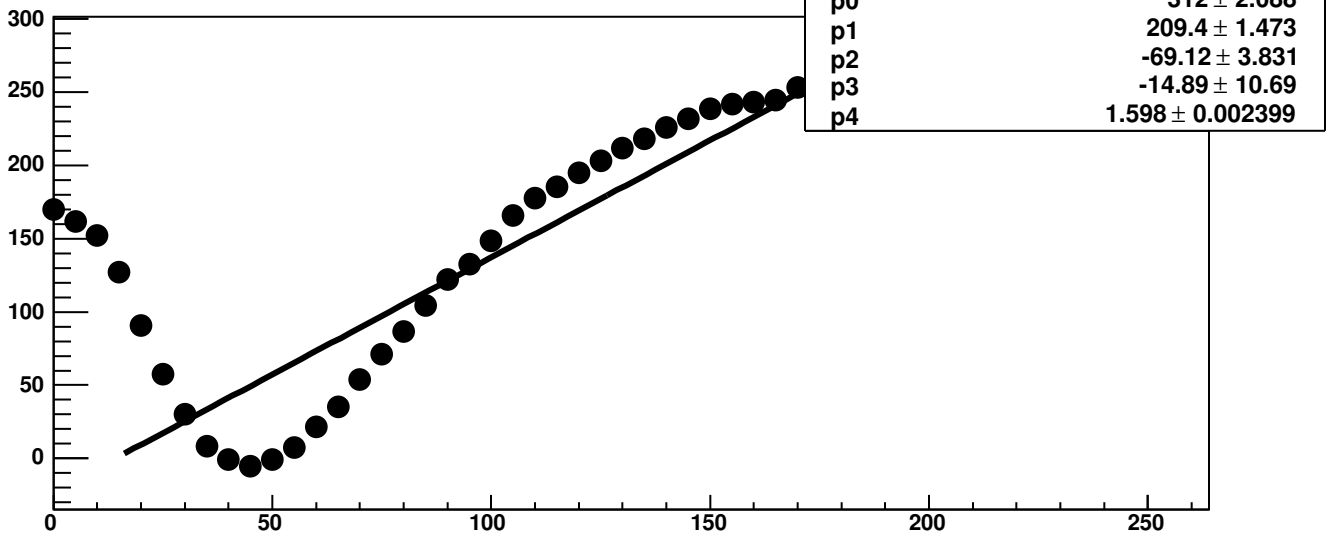
Chip 4, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold



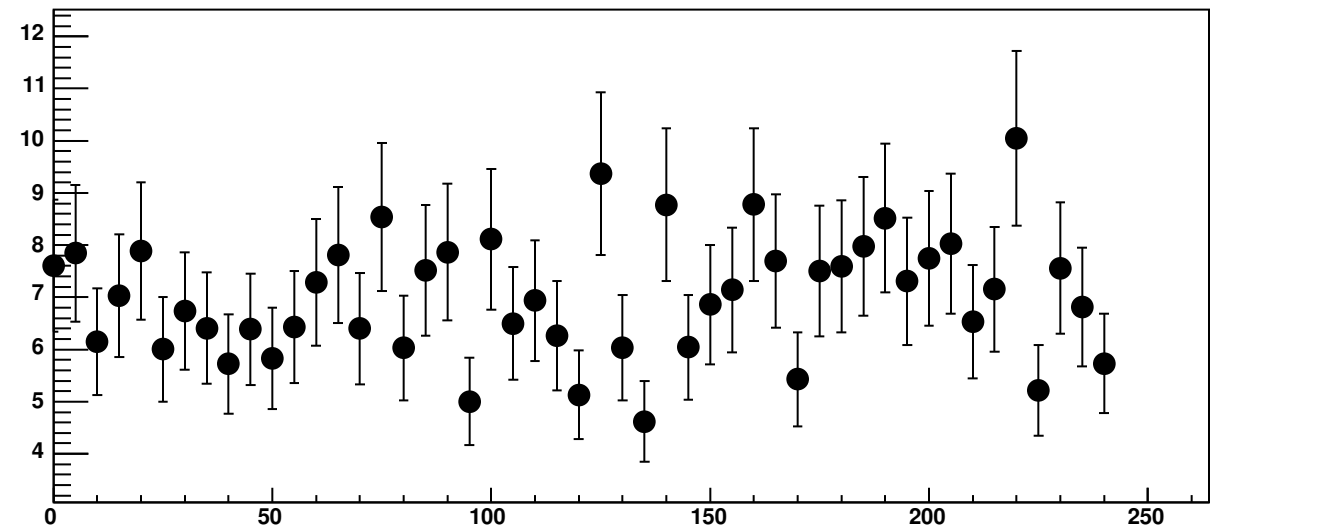
Chip 4, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold



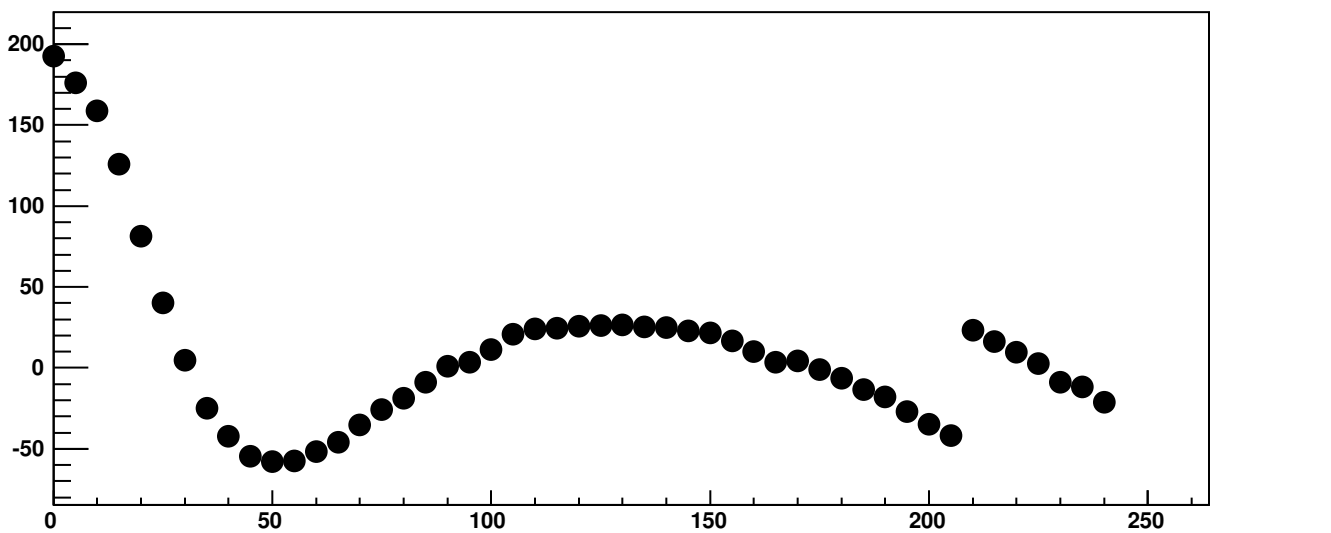
Chip 4, Channel 15, Enable 5, DAC=4000, ADC Mean vs Hold



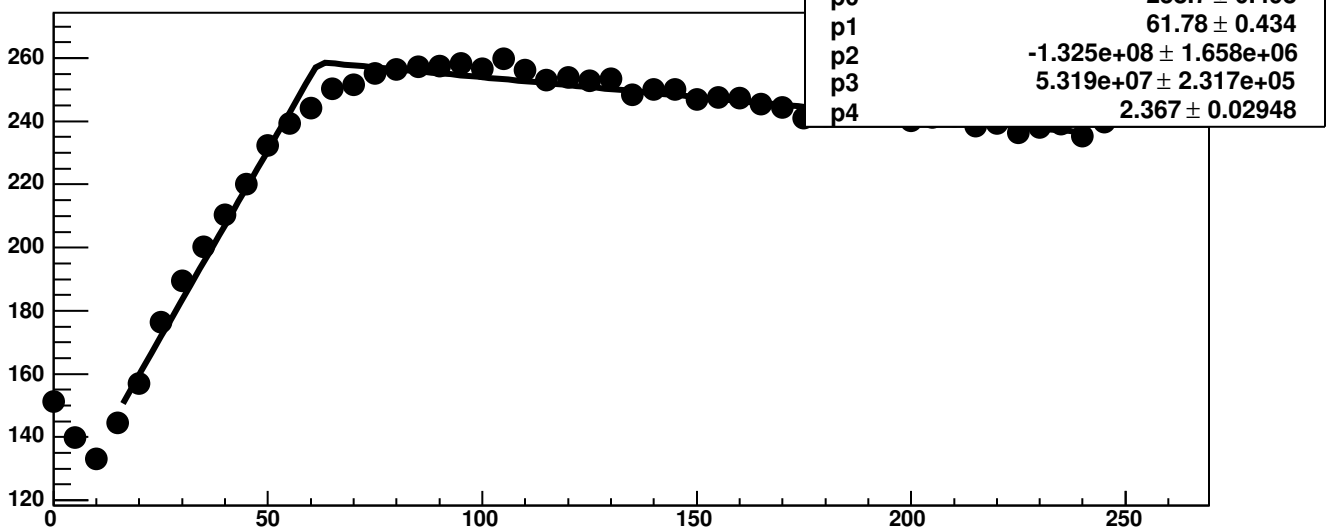
Chip 4, Channel 15, Enable 5, DAC=4000, ADC Noise vs Hold



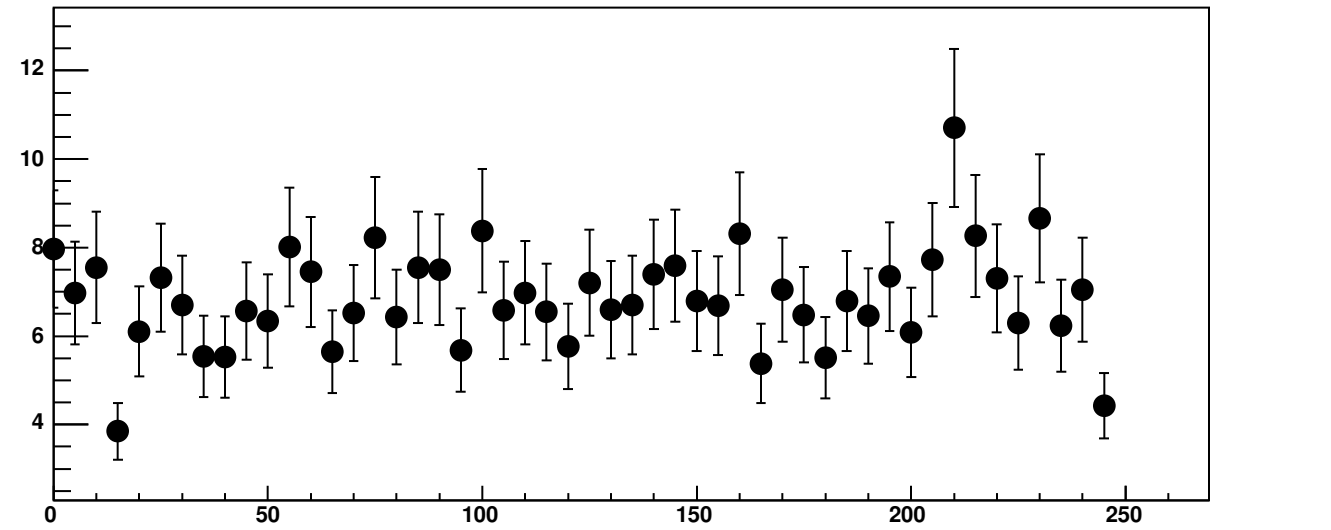
Chip 4, Channel 15, Enable 5, DAC=4000, ADC Residuals vs Hold



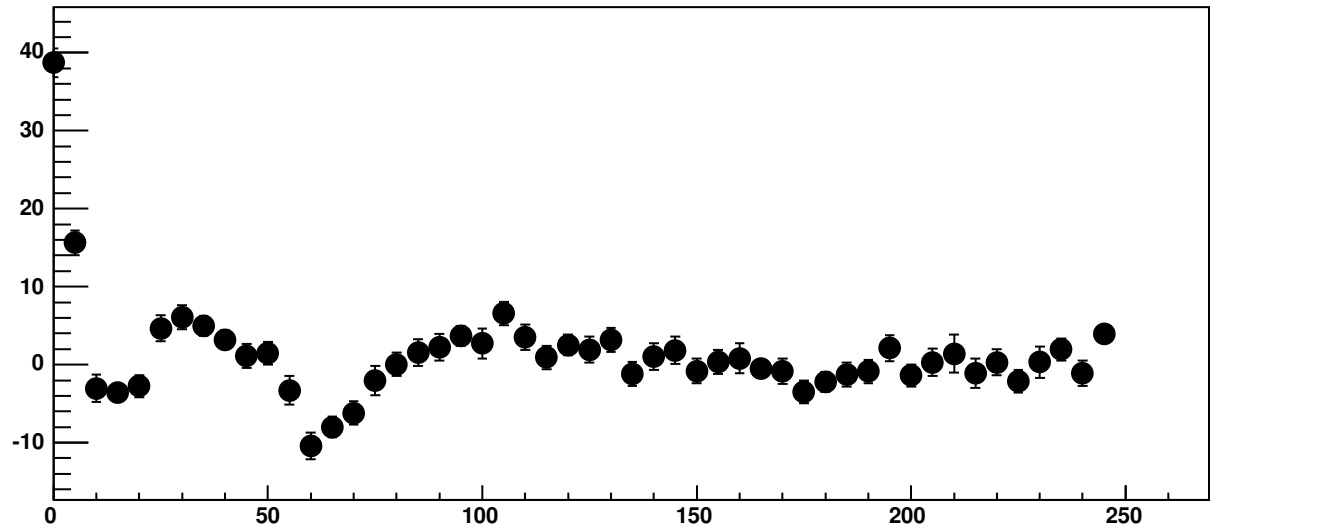
Chip 4, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold



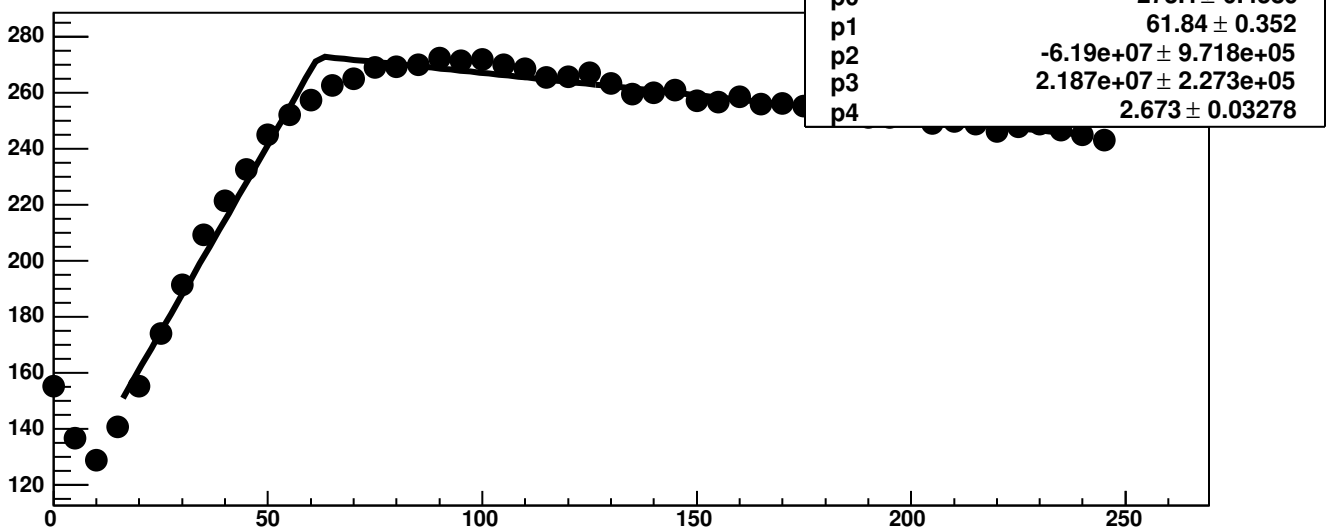
Chip 4, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold



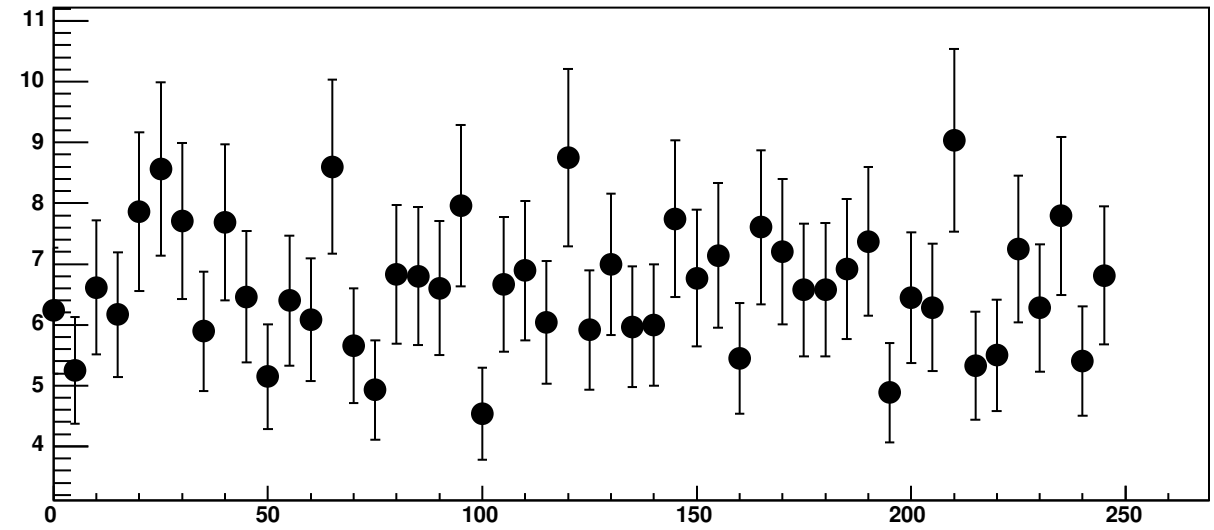
Chip 4, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold



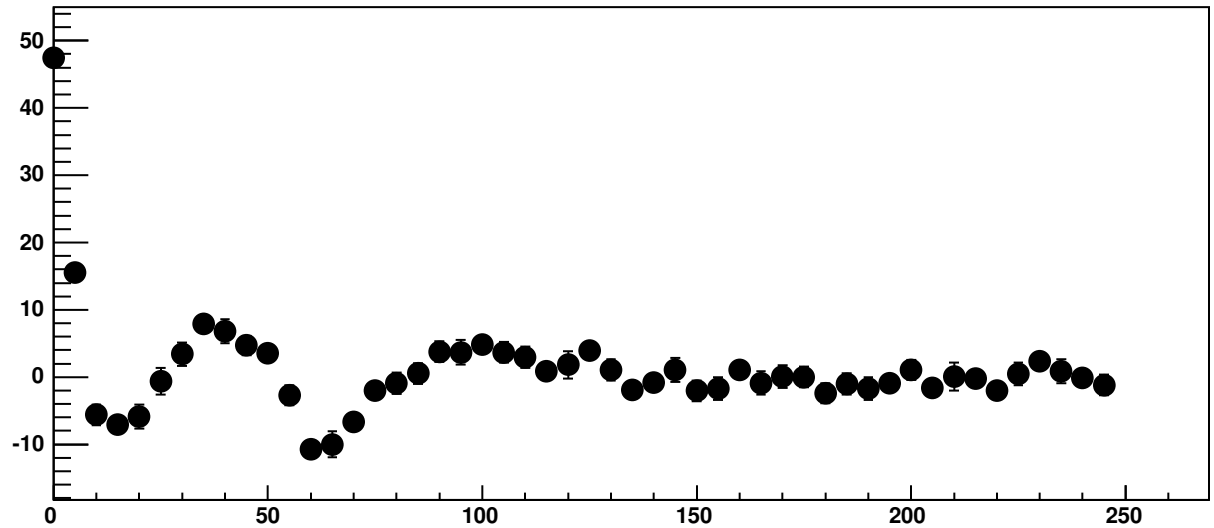
Chip 4, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold



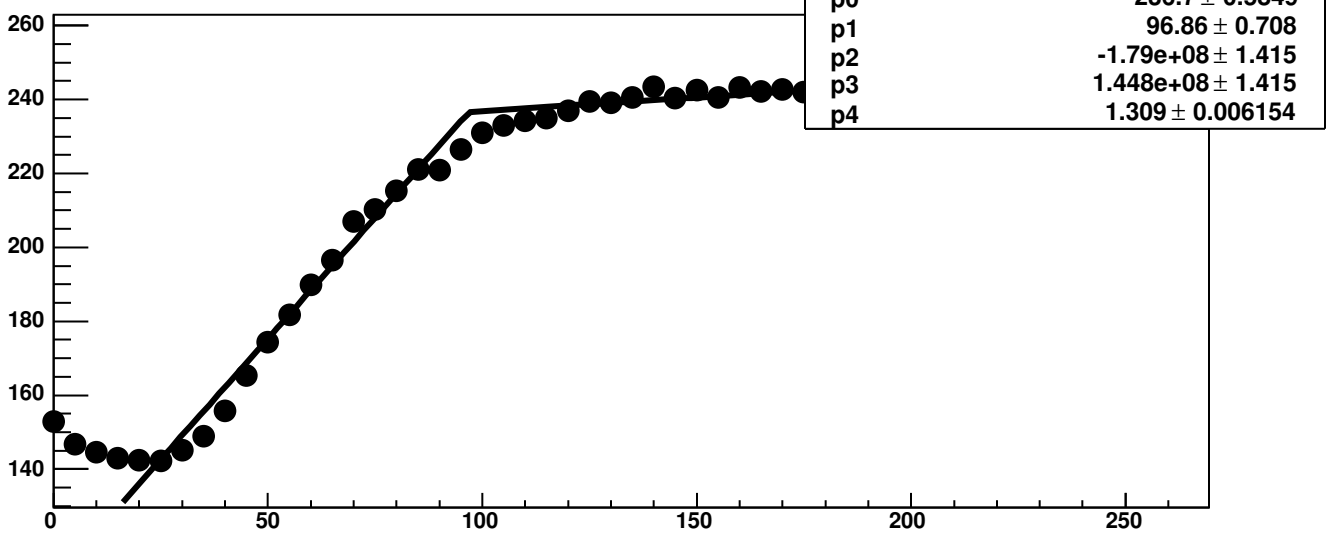
Chip 4, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold



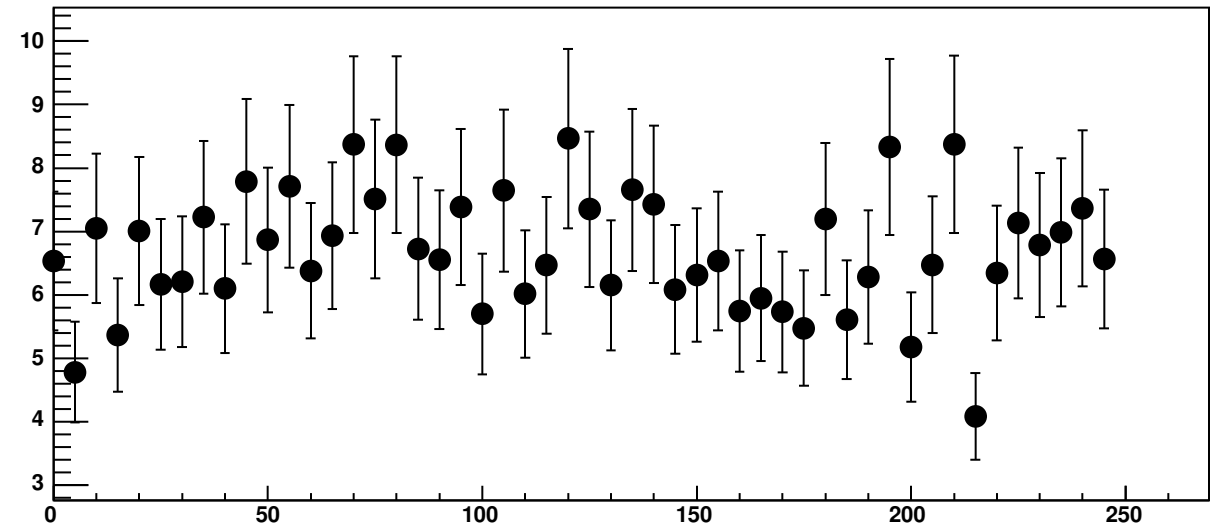
Chip 4, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold



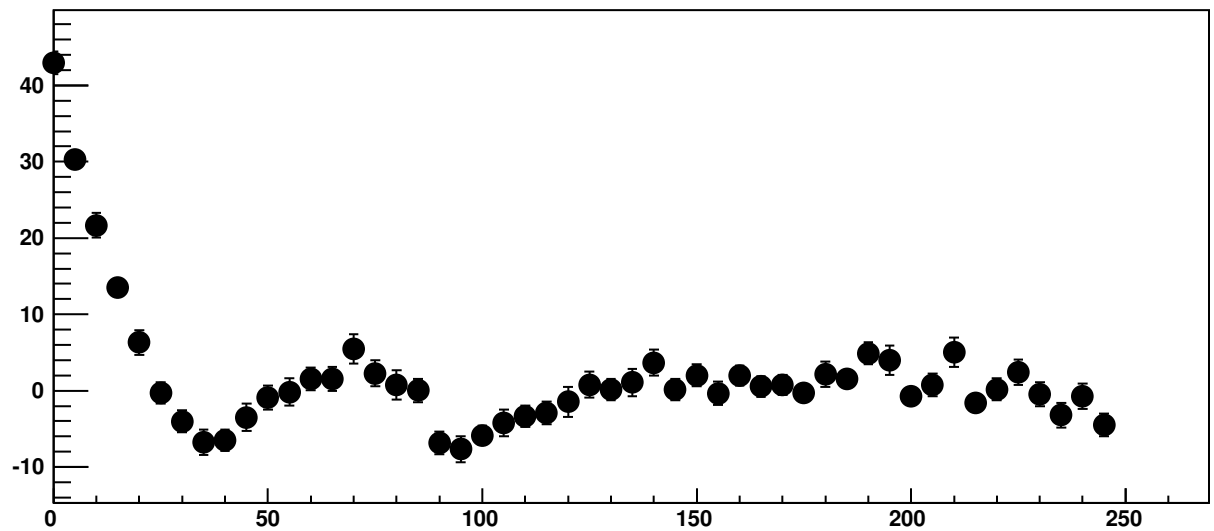
Chip 4, Channel 16, Enable 2, DAC=4000, ADC Mean vs Hold



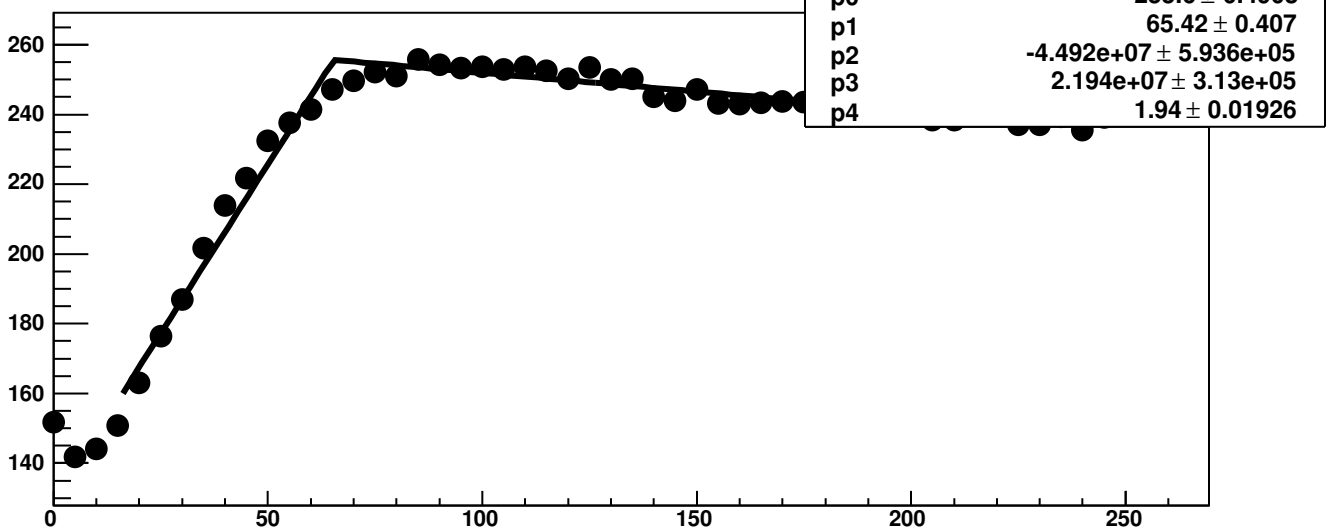
Chip 4, Channel 16, Enable 2, DAC=4000, ADC Noise vs Hold



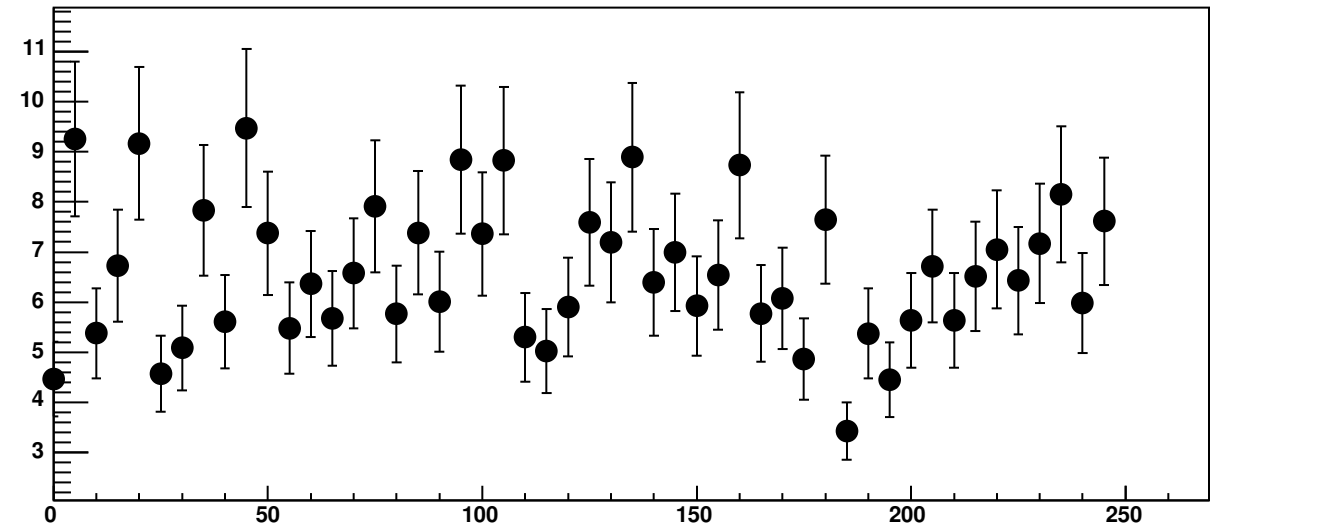
Chip 4, Channel 16, Enable 2, DAC=4000, ADC Residuals vs Hold



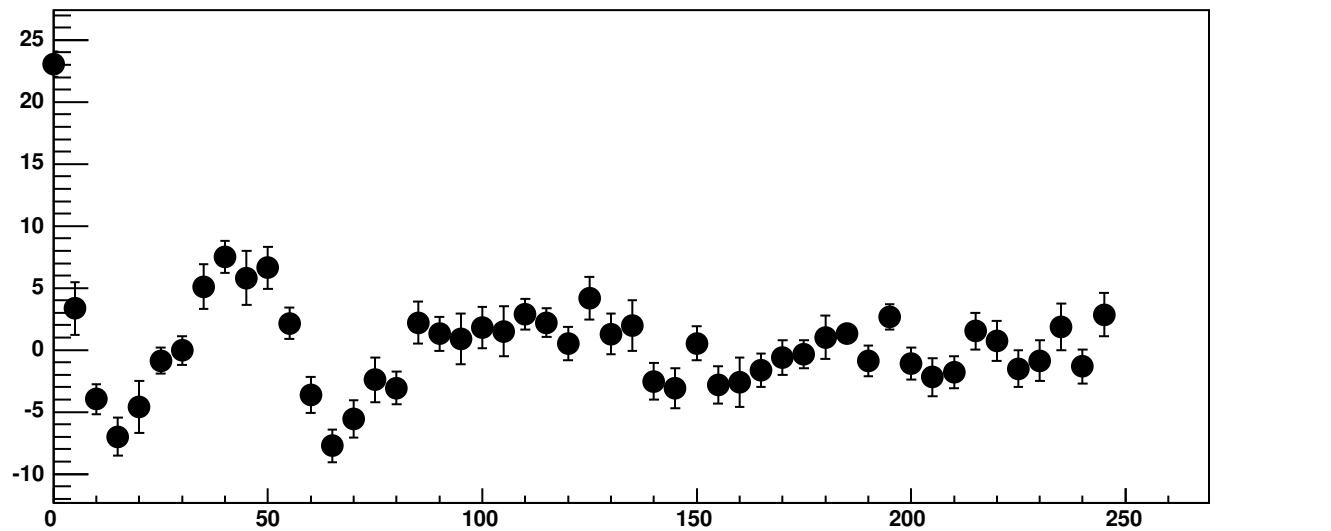
Chip 4, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold



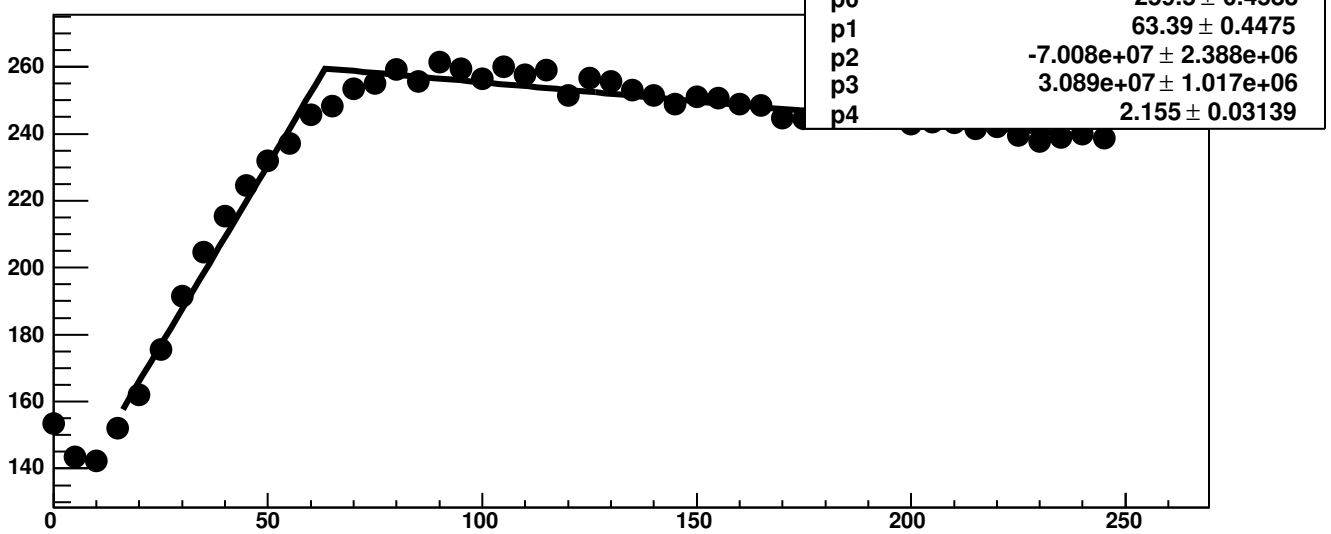
Chip 4, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



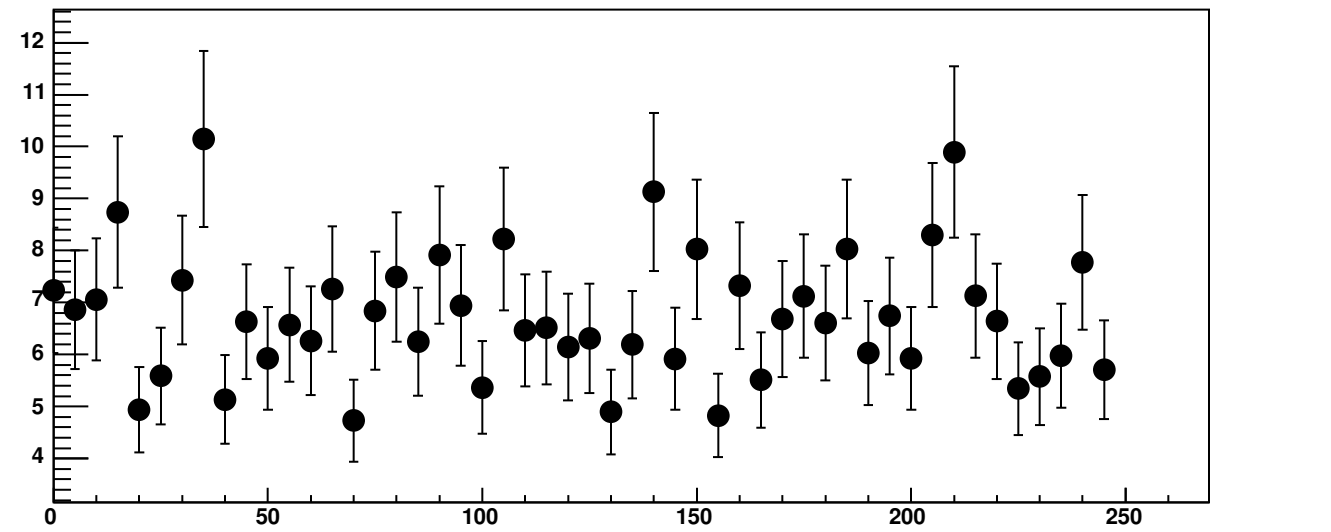
Chip 4, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold



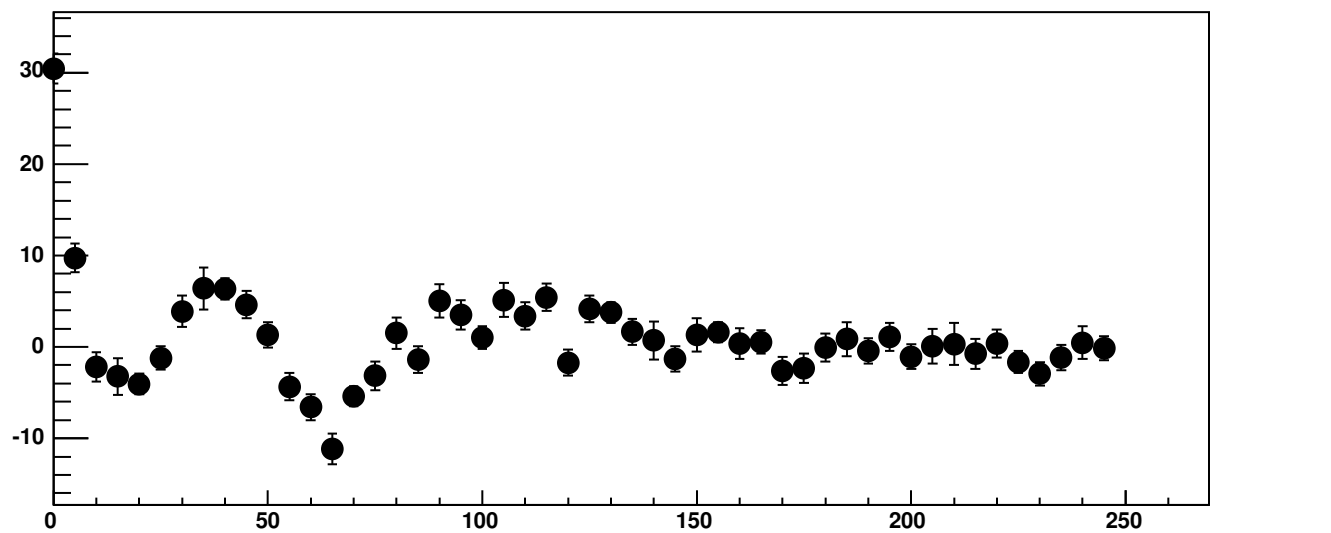
Chip 4, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

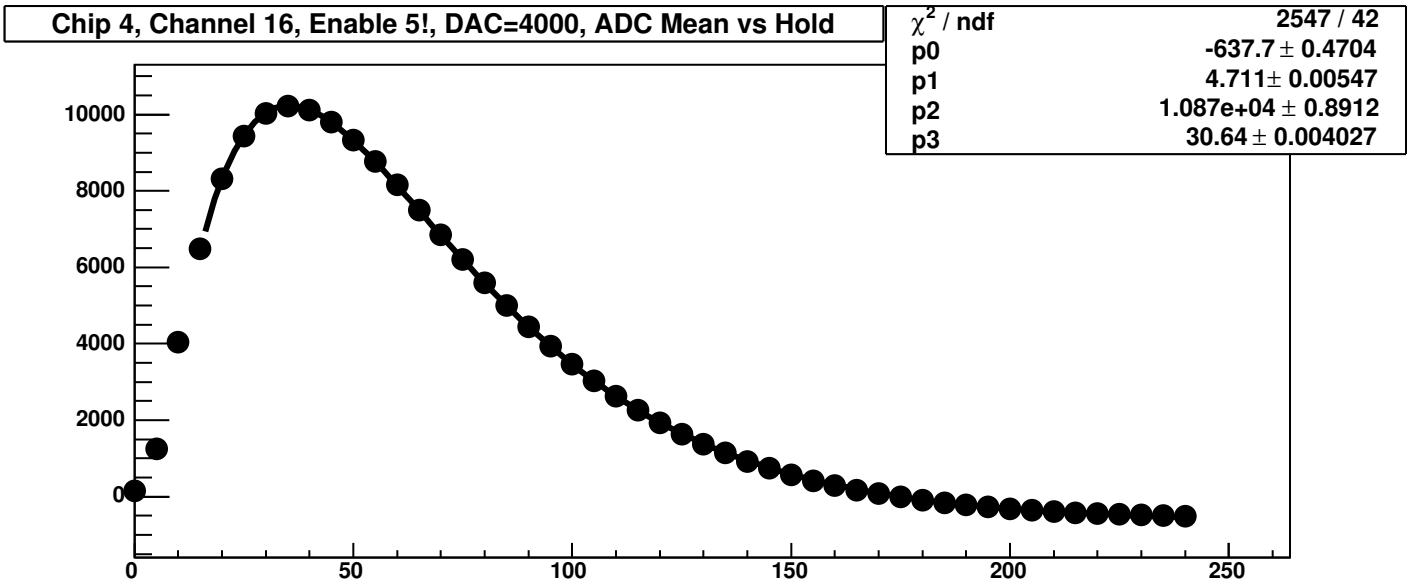


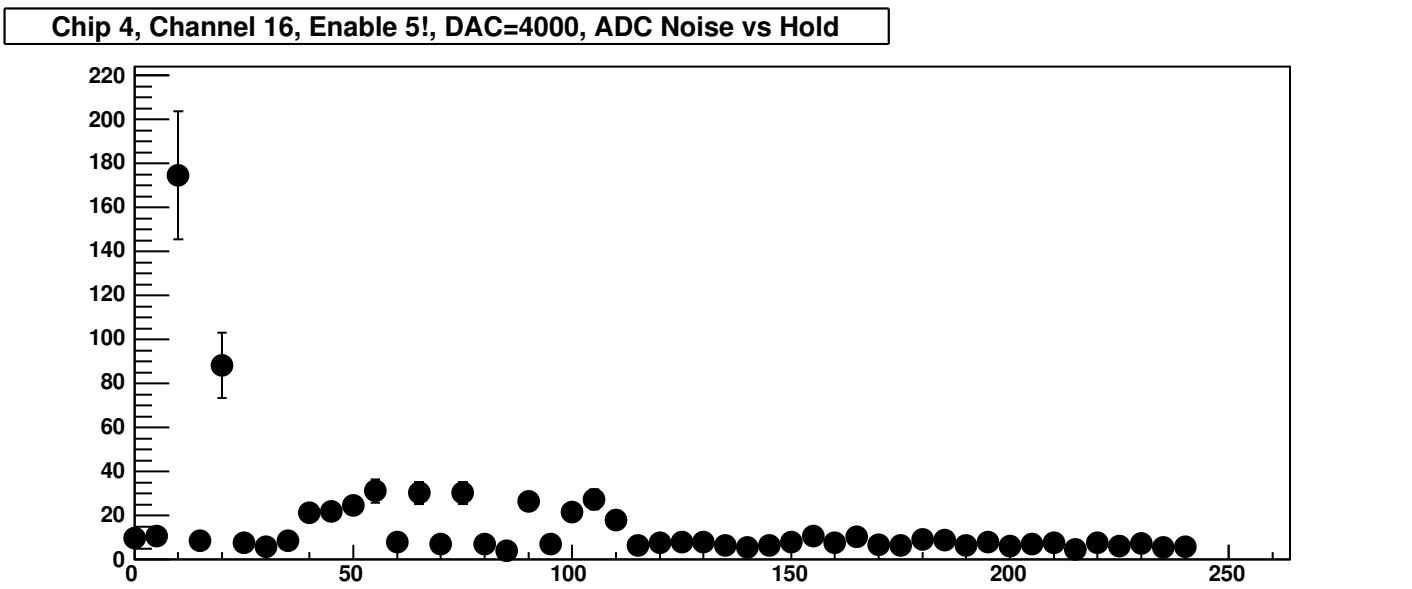
Chip 4, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold

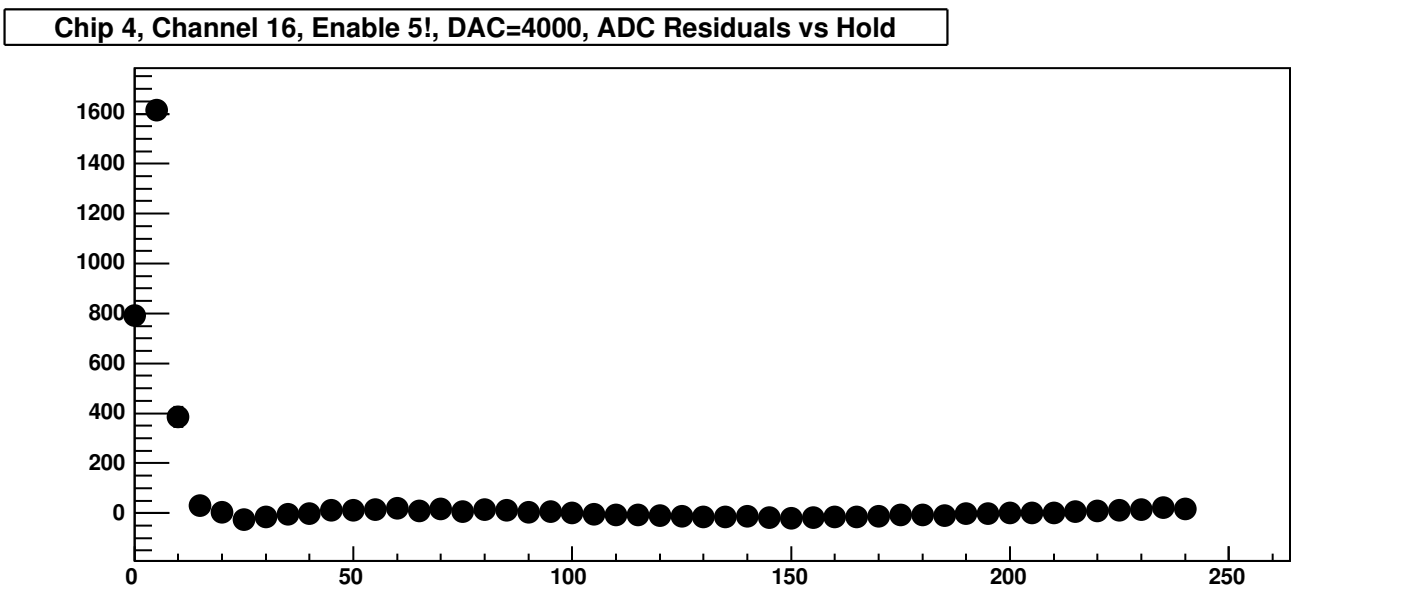


Chip 4, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

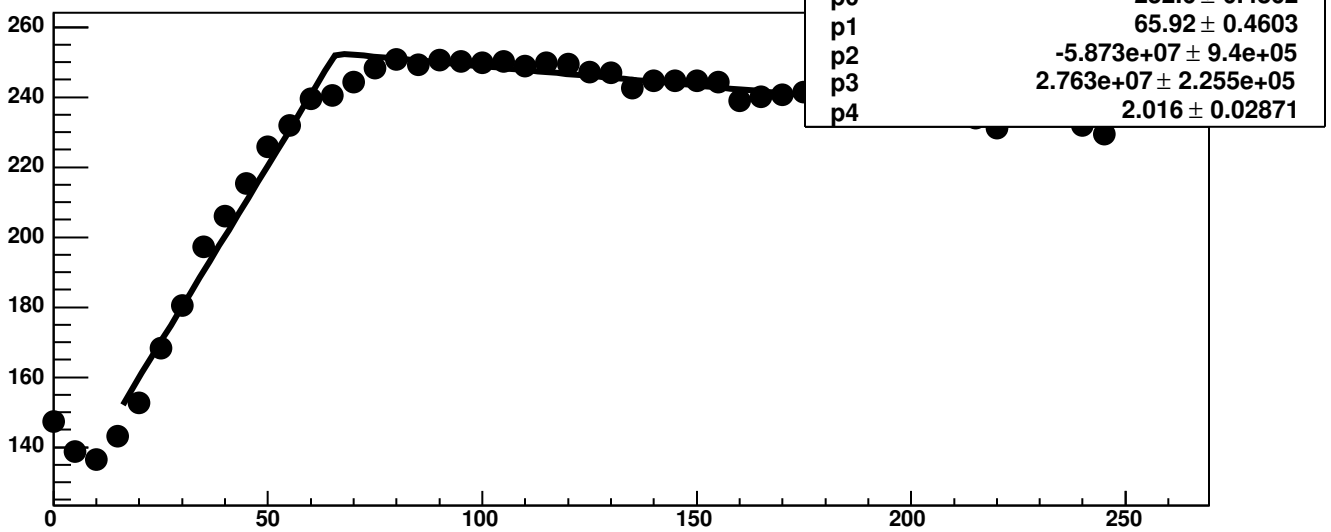



Chip 4, Channel 16, Enable 5!, DAC=4000, ADC Mean vs Hold
χ² / ndf 2547 / 42
p0 -637.7 ± 0.4704
p1 4.711 ± 0.00547
p2 1.087e+04 ± 0.8912
p3 30.64 ± 0.004027
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

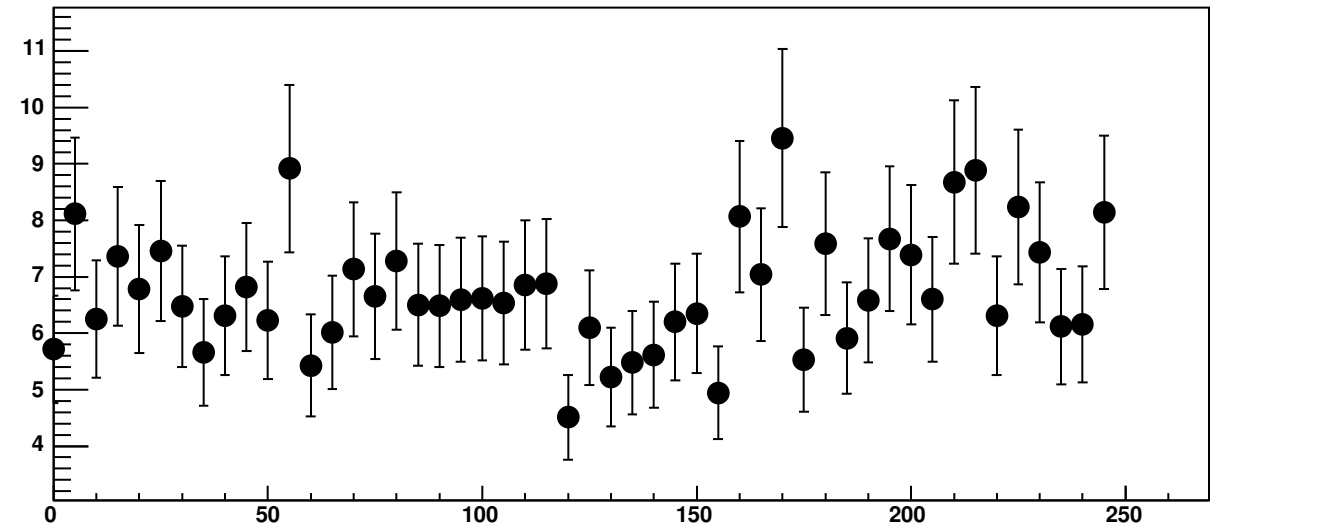

Chip 4, Channel 16, Enable 5!, DAC=4000, ADC Noise vs Hold
220
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 4, Channel 16, Enable 5!, DAC=4000, ADC Residuals vs Hold
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

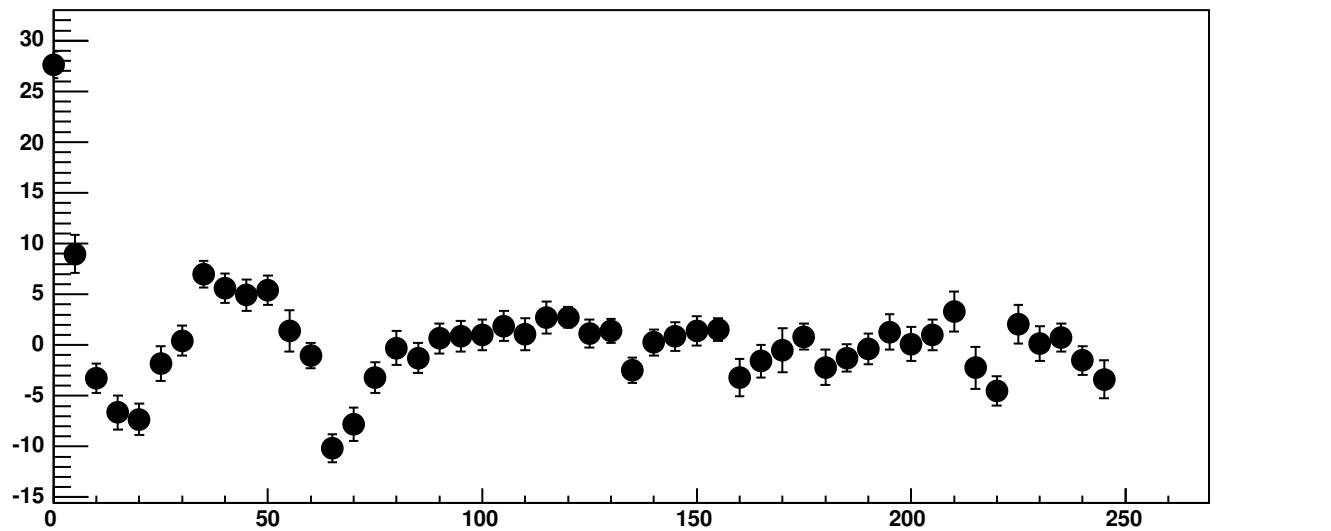
Chip 4, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold

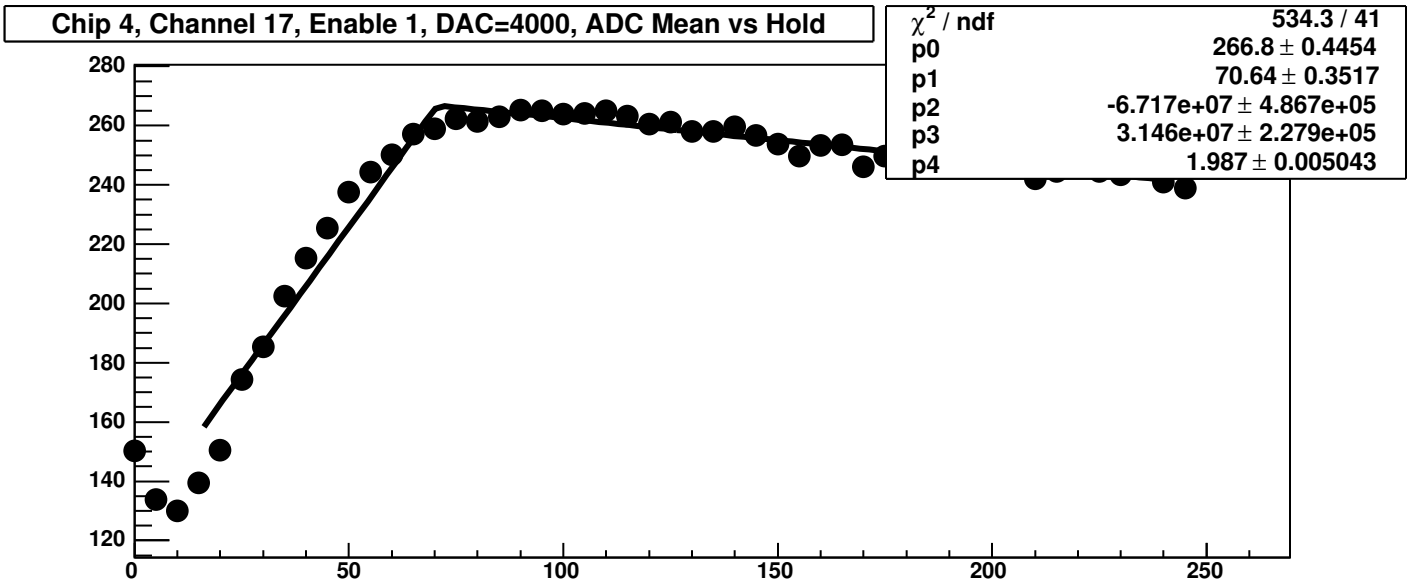


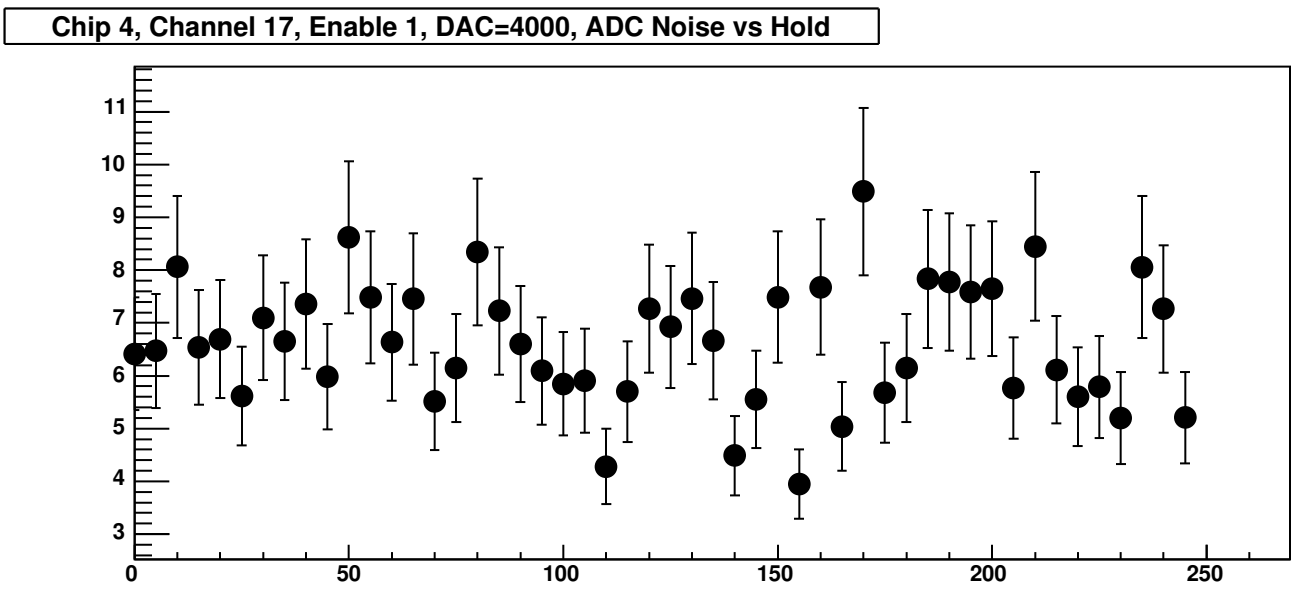
Chip 4, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold

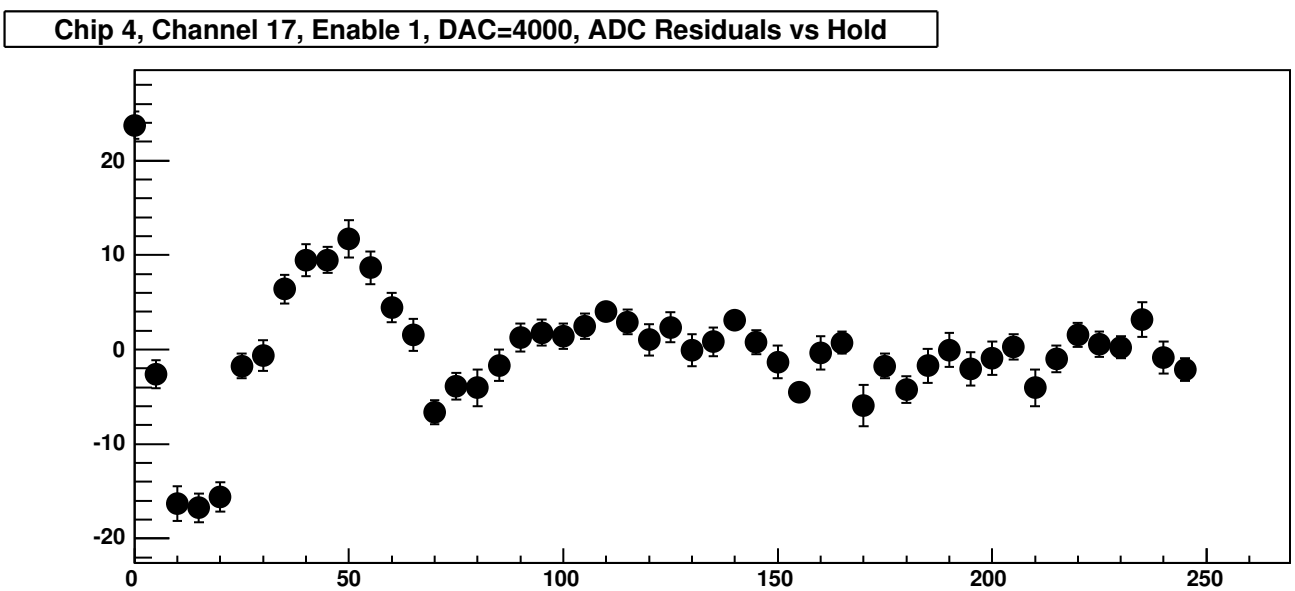


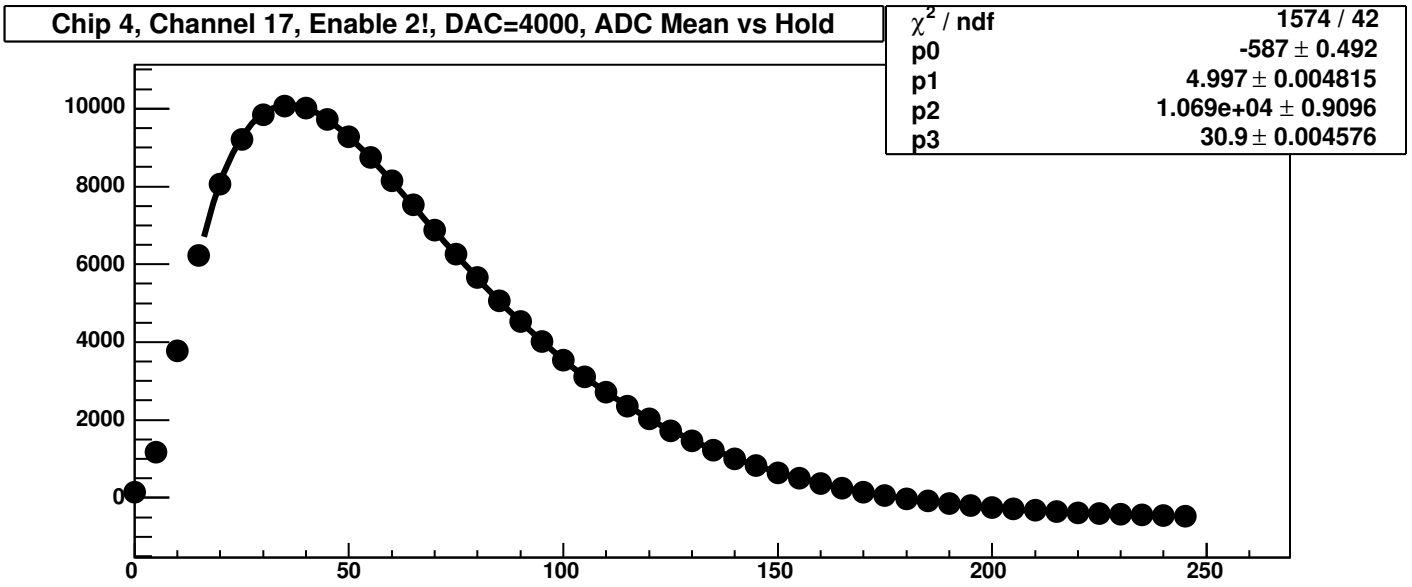
Chip 4, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold

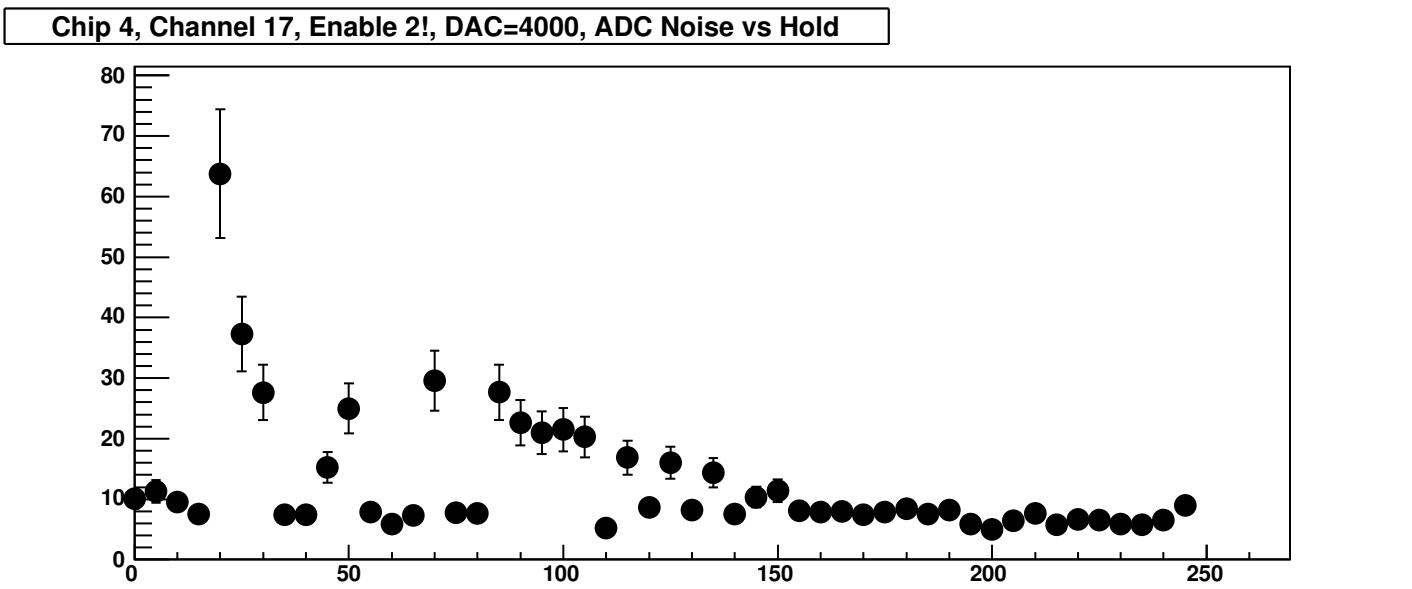


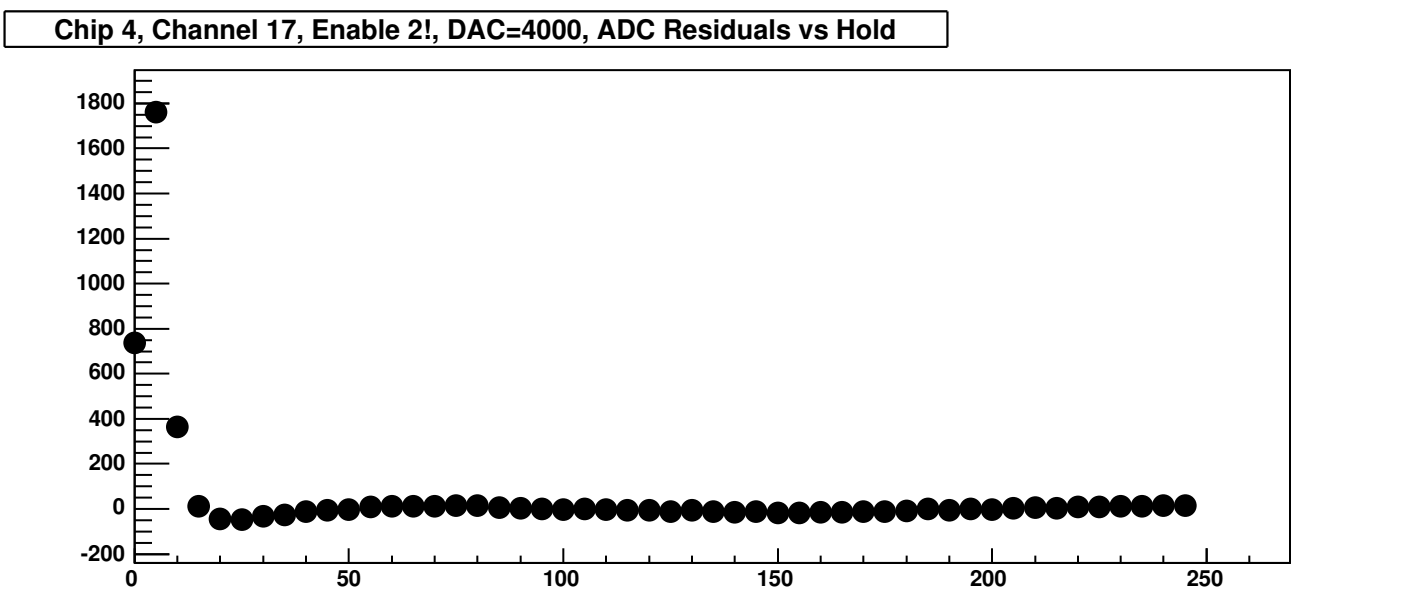

Chip 4, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 534.3 / 41
p0 266.8 ± 0.4454
p1 70.64 ± 0.3517
p2 -6.717e+07 ± 4.867e+05
p3 3.146e+07 ± 2.279e+05
p4 1.987 ± 0.005043
280
260
240
220
200
180
160
140
120
0
50
100
150
200
250


Chip 4, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

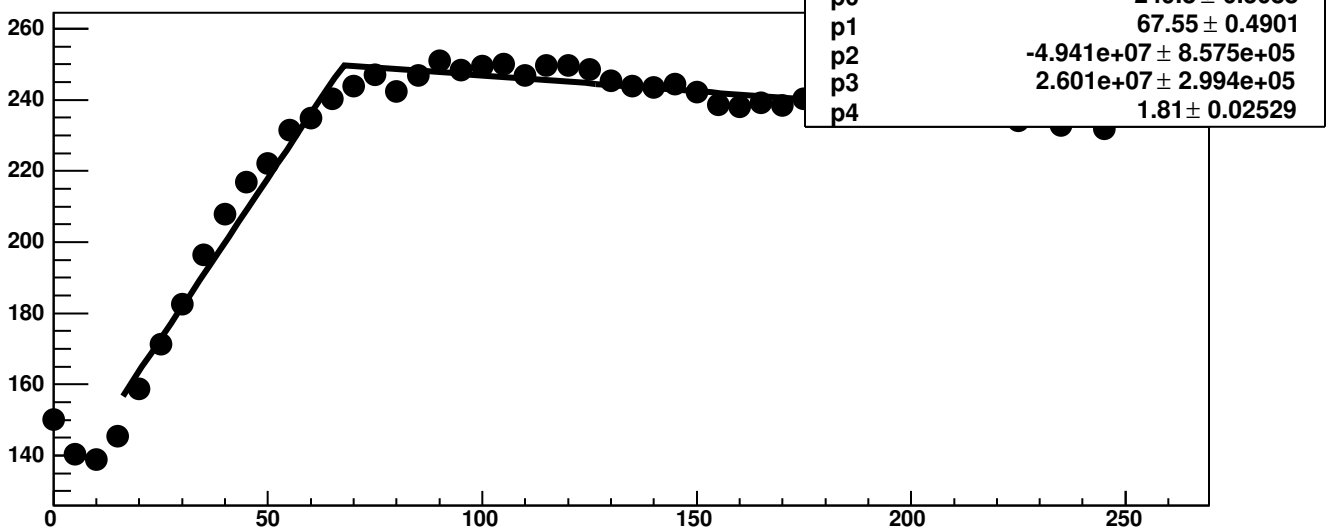

Chip 4, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold
20
10
0
-10
-20
0
50
100
150
200
250


Chip 4, Channel 17, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1574 / 42
p0 -587 ± 0.492
p1 4.997 ± 0.004815
p2 1.069e+04 ± 0.9096
p3 30.9 ± 0.004576
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

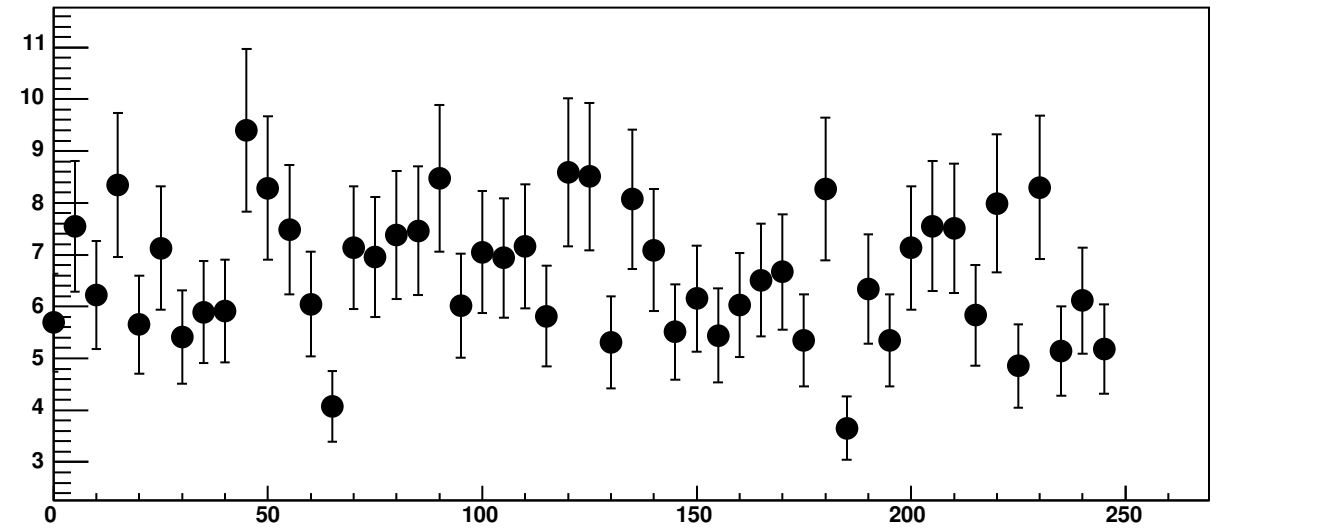

Chip 4, Channel 17, Enable 2!, DAC=4000, ADC Noise vs Hold
80
70
60
50
40
30
20
10
0
0
50
100
150
200
250


Chip 4, Channel 17, Enable 2!, DAC=4000, ADC Residuals vs Hold
1800
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250

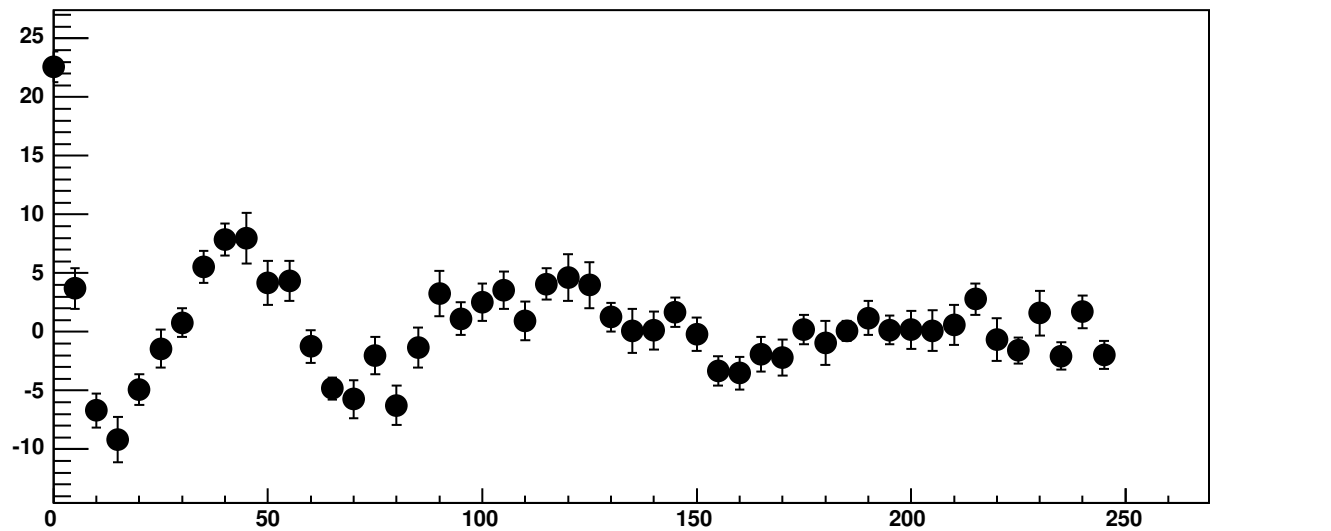
Chip 4, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



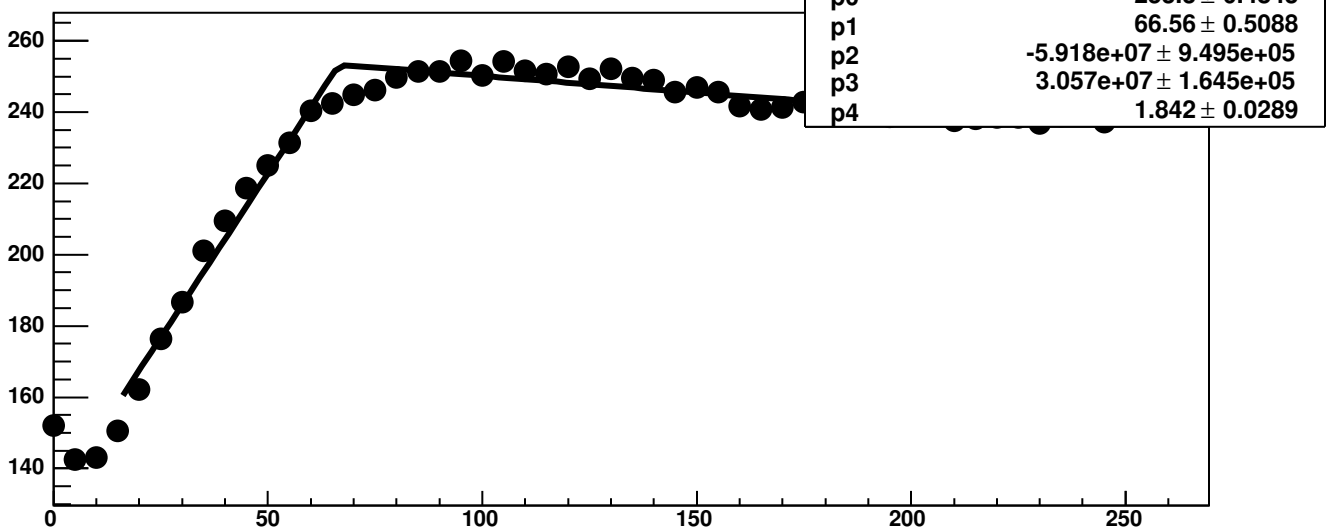
Chip 4, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



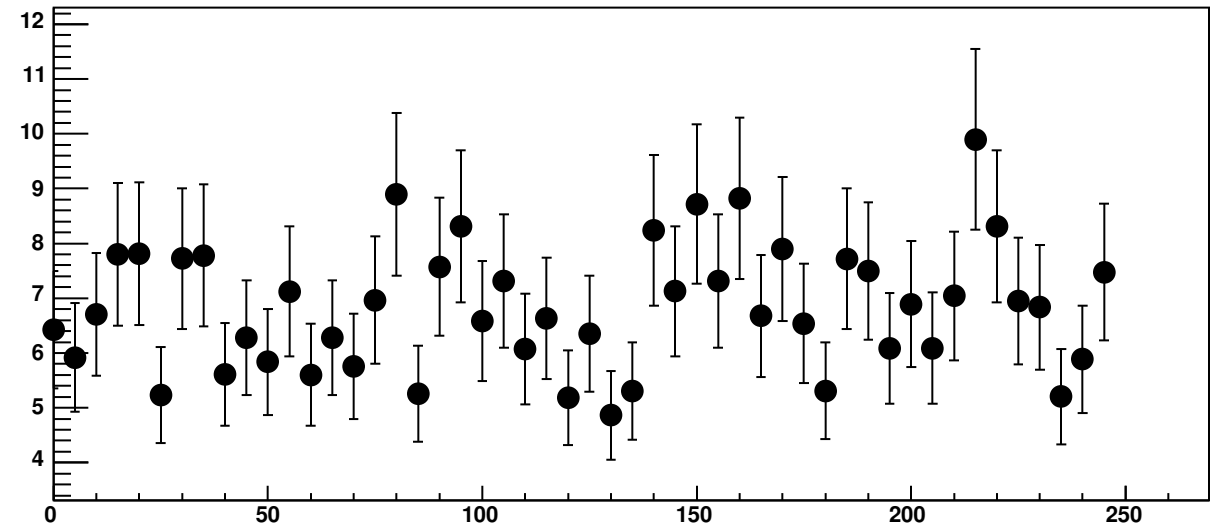
Chip 4, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold



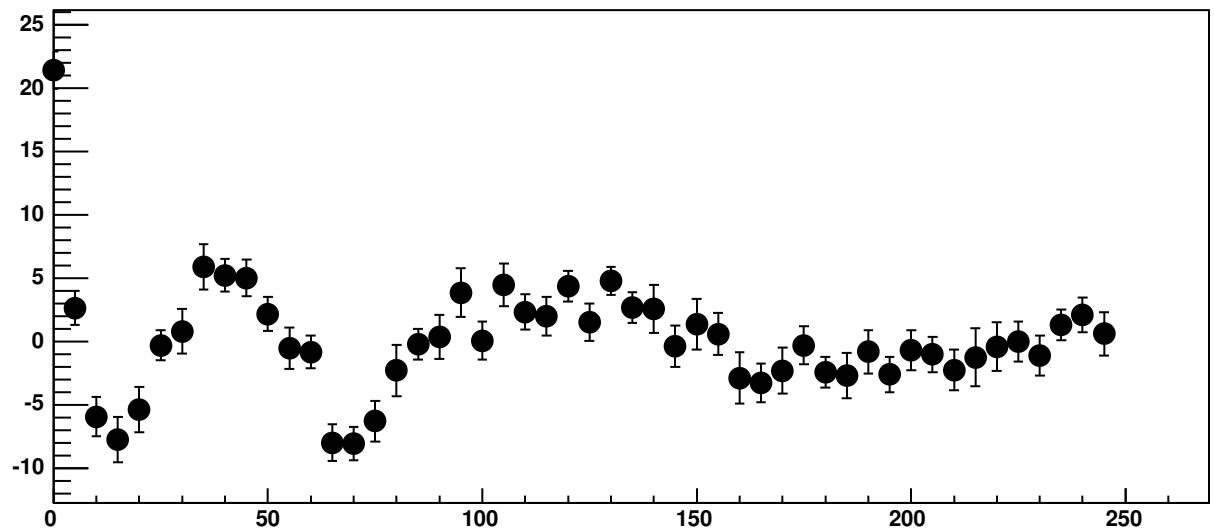
Chip 4, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



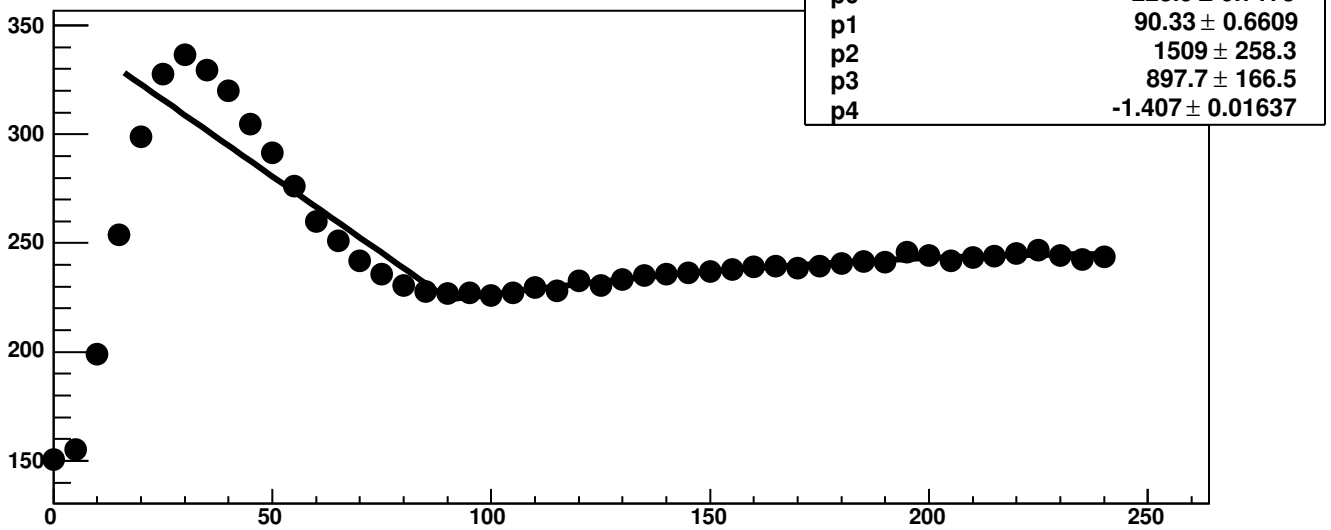
Chip 4, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



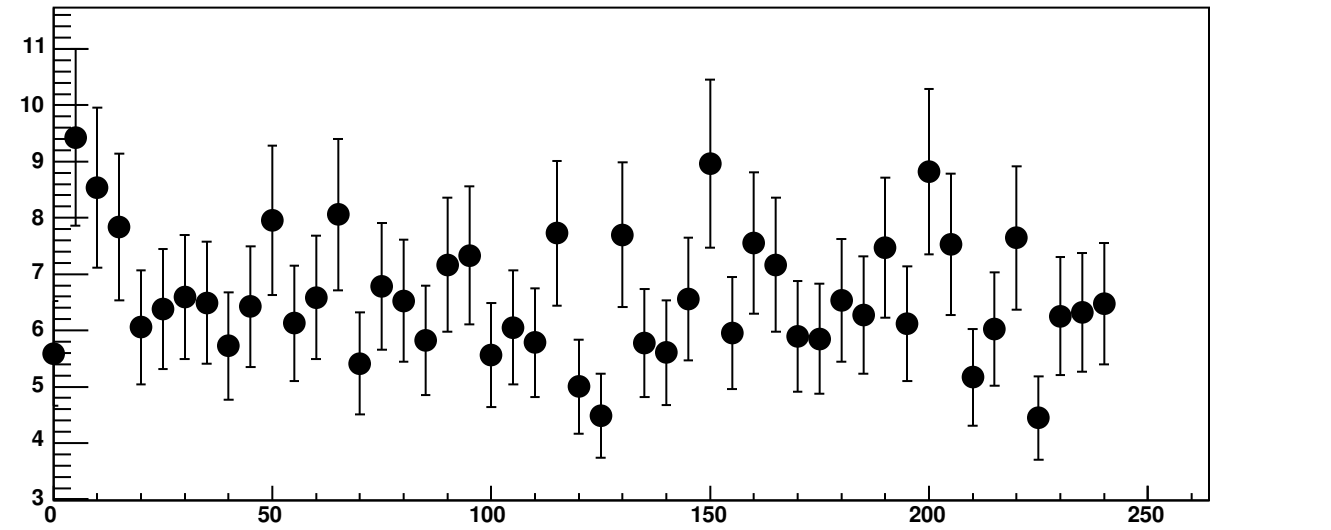
Chip 4, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold



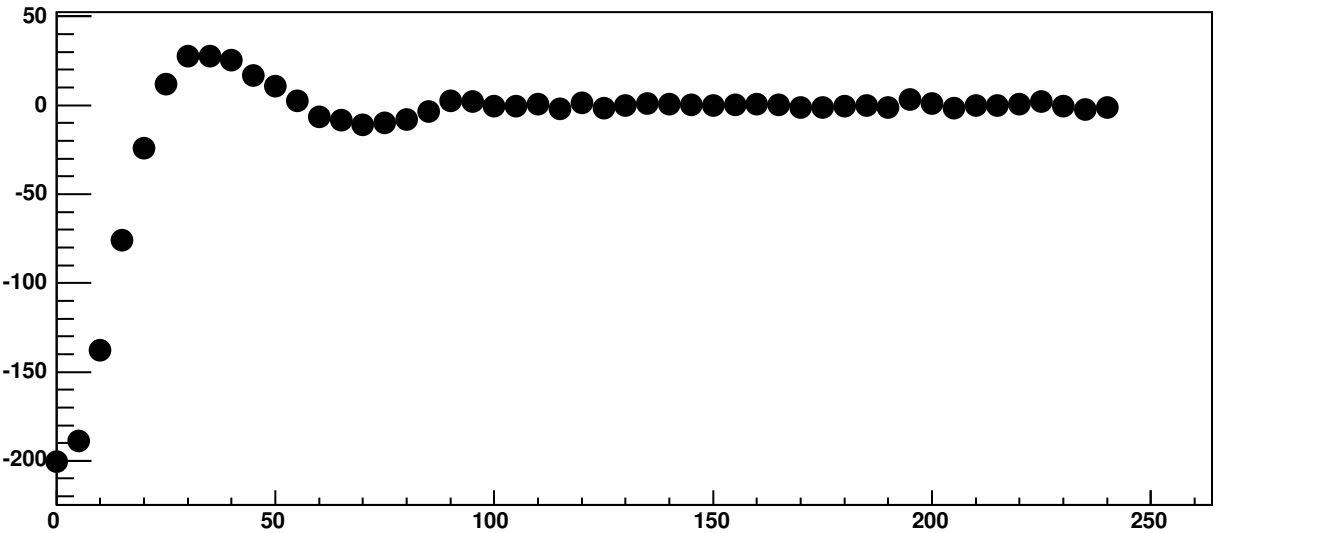
Chip 4, Channel 17, Enable 5, DAC=4000, ADC Mean vs Hold



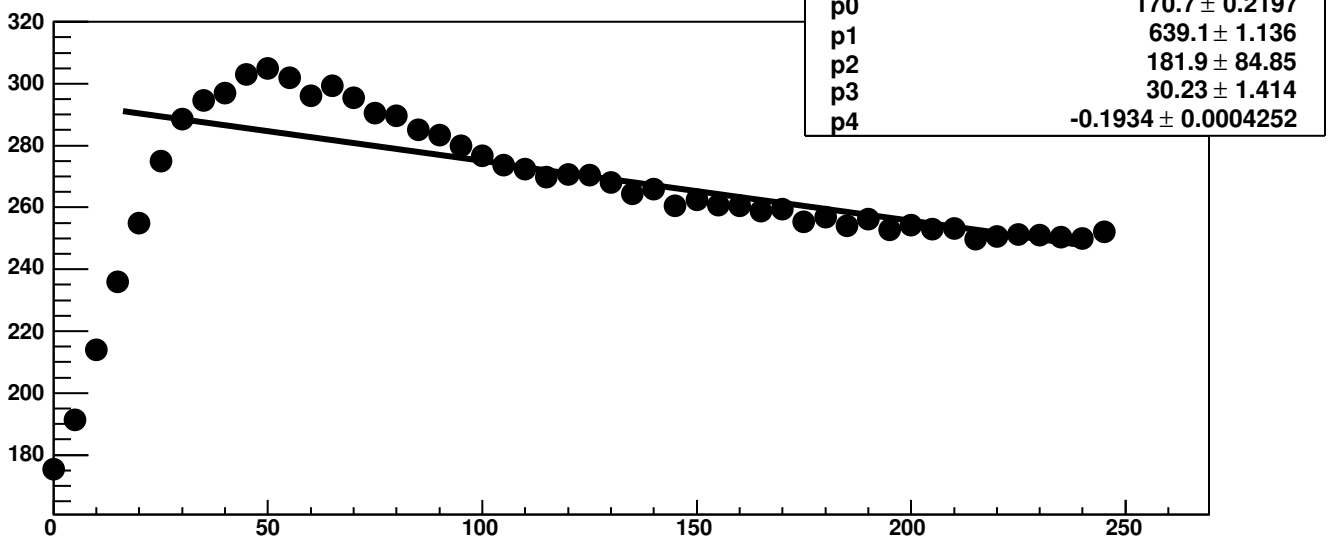
Chip 4, Channel 17, Enable 5, DAC=4000, ADC Noise vs Hold



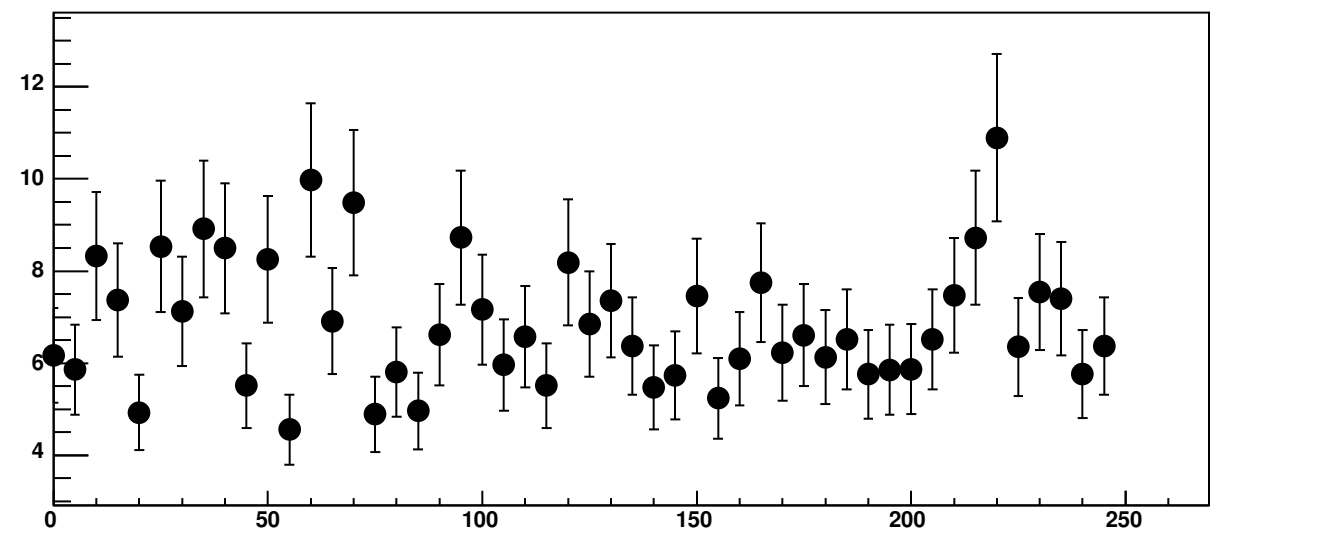
Chip 4, Channel 17, Enable 5, DAC=4000, ADC Residuals vs Hold



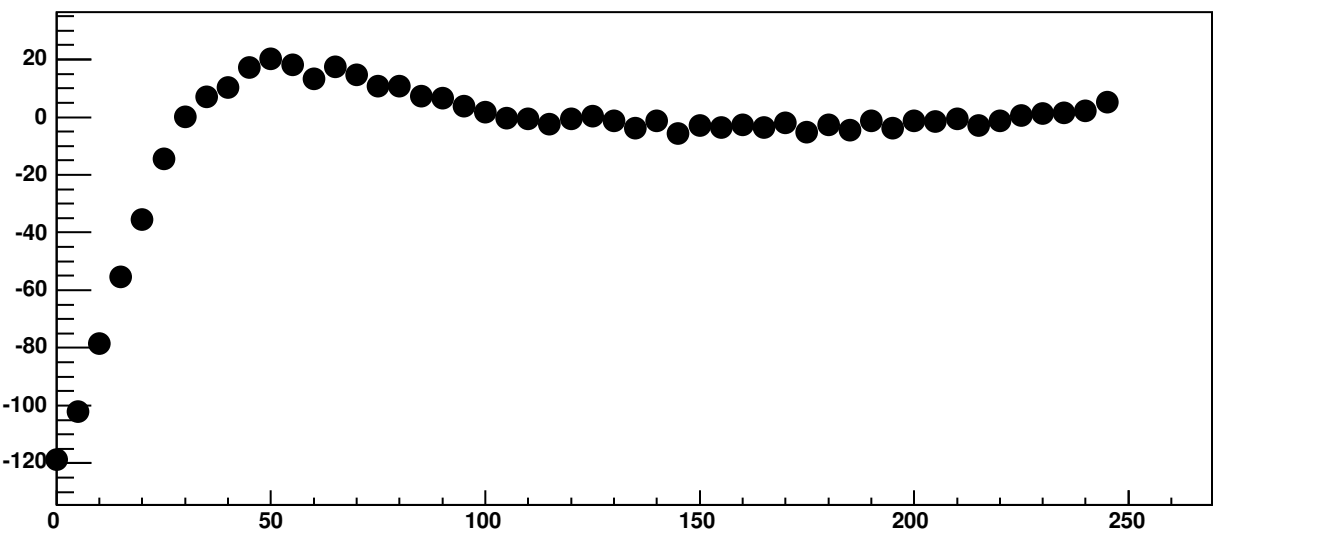
Chip 5, Channel 0, Enable 0, DAC=4000, ADC Mean vs Hold



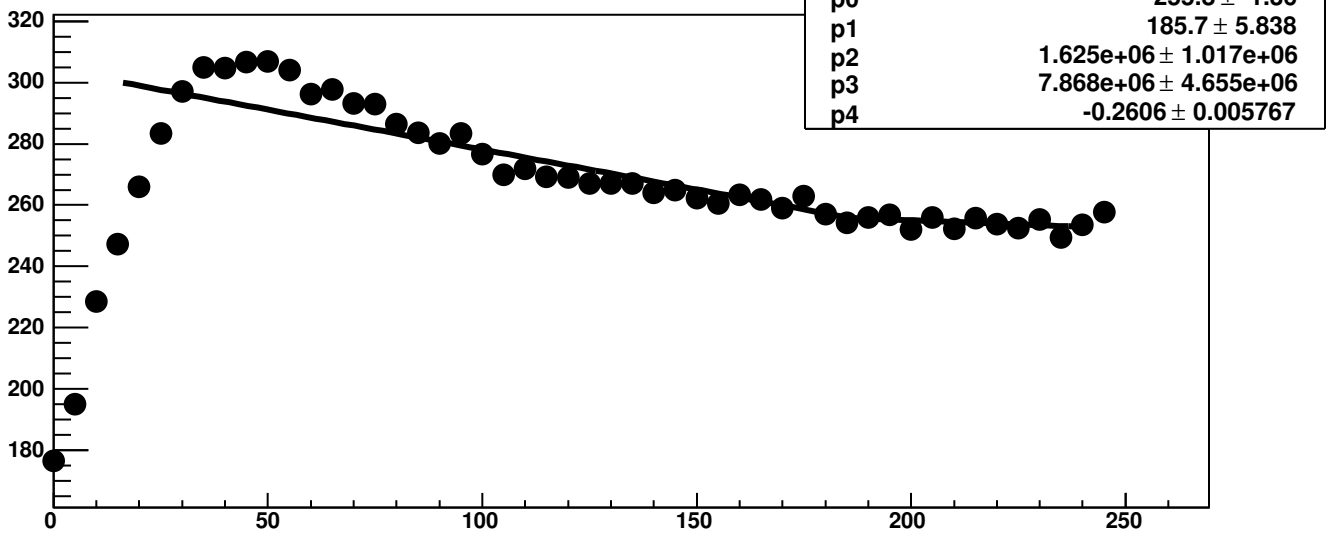
Chip 5, Channel 0, Enable 0, DAC=4000, ADC Noise vs Hold



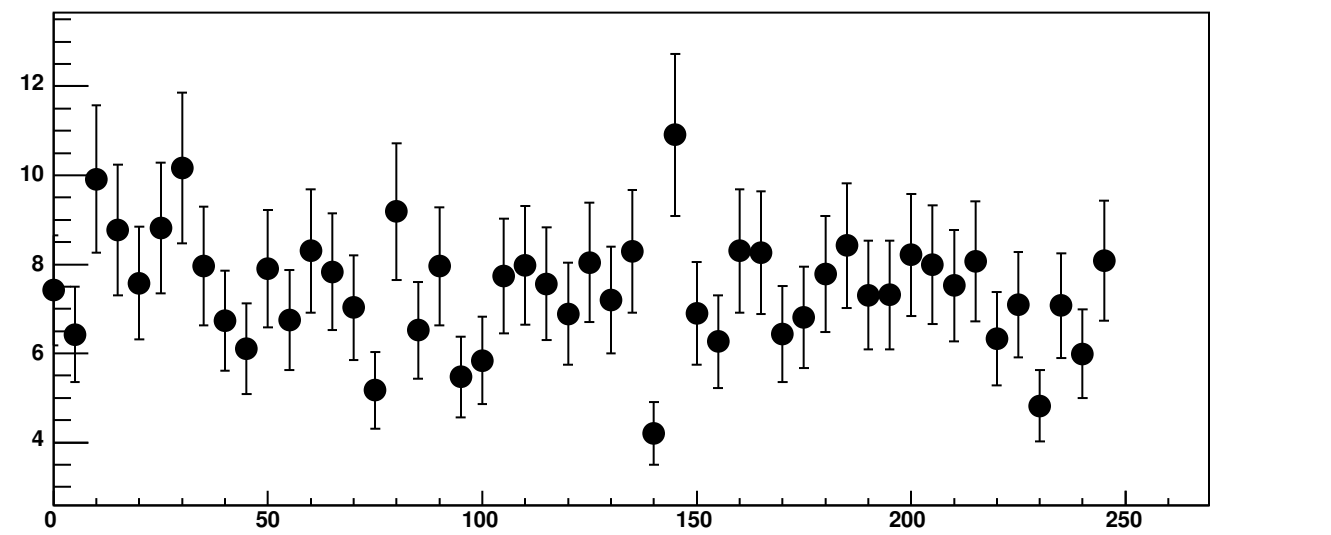
Chip 5, Channel 0, Enable 0, DAC=4000, ADC Residuals vs Hold



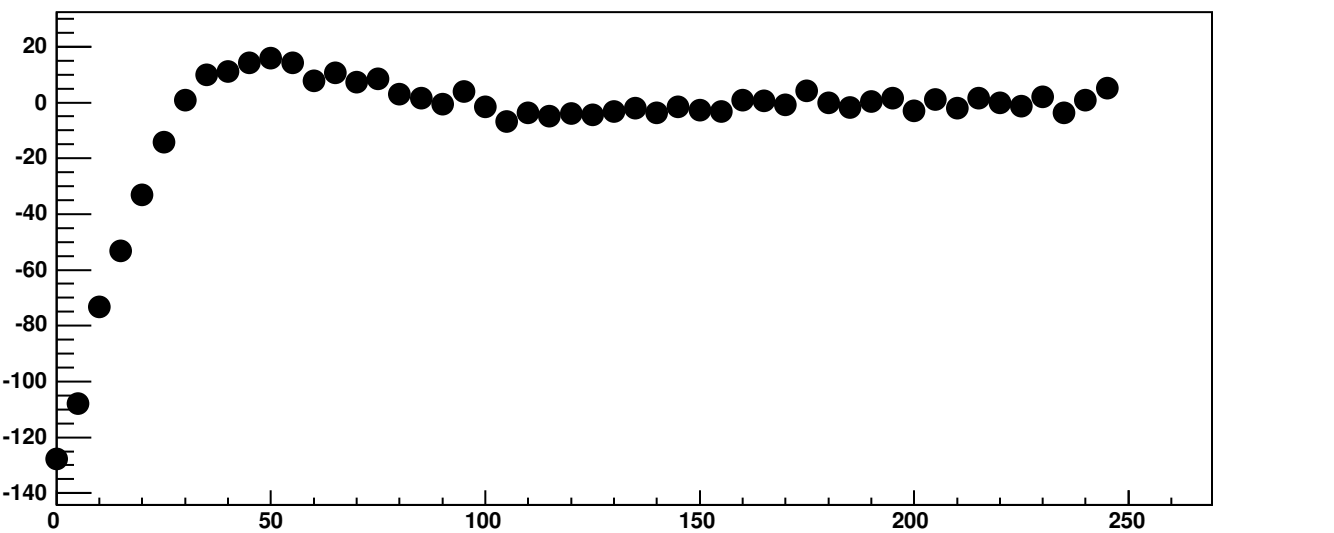
Chip 5, Channel 0, Enable 1, DAC=4000, ADC Mean vs Hold



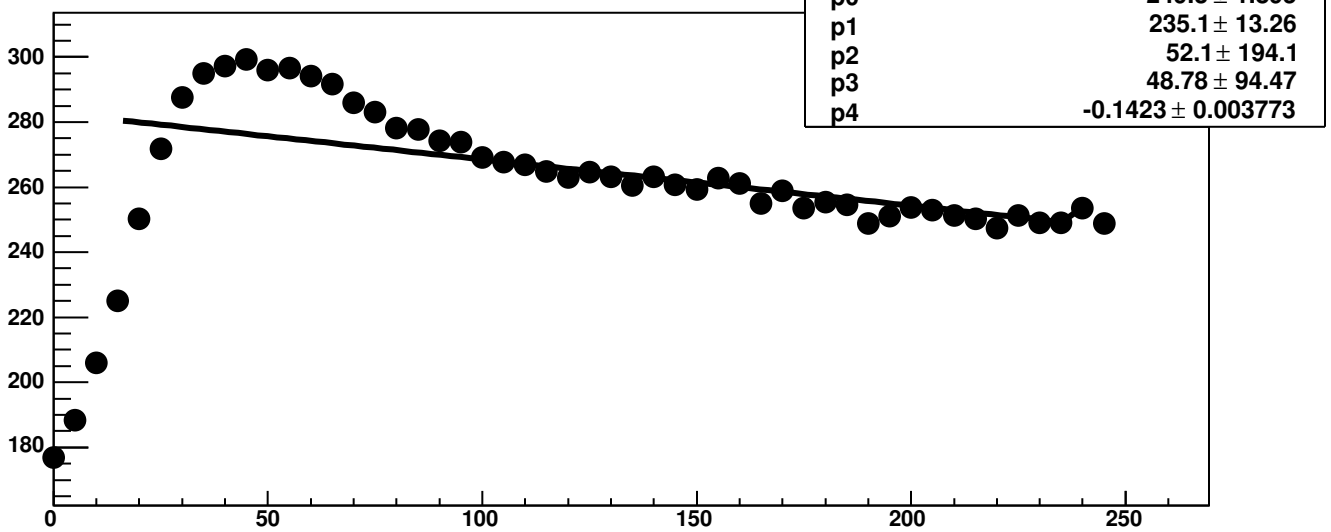
Chip 5, Channel 0, Enable 1, DAC=4000, ADC Noise vs Hold



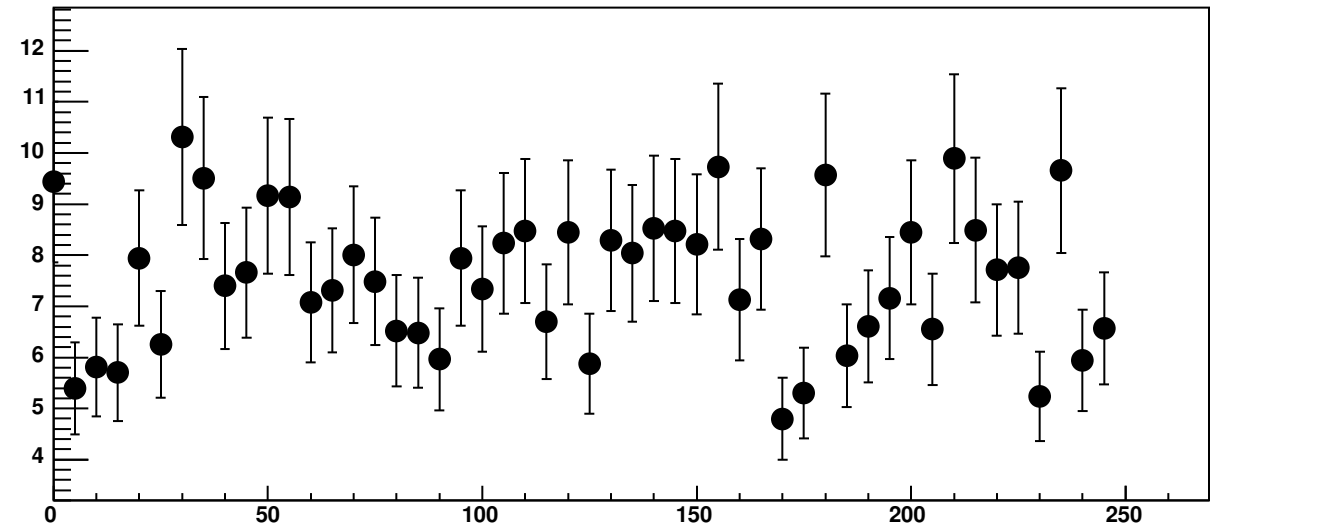
Chip 5, Channel 0, Enable 1, DAC=4000, ADC Residuals vs Hold



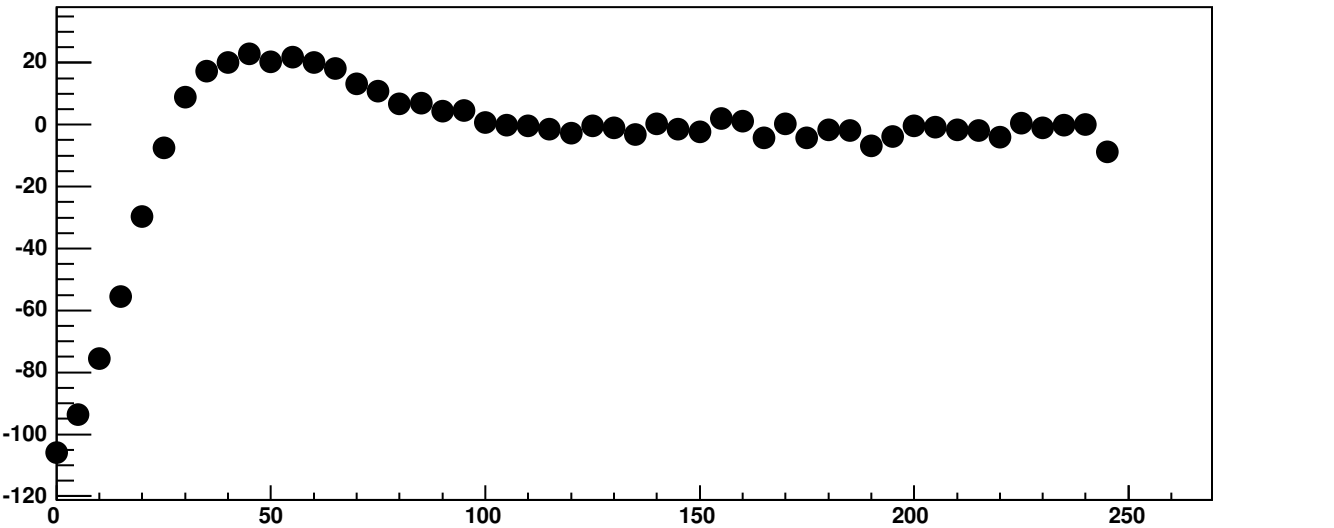
Chip 5, Channel 0, Enable 2, DAC=4000, ADC Mean vs Hold



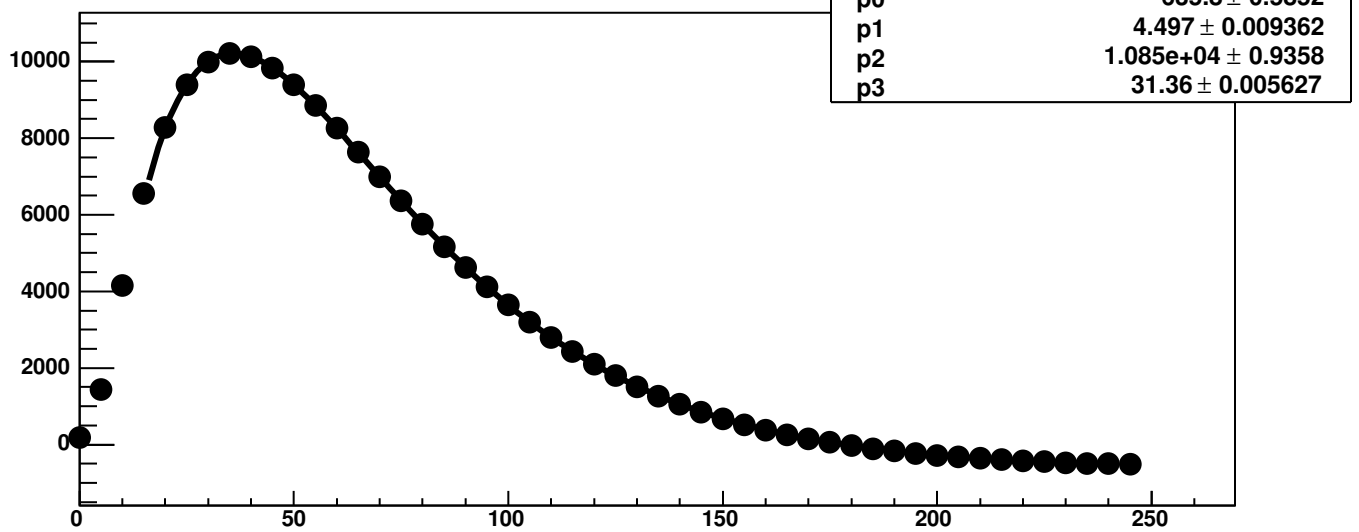
Chip 5, Channel 0, Enable 2, DAC=4000, ADC Noise vs Hold



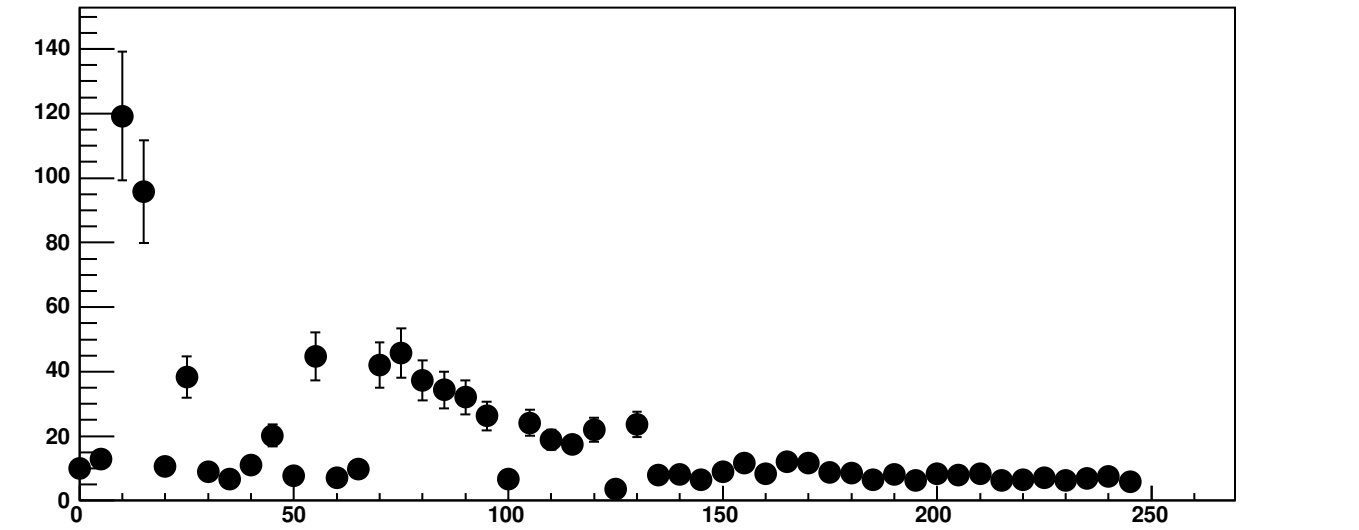
Chip 5, Channel 0, Enable 2, DAC=4000, ADC Residuals vs Hold



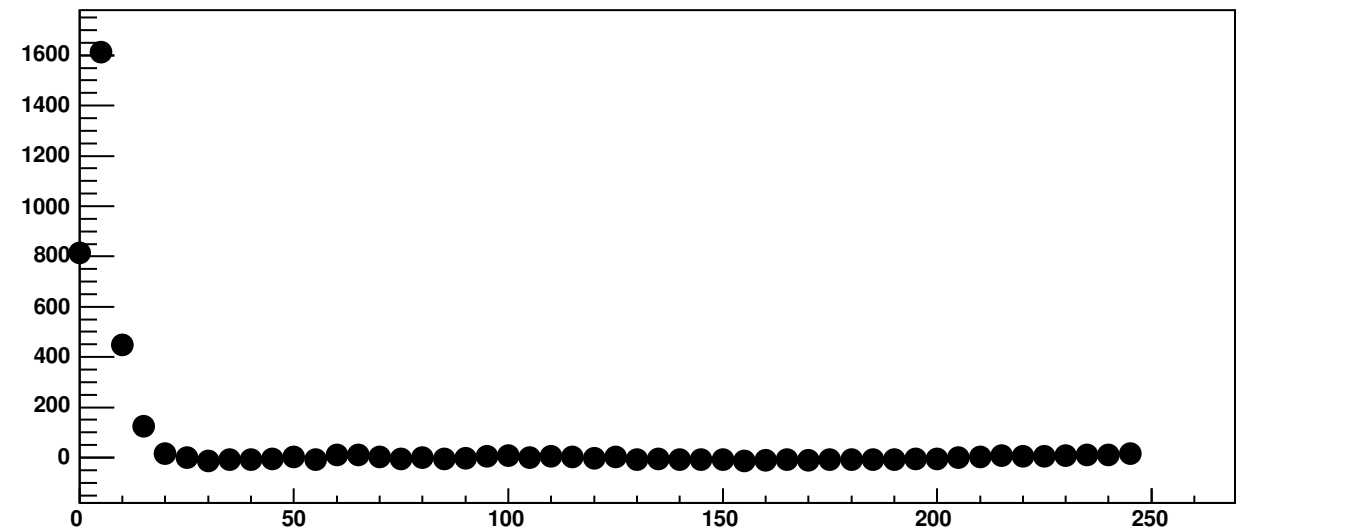
Chip 5, Channel 0, Enable 3!, DAC=4000, ADC Mean vs Hold



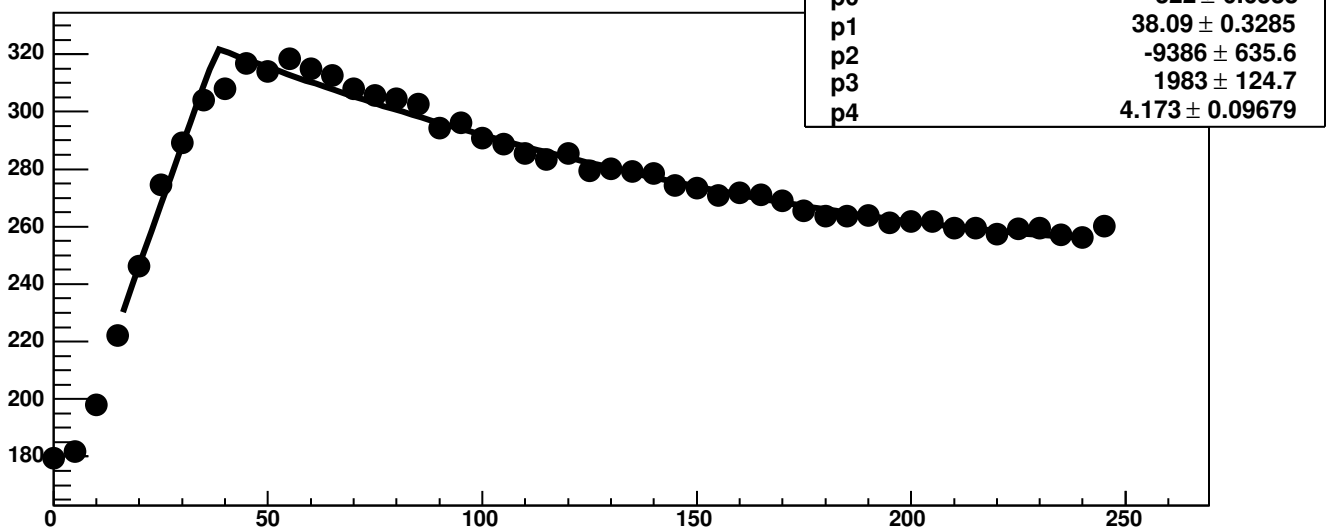
Chip 5, Channel 0, Enable 3!, DAC=4000, ADC Noise vs Hold



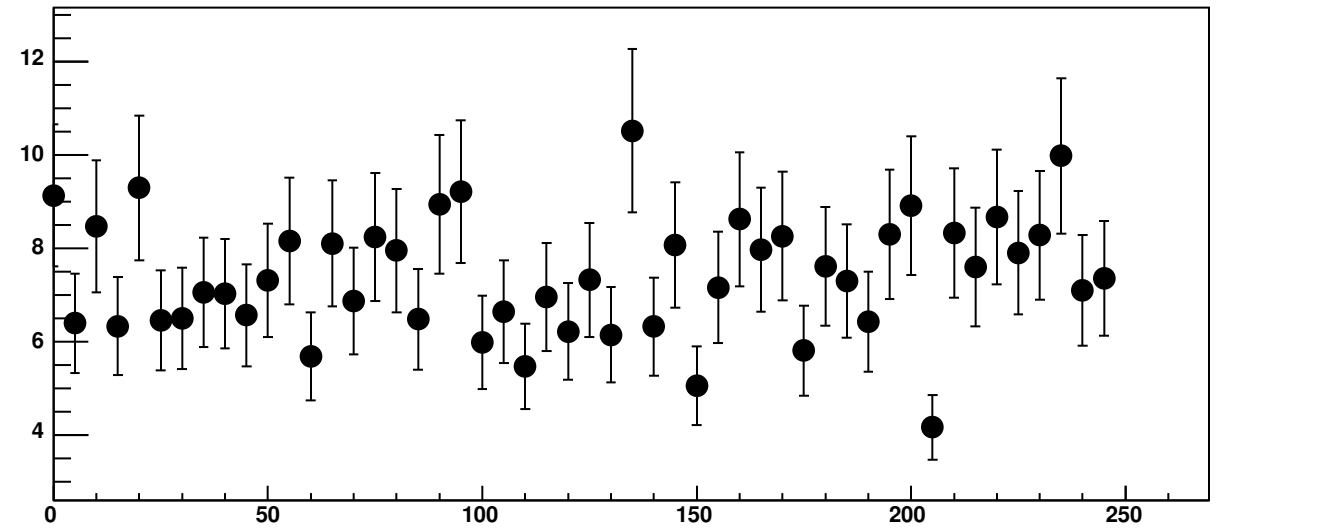
Chip 5, Channel 0, Enable 3!, DAC=4000, ADC Residuals vs Hold



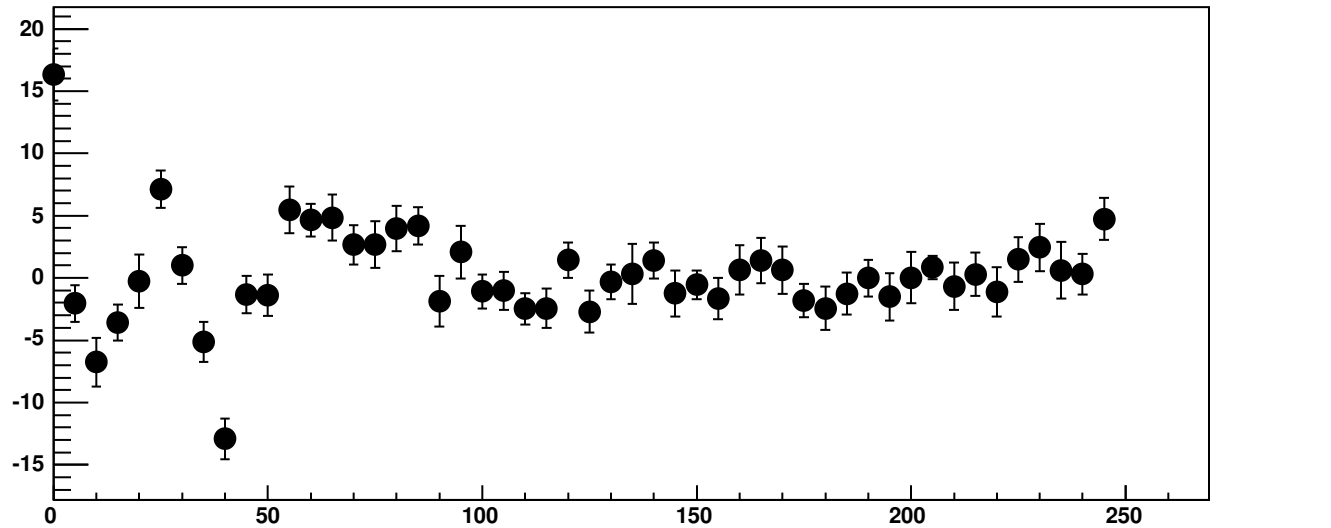
Chip 5, Channel 0, Enable 4, DAC=4000, ADC Mean vs Hold



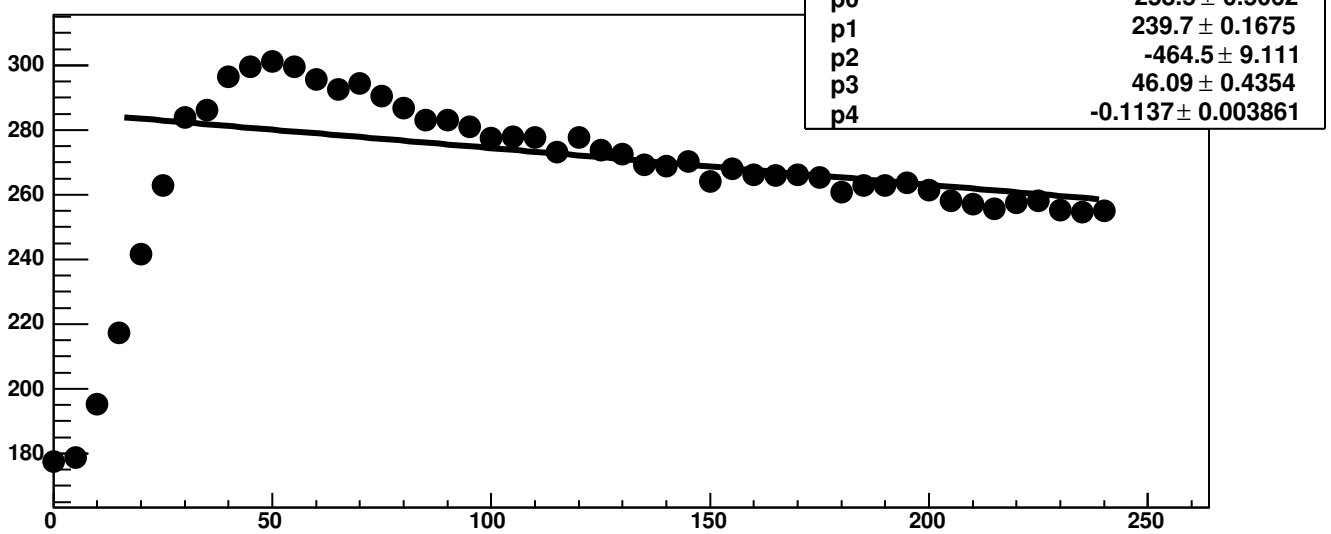
Chip 5, Channel 0, Enable 4, DAC=4000, ADC Noise vs Hold



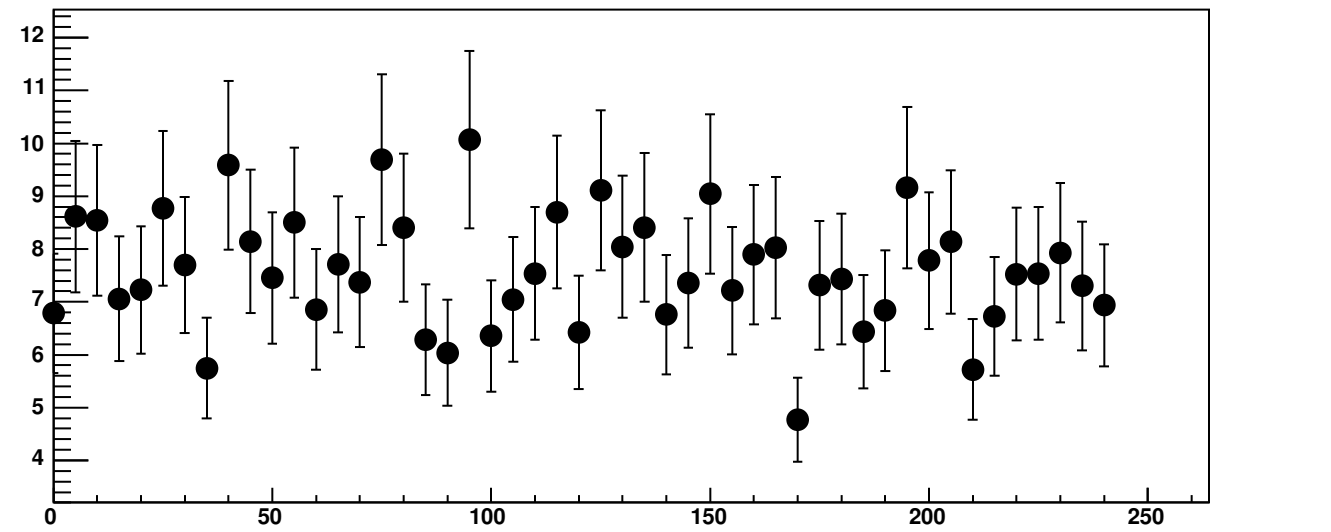
Chip 5, Channel 0, Enable 4, DAC=4000, ADC Residuals vs Hold



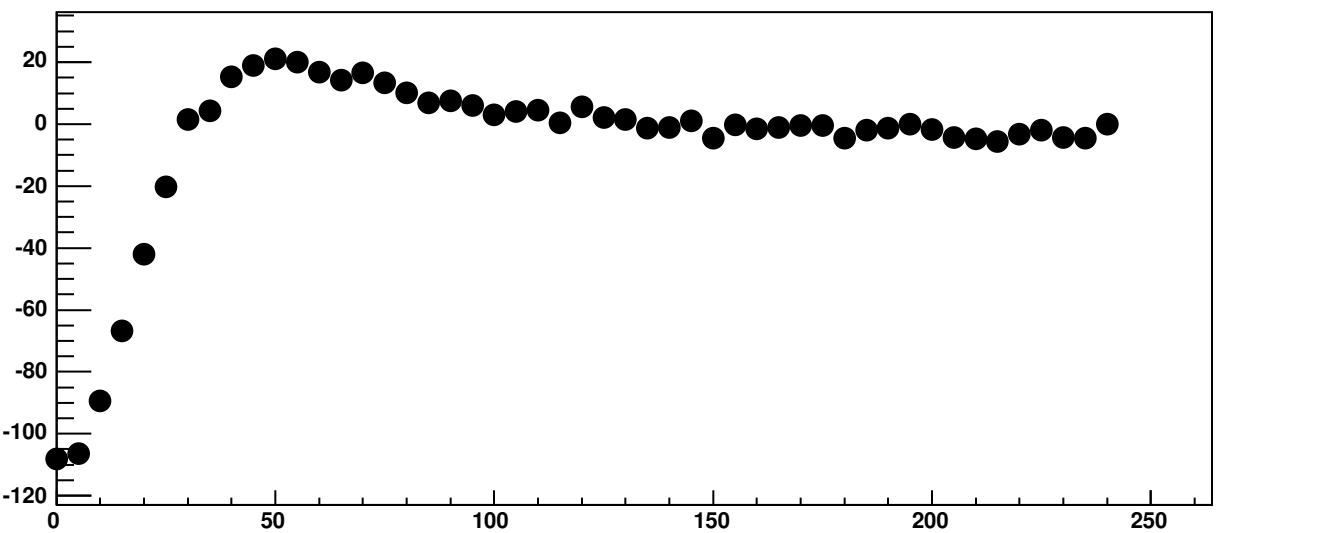
Chip 5, Channel 0, Enable 5, DAC=4000, ADC Mean vs Hold



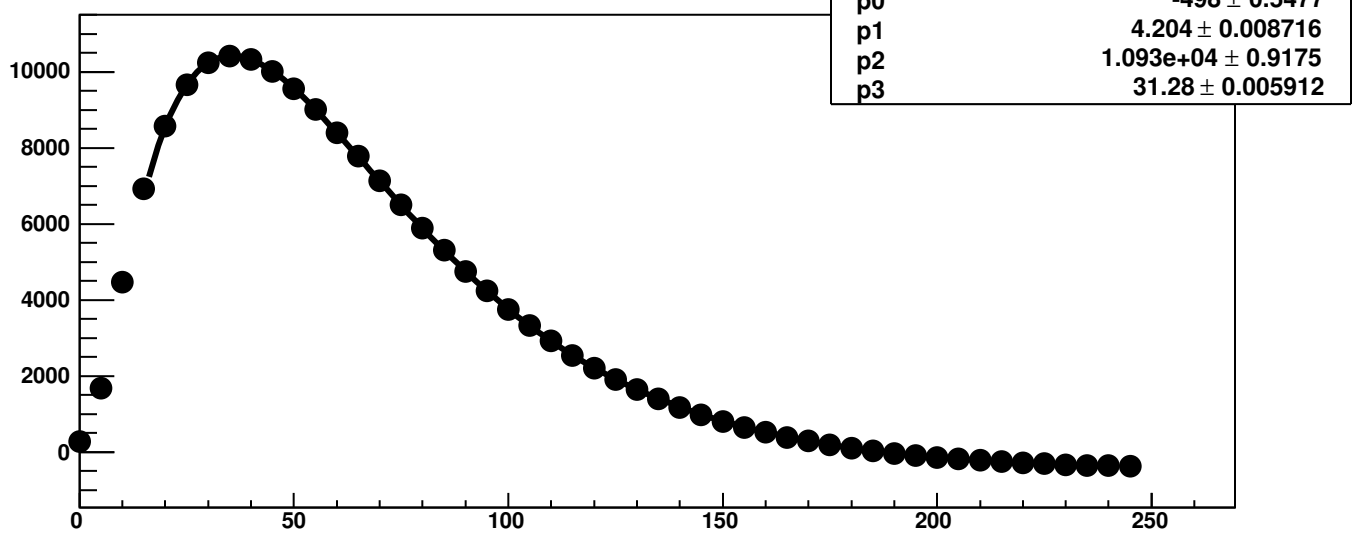
Chip 5, Channel 0, Enable 5, DAC=4000, ADC Noise vs Hold



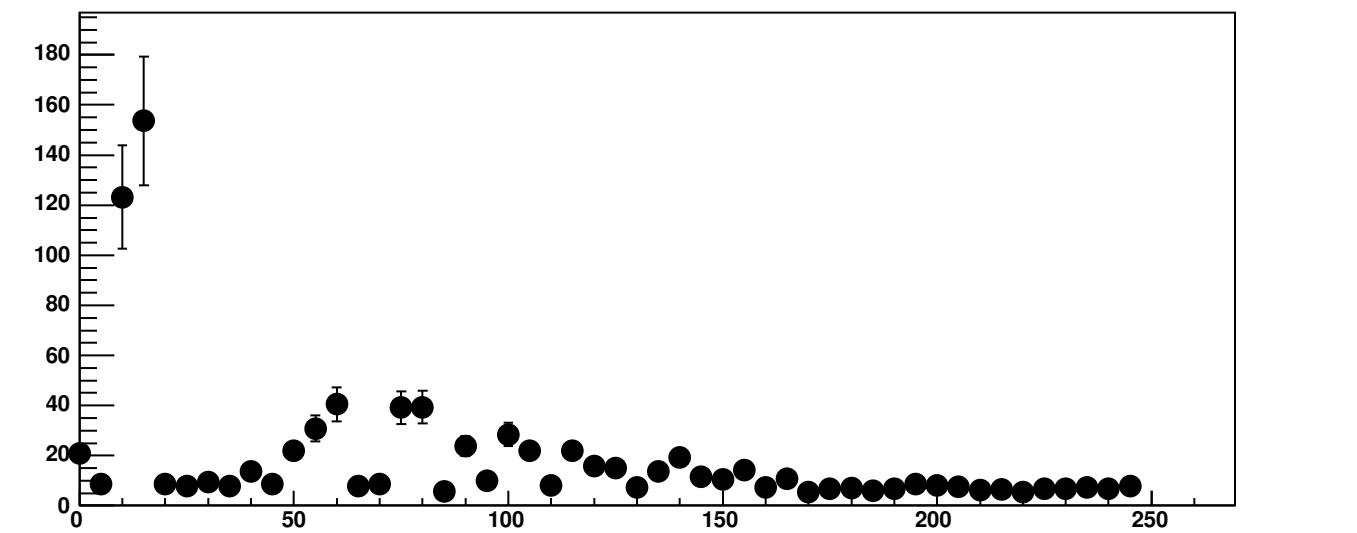
Chip 5, Channel 0, Enable 5, DAC=4000, ADC Residuals vs Hold



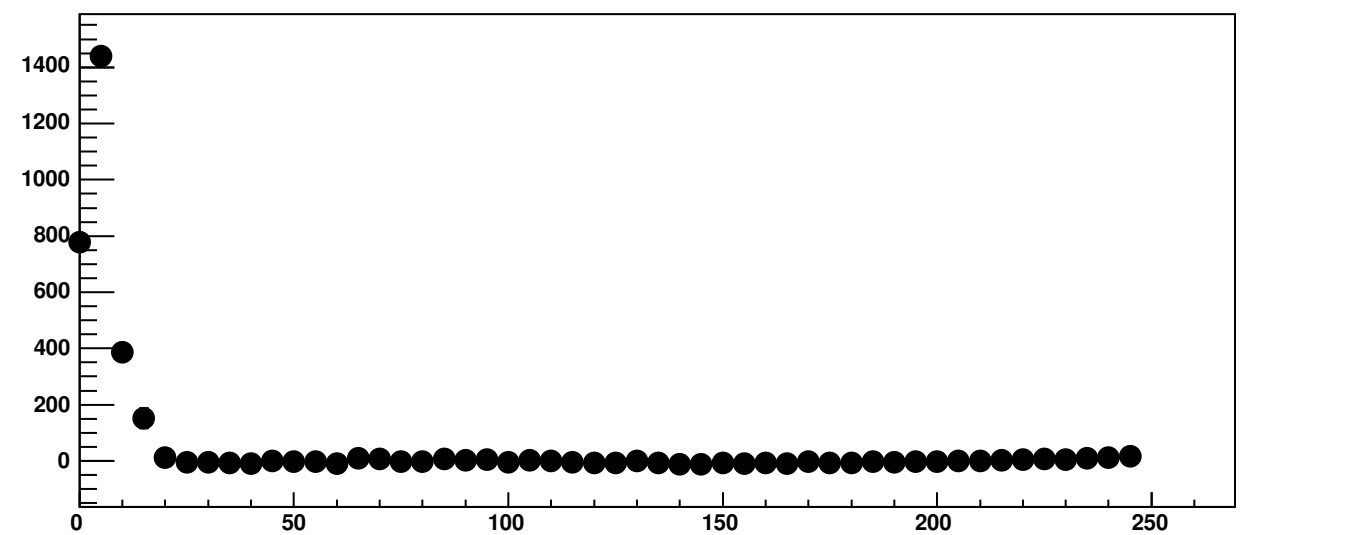
Chip 5, Channel 1, Enable 0!, DAC=4000, ADC Mean vs Hold



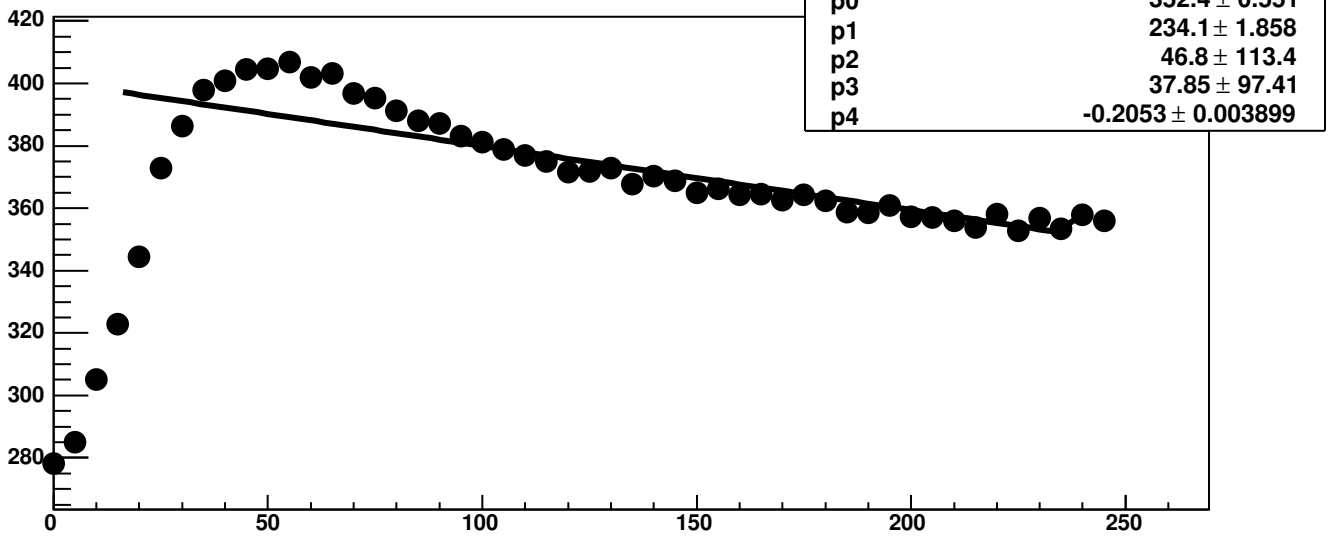
Chip 5, Channel 1, Enable 0!, DAC=4000, ADC Noise vs Hold



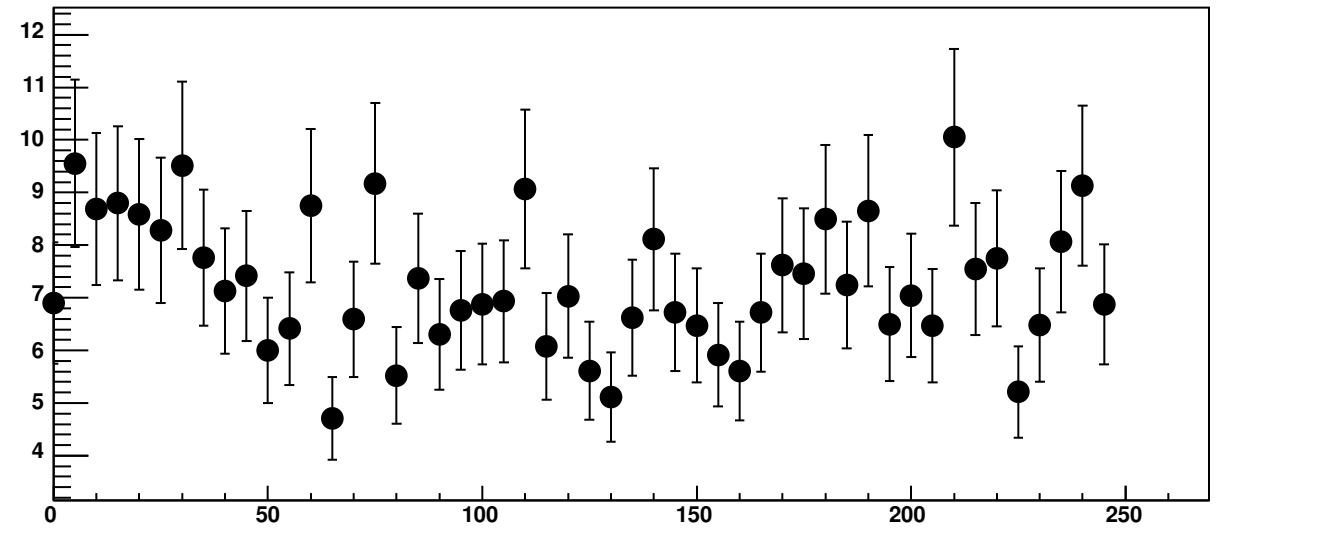
Chip 5, Channel 1, Enable 0!, DAC=4000, ADC Residuals vs Hold



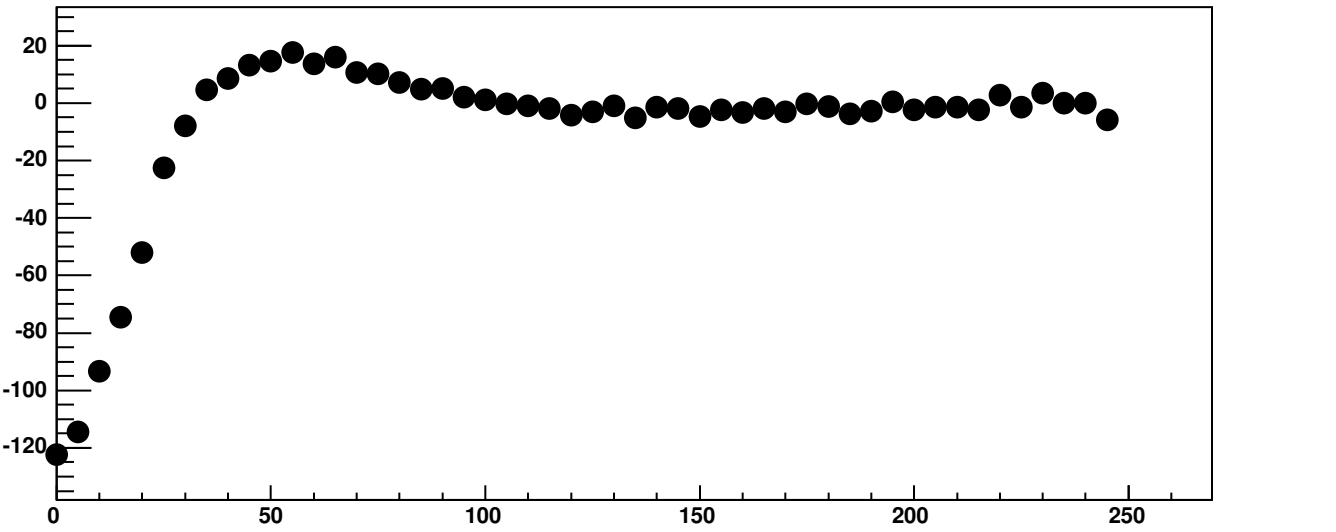
Chip 5, Channel 1, Enable 1, DAC=4000, ADC Mean vs Hold



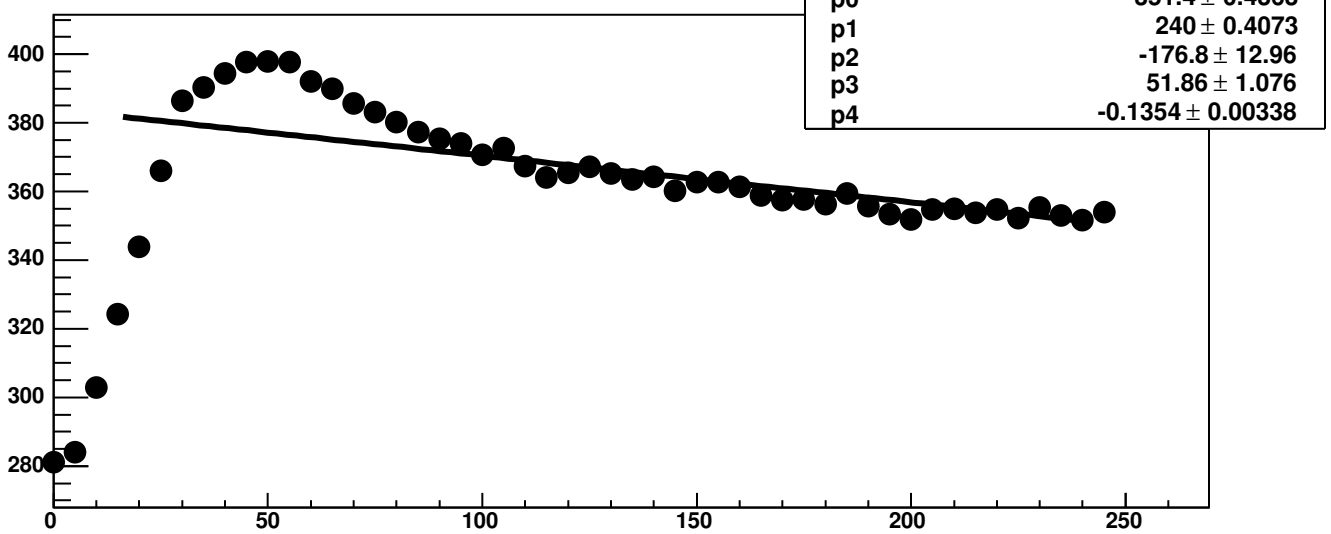
Chip 5, Channel 1, Enable 1, DAC=4000, ADC Noise vs Hold



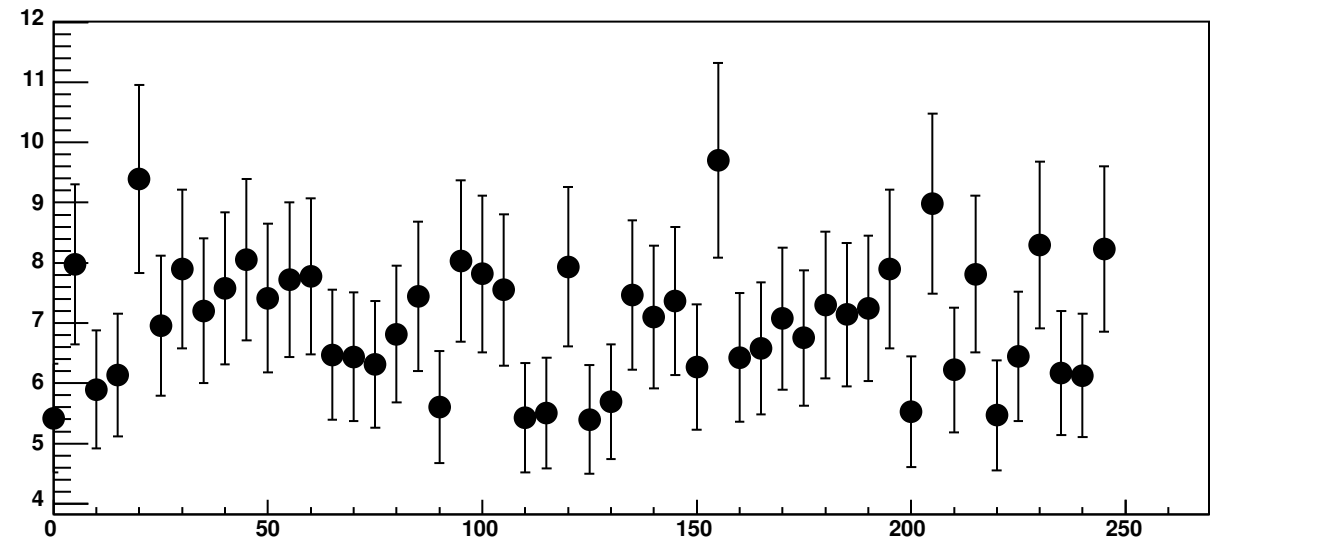
Chip 5, Channel 1, Enable 1, DAC=4000, ADC Residuals vs Hold



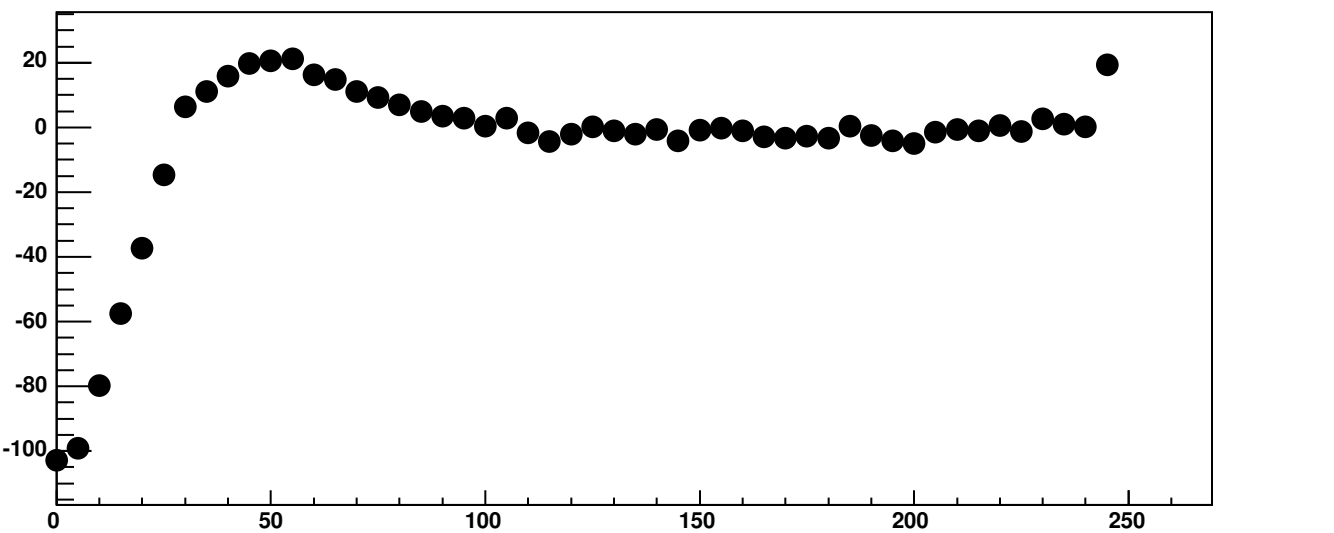
Chip 5, Channel 1, Enable 2, DAC=4000, ADC Mean vs Hold



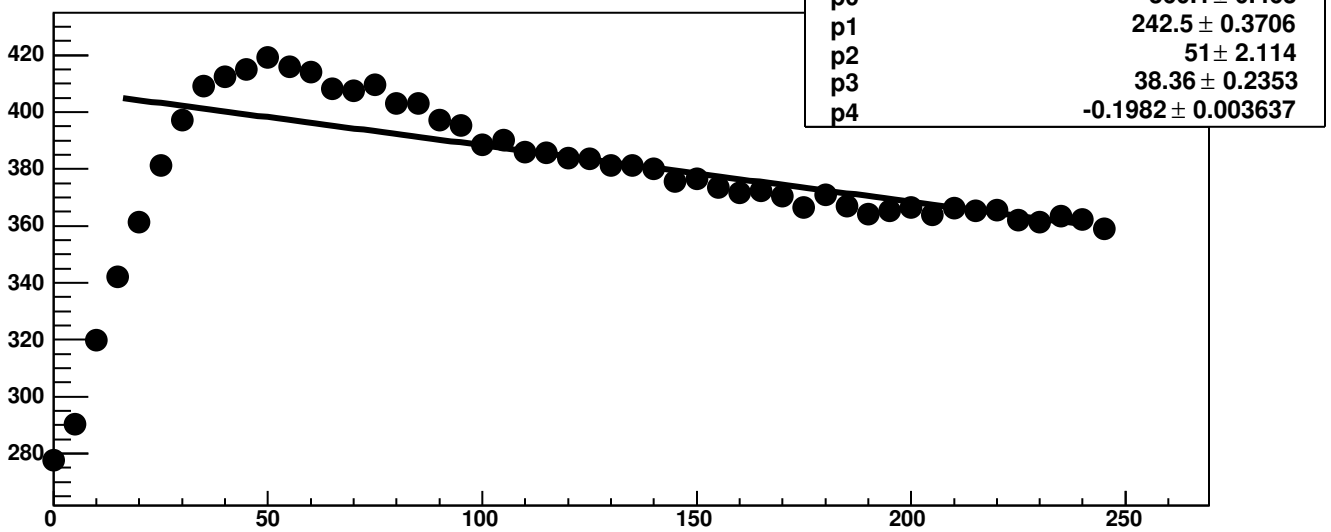
Chip 5, Channel 1, Enable 2, DAC=4000, ADC Noise vs Hold



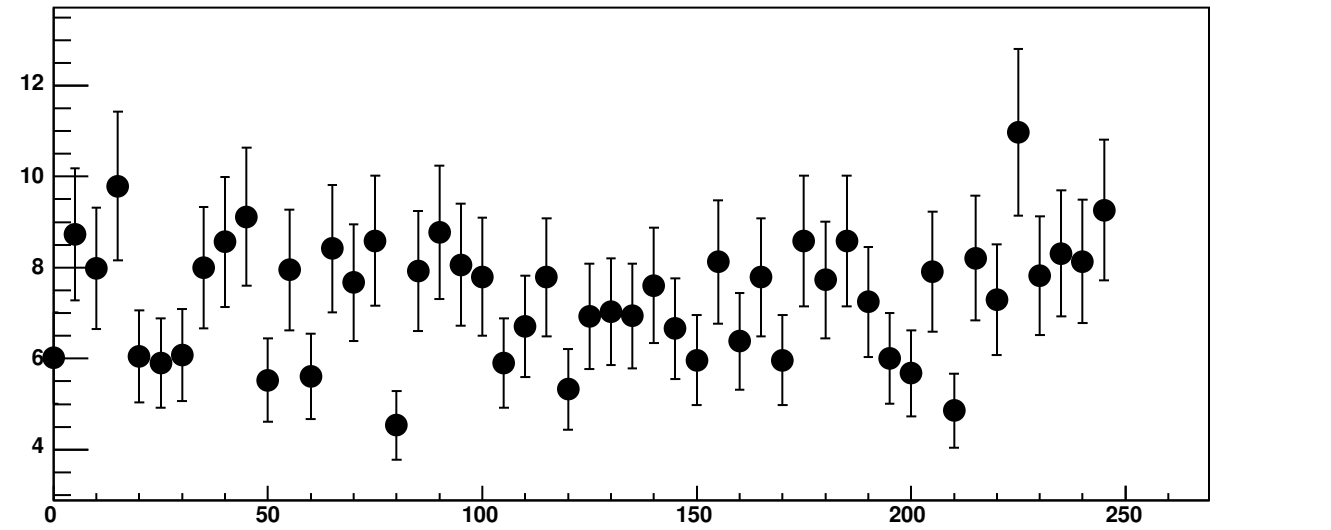
Chip 5, Channel 1, Enable 2, DAC=4000, ADC Residuals vs Hold



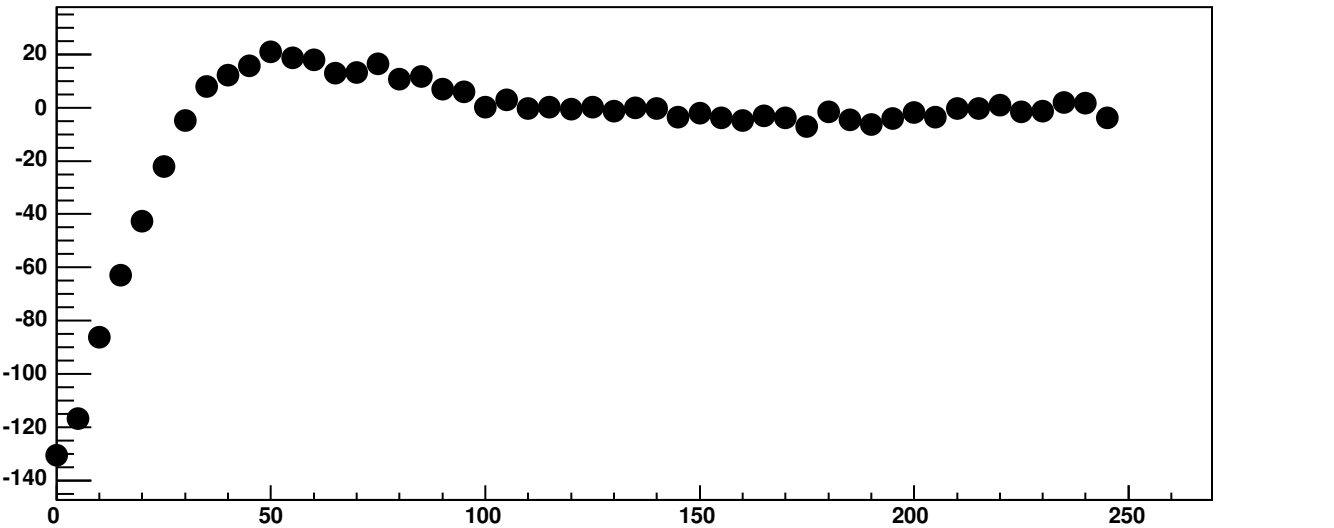
Chip 5, Channel 1, Enable 3, DAC=4000, ADC Mean vs Hold



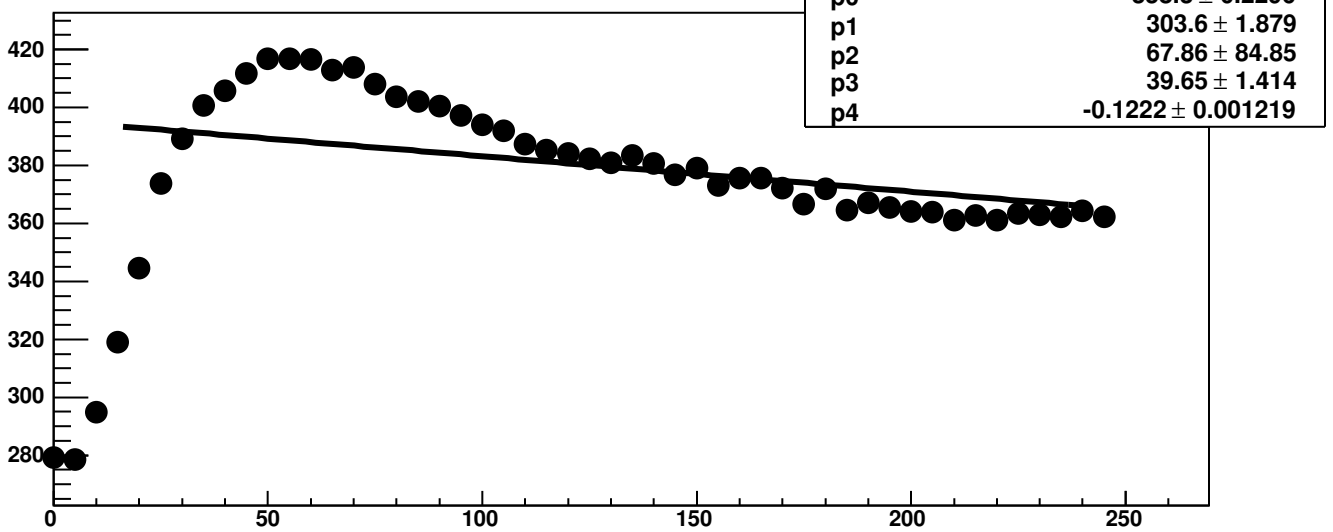
Chip 5, Channel 1, Enable 3, DAC=4000, ADC Noise vs Hold



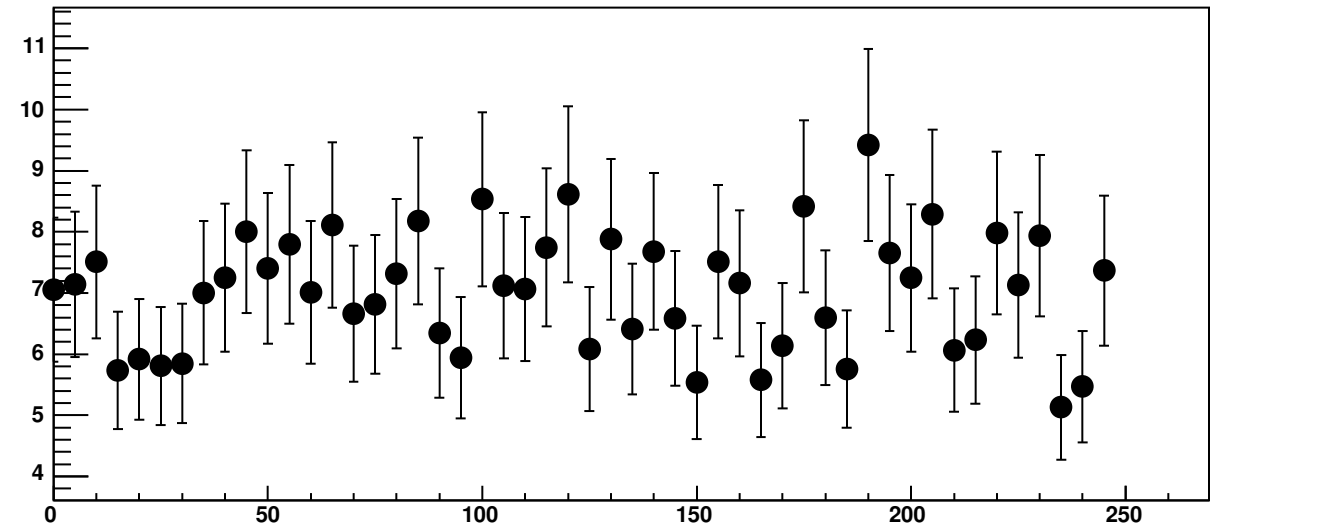
Chip 5, Channel 1, Enable 3, DAC=4000, ADC Residuals vs Hold



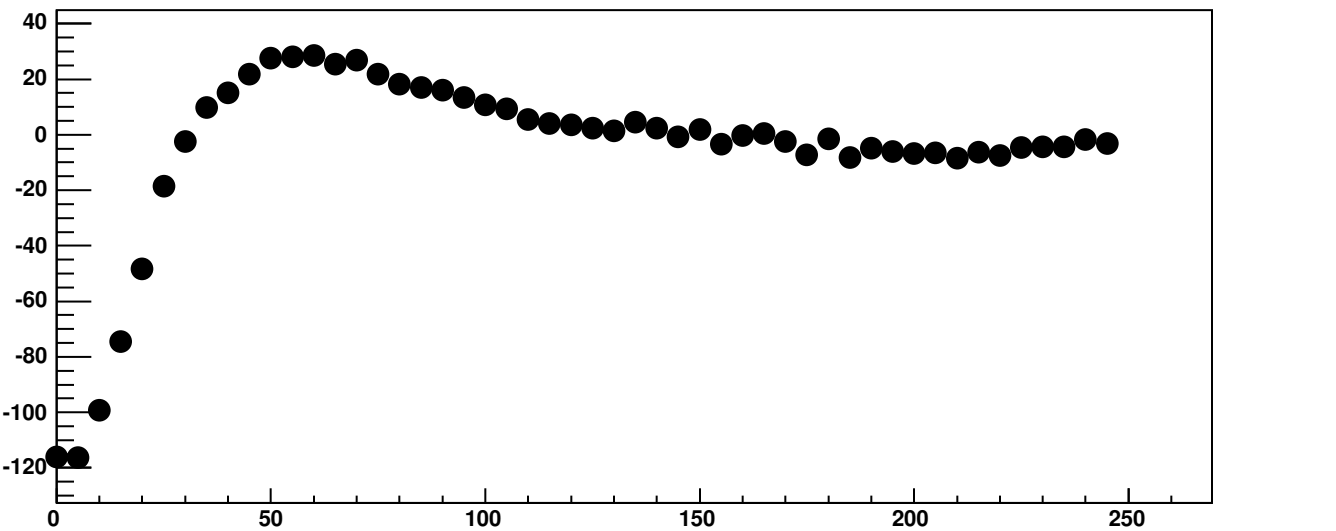
Chip 5, Channel 1, Enable 4, DAC=4000, ADC Mean vs Hold



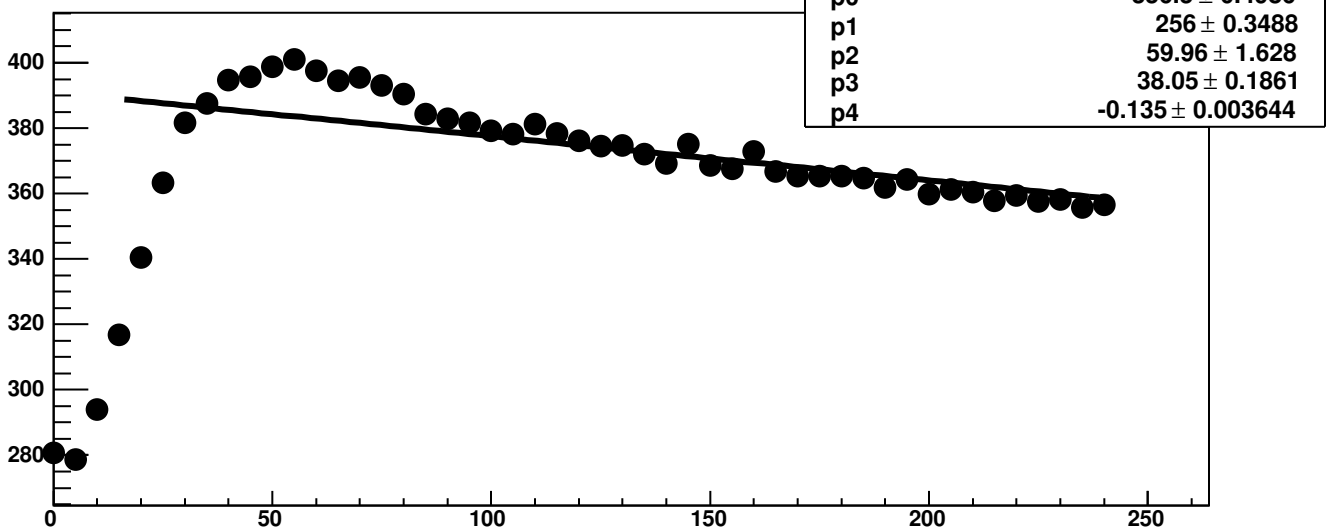
Chip 5, Channel 1, Enable 4, DAC=4000, ADC Noise vs Hold



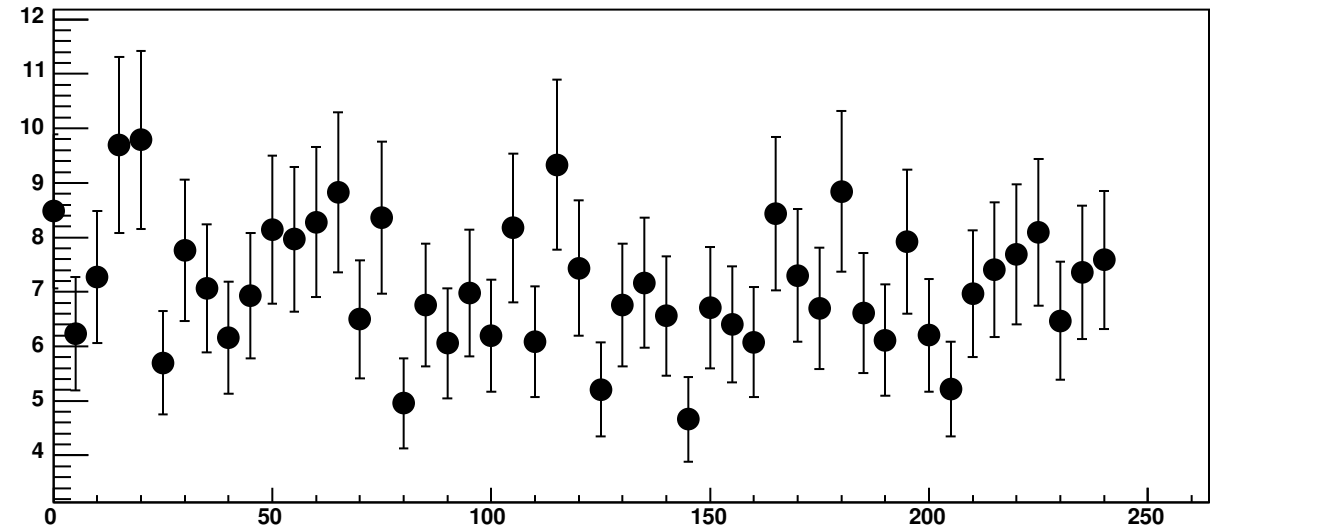
Chip 5, Channel 1, Enable 4, DAC=4000, ADC Residuals vs Hold



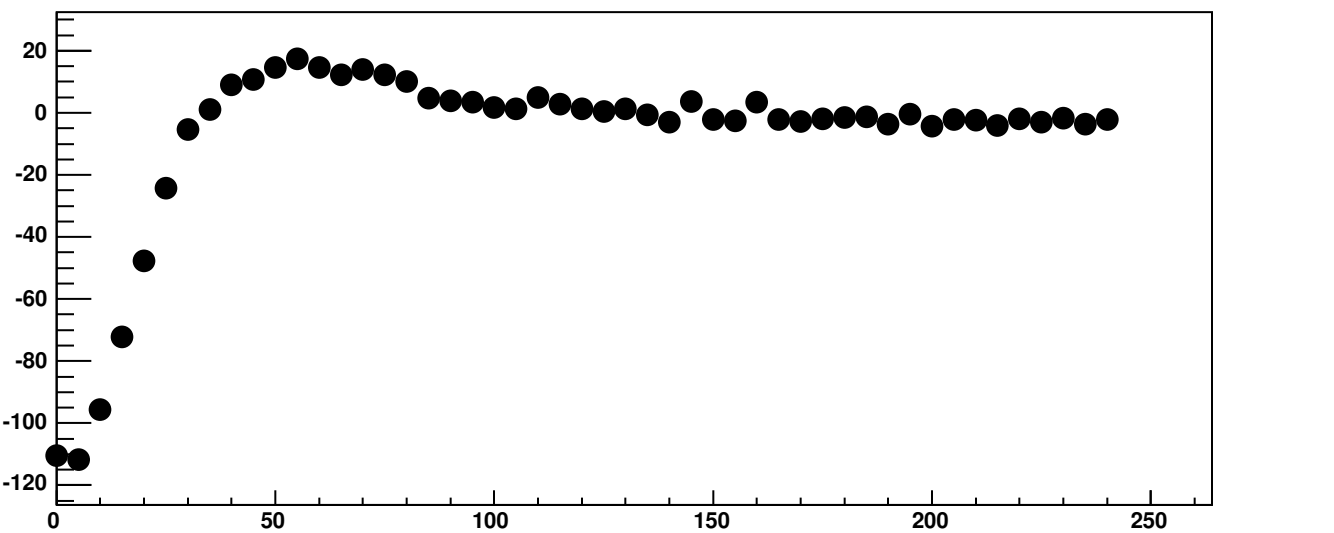
Chip 5, Channel 1, Enable 5, DAC=4000, ADC Mean vs Hold



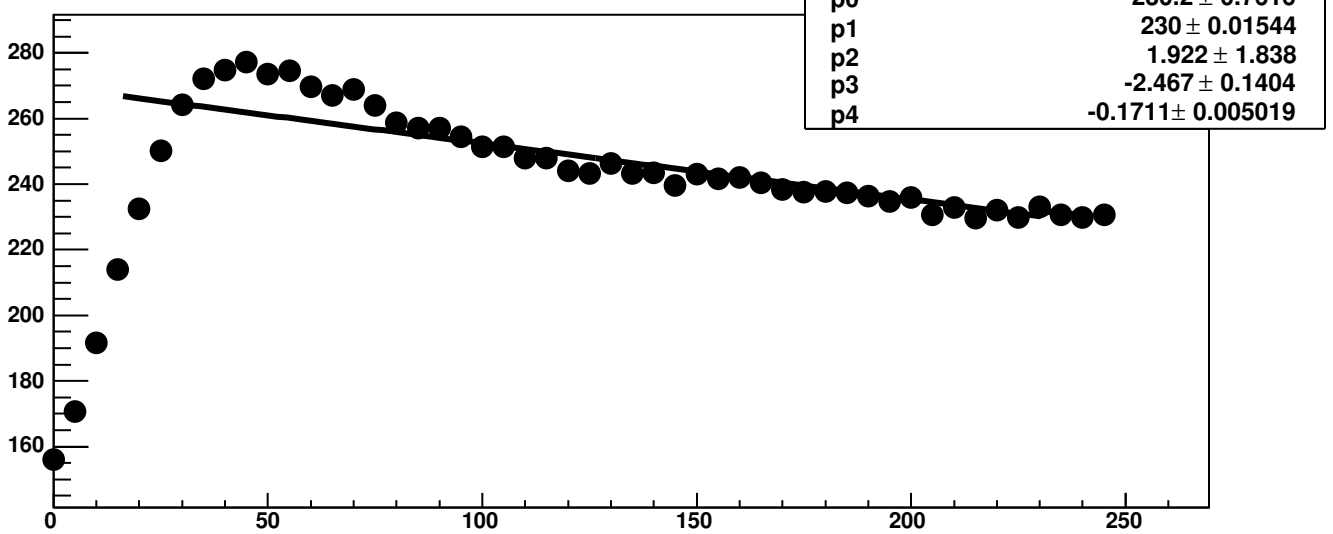
Chip 5, Channel 1, Enable 5, DAC=4000, ADC Noise vs Hold



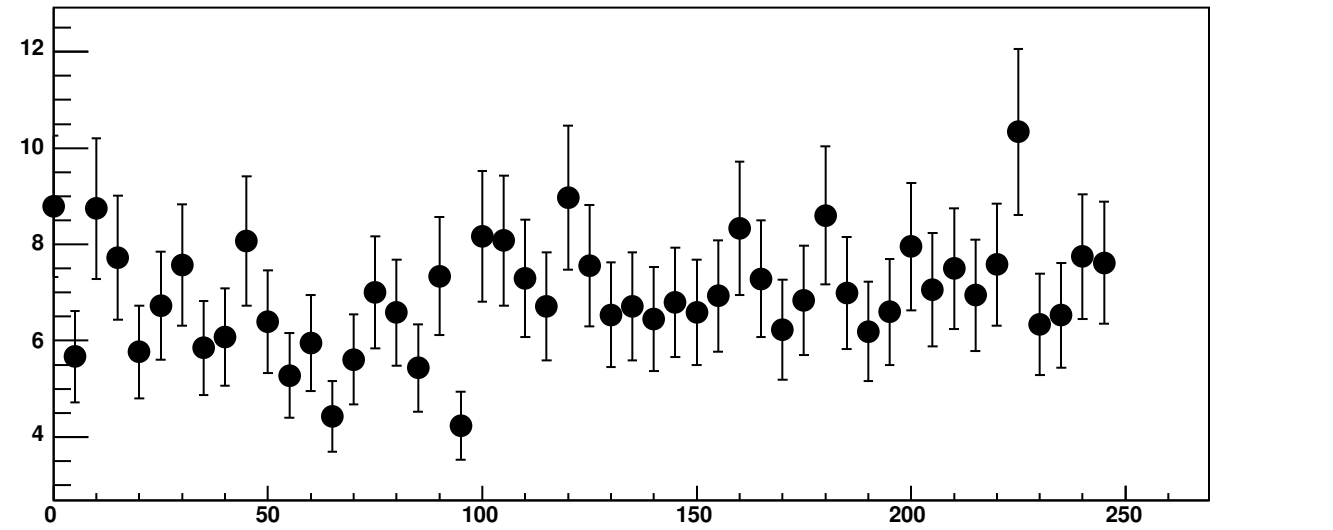
Chip 5, Channel 1, Enable 5, DAC=4000, ADC Residuals vs Hold



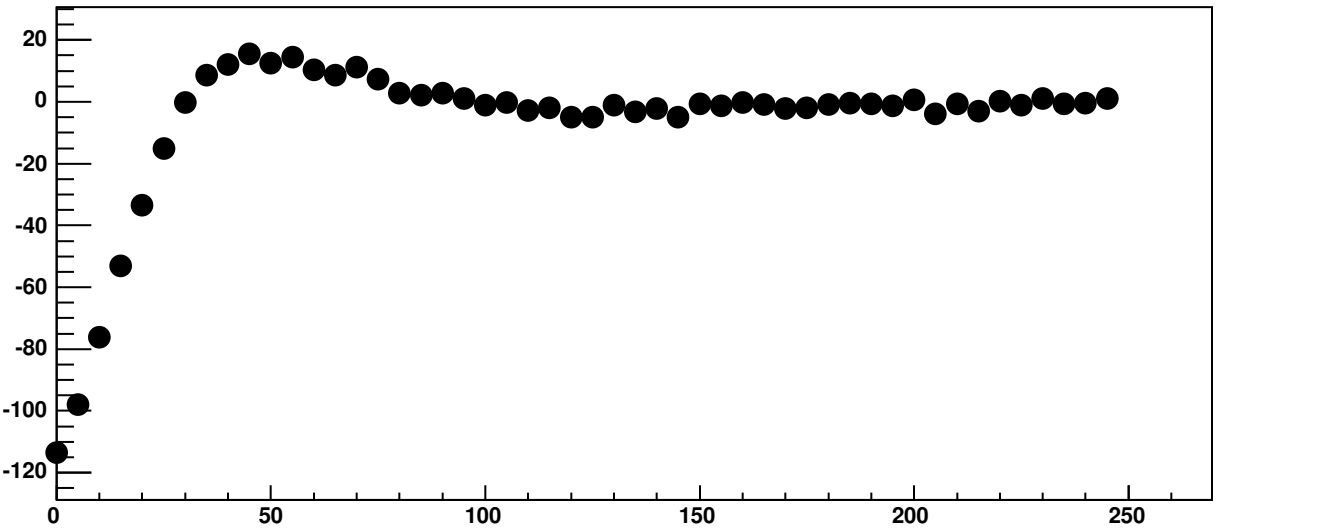
Chip 5, Channel 2, Enable 0, DAC=4000, ADC Mean vs Hold



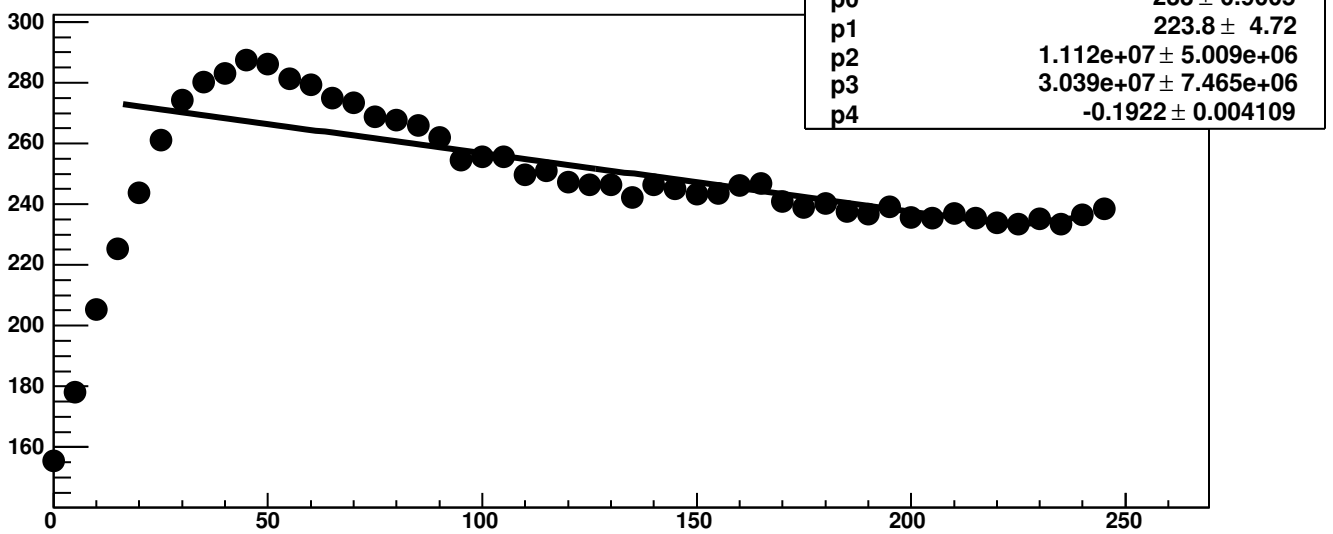
Chip 5, Channel 2, Enable 0, DAC=4000, ADC Noise vs Hold



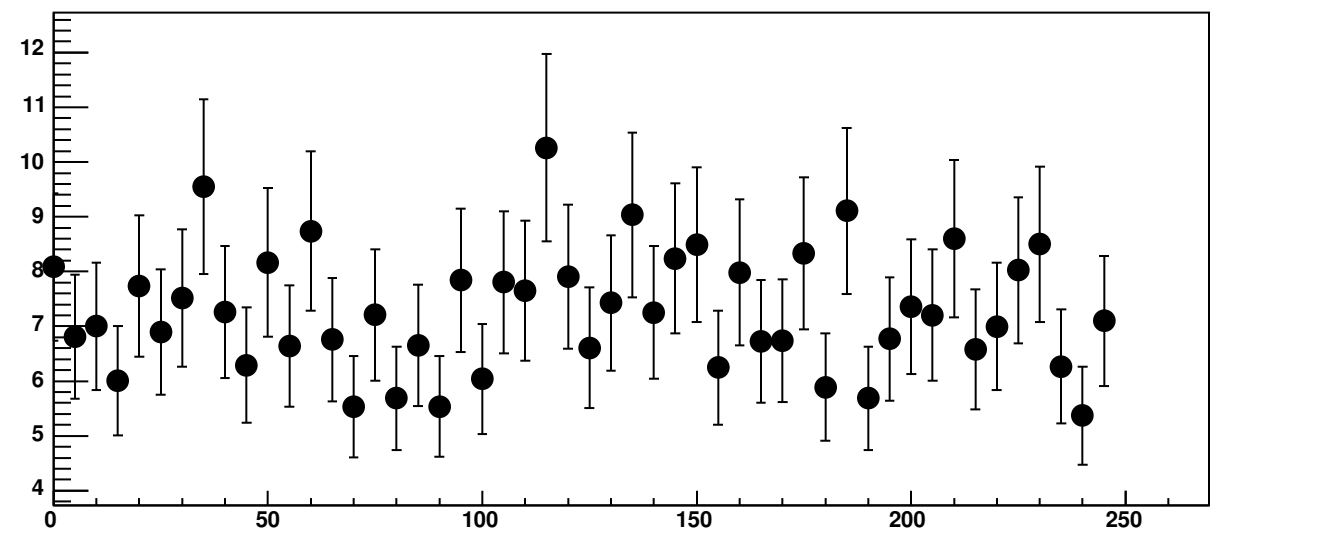
Chip 5, Channel 2, Enable 0, DAC=4000, ADC Residuals vs Hold



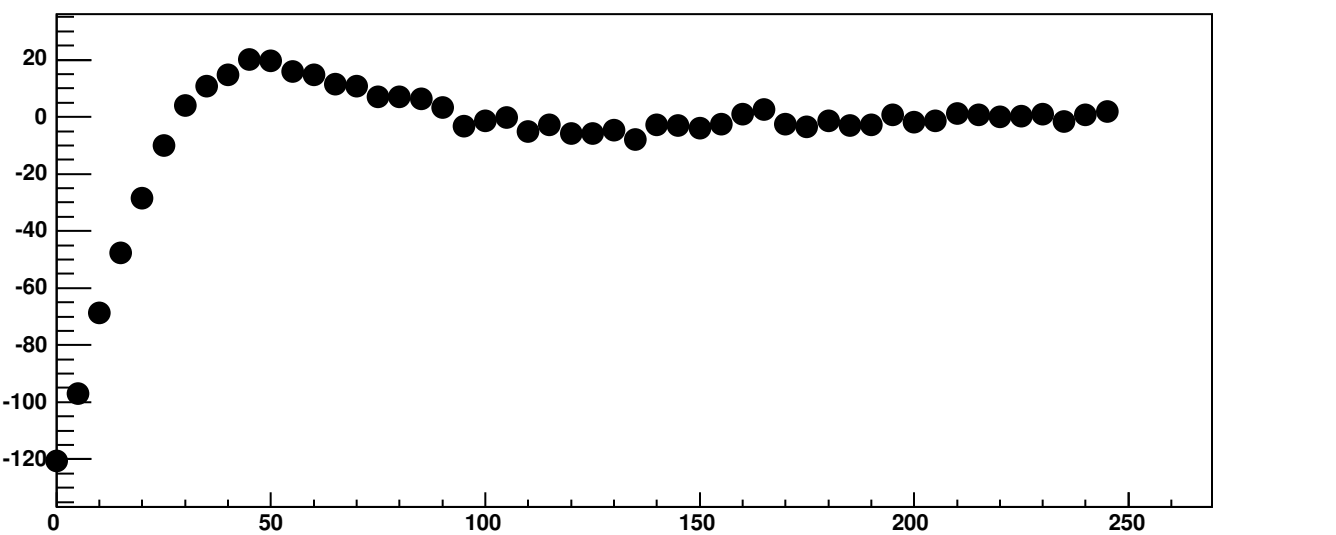
Chip 5, Channel 2, Enable 1, DAC=4000, ADC Mean vs Hold



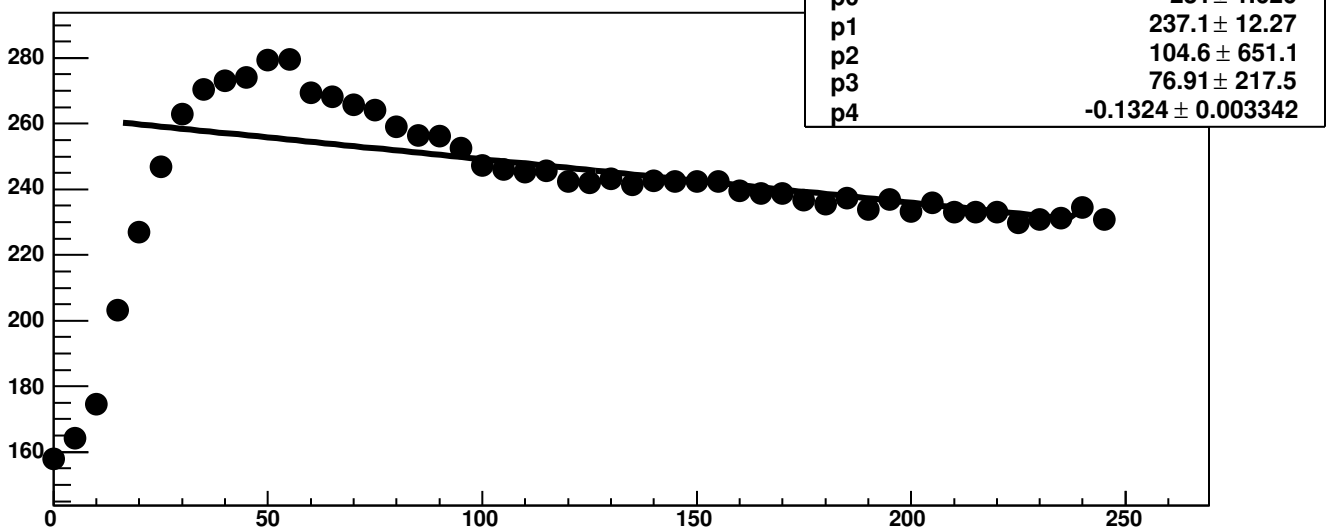
Chip 5, Channel 2, Enable 1, DAC=4000, ADC Noise vs Hold



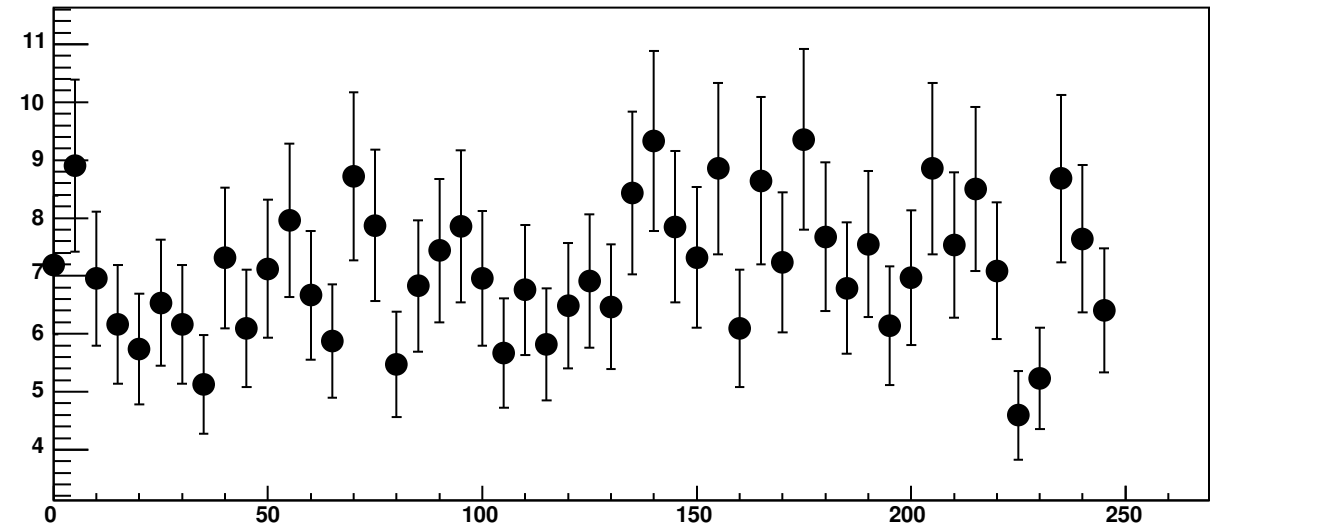
Chip 5, Channel 2, Enable 1, DAC=4000, ADC Residuals vs Hold



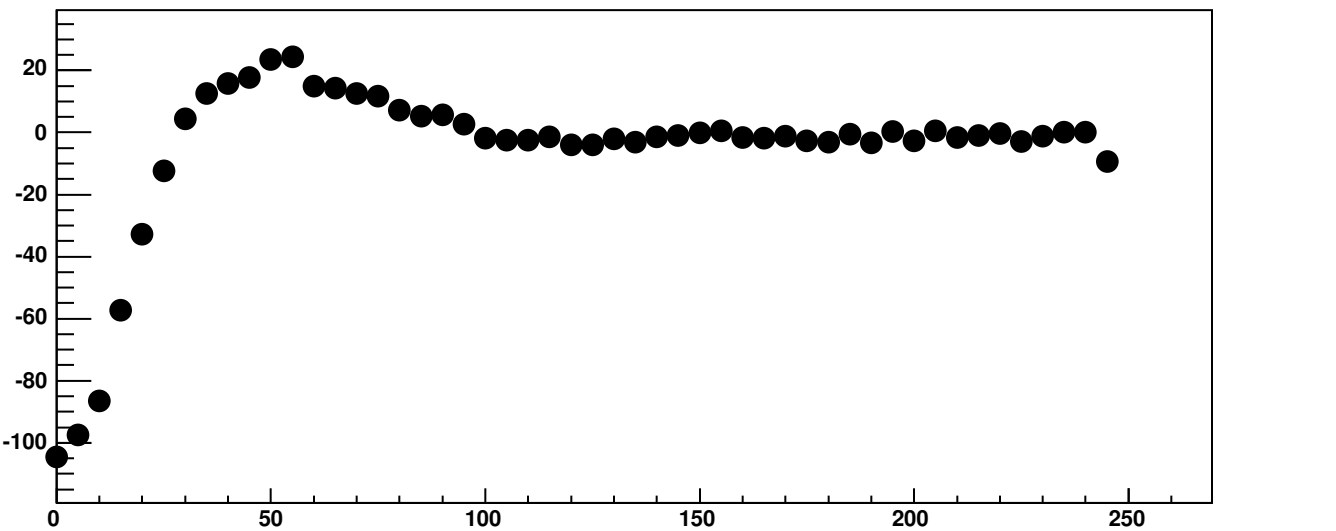
Chip 5, Channel 2, Enable 2, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 2, Enable 2, DAC=4000, ADC Noise vs Hold

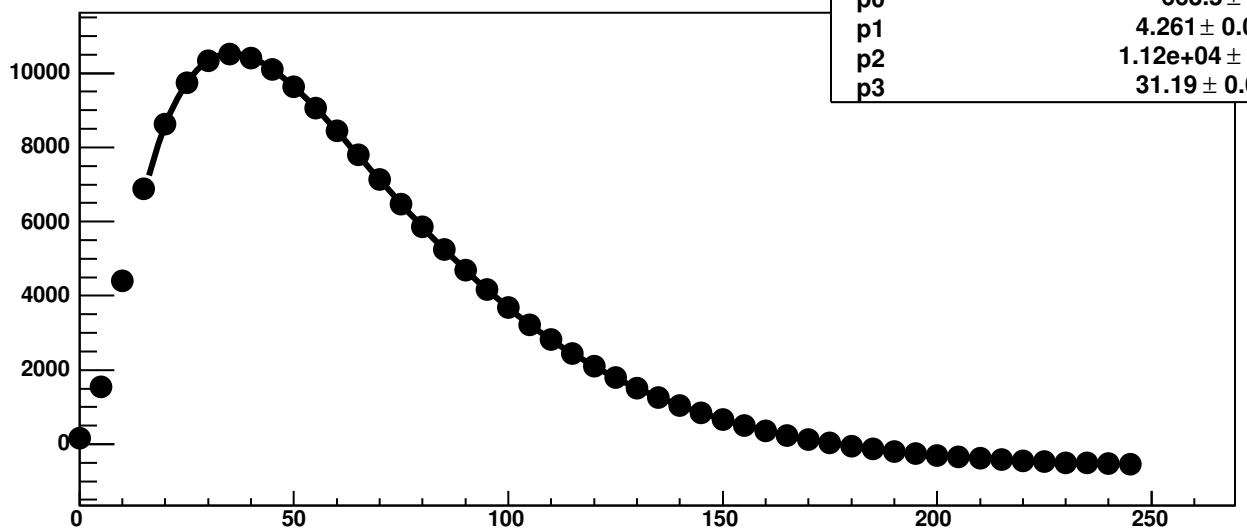


Chip 5, Channel 2, Enable 2, DAC=4000, ADC Residuals vs Hold

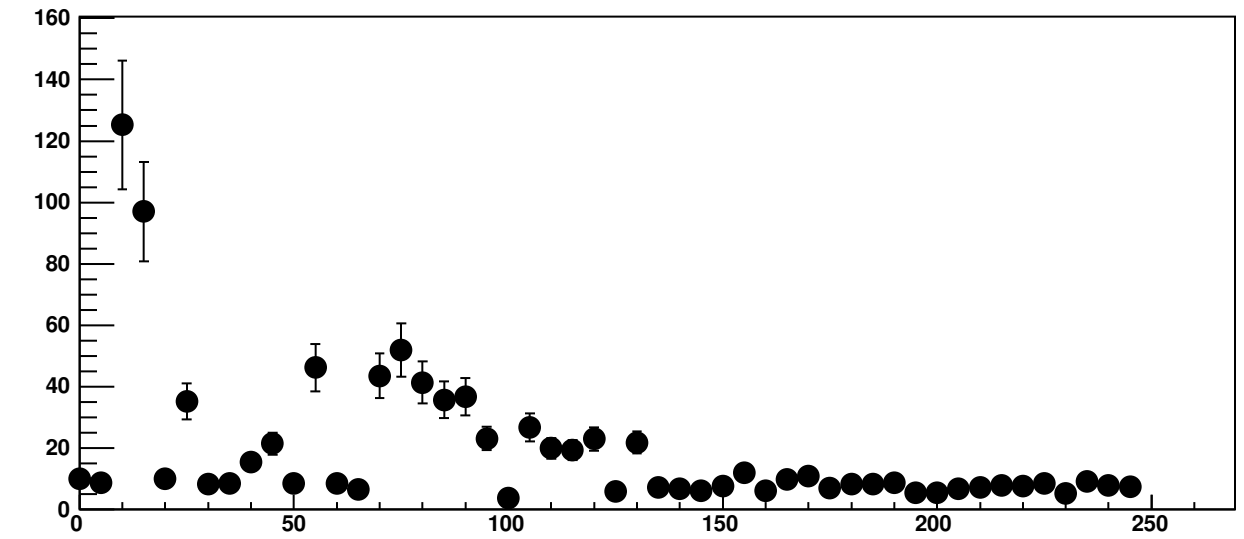


Chip 5, Channel 2, Enable 3!, DAC=4000, ADC Mean vs Hold

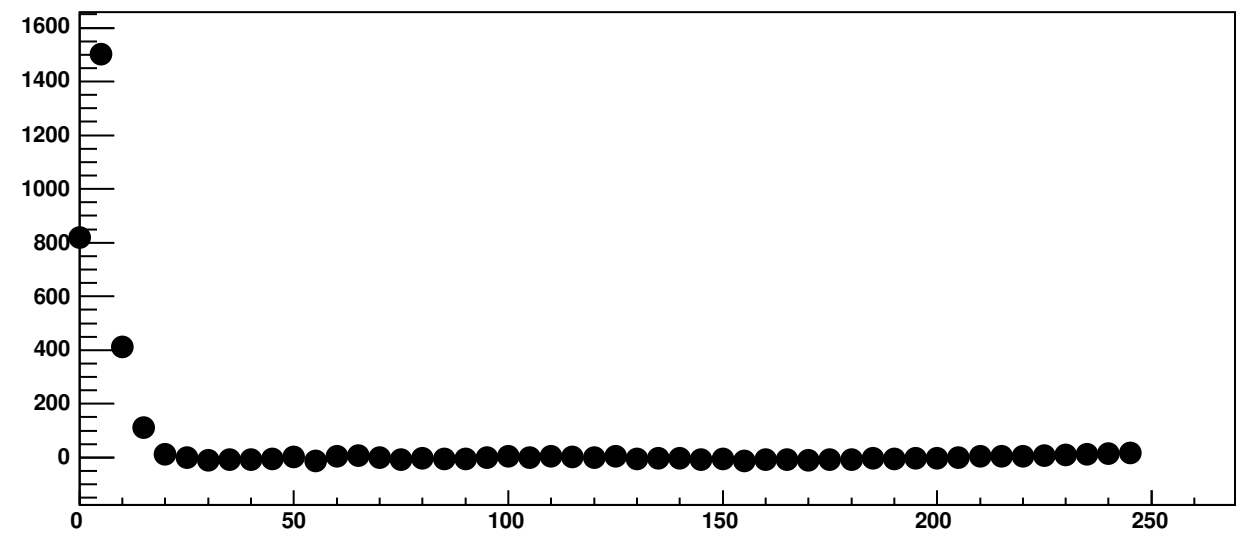
χ^2 / ndf	550.6 / 42
p0	-663.5 ± 0.5662
p1	4.261 ± 0.009115
p2	1.12e+04 ± 0.9729
p3	31.19 ± 0.005461



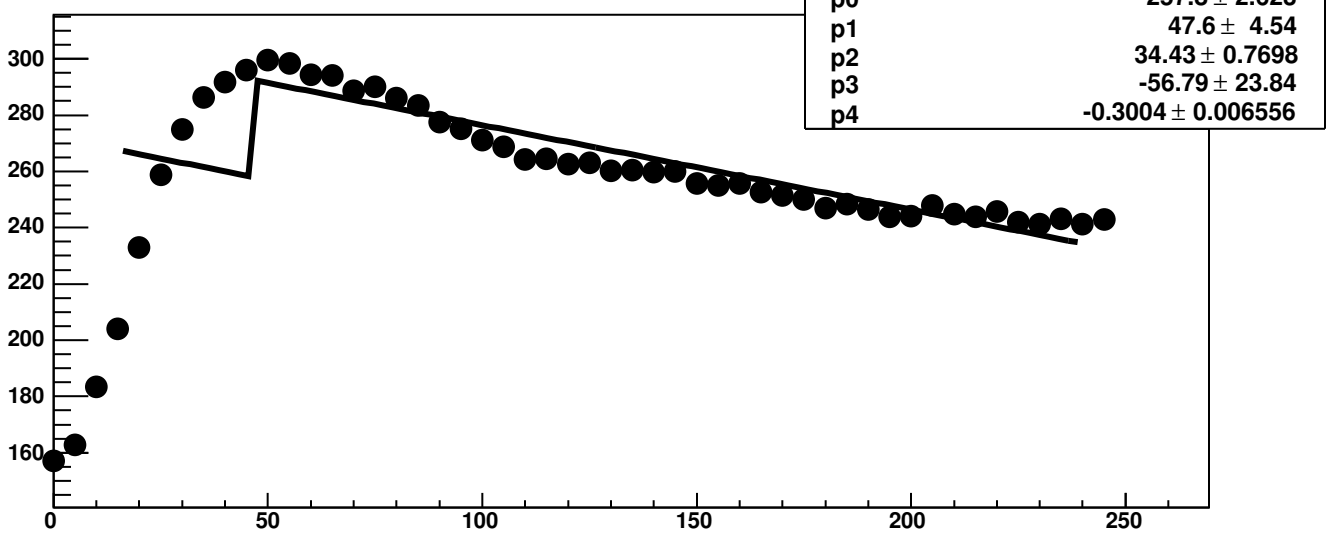
Chip 5, Channel 2, Enable 3!, DAC=4000, ADC Noise vs Hold



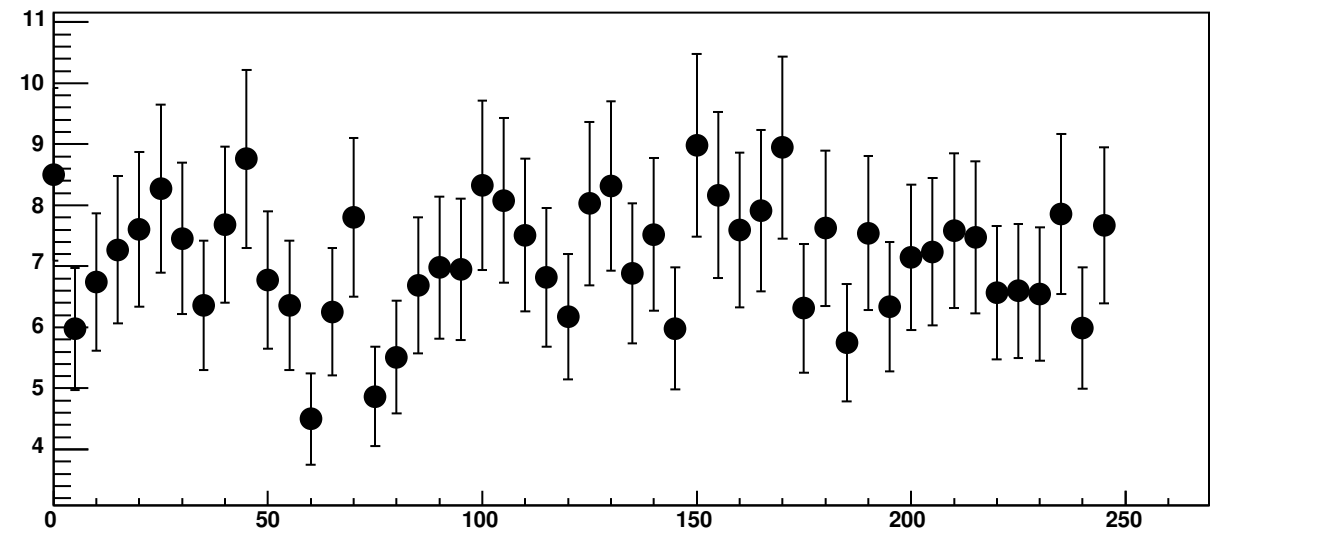
Chip 5, Channel 2, Enable 3!, DAC=4000, ADC Residuals vs Hold



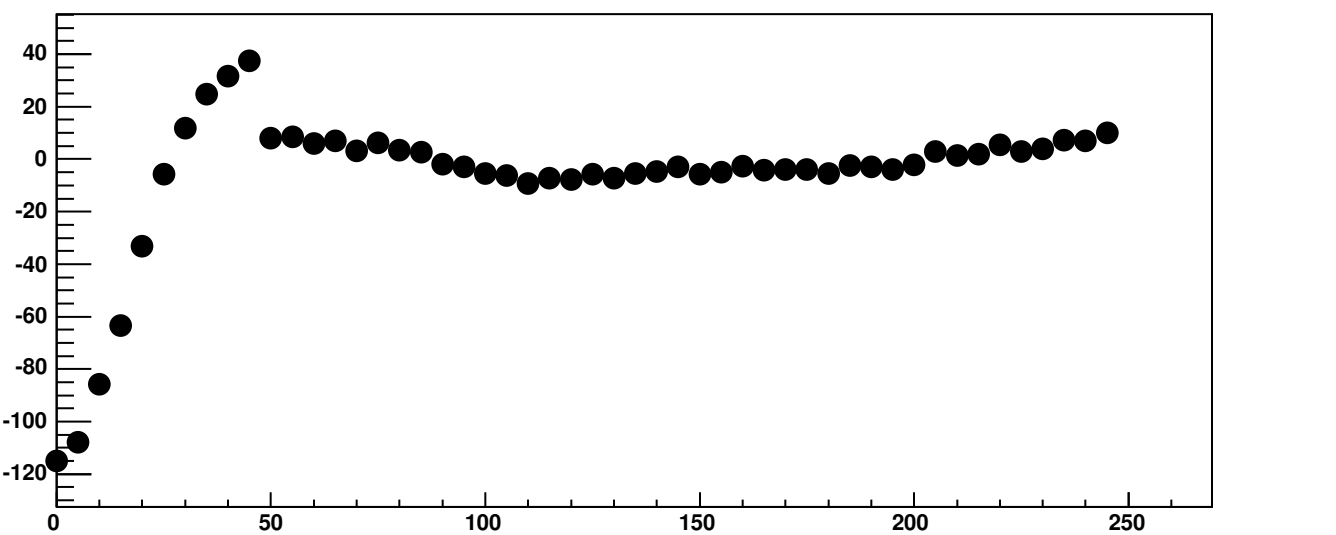
Chip 5, Channel 2, Enable 4, DAC=4000, ADC Mean vs Hold



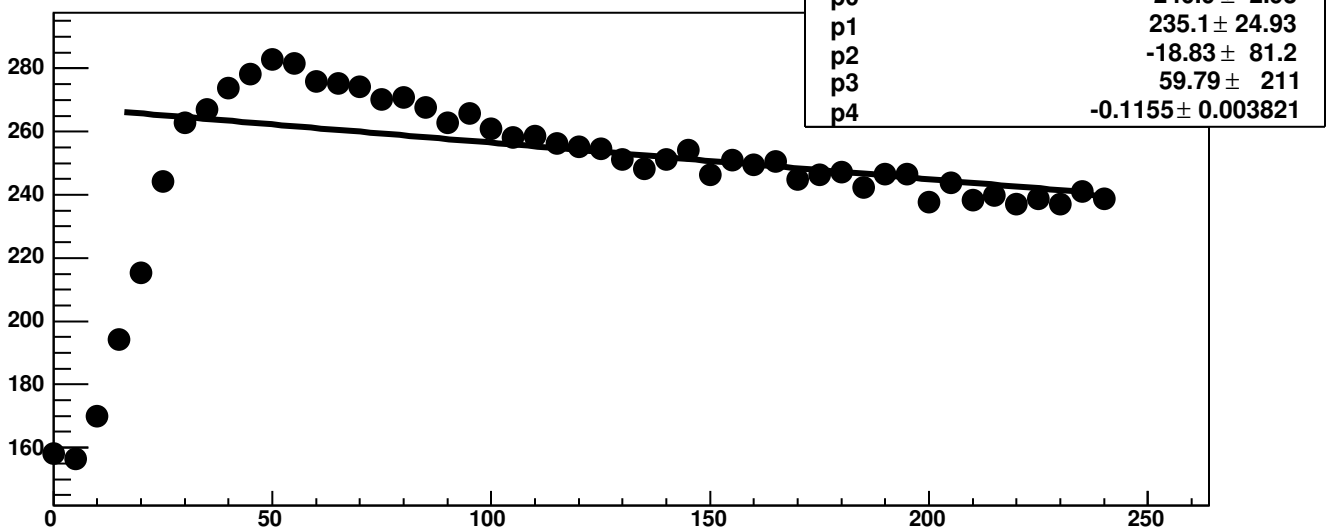
Chip 5, Channel 2, Enable 4, DAC=4000, ADC Noise vs Hold



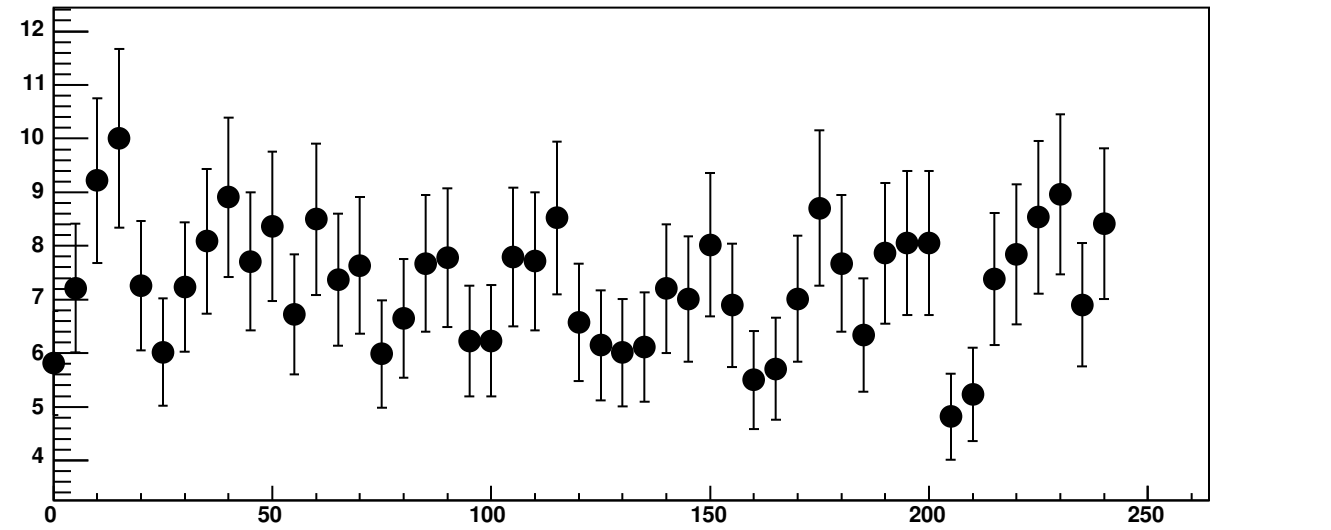
Chip 5, Channel 2, Enable 4, DAC=4000, ADC Residuals vs Hold



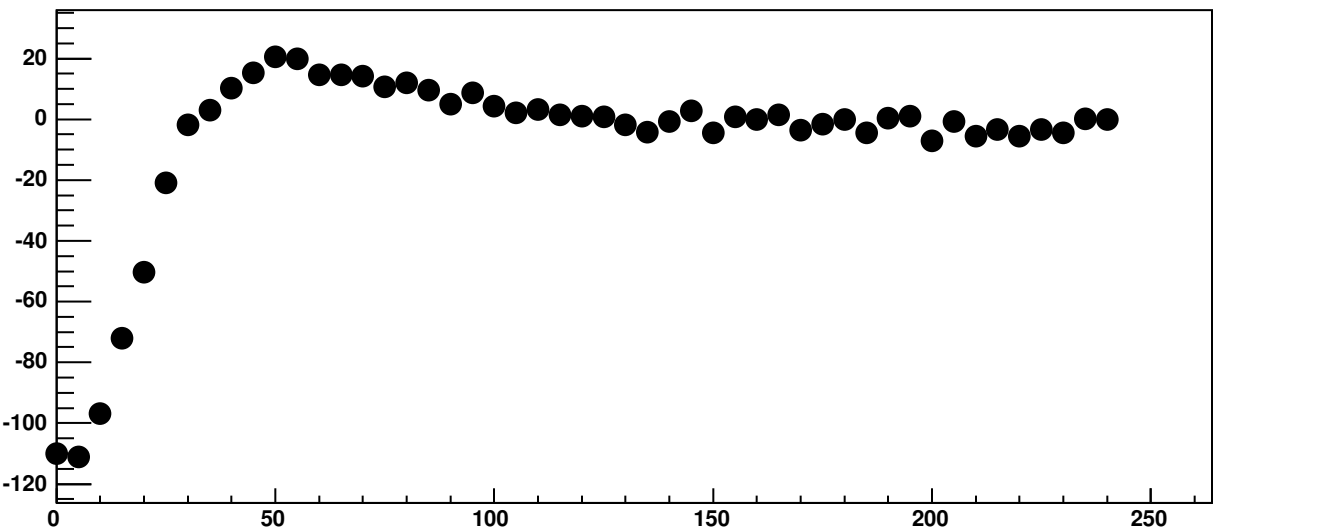
Chip 5, Channel 2, Enable 5, DAC=4000, ADC Mean vs Hold



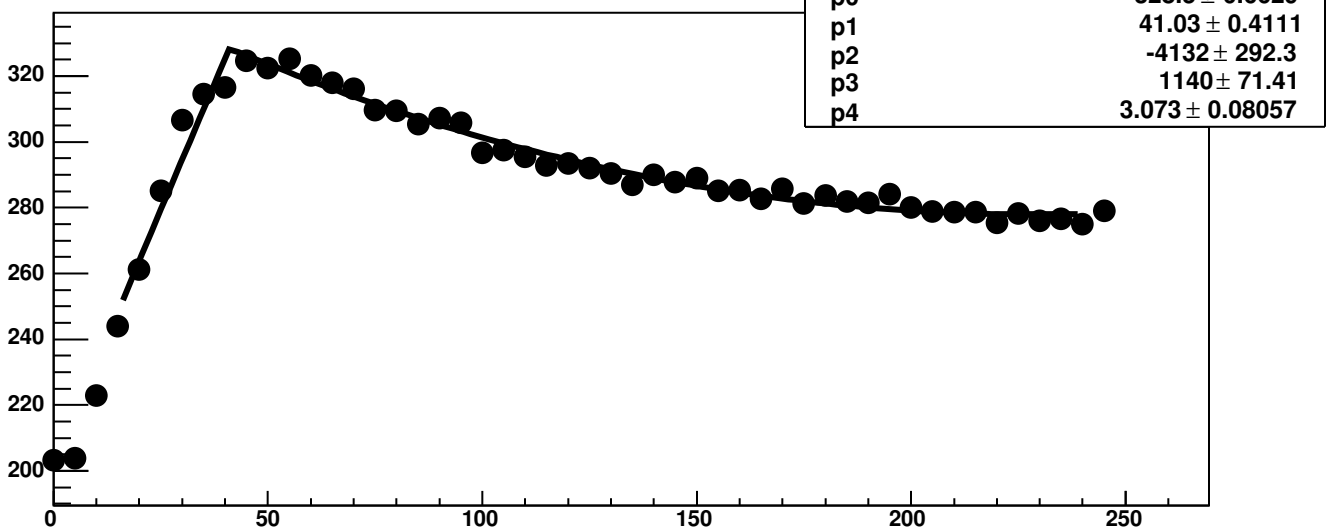
Chip 5, Channel 2, Enable 5, DAC=4000, ADC Noise vs Hold



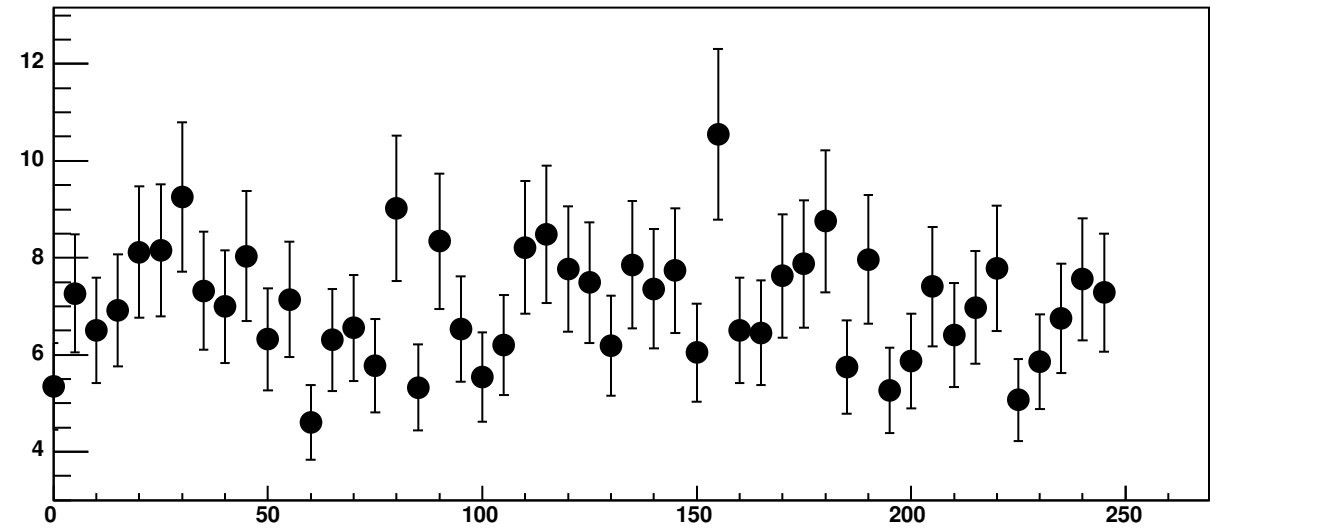
Chip 5, Channel 2, Enable 5, DAC=4000, ADC Residuals vs Hold



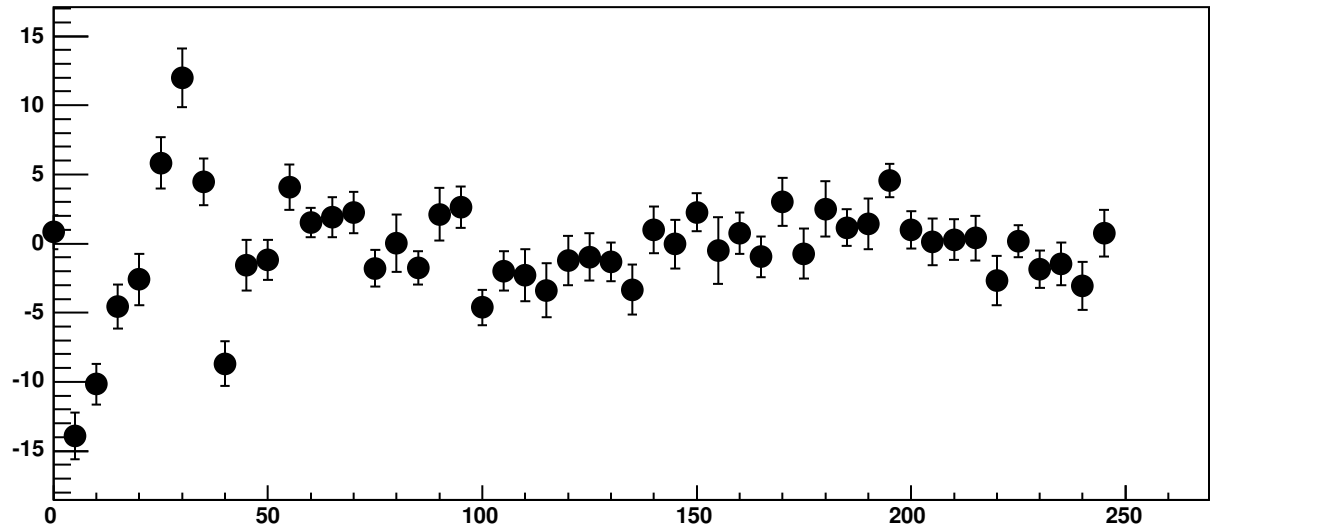
Chip 5, Channel 3, Enable 0, DAC=4000, ADC Mean vs Hold

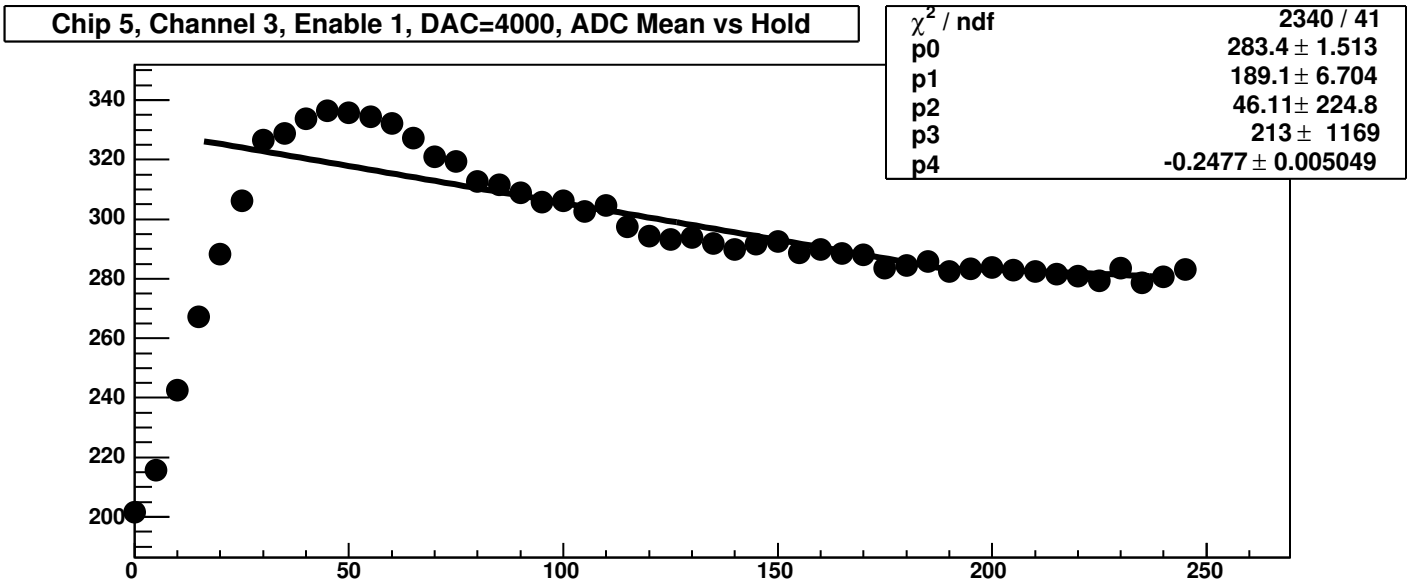


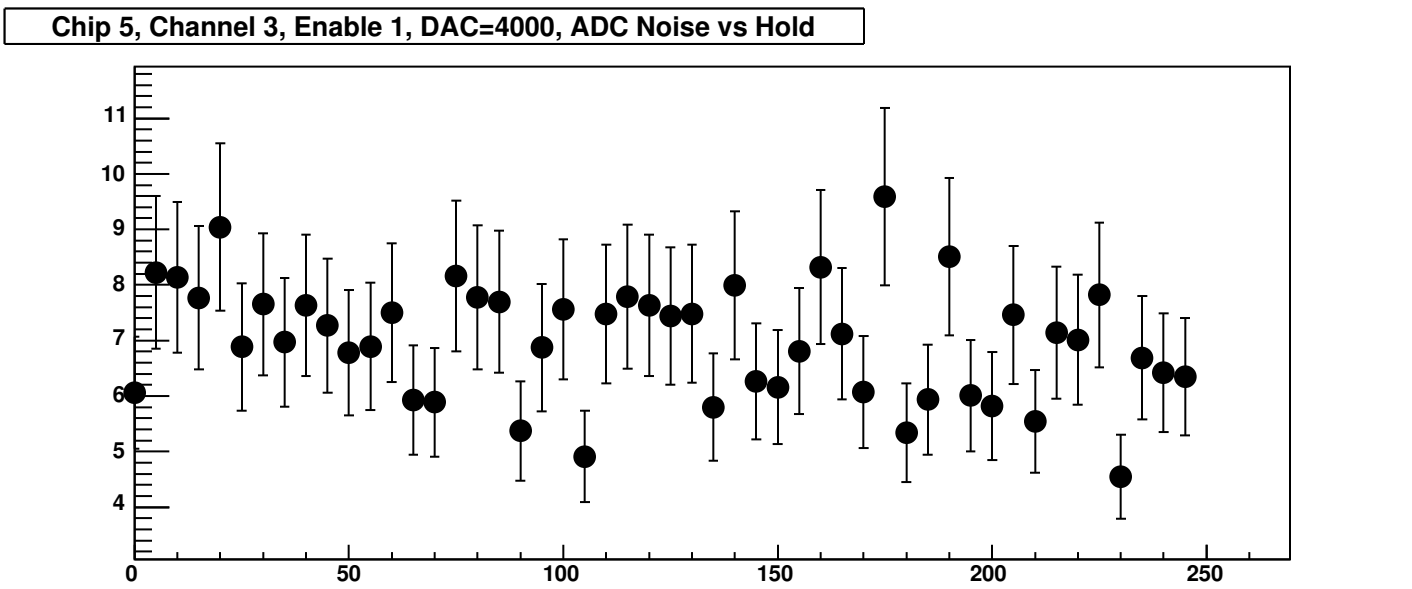
Chip 5, Channel 3, Enable 0, DAC=4000, ADC Noise vs Hold

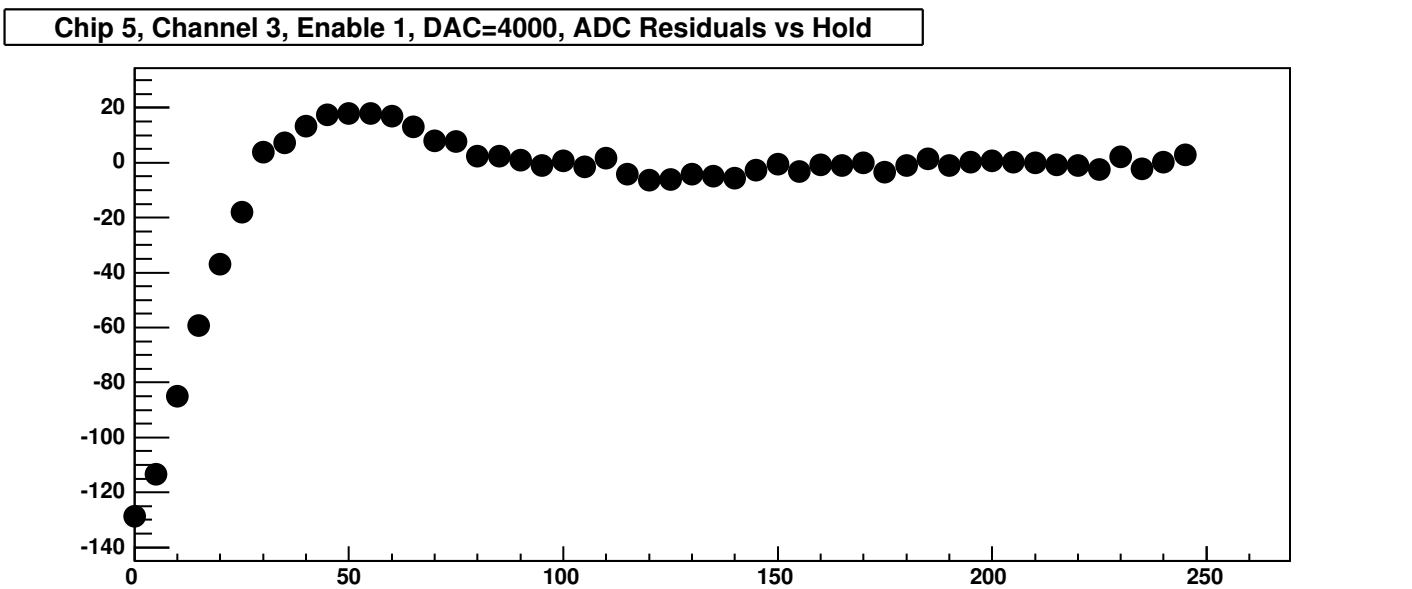


Chip 5, Channel 3, Enable 0, DAC=4000, ADC Residuals vs Hold

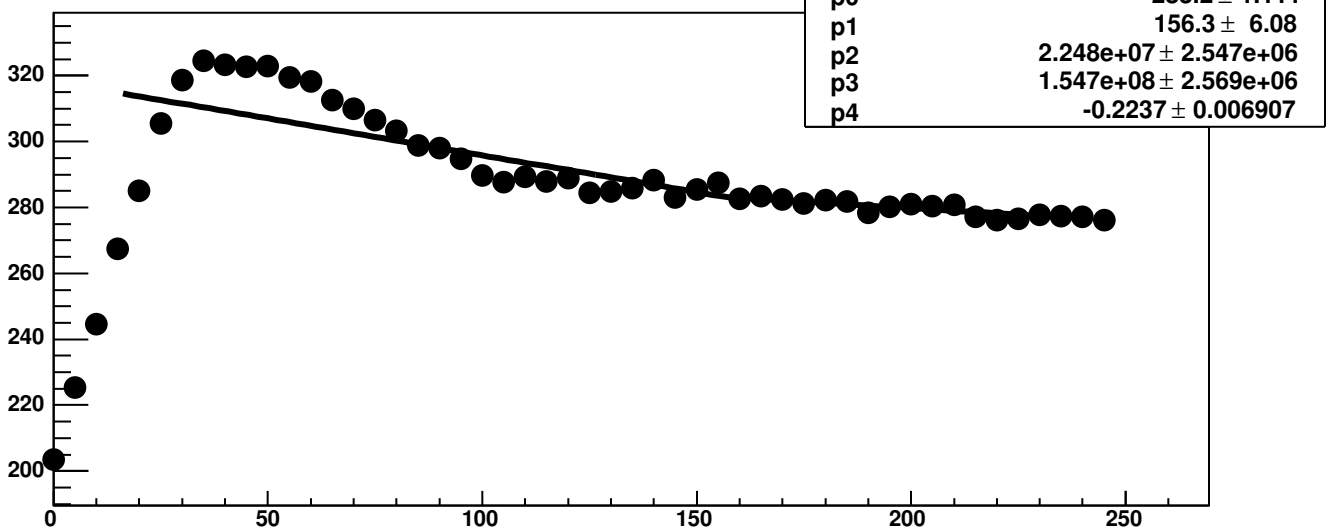



Chip 5, Channel 3, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 2340 / 41
p0 283.4 ± 1.513
p1 189.1 ± 6.704
p2 46.11 ± 224.8
p3 213 ± 1169
p4 -0.2477 ± 0.005049
340
320
300
280
260
240
220
200
0
50
100
150
200
250

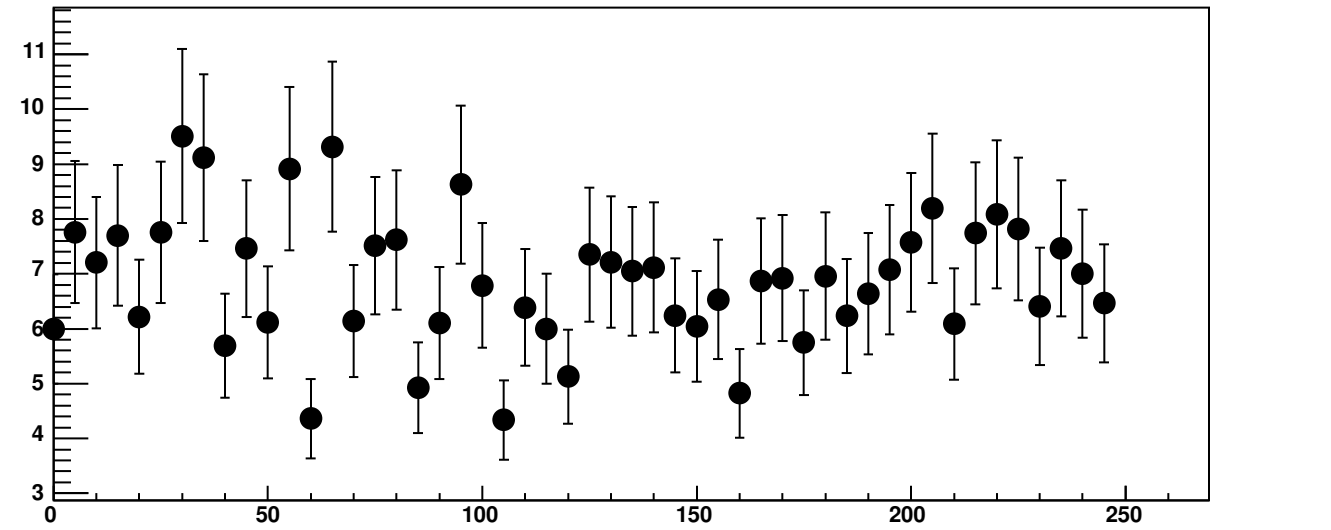

Chip 5, Channel 3, Enable 1, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 5, Channel 3, Enable 1, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
-80
-100
-120
-140
0
50
100
150
200
250

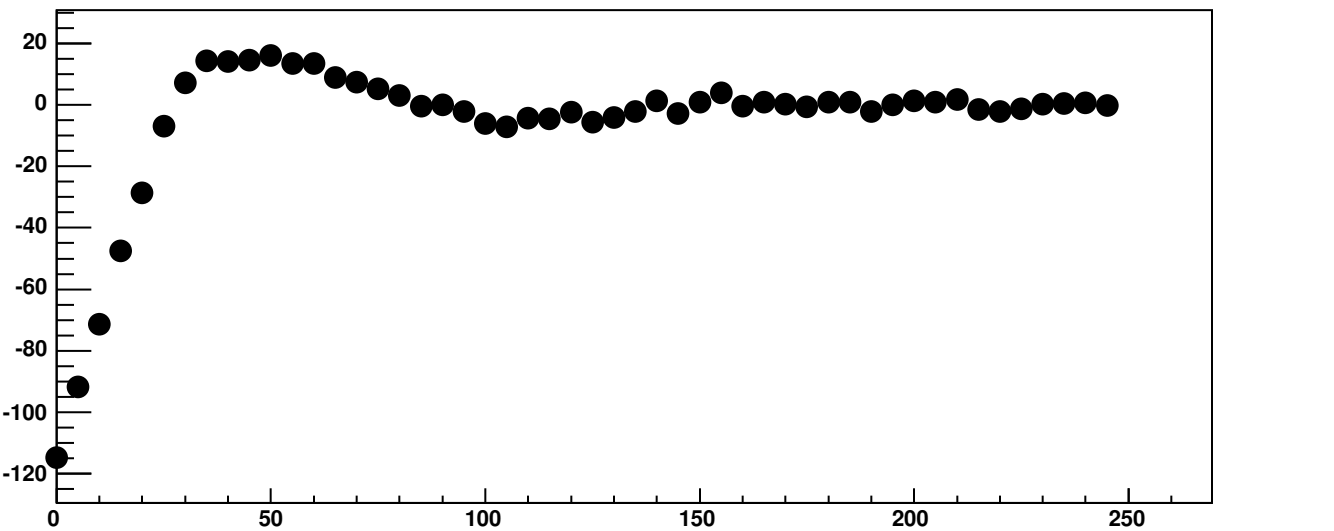
Chip 5, Channel 3, Enable 2, DAC=4000, ADC Mean vs Hold

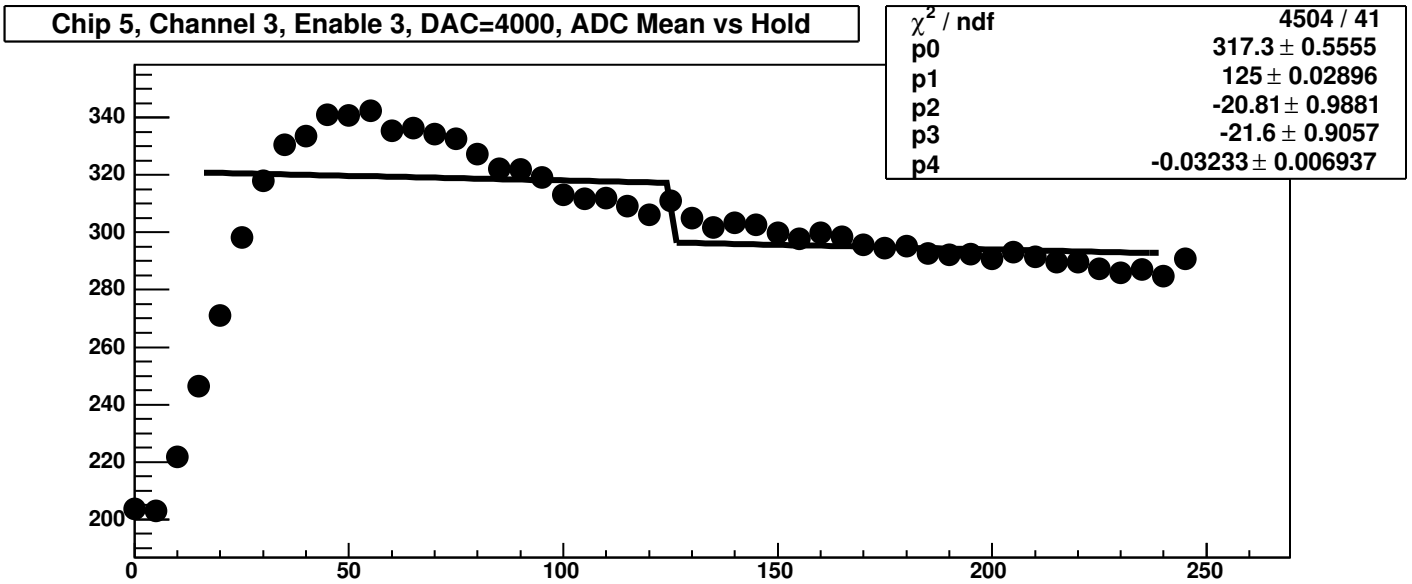


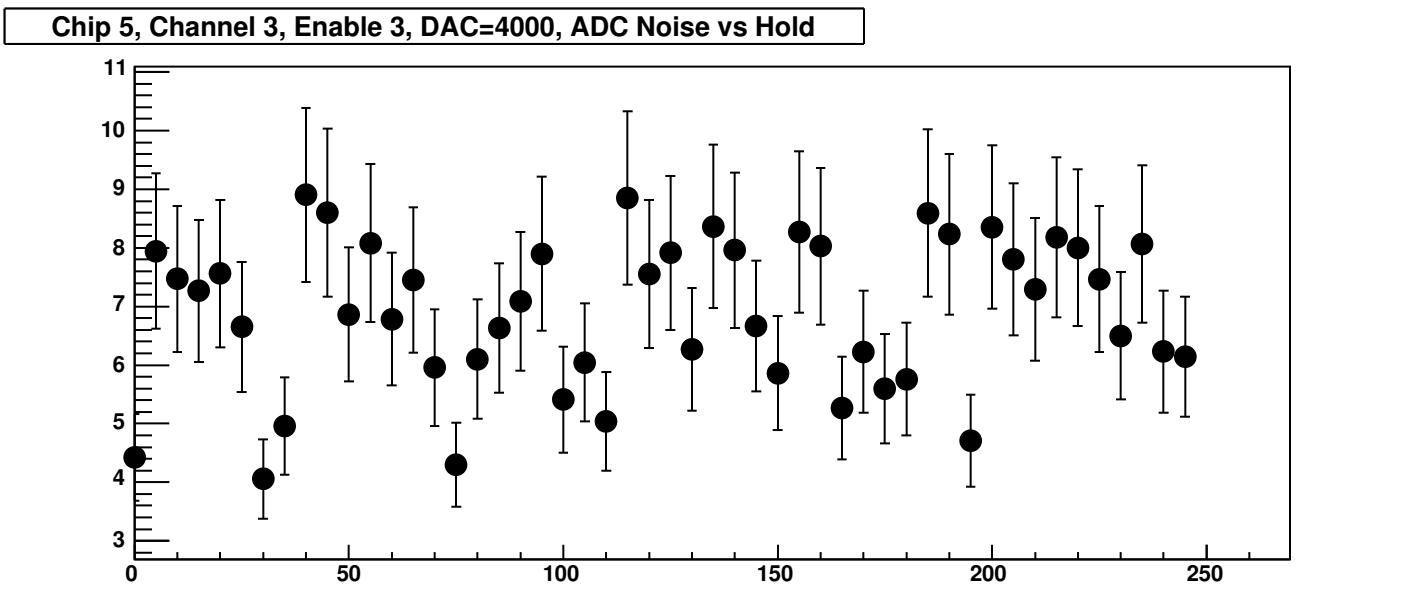
Chip 5, Channel 3, Enable 2, DAC=4000, ADC Noise vs Hold

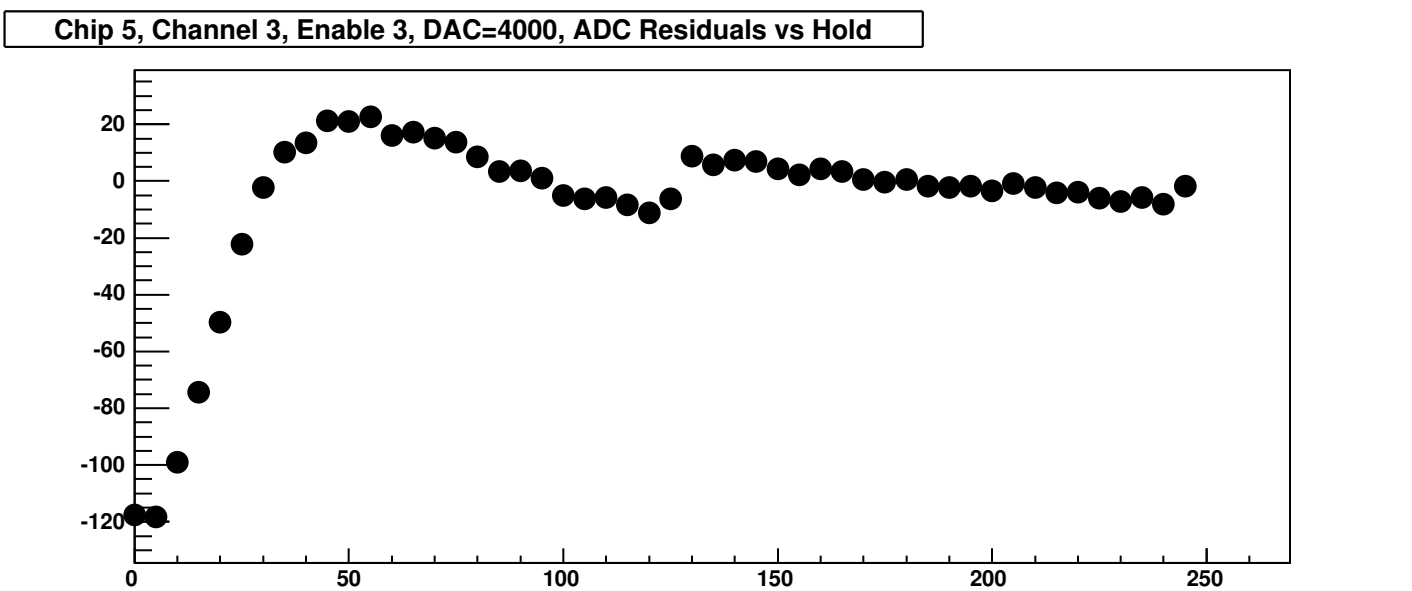


Chip 5, Channel 3, Enable 2, DAC=4000, ADC Residuals vs Hold

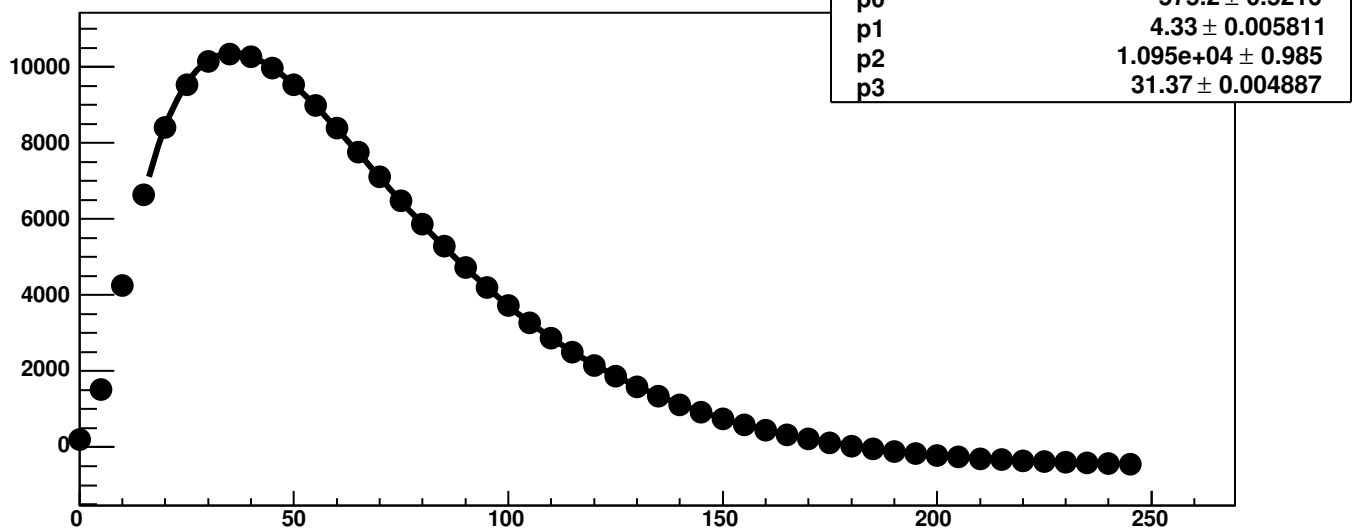



Chip 5, Channel 3, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 4504 / 41
p0 317.3 ± 0.5555
p1 125 ± 0.02896
p2 -20.81 ± 0.9881
p3 -21.6 ± 0.9057
p4 -0.03233 ± 0.006937
340
320
300
280
260
240
220
200
0
50
100
150
200
250

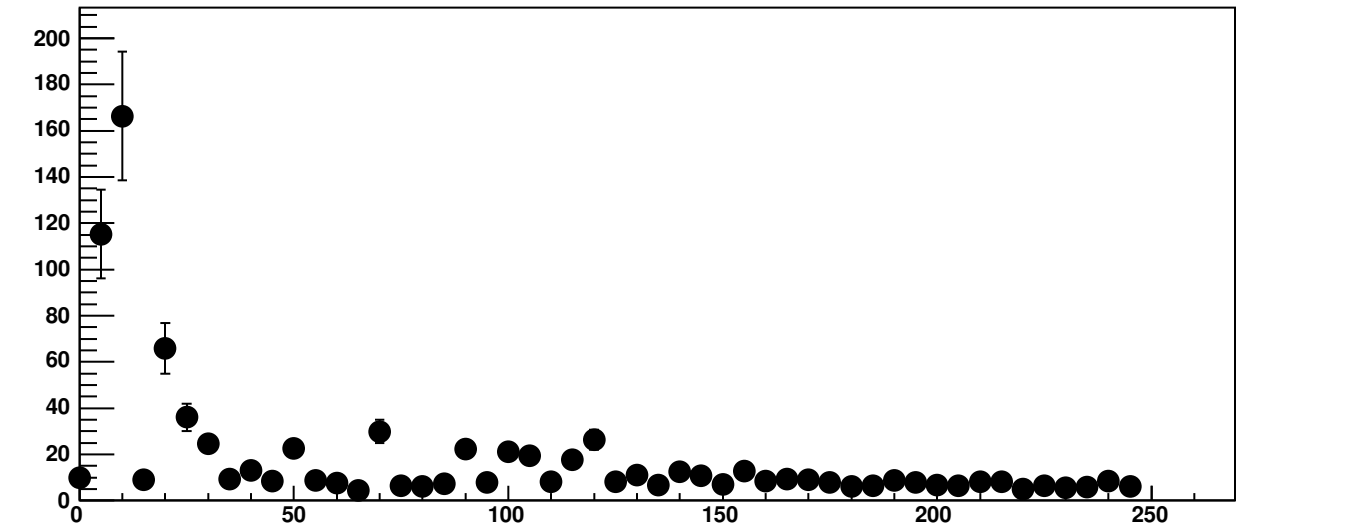

Chip 5, Channel 3, Enable 3, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 5, Channel 3, Enable 3, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
-80
-100
-120
0
50
100
150
200
250

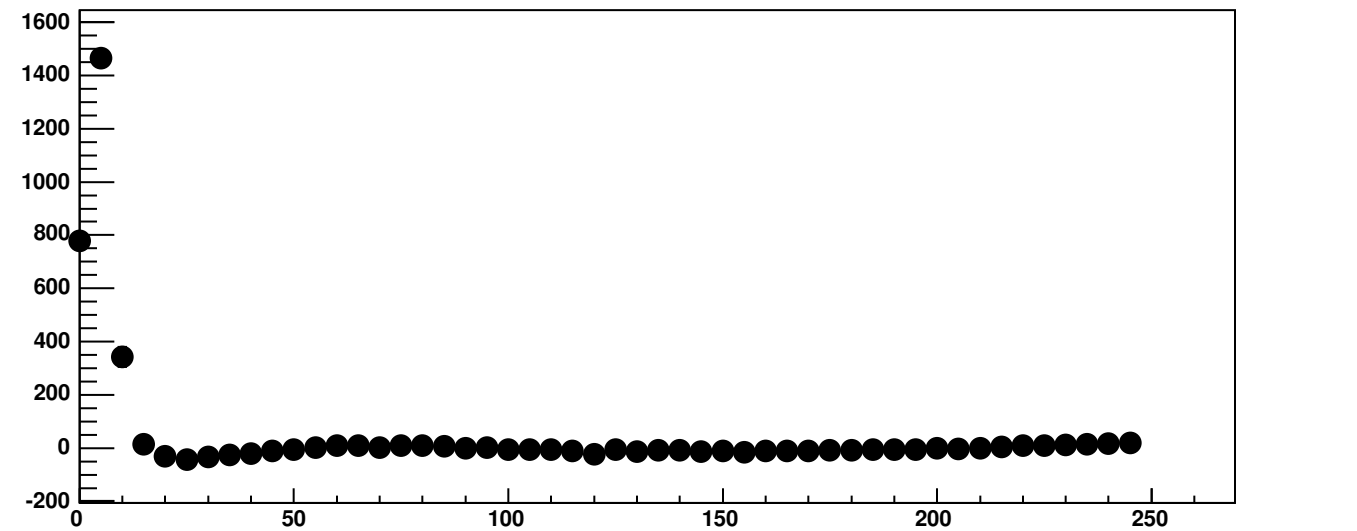
Chip 5, Channel 3, Enable 4!, DAC=4000, ADC Mean vs Hold



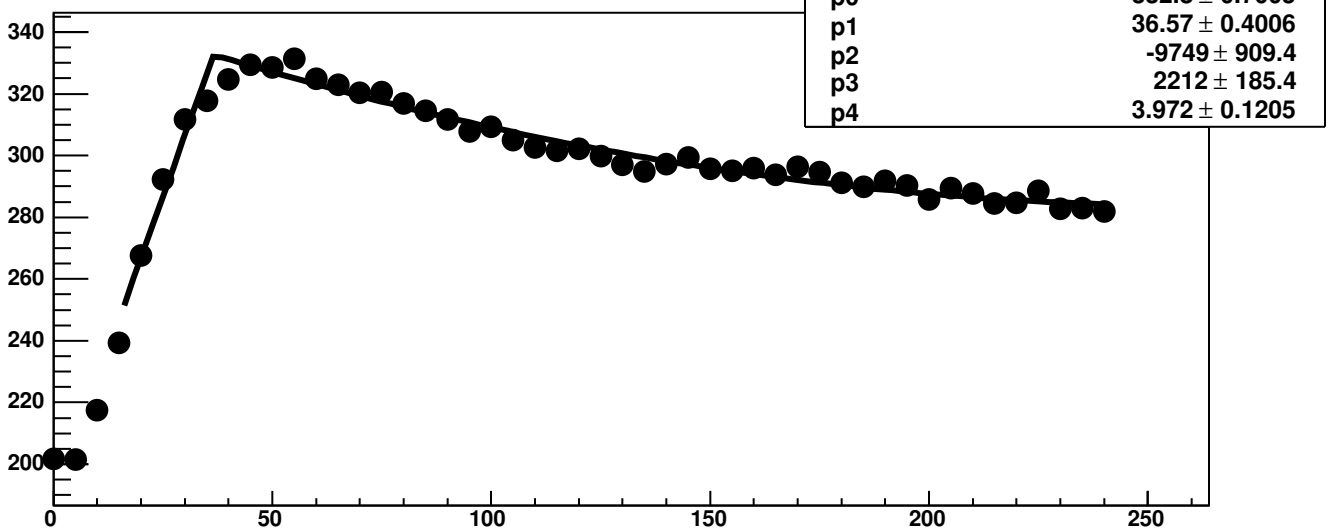
Chip 5, Channel 3, Enable 4!, DAC=4000, ADC Noise vs Hold



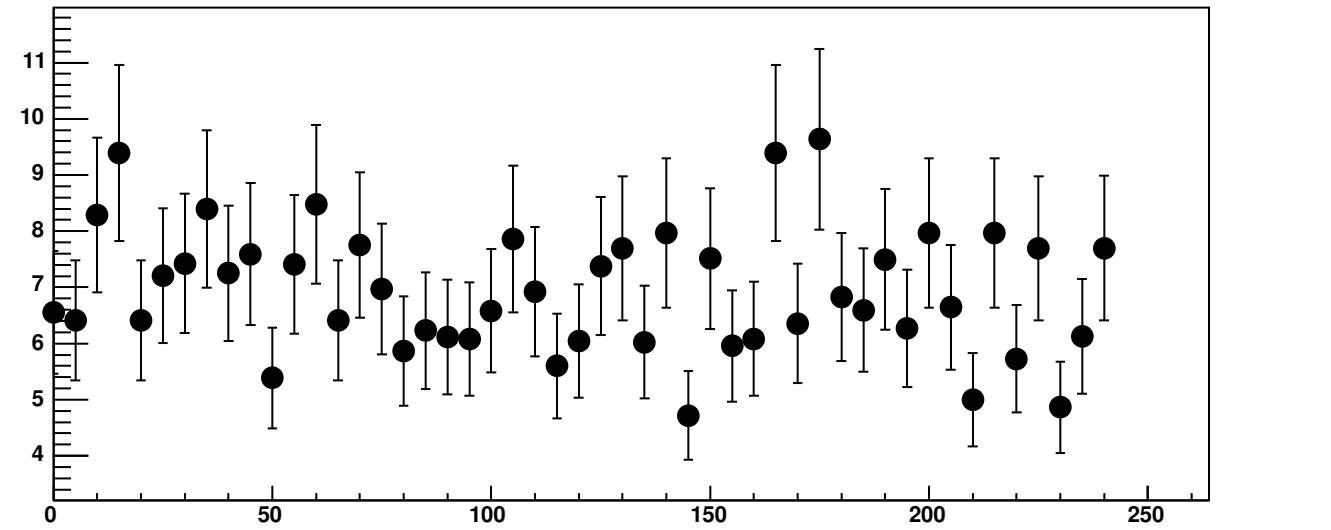
Chip 5, Channel 3, Enable 4!, DAC=4000, ADC Residuals vs Hold



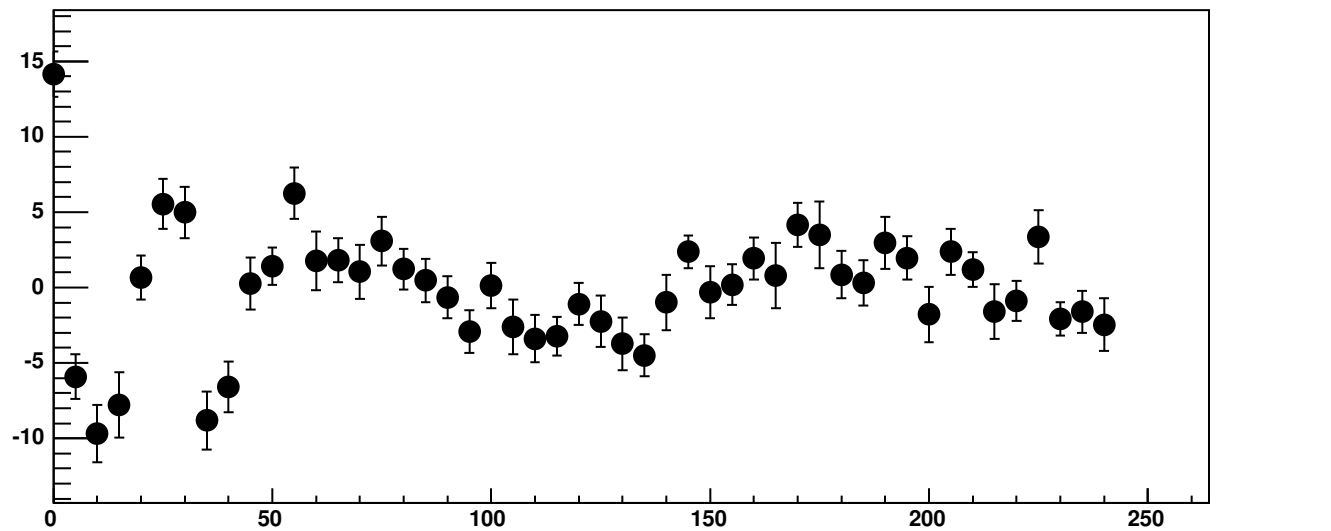
Chip 5, Channel 3, Enable 5, DAC=4000, ADC Mean vs Hold



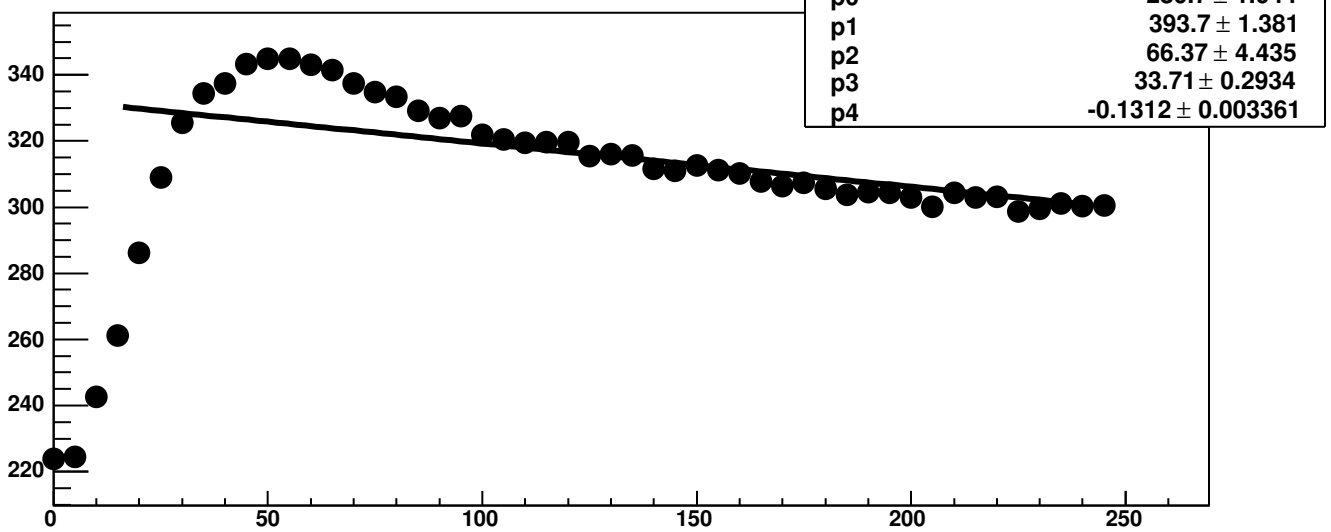
Chip 5, Channel 3, Enable 5, DAC=4000, ADC Noise vs Hold



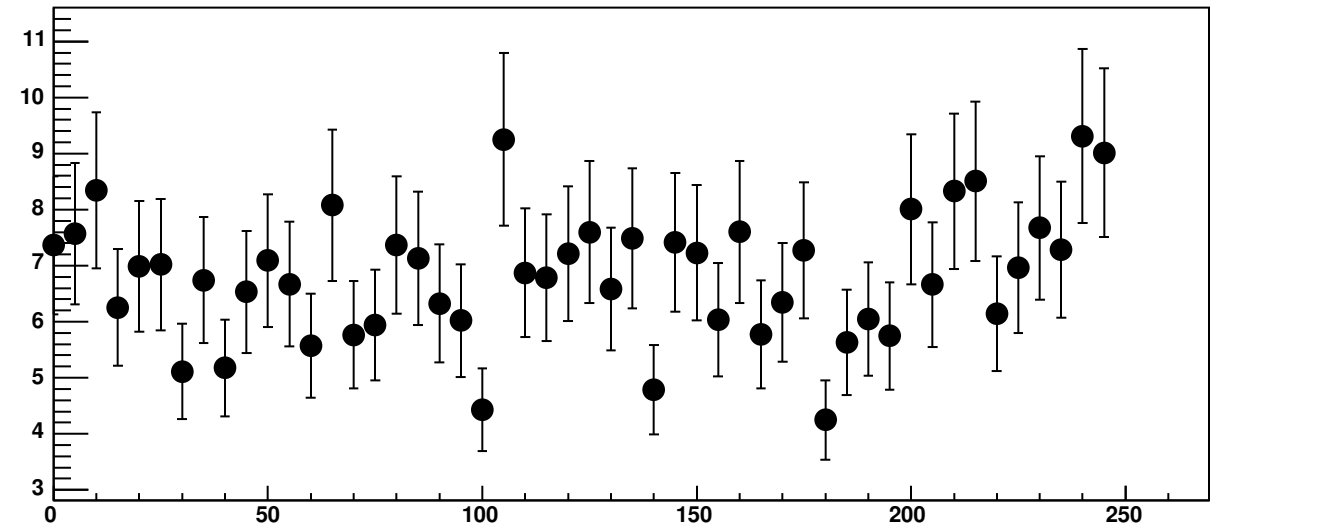
Chip 5, Channel 3, Enable 5, DAC=4000, ADC Residuals vs Hold



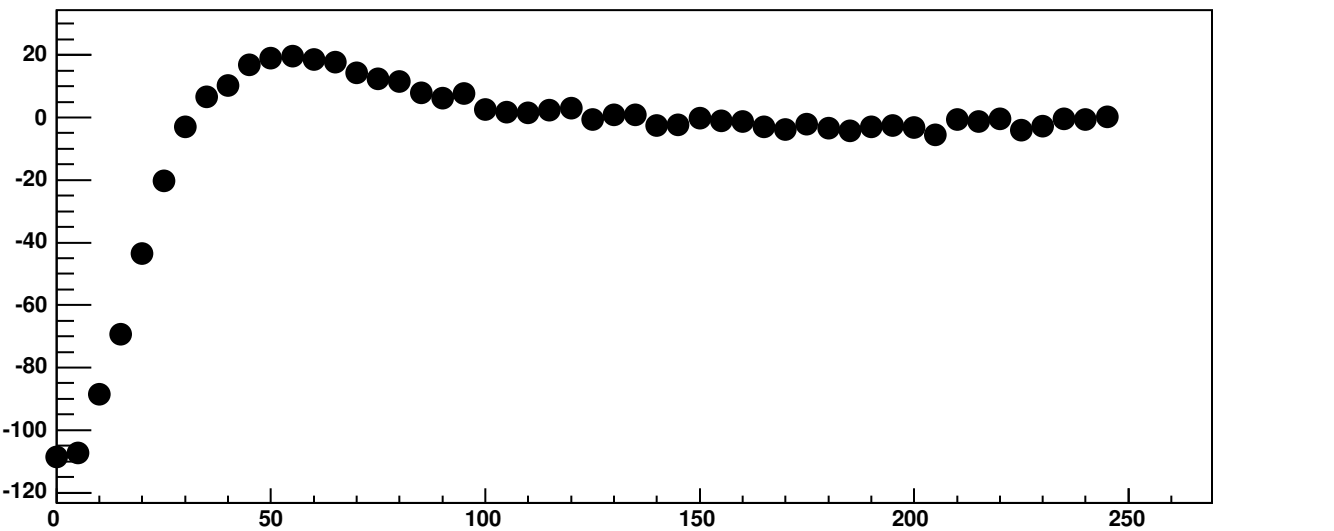
Chip 5, Channel 4, Enable 0, DAC=4000, ADC Mean vs Hold

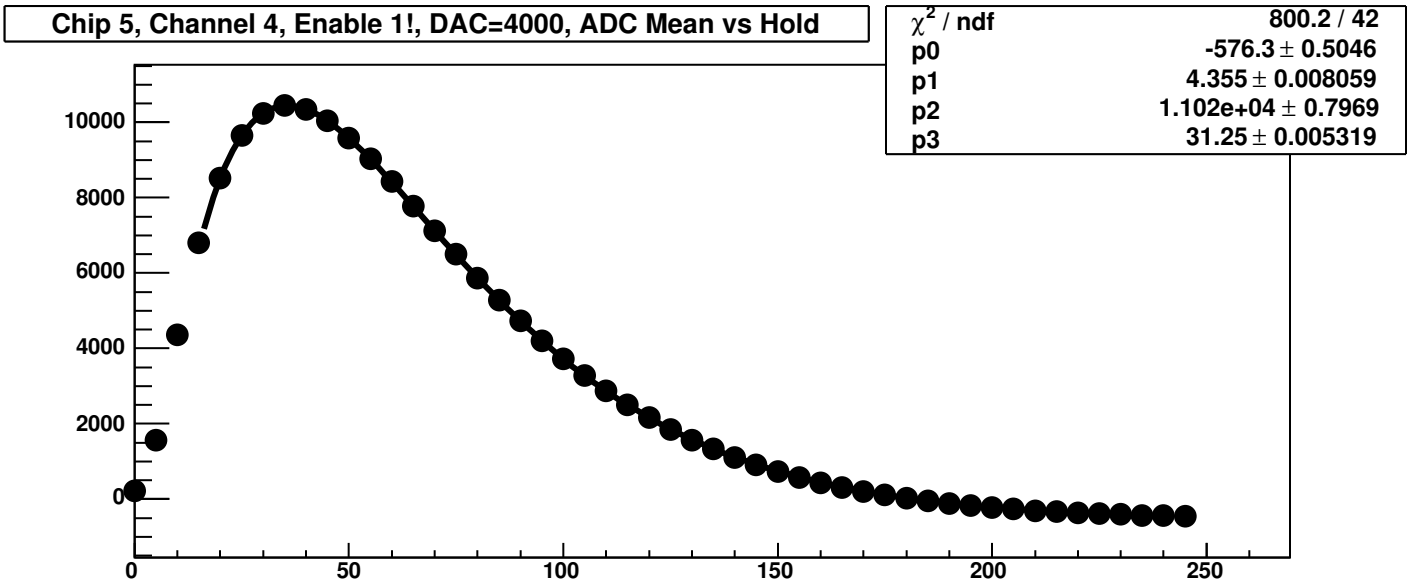


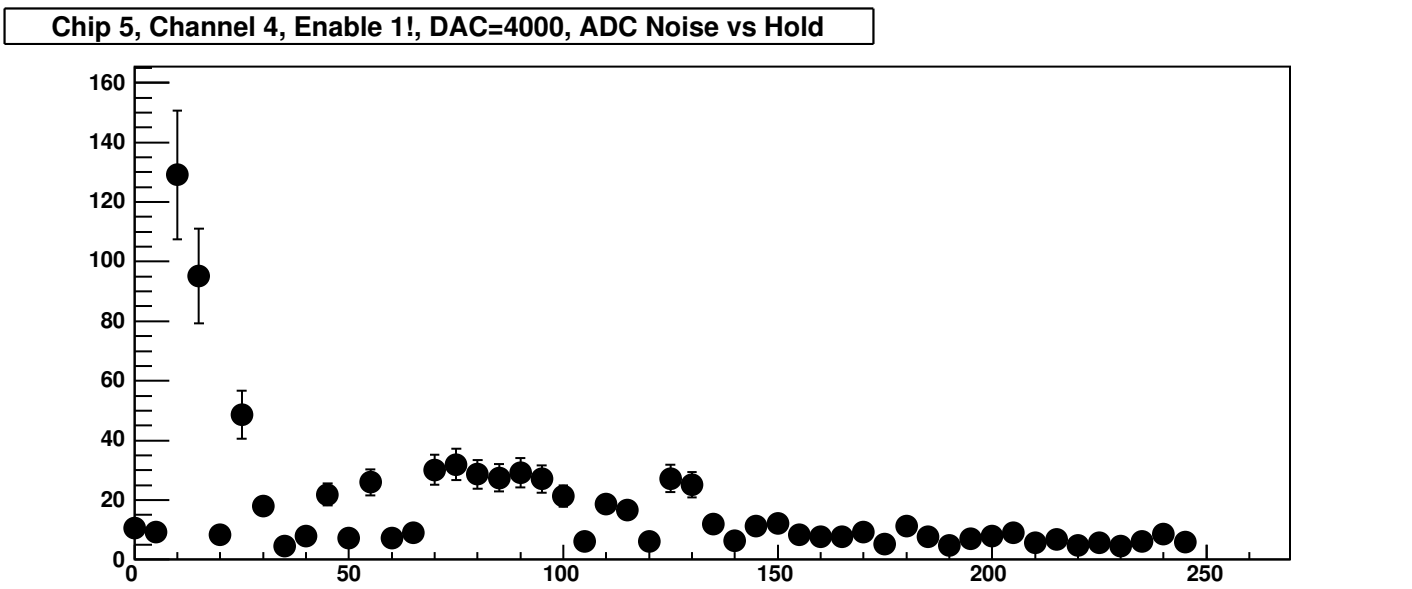
Chip 5, Channel 4, Enable 0, DAC=4000, ADC Noise vs Hold

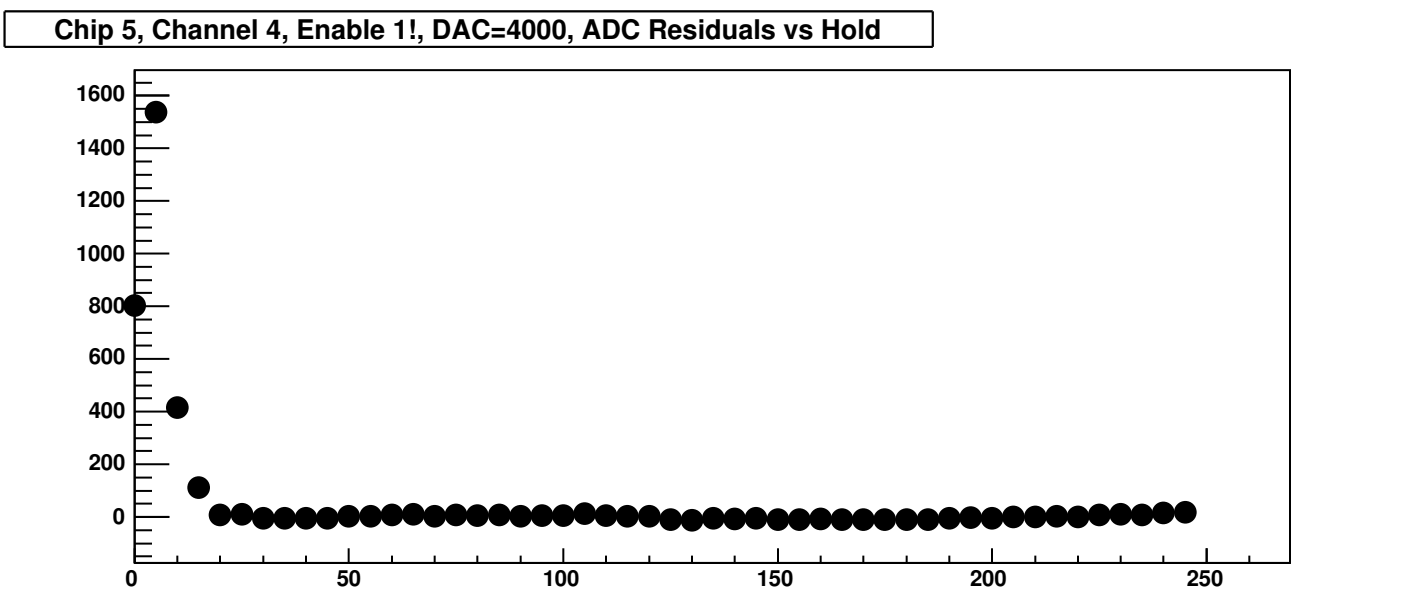


Chip 5, Channel 4, Enable 0, DAC=4000, ADC Residuals vs Hold

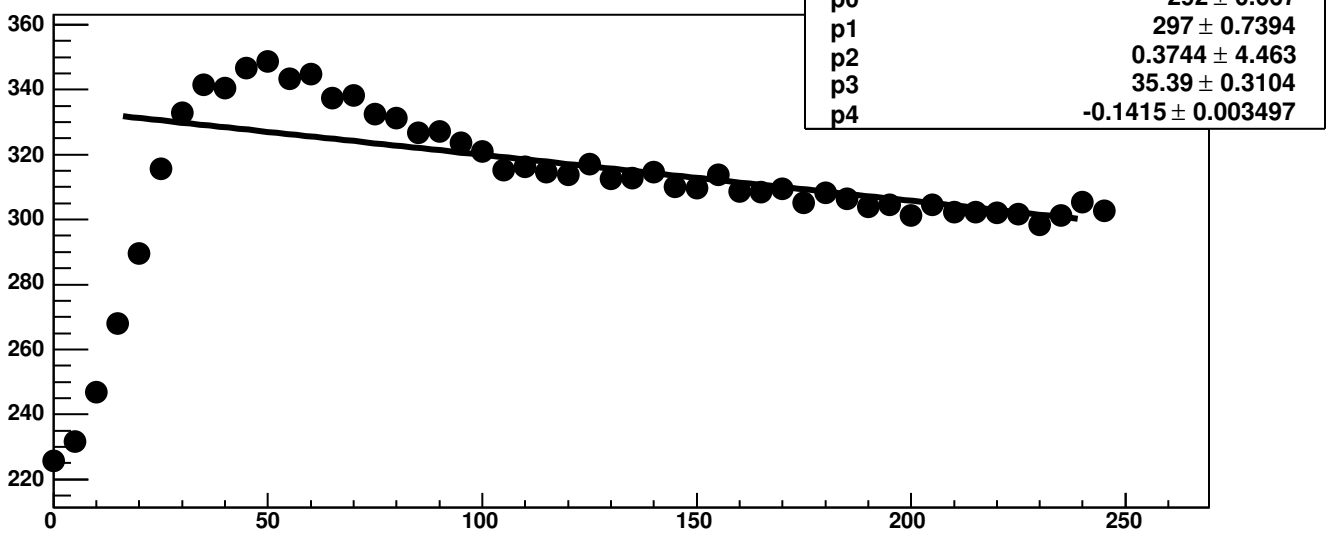



Chip 5, Channel 4, Enable 1!, DAC=4000, ADC Mean vs Hold
χ² / ndf 800.2 / 42
p0 -576.3 ± 0.5046
p1 4.355 ± 0.008059
p2 1.102e+04 ± 0.7969
p3 31.25 ± 0.005319
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

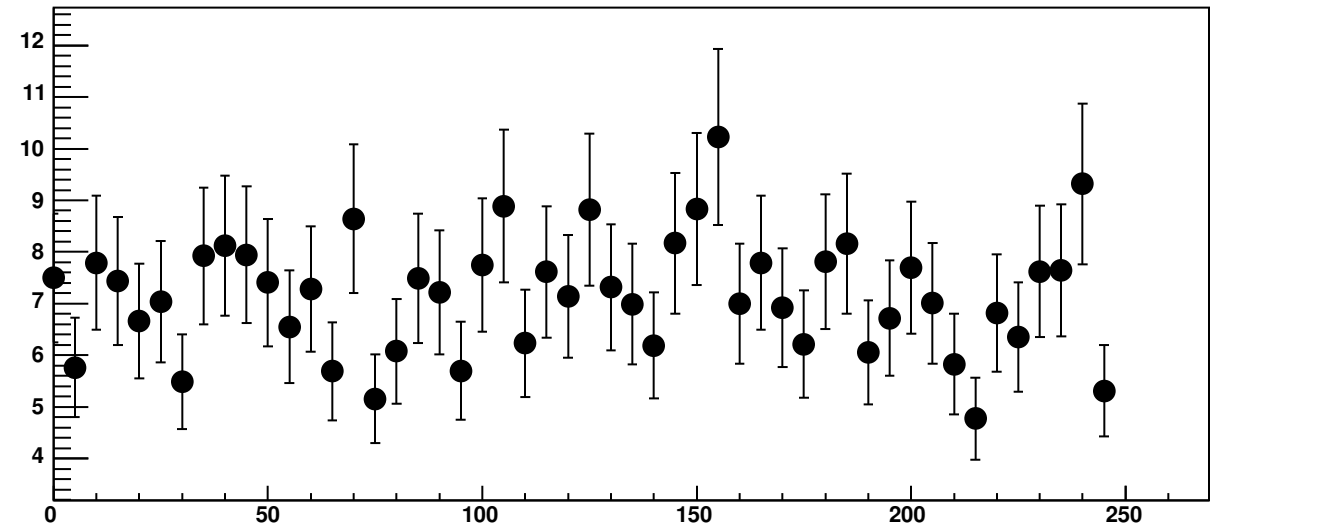

Chip 5, Channel 4, Enable 1!, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 5, Channel 4, Enable 1!, DAC=4000, ADC Residuals vs Hold
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

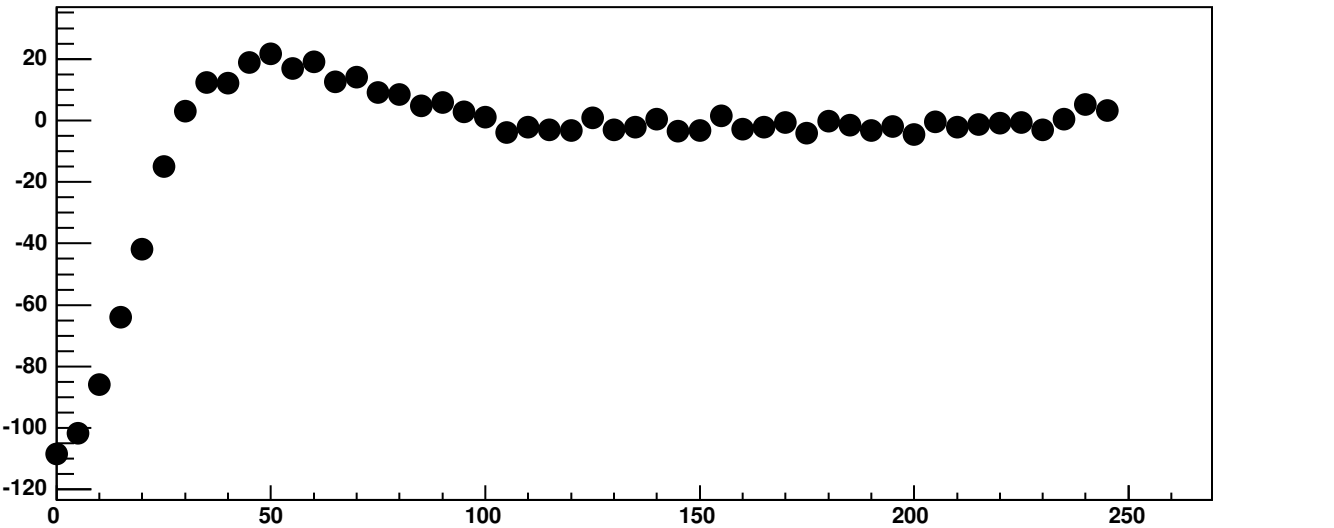
Chip 5, Channel 4, Enable 2, DAC=4000, ADC Mean vs Hold



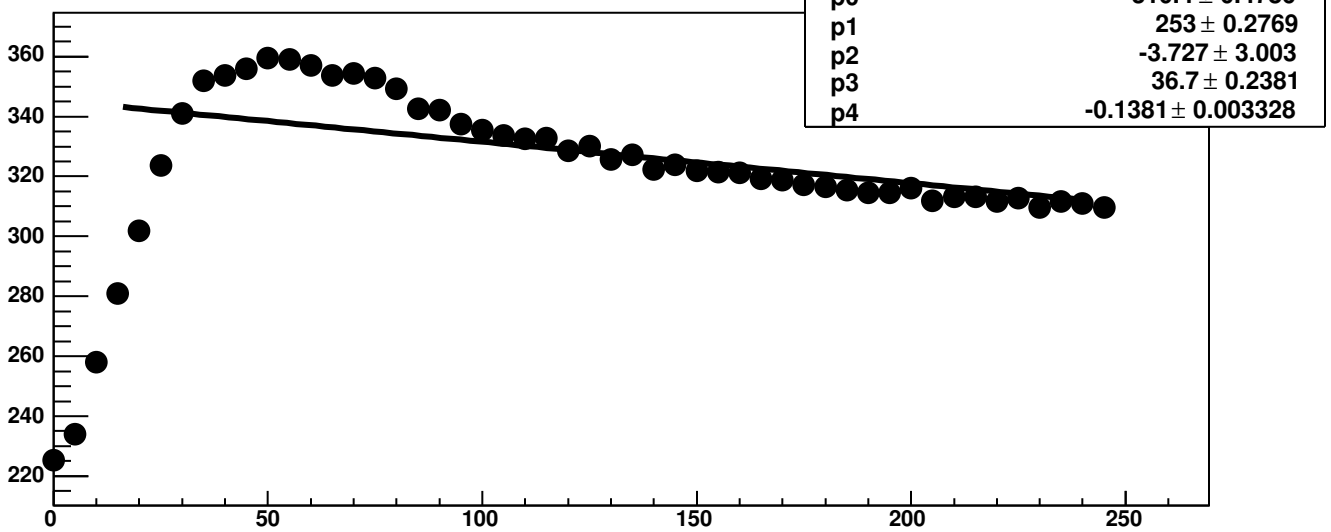
Chip 5, Channel 4, Enable 2, DAC=4000, ADC Noise vs Hold



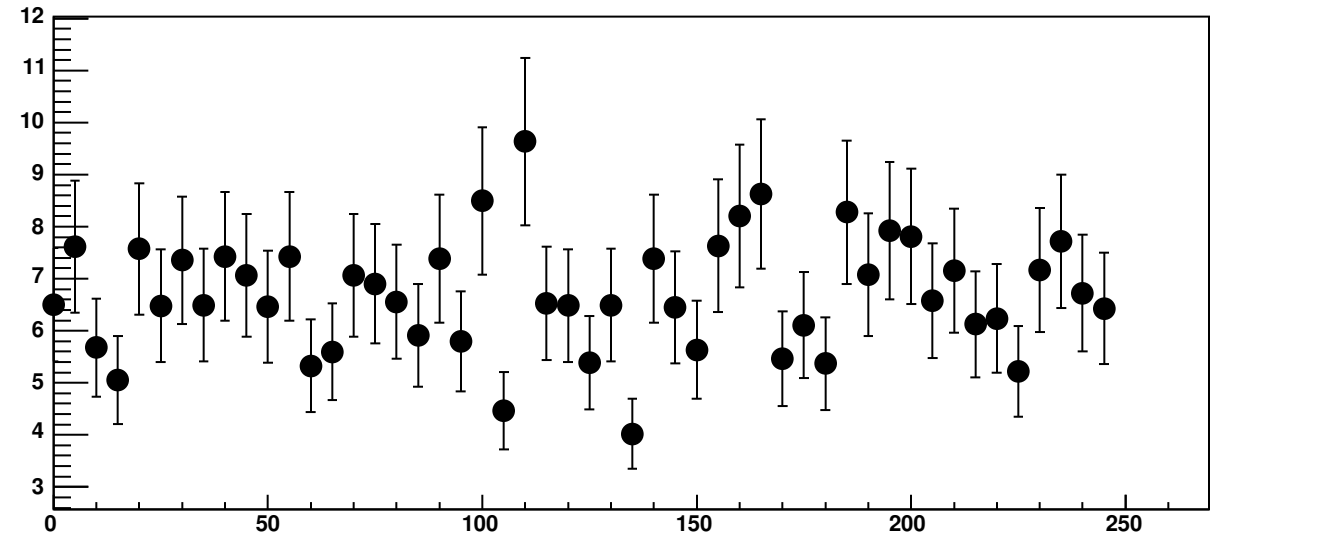
Chip 5, Channel 4, Enable 2, DAC=4000, ADC Residuals vs Hold



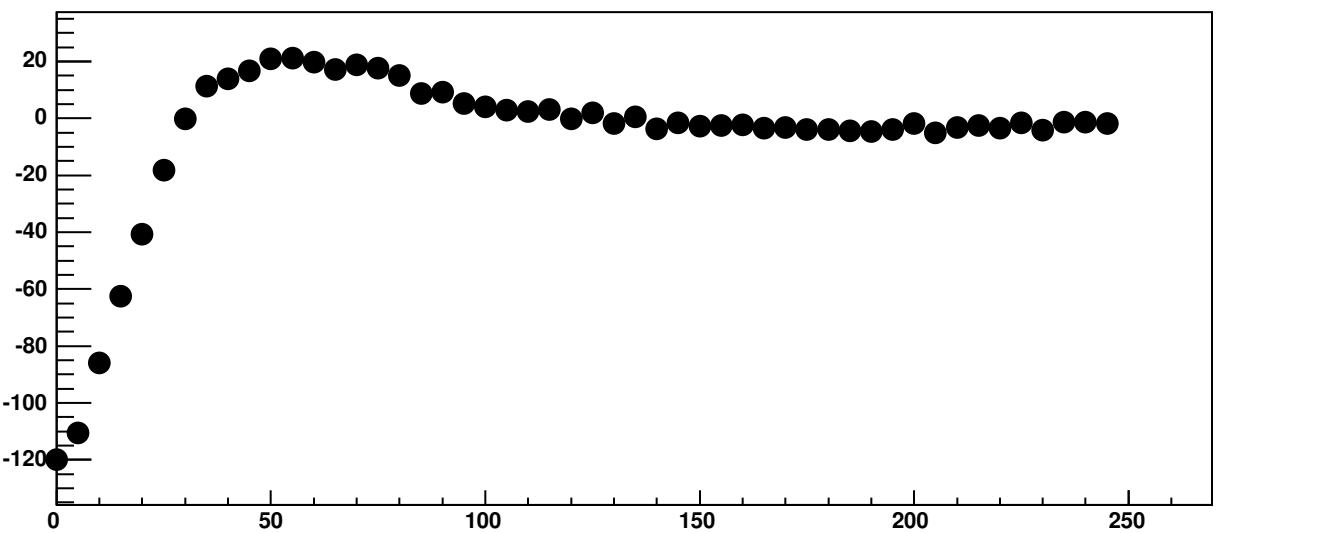
Chip 5, Channel 4, Enable 3, DAC=4000, ADC Mean vs Hold



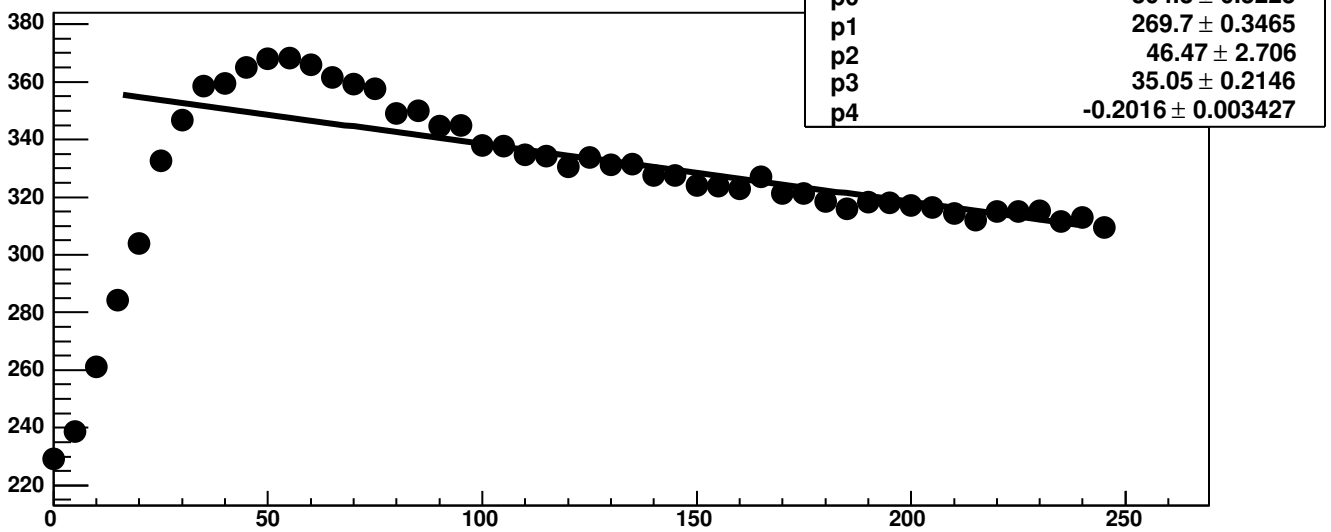
Chip 5, Channel 4, Enable 3, DAC=4000, ADC Noise vs Hold



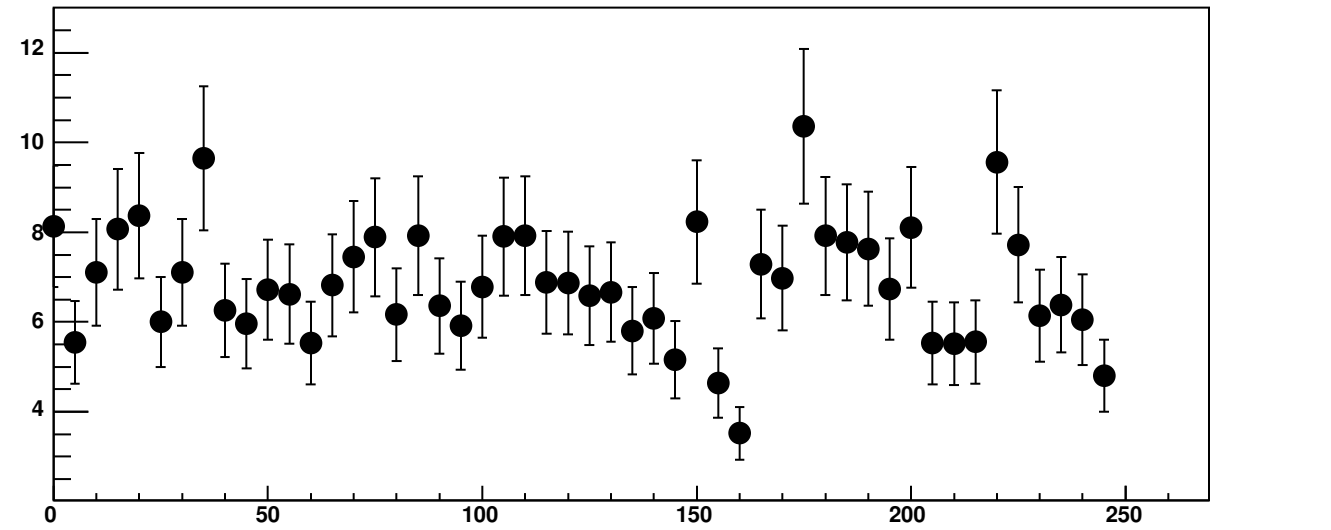
Chip 5, Channel 4, Enable 3, DAC=4000, ADC Residuals vs Hold



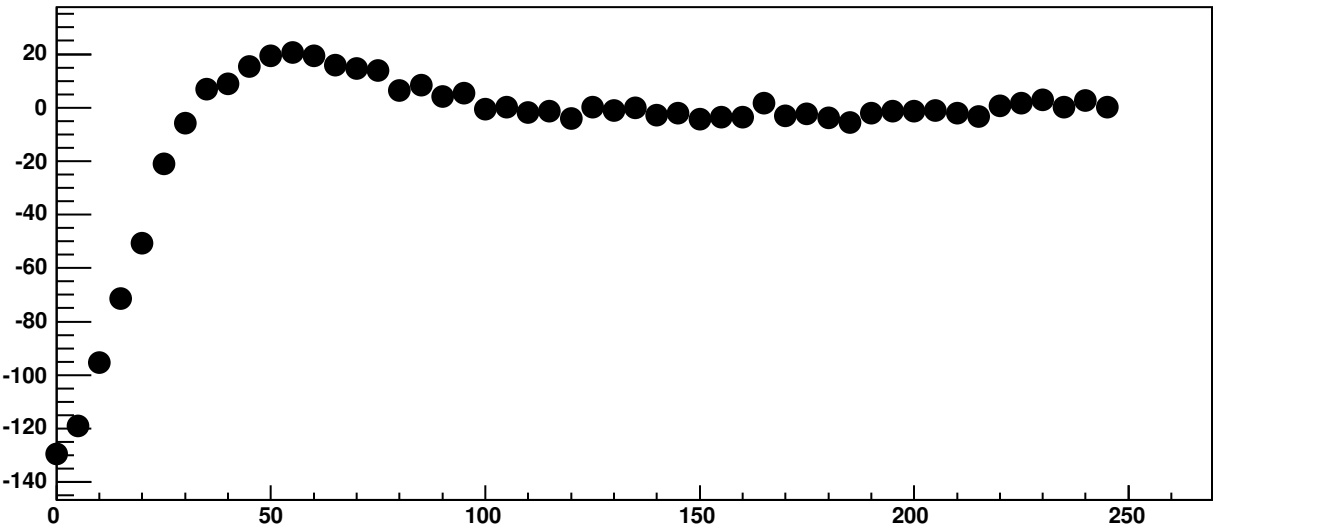
Chip 5, Channel 4, Enable 4, DAC=4000, ADC Mean vs Hold

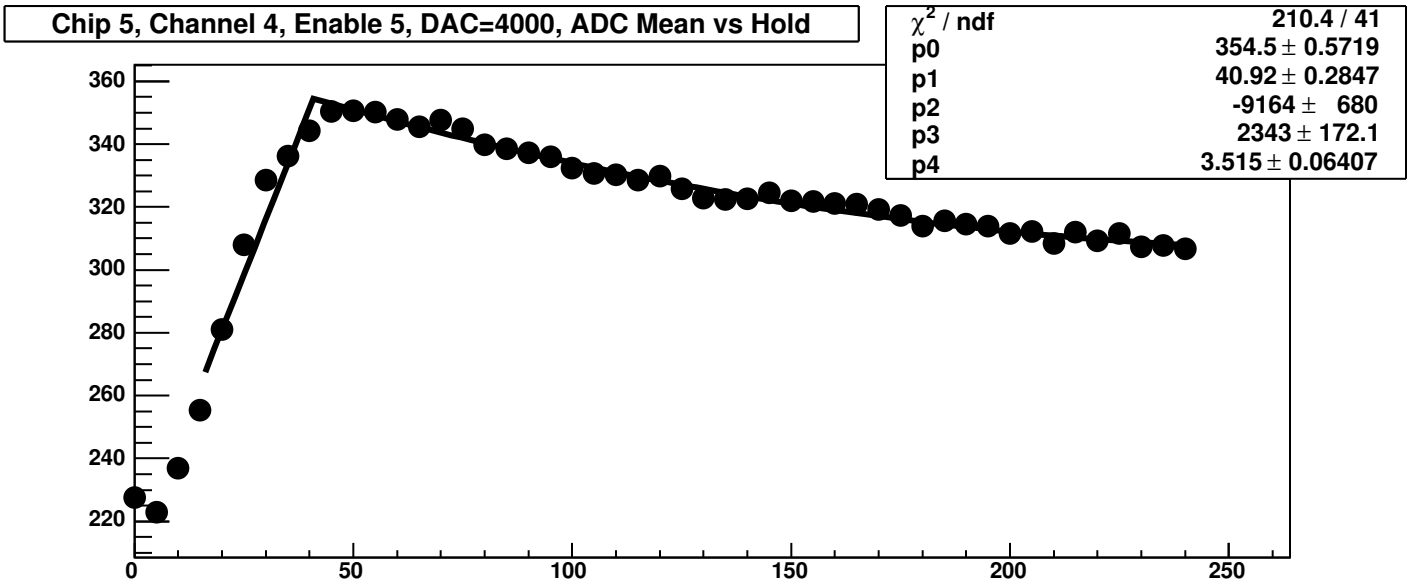


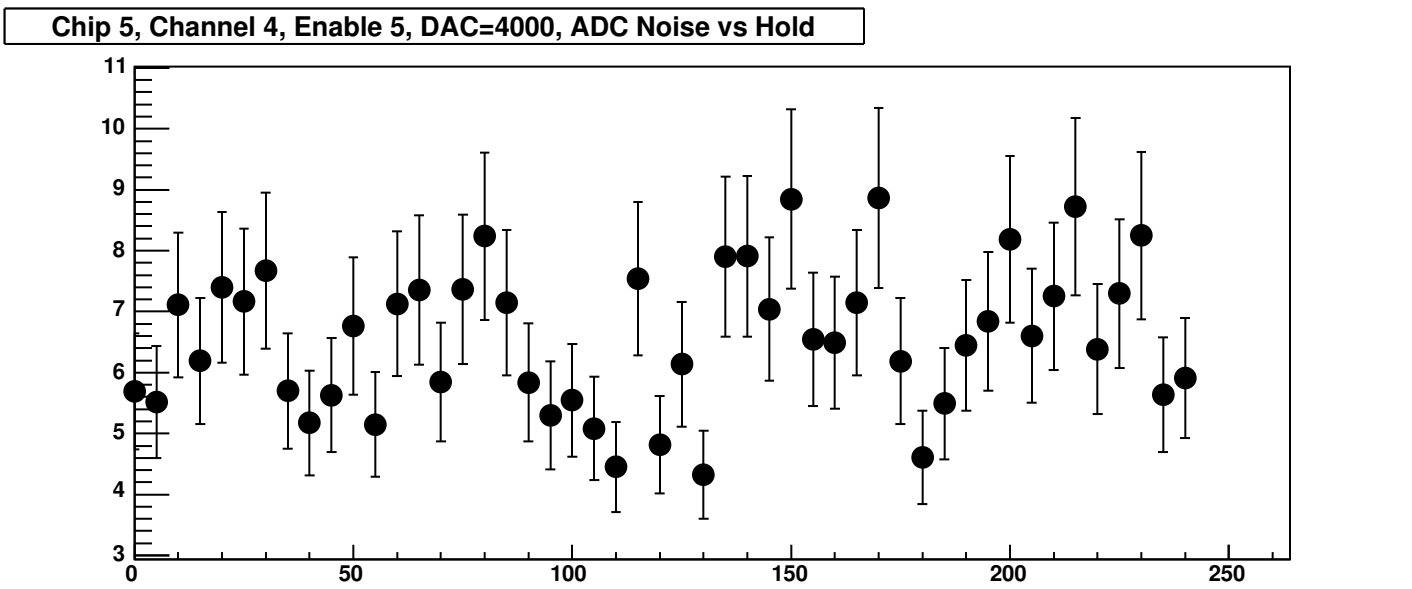
Chip 5, Channel 4, Enable 4, DAC=4000, ADC Noise vs Hold

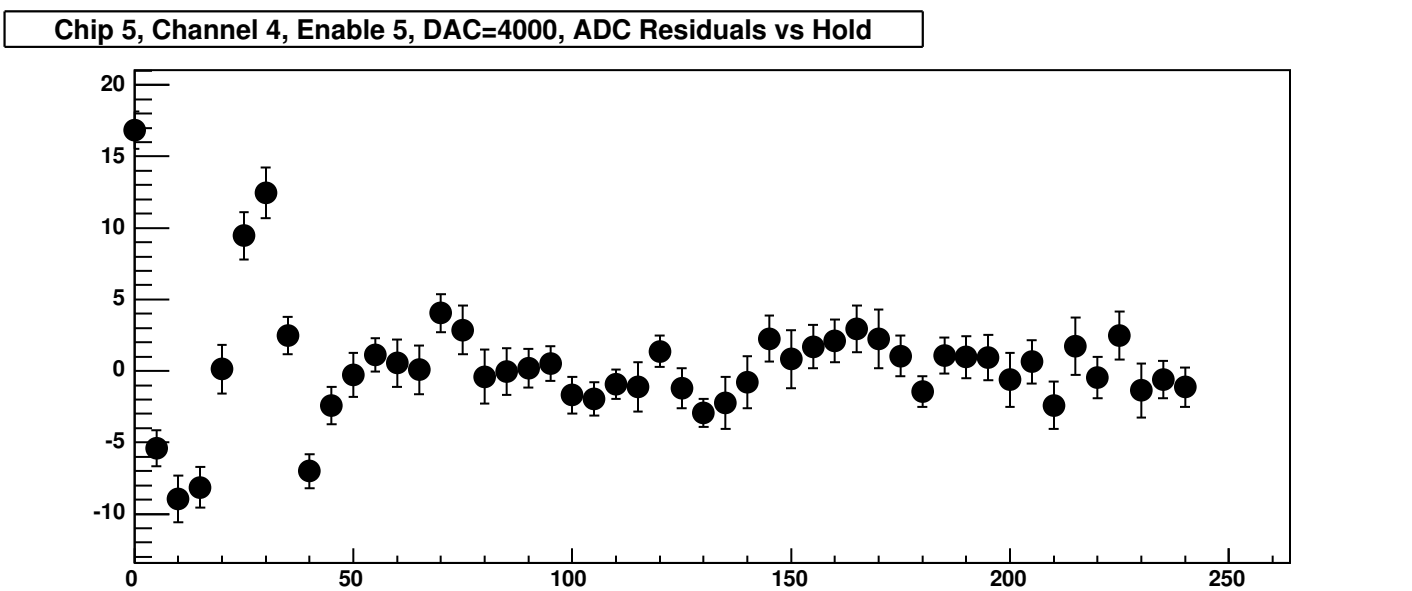


Chip 5, Channel 4, Enable 4, DAC=4000, ADC Residuals vs Hold

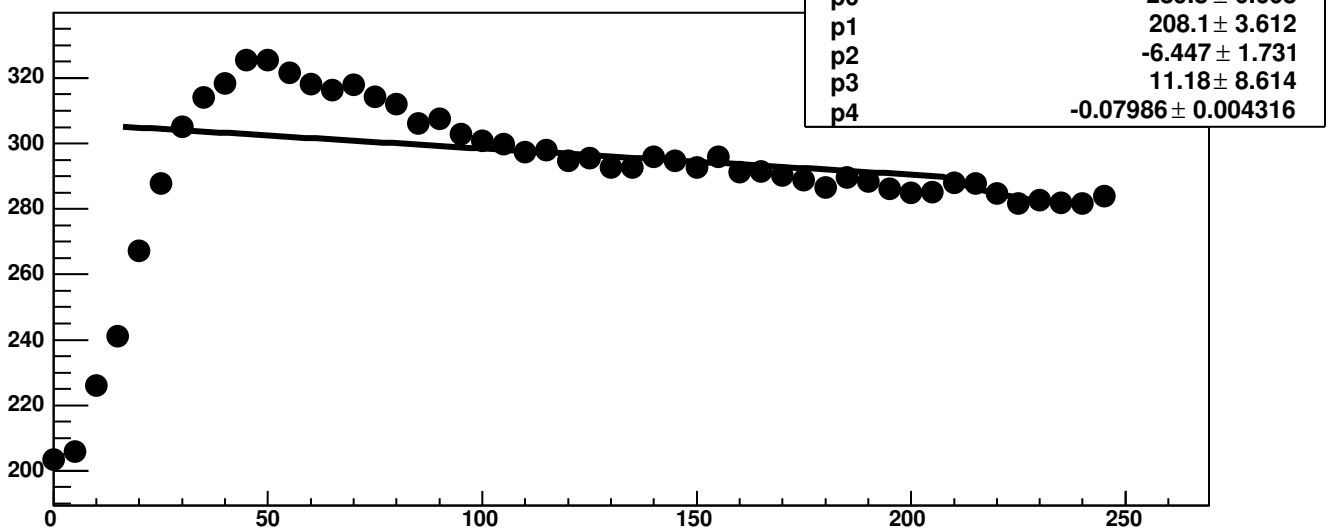



Chip 5, Channel 4, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 210.4 / 41
p0 354.5 ± 0.5719
p1 40.92 ± 0.2847
p2 -9164 ± 680
p3 2343 ± 172.1
p4 3.515 ± 0.06407
360
340
320
300
280
260
240
220
0
50
100
150
200
250

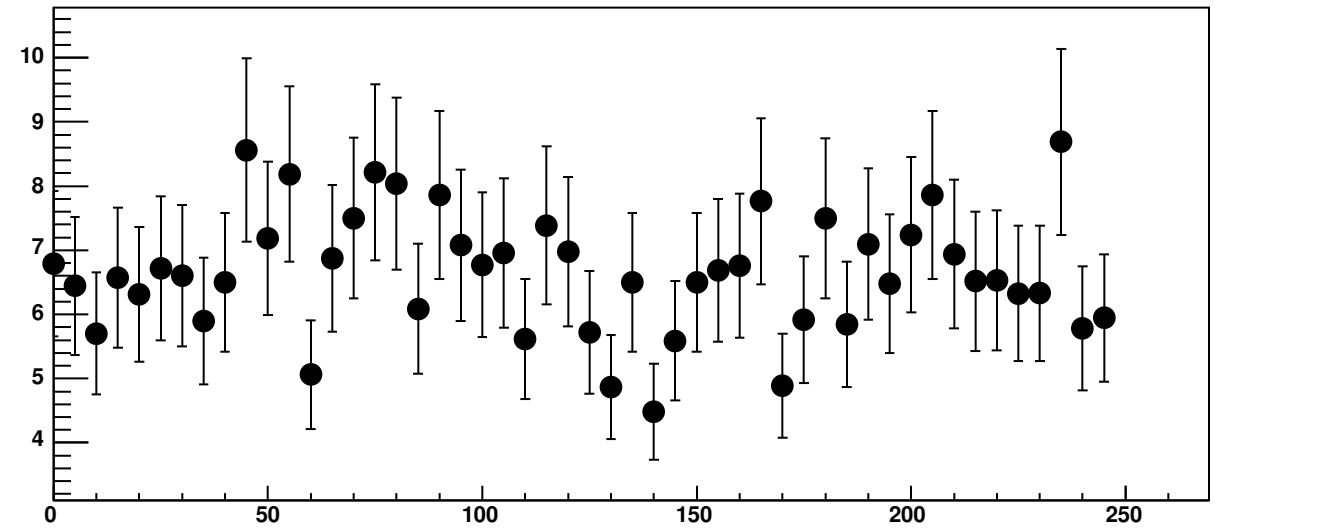

Chip 5, Channel 4, Enable 5, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 5, Channel 4, Enable 5, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
0
50
100
150
200
250

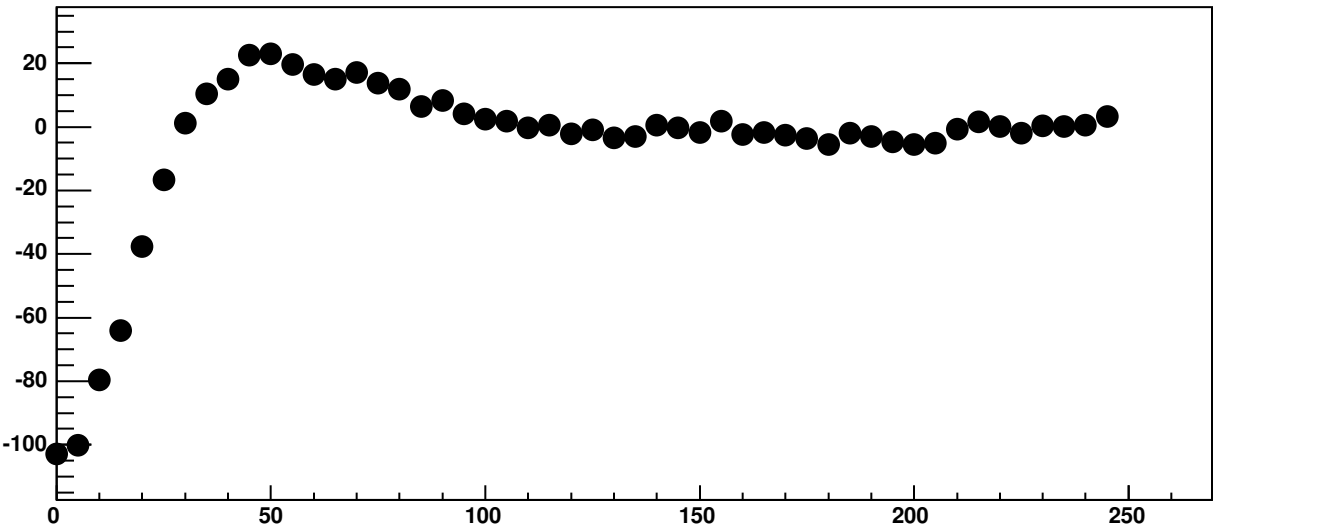
Chip 5, Channel 5, Enable 0, DAC=4000, ADC Mean vs Hold



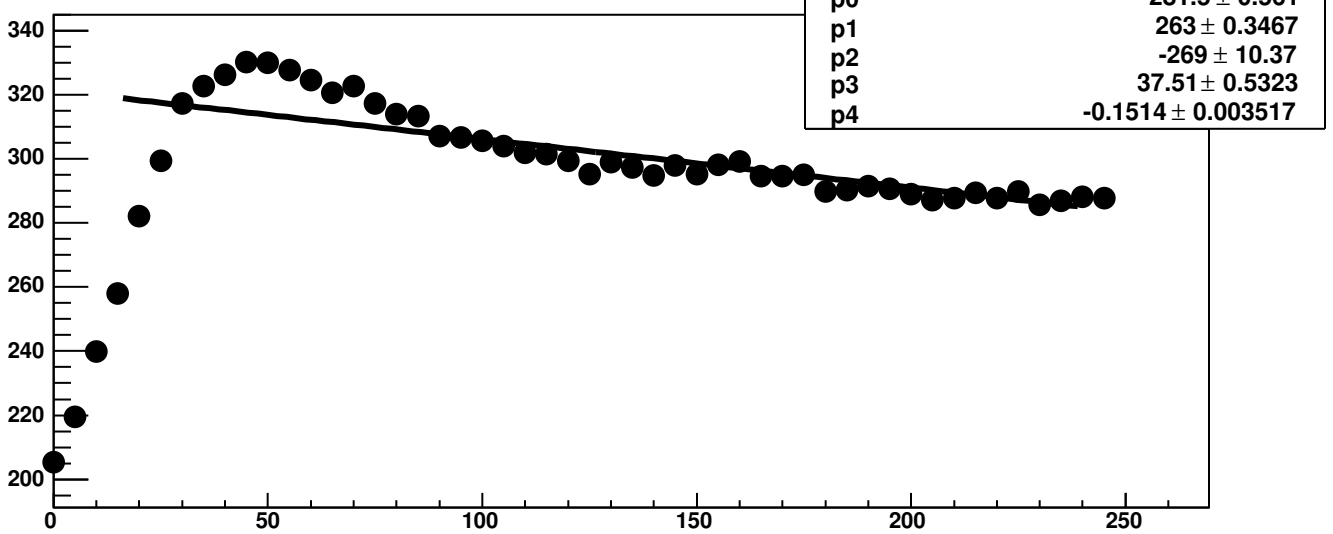
Chip 5, Channel 5, Enable 0, DAC=4000, ADC Noise vs Hold



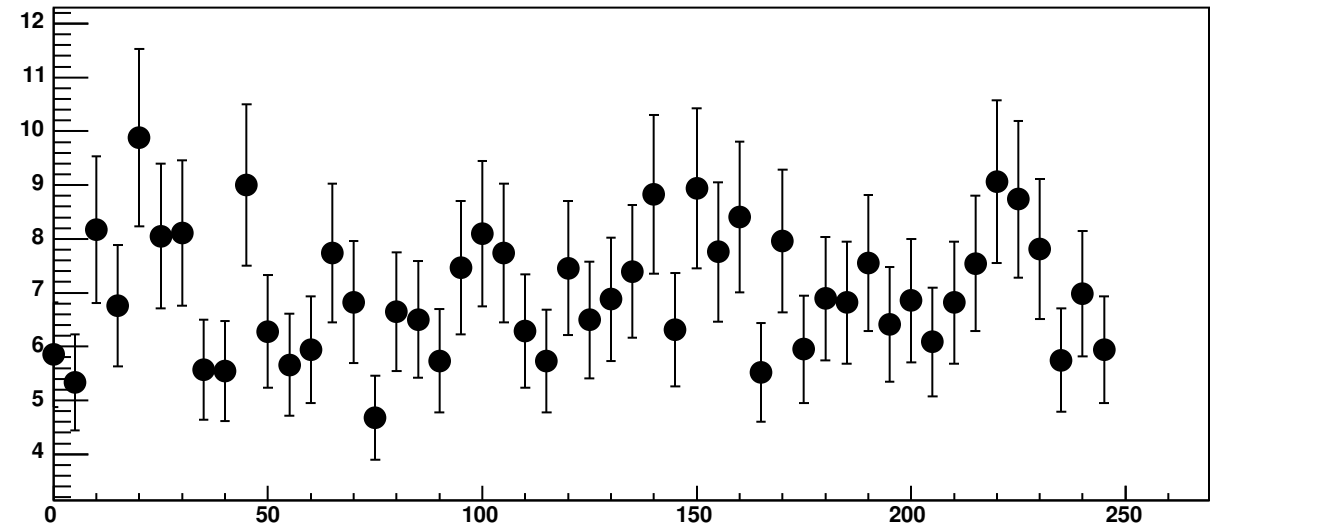
Chip 5, Channel 5, Enable 0, DAC=4000, ADC Residuals vs Hold



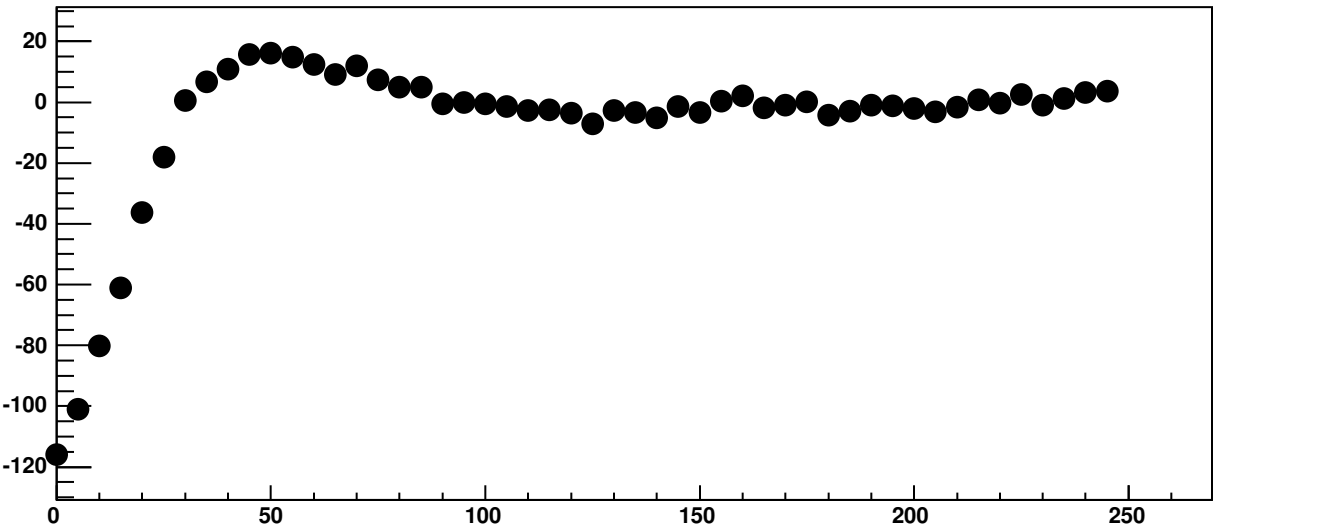
Chip 5, Channel 5, Enable 1, DAC=4000, ADC Mean vs Hold



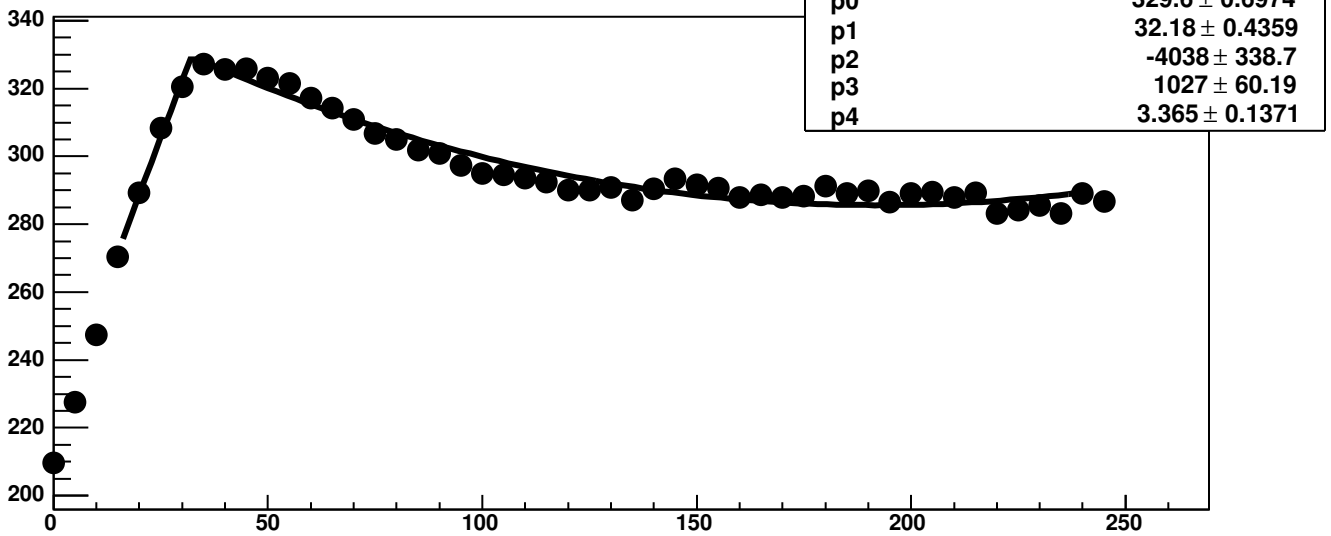
Chip 5, Channel 5, Enable 1, DAC=4000, ADC Noise vs Hold



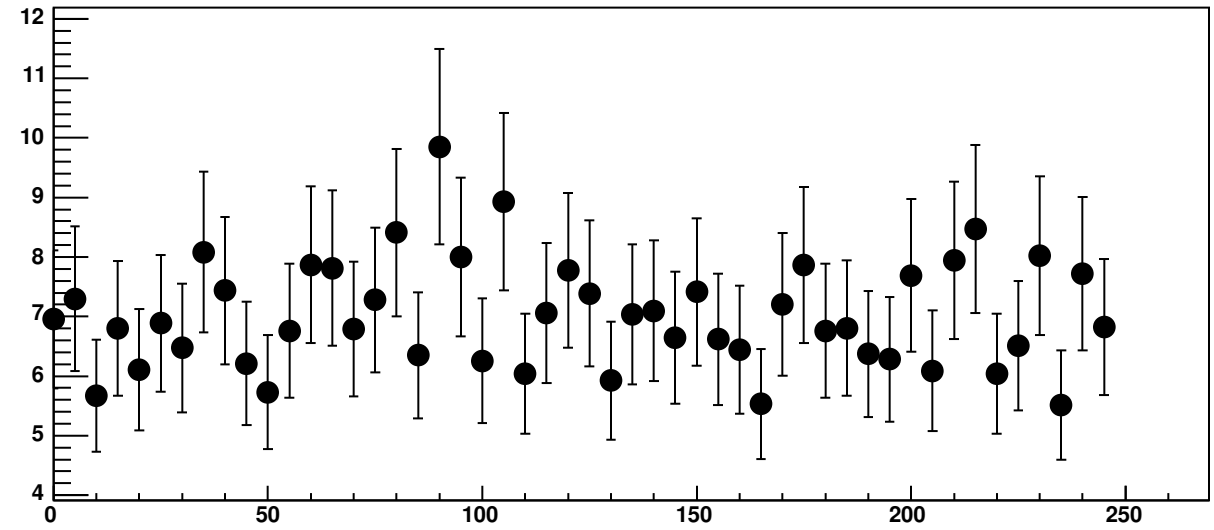
Chip 5, Channel 5, Enable 1, DAC=4000, ADC Residuals vs Hold



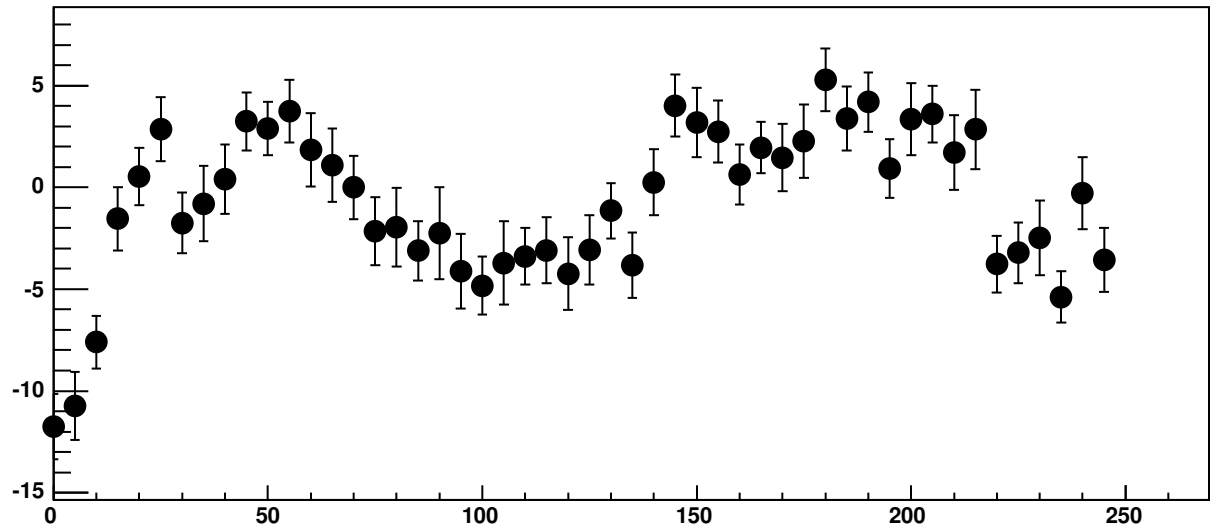
Chip 5, Channel 5, Enable 2, DAC=4000, ADC Mean vs Hold



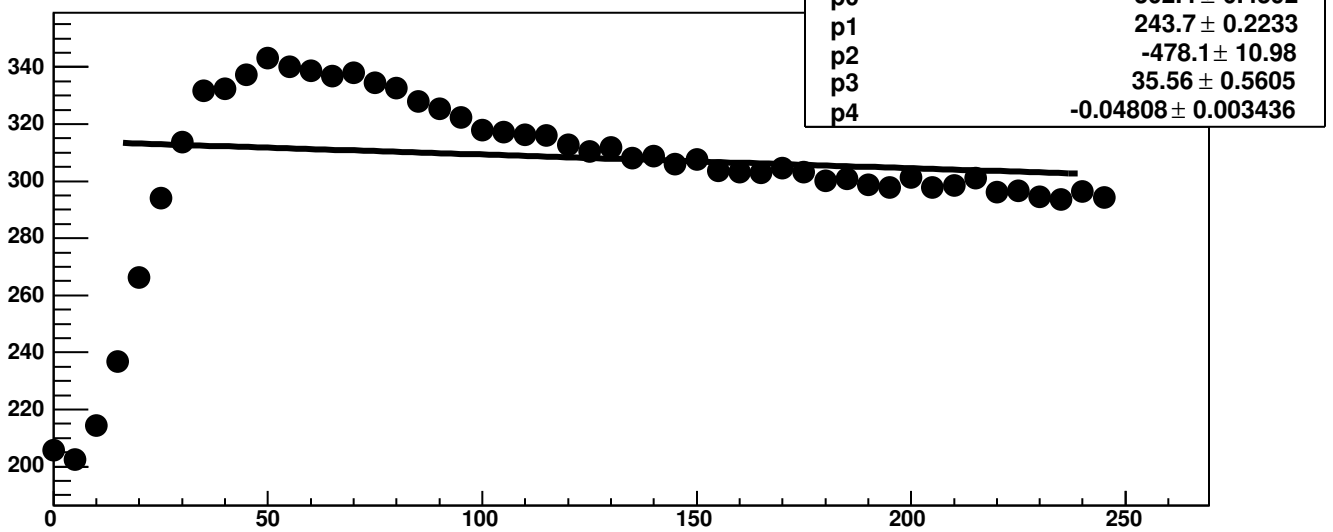
Chip 5, Channel 5, Enable 2, DAC=4000, ADC Noise vs Hold



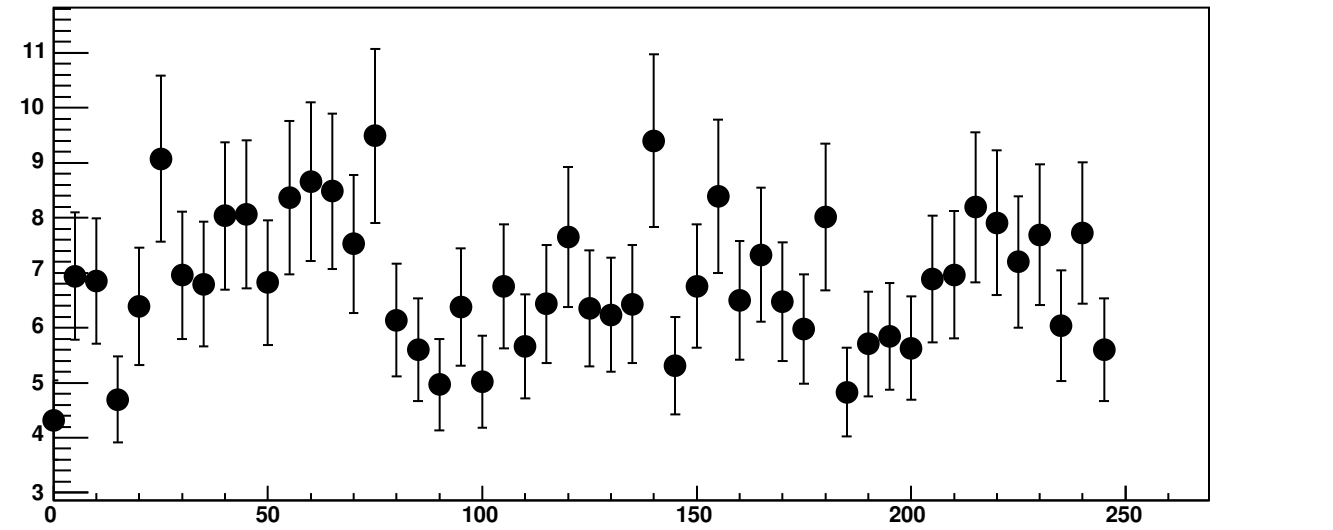
Chip 5, Channel 5, Enable 2, DAC=4000, ADC Residuals vs Hold



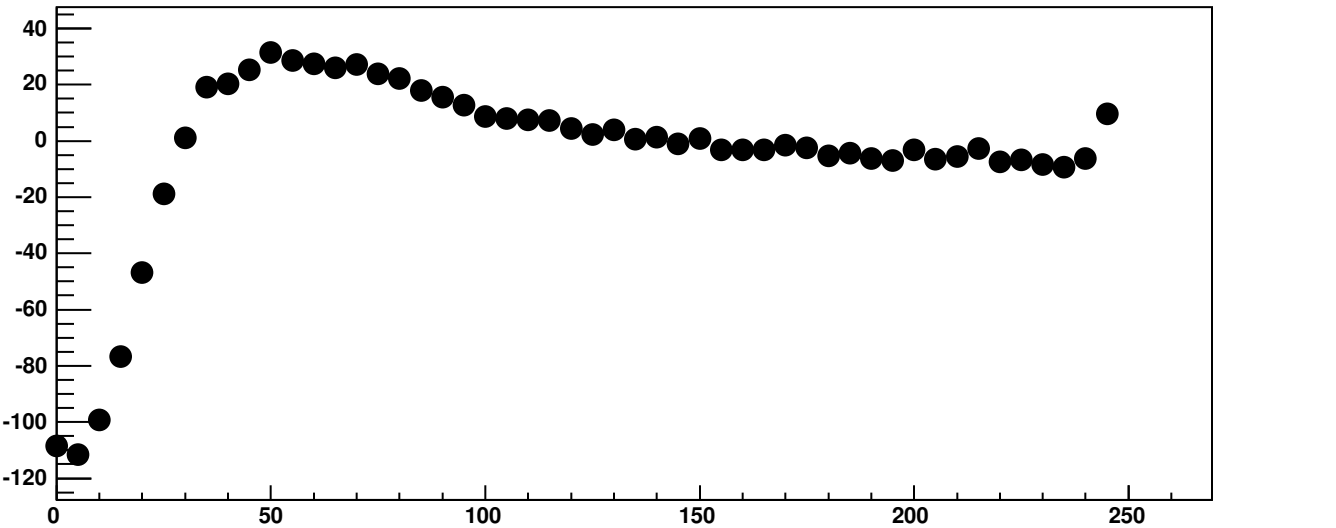
Chip 5, Channel 5, Enable 3, DAC=4000, ADC Mean vs Hold

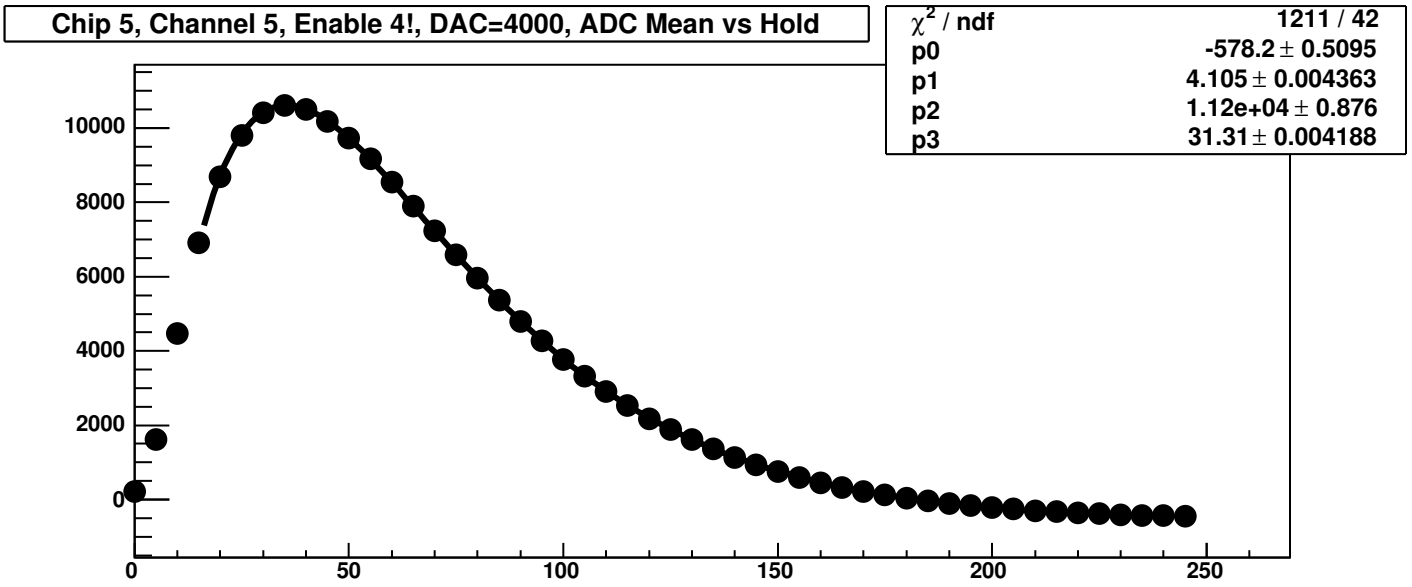


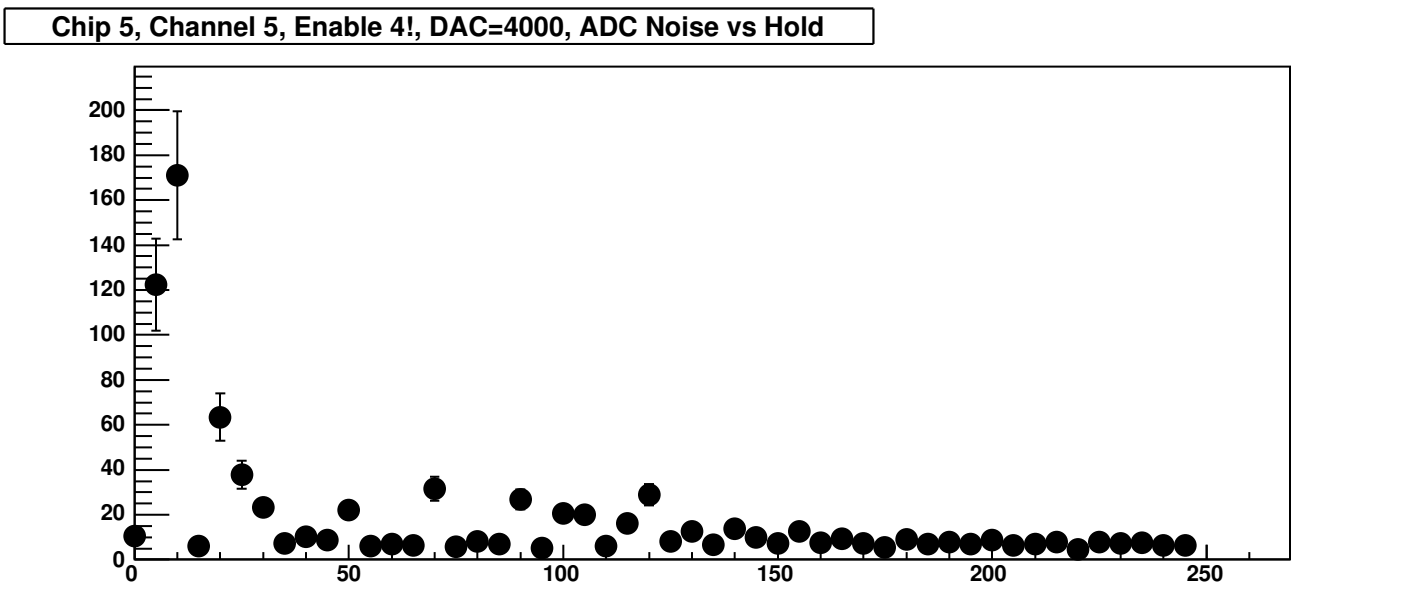
Chip 5, Channel 5, Enable 3, DAC=4000, ADC Noise vs Hold

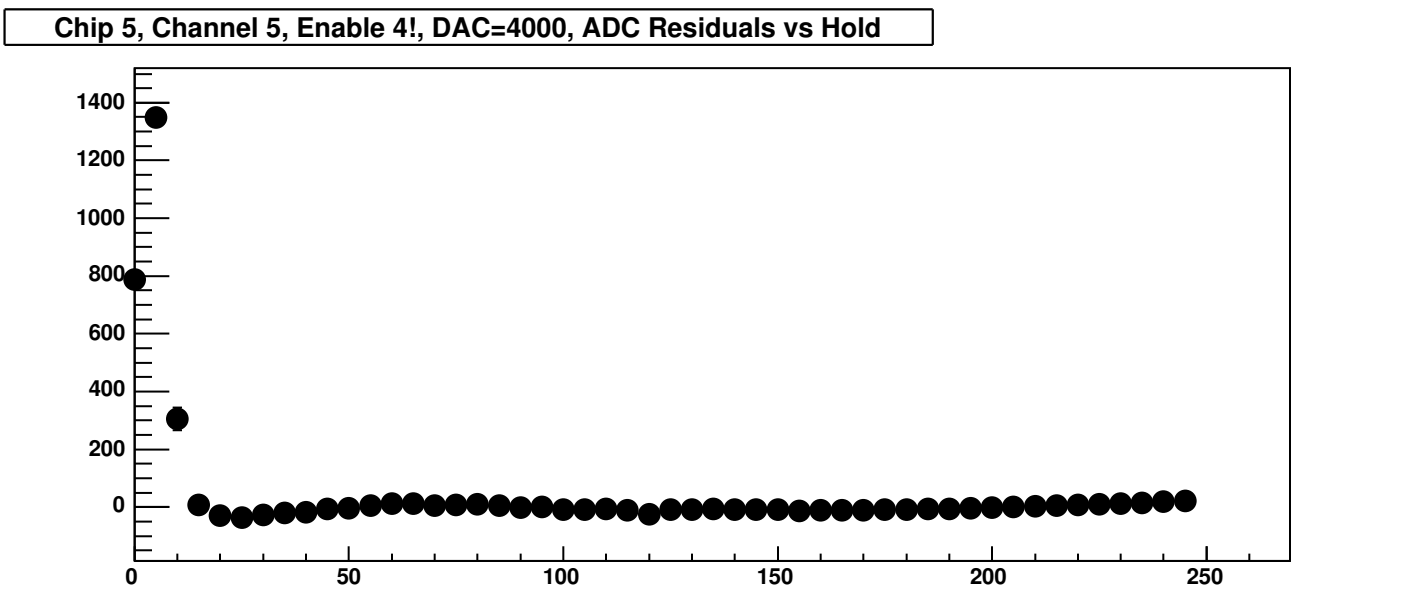


Chip 5, Channel 5, Enable 3, DAC=4000, ADC Residuals vs Hold

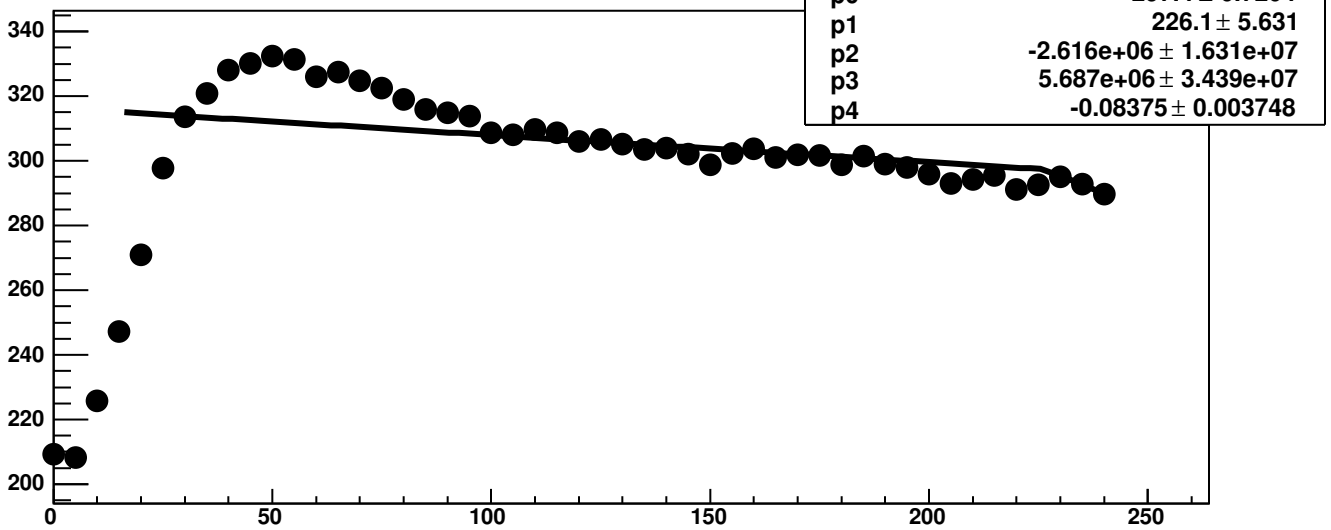



Chip 5, Channel 5, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1211 / 42
p0 -578.2 ± 0.5095
p1 4.105 ± 0.004363
p2 1.12e+04 ± 0.876
p3 31.31 ± 0.004188
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

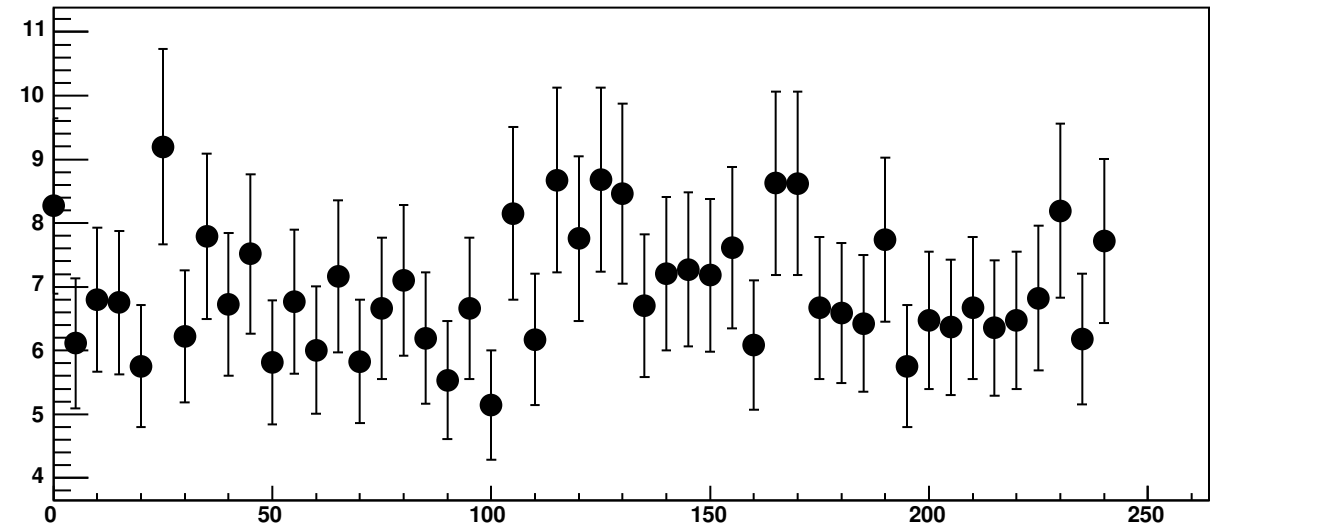

Chip 5, Channel 5, Enable 4!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 5, Channel 5, Enable 4!, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

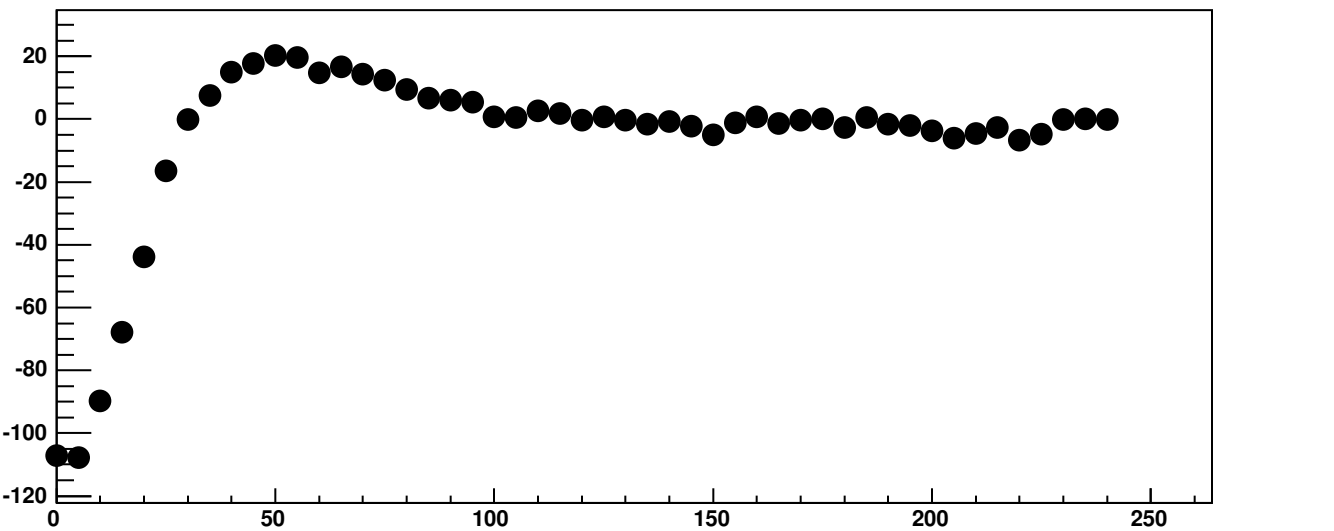
Chip 5, Channel 5, Enable 5, DAC=4000, ADC Mean vs Hold



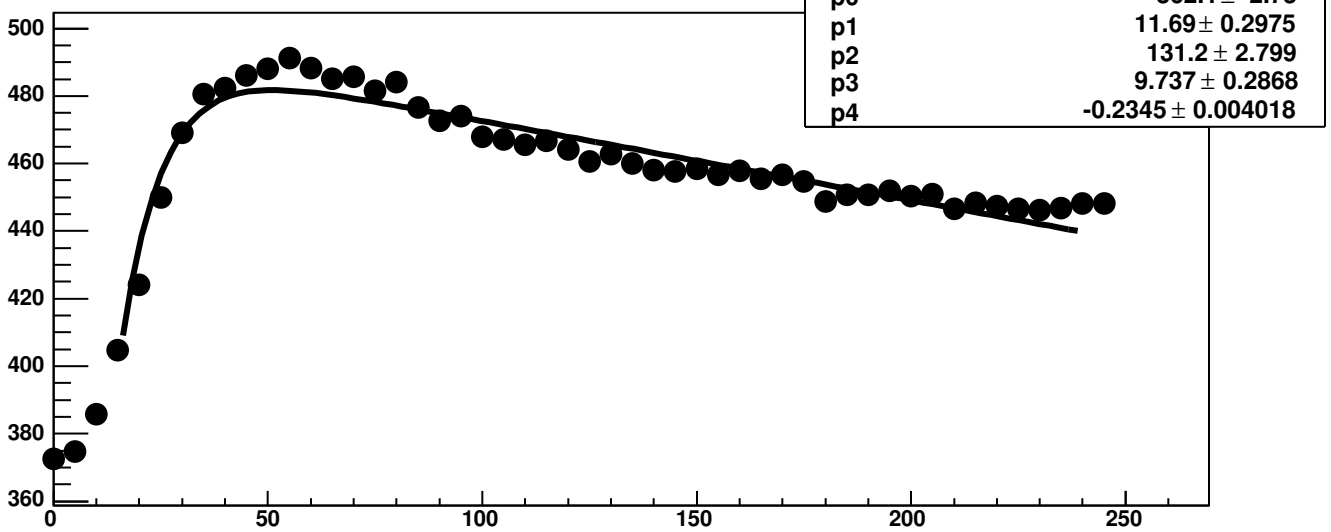
Chip 5, Channel 5, Enable 5, DAC=4000, ADC Noise vs Hold



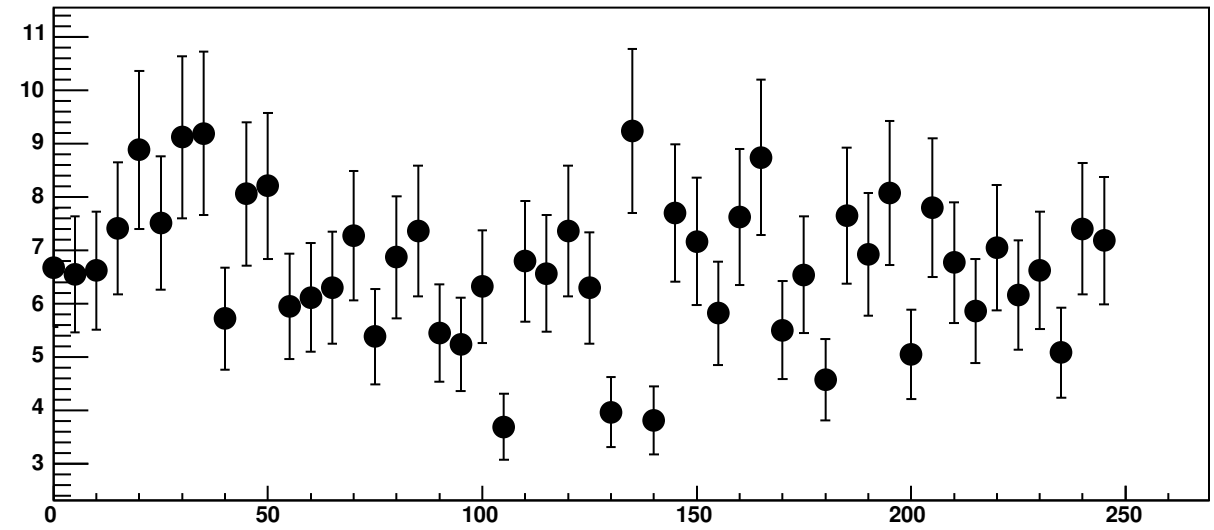
Chip 5, Channel 5, Enable 5, DAC=4000, ADC Residuals vs Hold



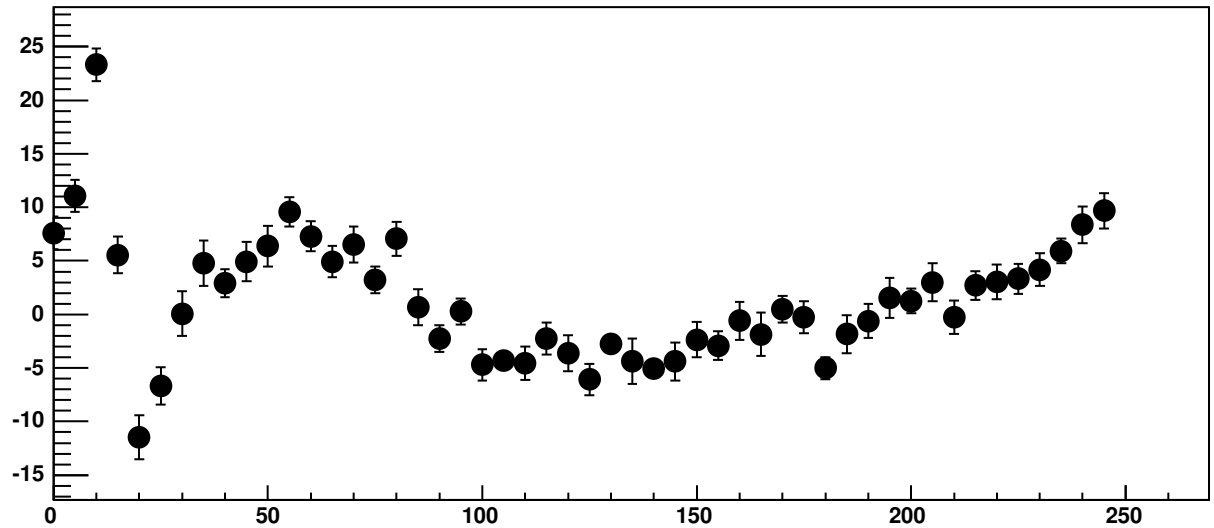
Chip 5, Channel 6, Enable 0, DAC=4000, ADC Mean vs Hold



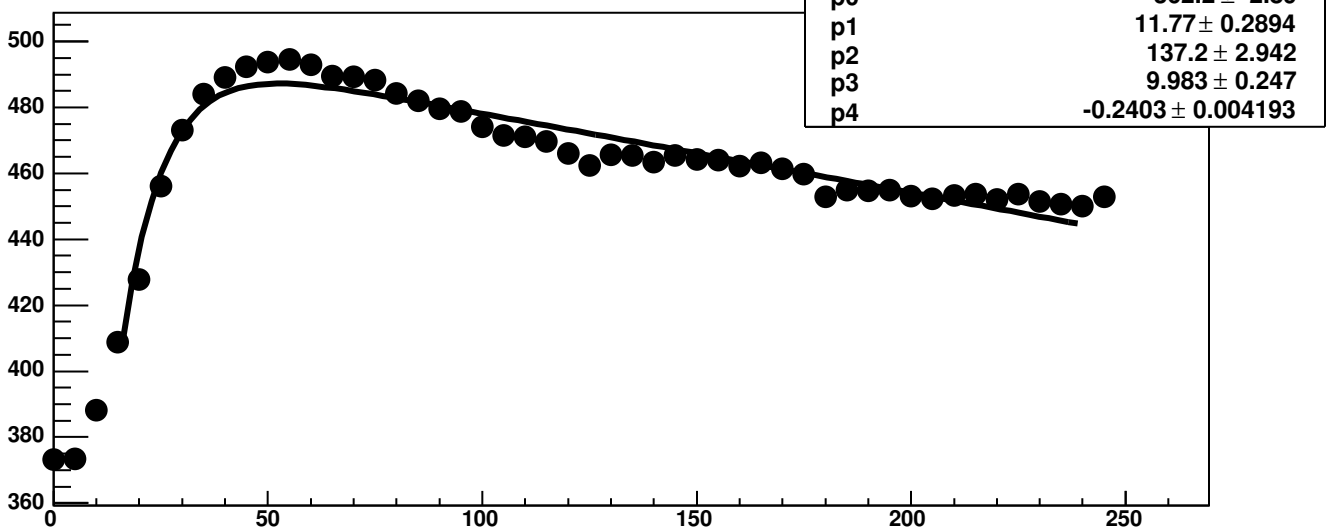
Chip 5, Channel 6, Enable 0, DAC=4000, ADC Noise vs Hold



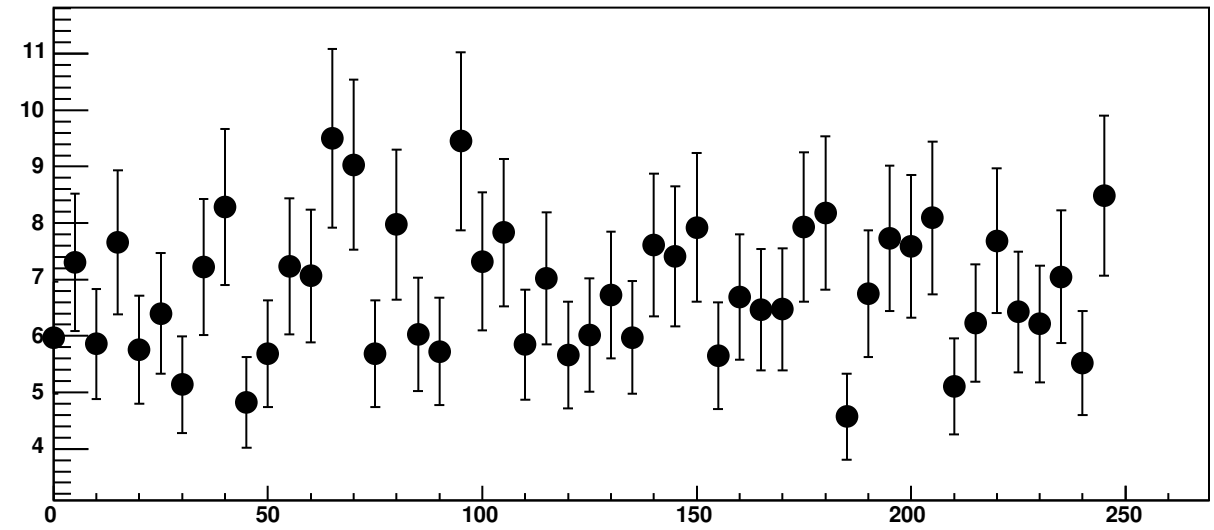
Chip 5, Channel 6, Enable 0, DAC=4000, ADC Residuals vs Hold



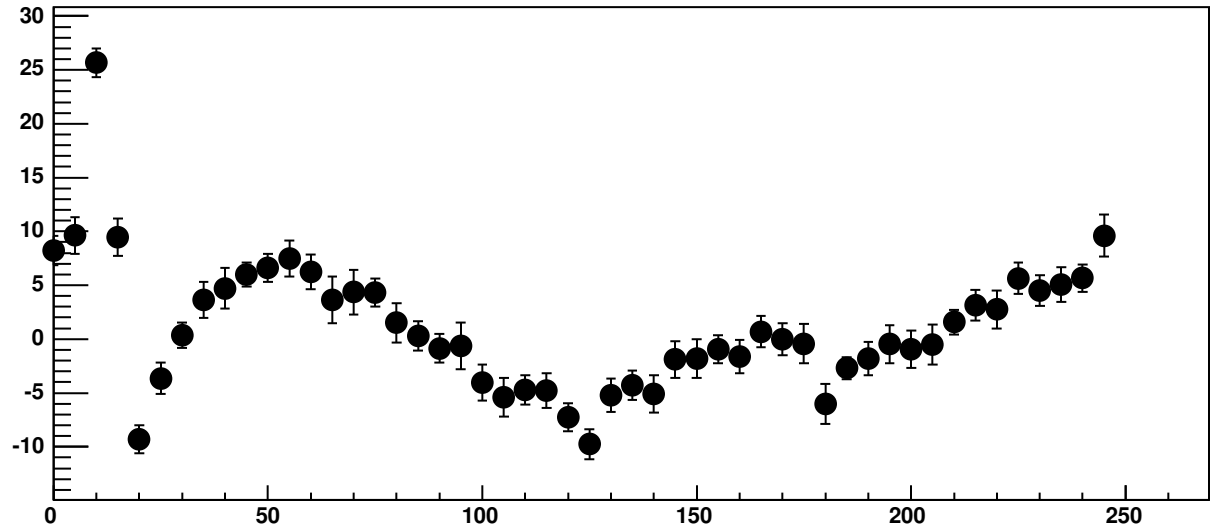
Chip 5, Channel 6, Enable 1, DAC=4000, ADC Mean vs Hold

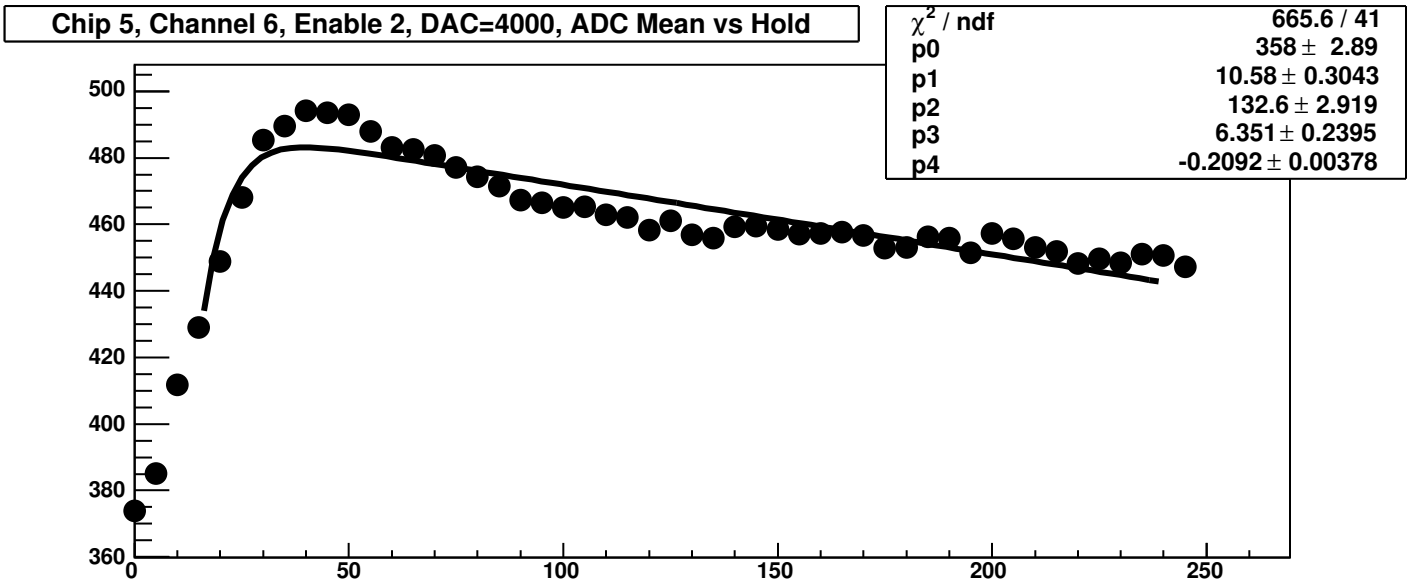


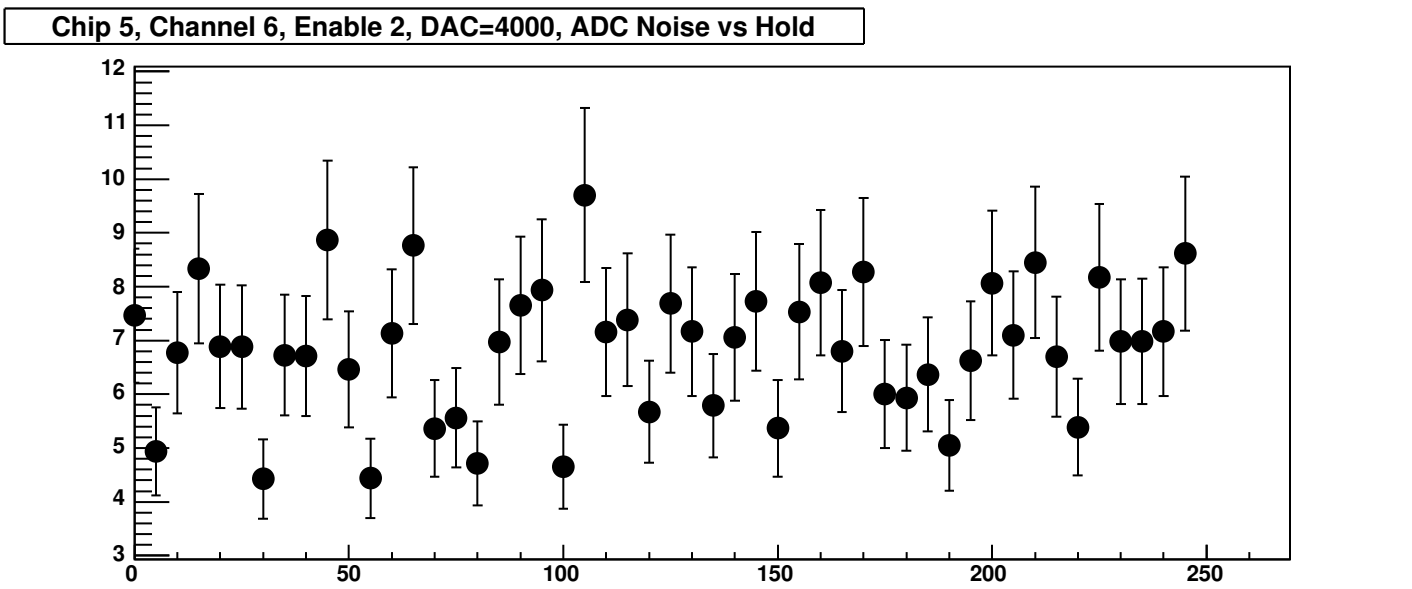
Chip 5, Channel 6, Enable 1, DAC=4000, ADC Noise vs Hold

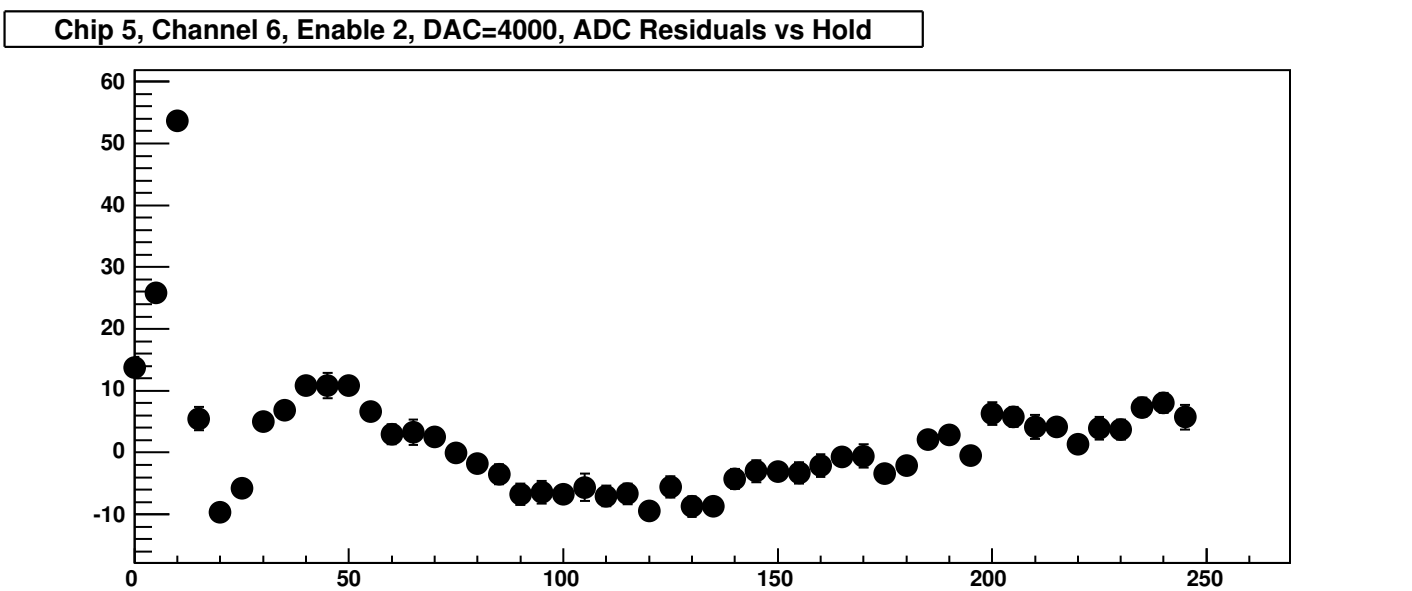


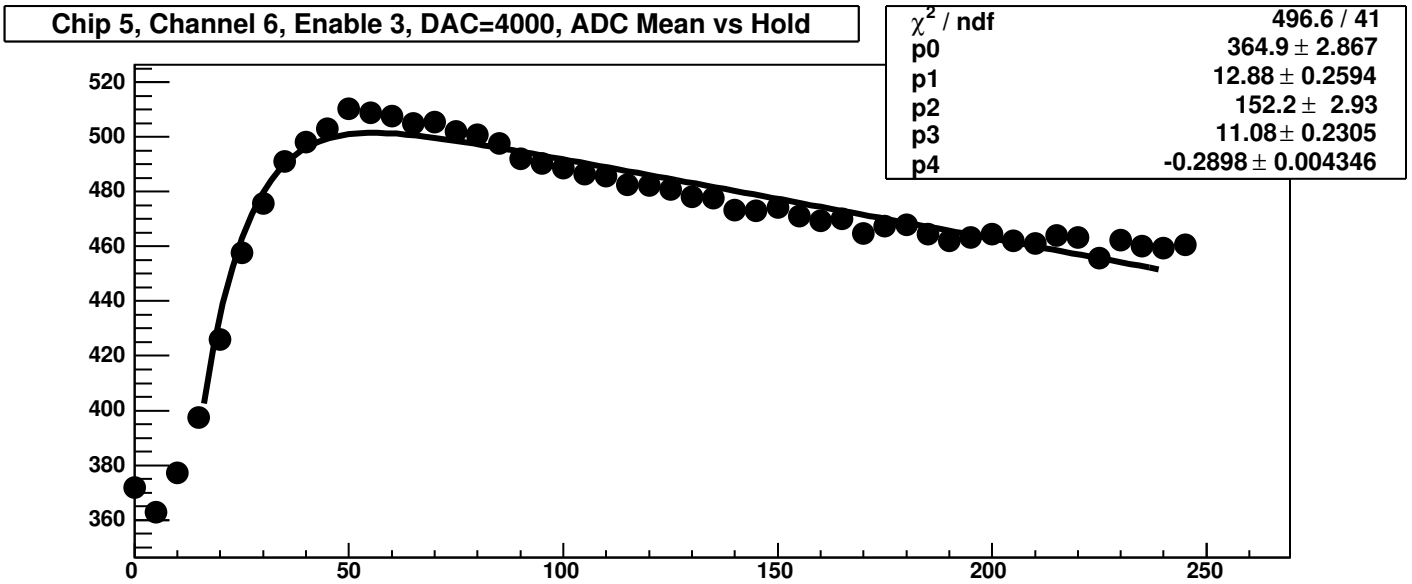
Chip 5, Channel 6, Enable 1, DAC=4000, ADC Residuals vs Hold

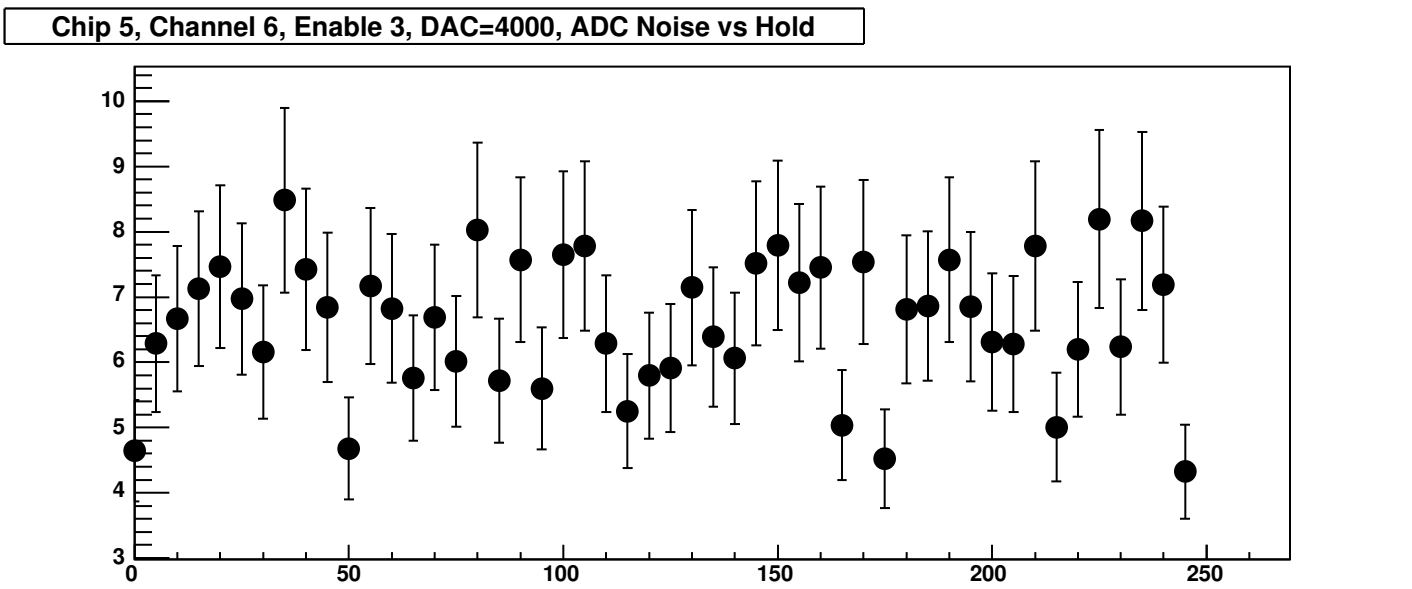


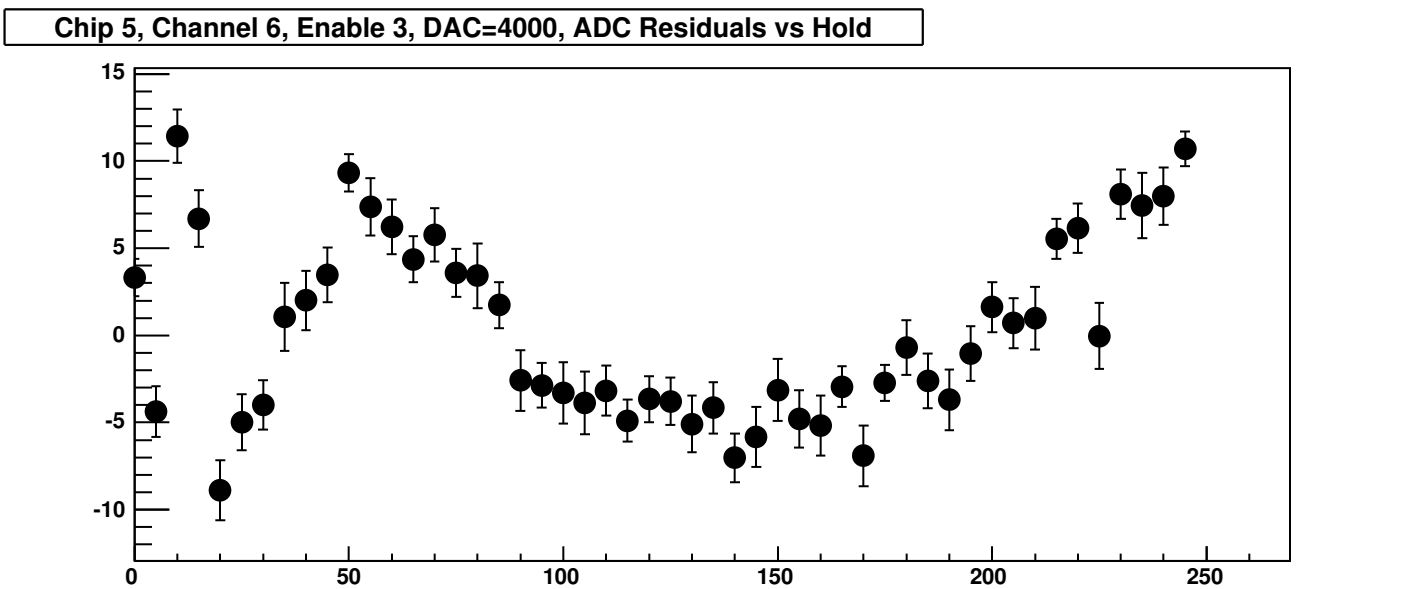

Chip 5, Channel 6, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 665.6 / 41
p0 358 ± 2.89
p1 10.58 ± 0.3043
p2 132.6 ± 2.919
p3 6.351 ± 0.2395
p4 -0.2092 ± 0.00378
500
480
460
440
420
400
380
360
0
50
100
150
200
250

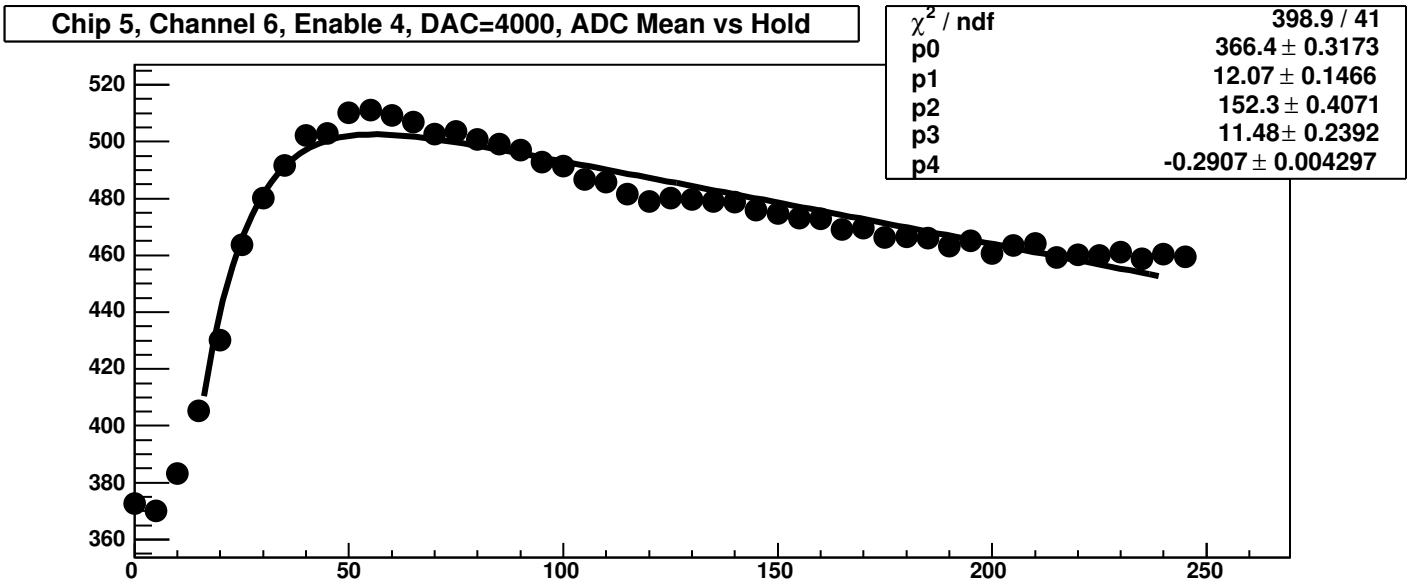

Chip 5, Channel 6, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

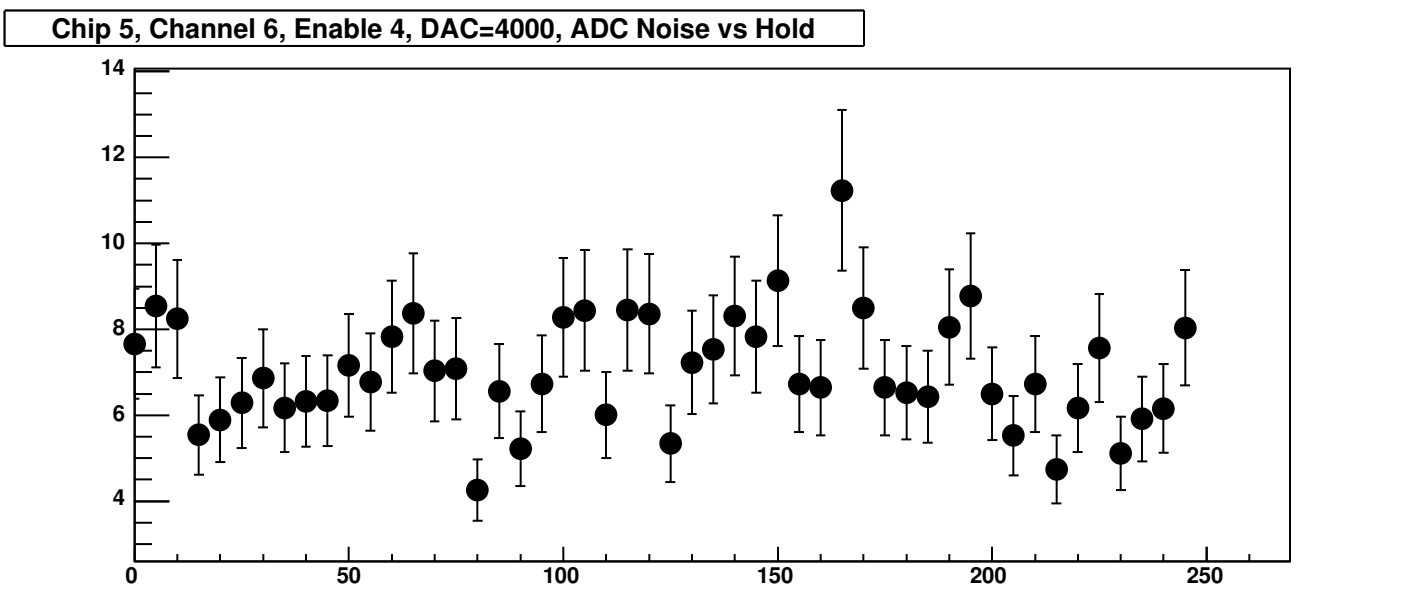

Chip 5, Channel 6, Enable 2, DAC=4000, ADC Residuals vs Hold
60
50
40
30
20
10
0
-10
0
50
100
150
200
250

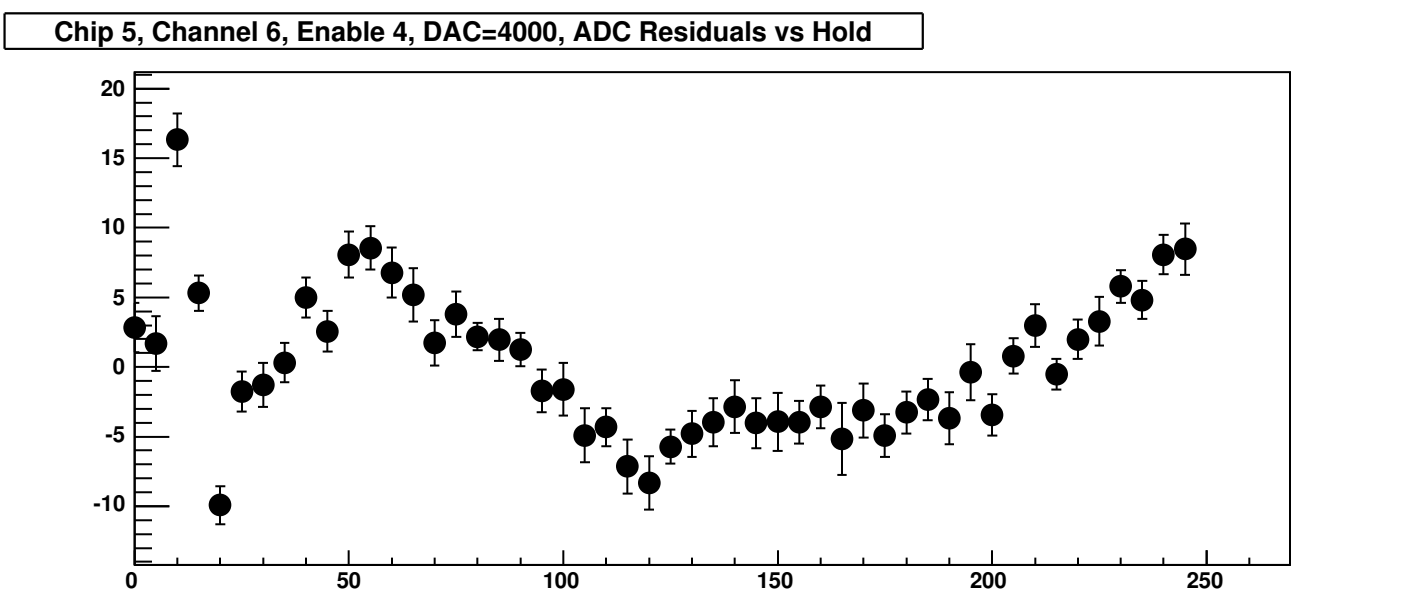

Chip 5, Channel 6, Enable 3, DAC=4000, ADC Mean vs Hold
χ² / ndf 496.6 / 41
p0 364.9 ± 2.867
p1 12.88 ± 0.2594
p2 152.2 ± 2.93
p3 11.08 ± 0.2305
p4 -0.2898 ± 0.004346
520
500
480
460
440
420
400
380
360
0
50
100
150
200
250


Chip 5, Channel 6, Enable 3, DAC=4000, ADC Noise vs Hold
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 5, Channel 6, Enable 3, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
0
50
100
150
200
250

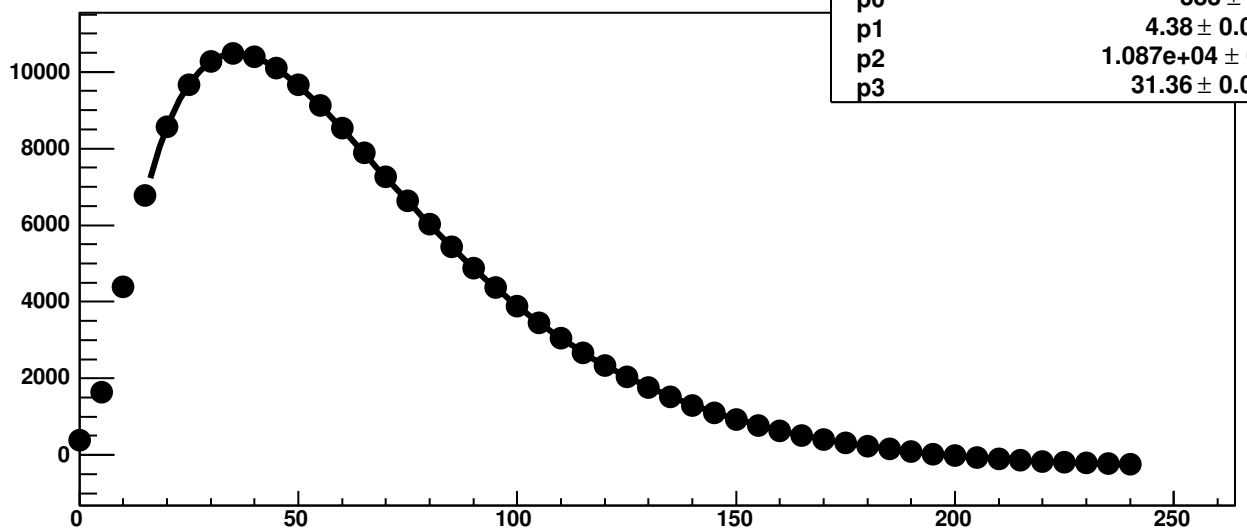

Chip 5, Channel 6, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 398.9 / 41
p0 366.4 ± 0.3173
p1 12.07 ± 0.1466
p2 152.3 ± 0.4071
p3 11.48 ± 0.2392
p4 -0.2907 ± 0.004297
520
500
480
460
440
420
400
380
360
0
50
100
150
200
250


Chip 5, Channel 6, Enable 4, DAC=4000, ADC Noise vs Hold
14
12
10
8
6
4
0
50
100
150
200
250

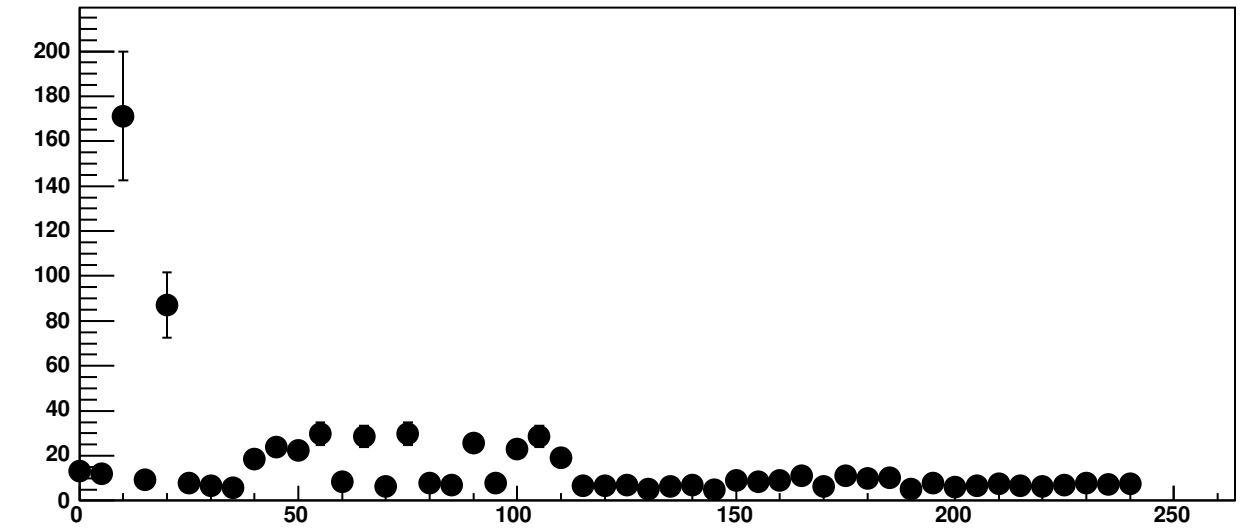

Chip 5, Channel 6, Enable 4, DAC=4000, ADC Residuals vs Hold
20
15
10
5
0
-5
-10
0
50
100
150
200
250

Chip 5, Channel 6, Enable 5!, DAC=4000, ADC Mean vs Hold

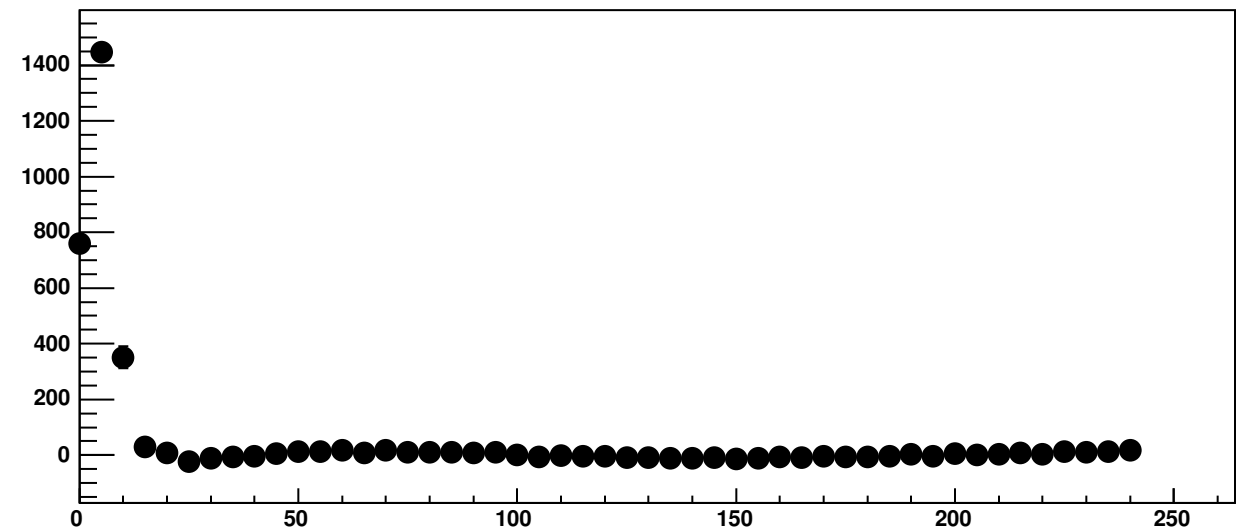
χ^2 / ndf	1504 / 42
p0	-383 ± 0.5369
p1	4.38 ± 0.005869
p2	1.087e+04 ± 0.8666
p3	31.36 ± 0.004333



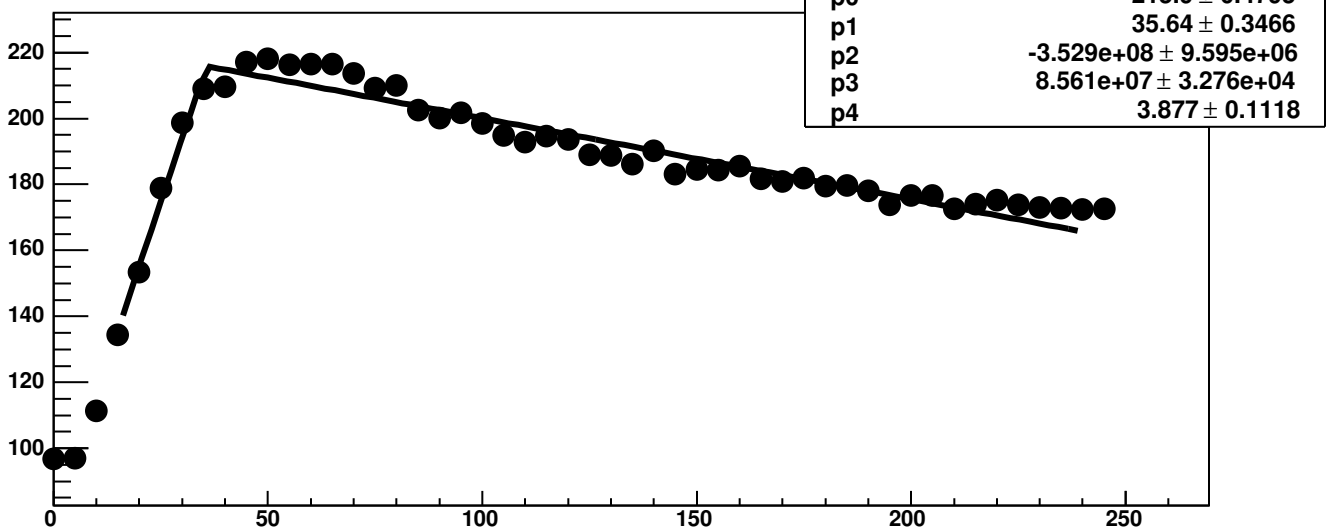
Chip 5, Channel 6, Enable 5!, DAC=4000, ADC Noise vs Hold



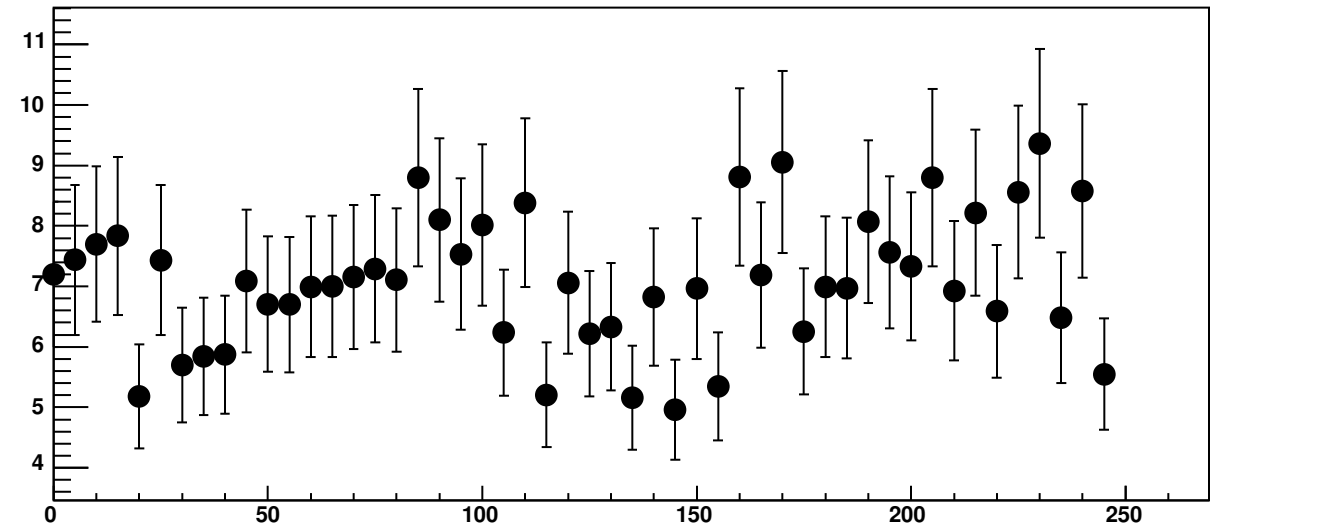
Chip 5, Channel 6, Enable 5!, DAC=4000, ADC Residuals vs Hold



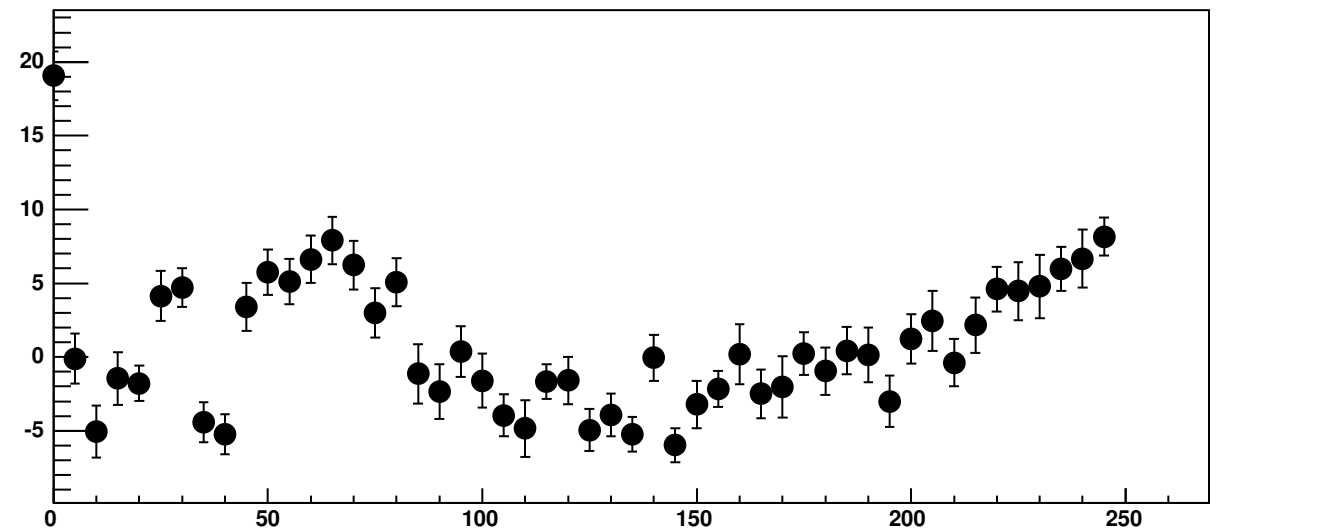
Chip 5, Channel 7, Enable 0, DAC=4000, ADC Mean vs Hold



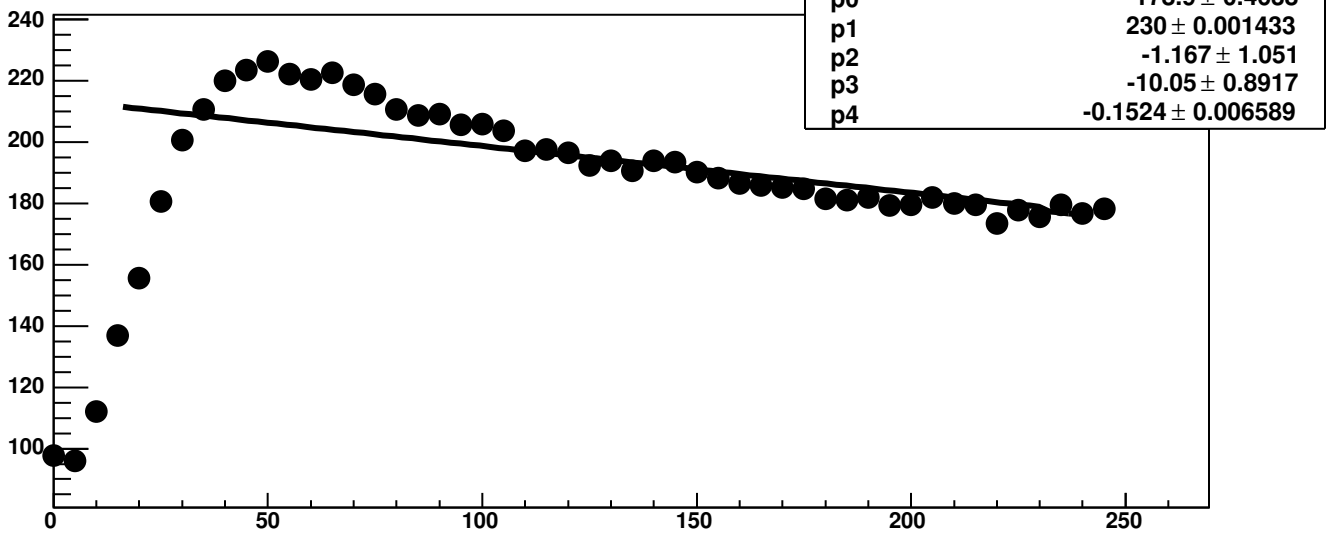
Chip 5, Channel 7, Enable 0, DAC=4000, ADC Noise vs Hold



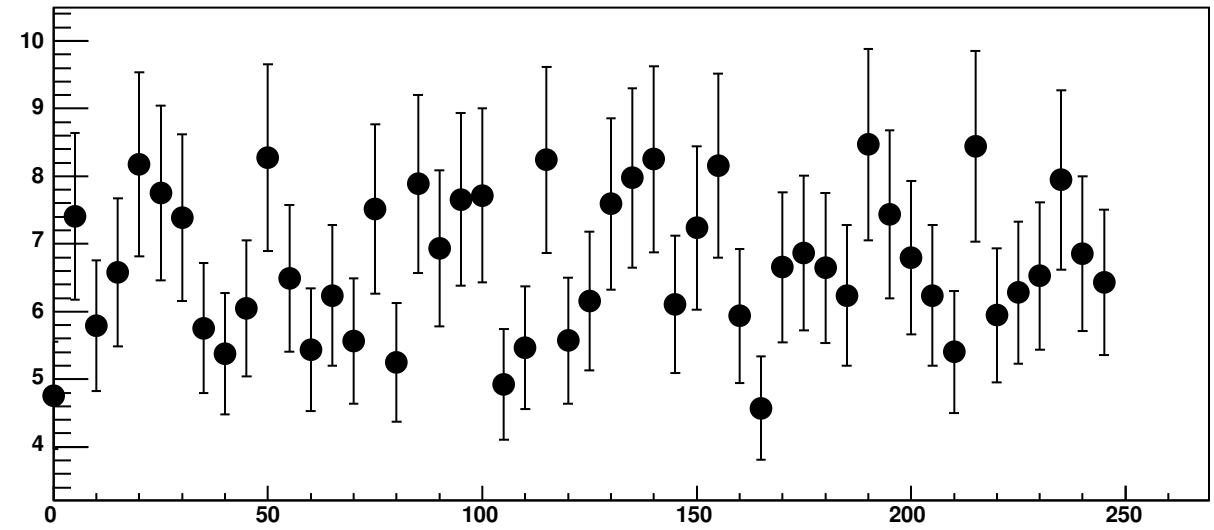
Chip 5, Channel 7, Enable 0, DAC=4000, ADC Residuals vs Hold



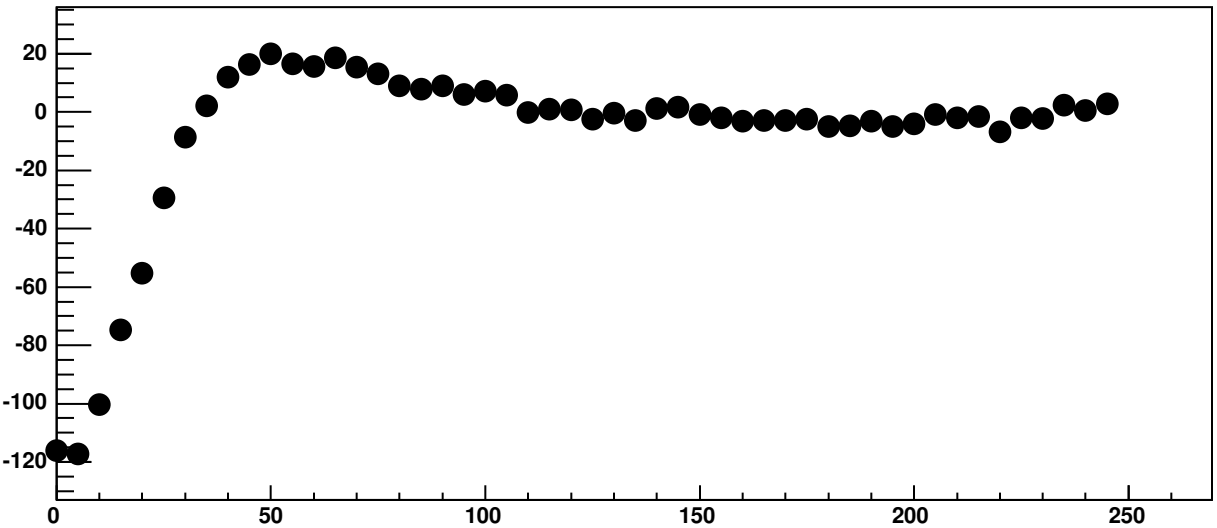
Chip 5, Channel 7, Enable 1, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 7, Enable 1, DAC=4000, ADC Noise vs Hold

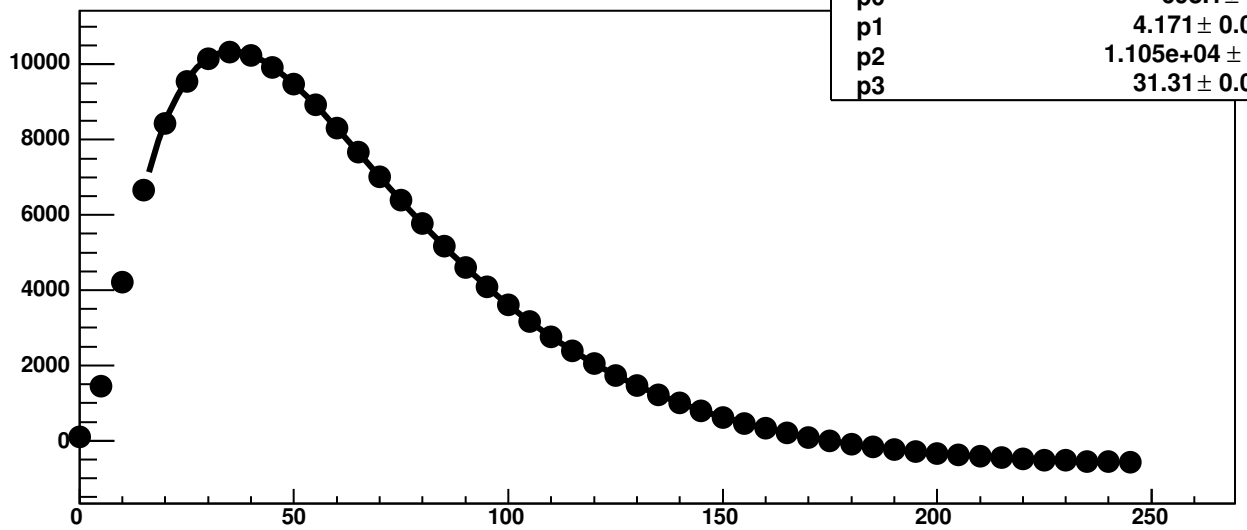


Chip 5, Channel 7, Enable 1, DAC=4000, ADC Residuals vs Hold

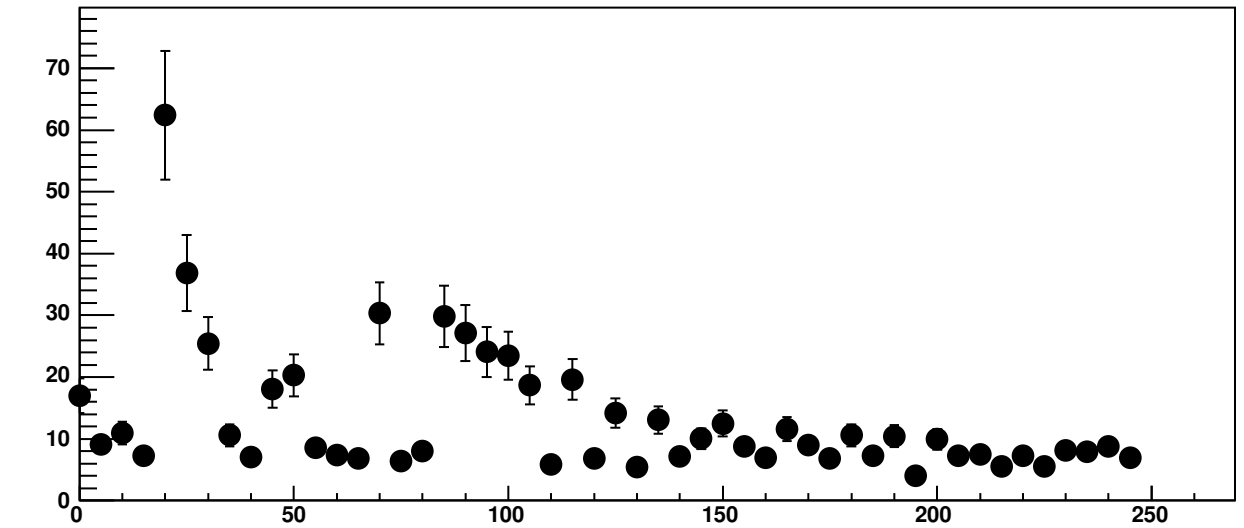


Chip 5, Channel 7, Enable 2!, DAC=4000, ADC Mean vs Hold

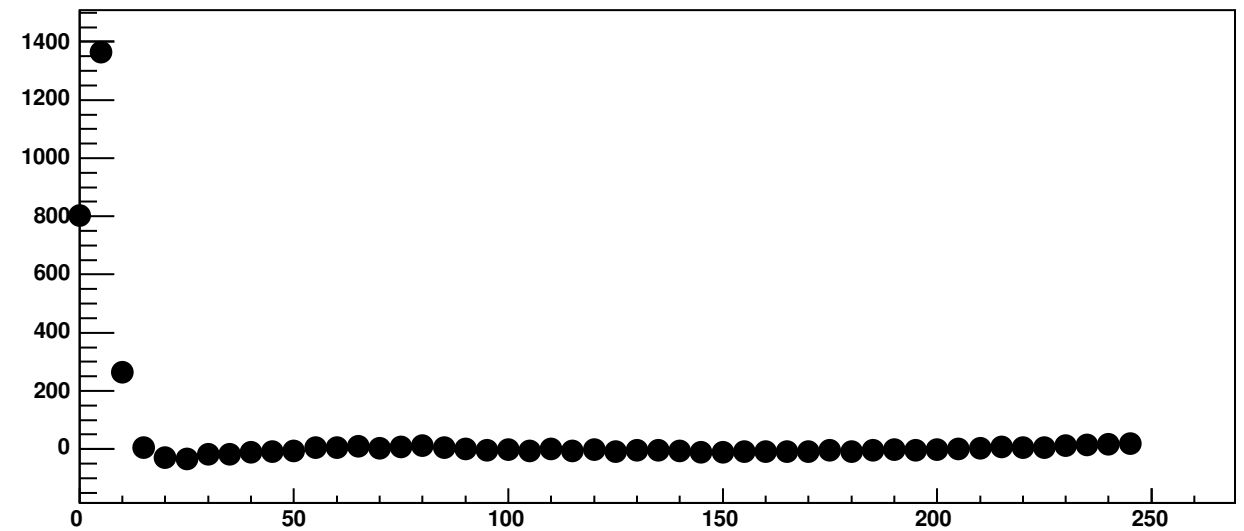
χ^2 / ndf	680.6 / 42
p0	-698.1 ± 0.5386
p1	4.171 ± 0.004998
p2	1.105e+04 ± 0.9739
p3	31.31 ± 0.004703



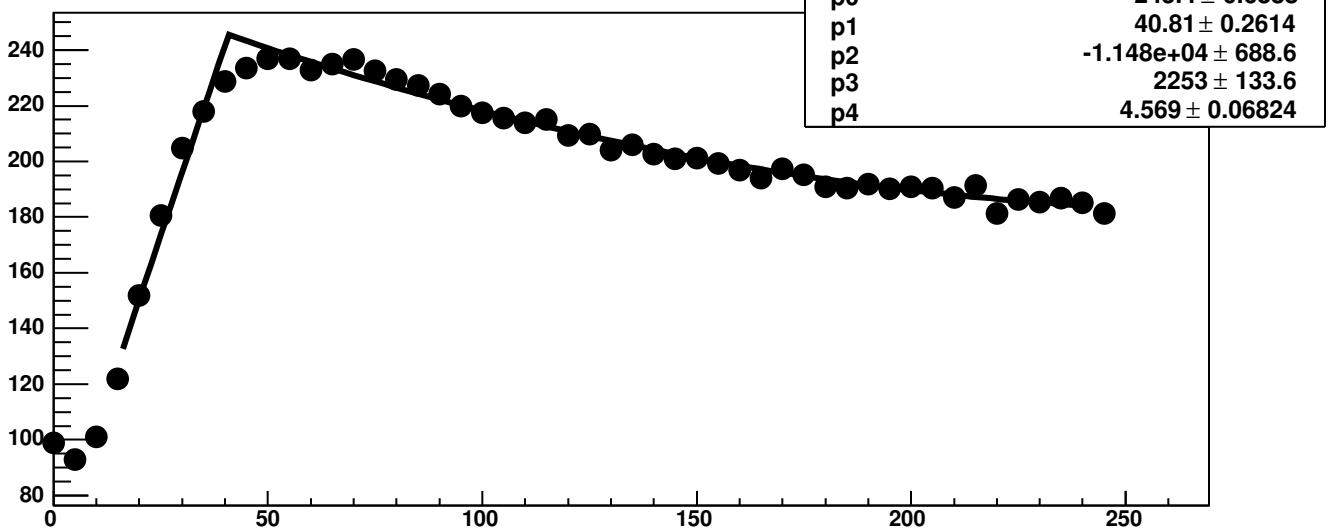
Chip 5, Channel 7, Enable 2!, DAC=4000, ADC Noise vs Hold



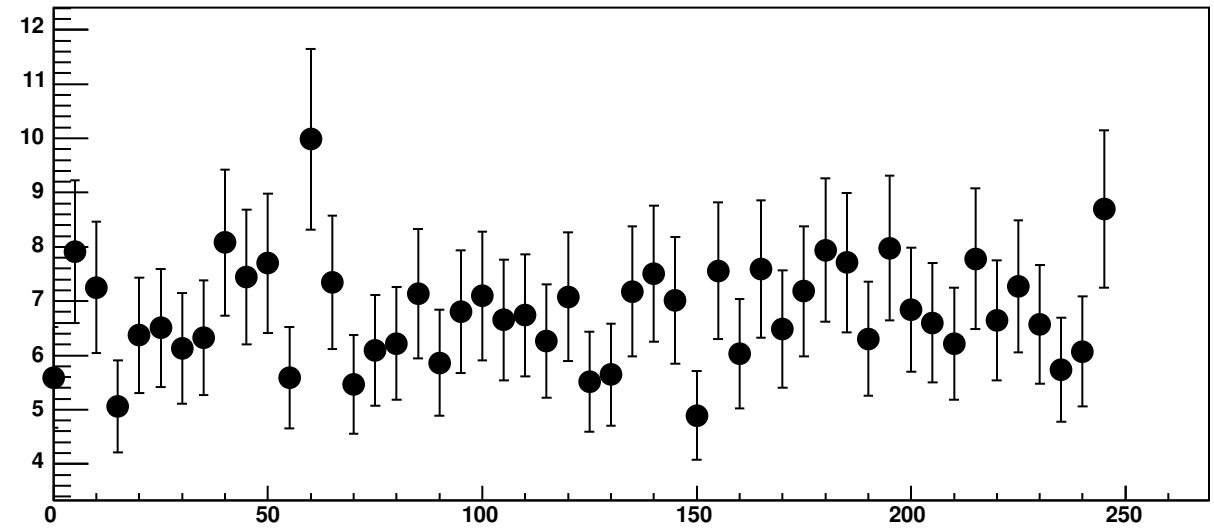
Chip 5, Channel 7, Enable 2!, DAC=4000, ADC Residuals vs Hold



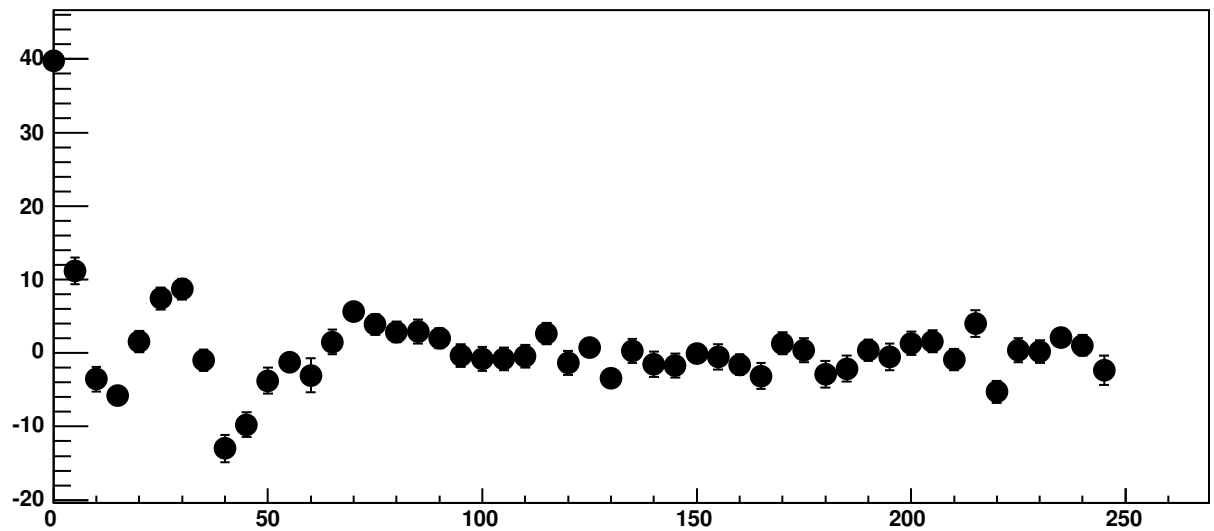
Chip 5, Channel 7, Enable 3, DAC=4000, ADC Mean vs Hold



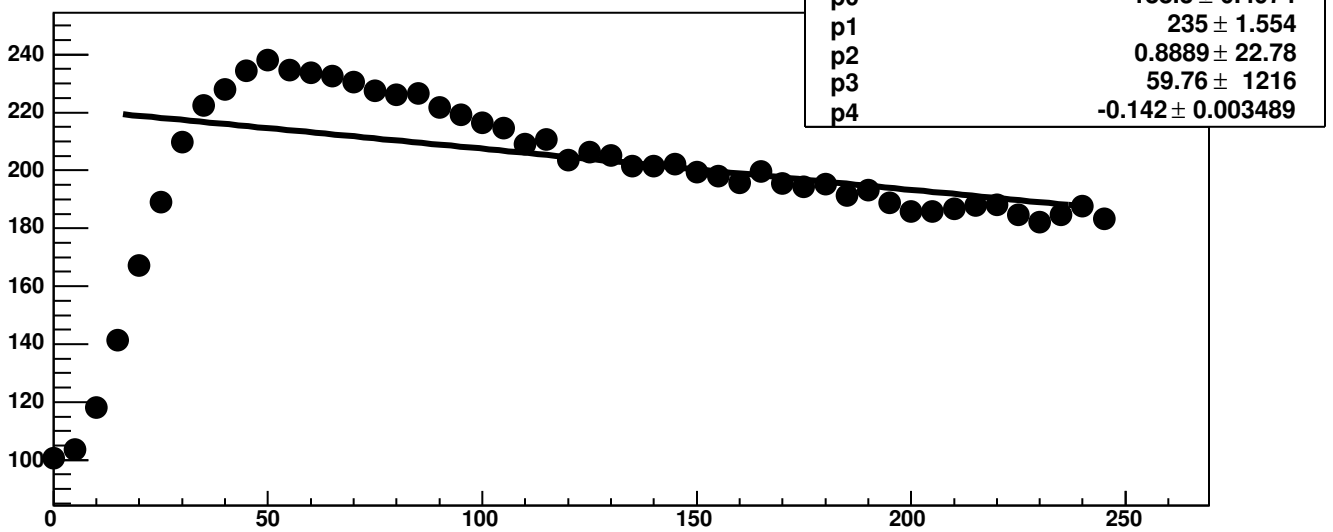
Chip 5, Channel 7, Enable 3, DAC=4000, ADC Noise vs Hold



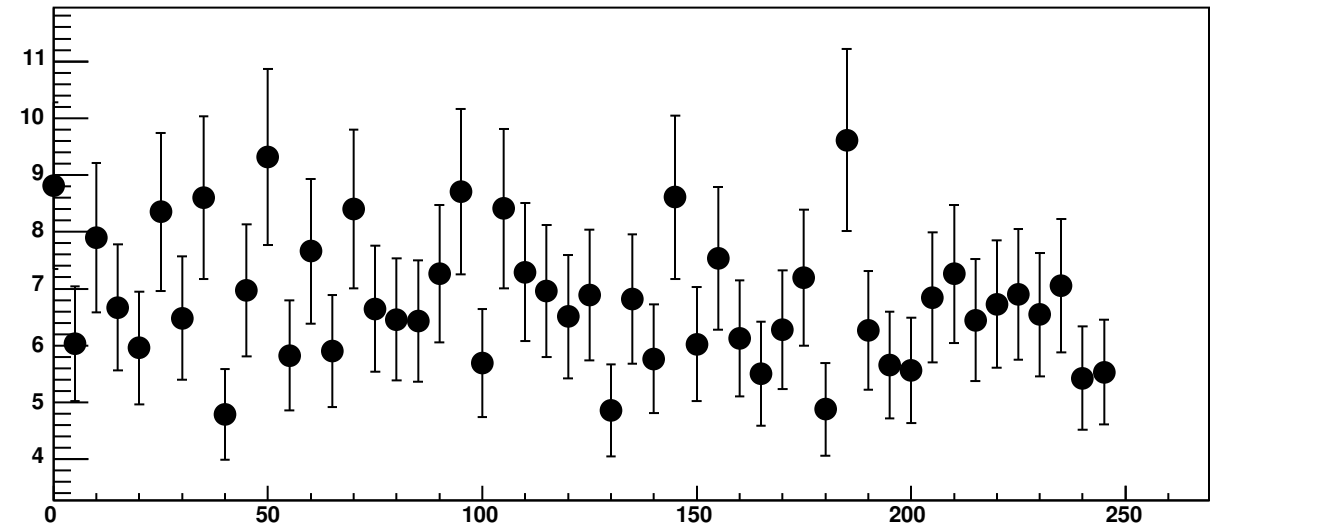
Chip 5, Channel 7, Enable 3, DAC=4000, ADC Residuals vs Hold



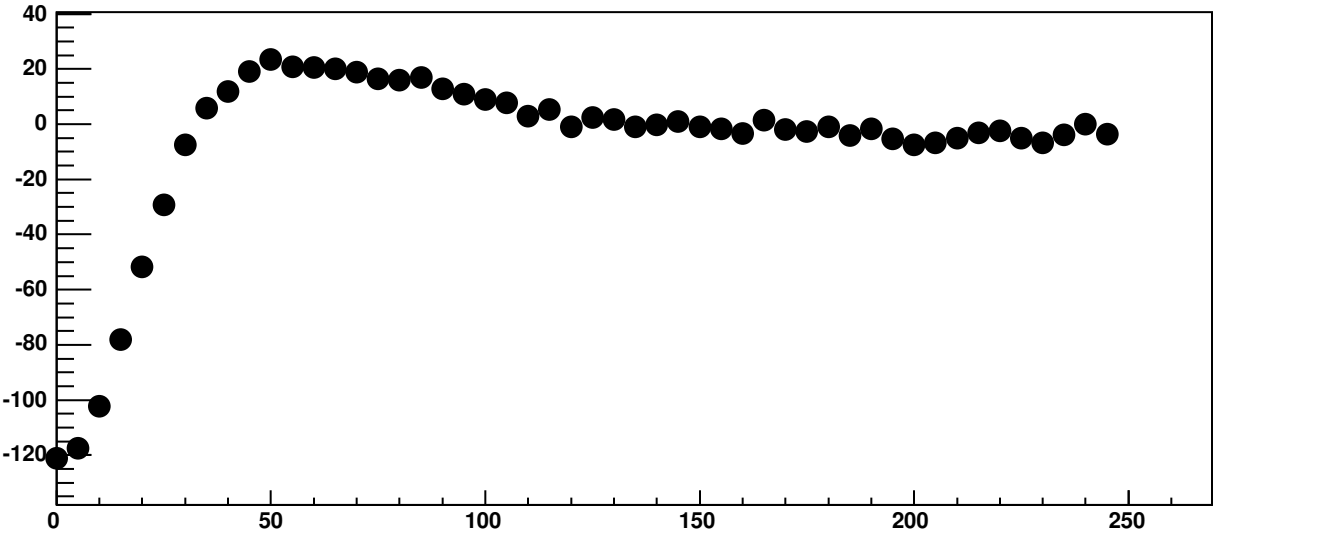
Chip 5, Channel 7, Enable 4, DAC=4000, ADC Mean vs Hold

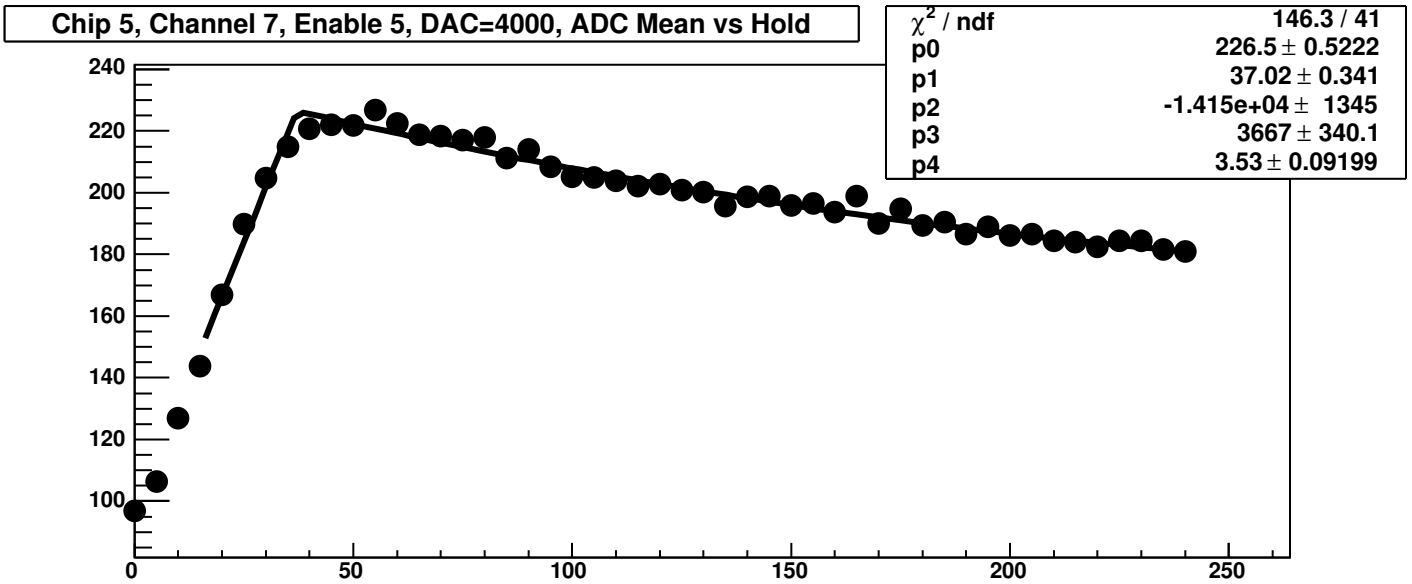


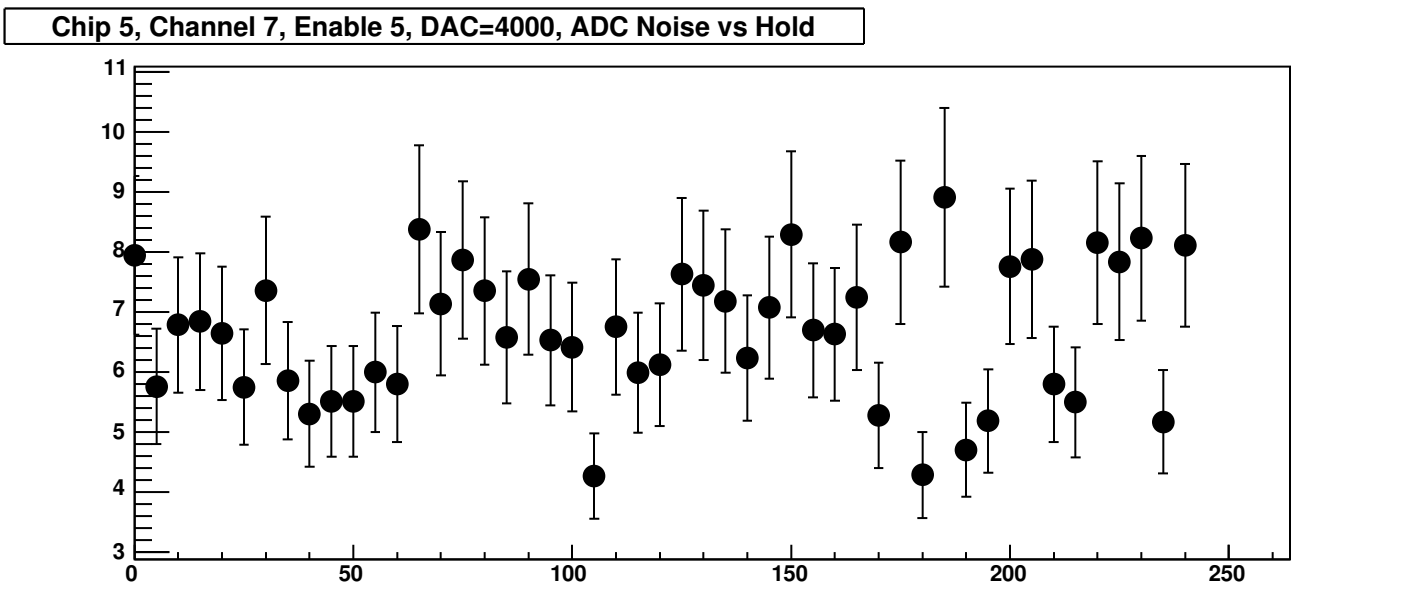
Chip 5, Channel 7, Enable 4, DAC=4000, ADC Noise vs Hold

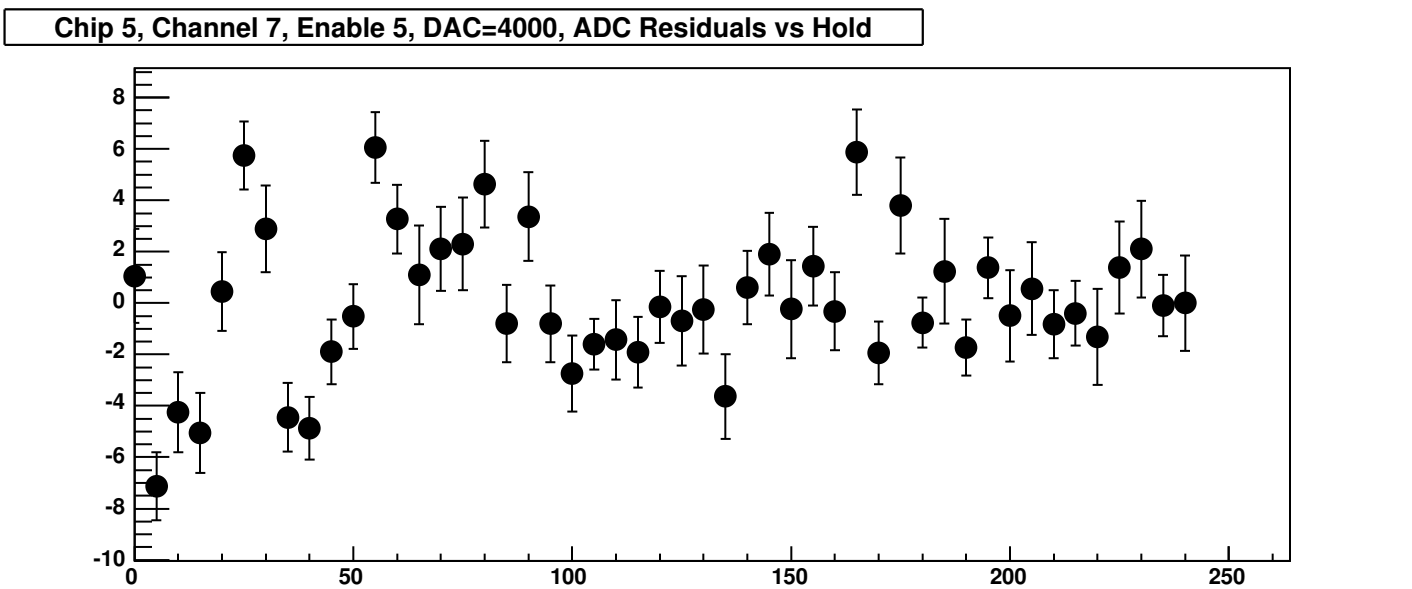


Chip 5, Channel 7, Enable 4, DAC=4000, ADC Residuals vs Hold

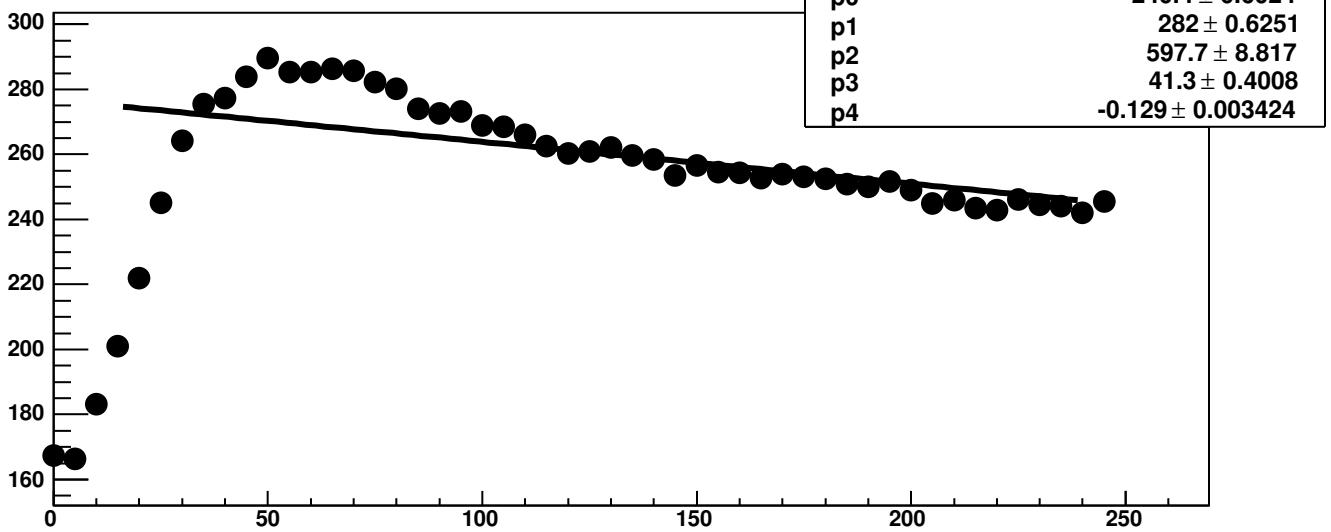



Chip 5, Channel 7, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 146.3 / 41
p0 226.5 ± 0.5222
p1 37.02 ± 0.341
p2 -1.415e+04 ± 1345
p3 3667 ± 340.1
p4 3.53 ± 0.09199

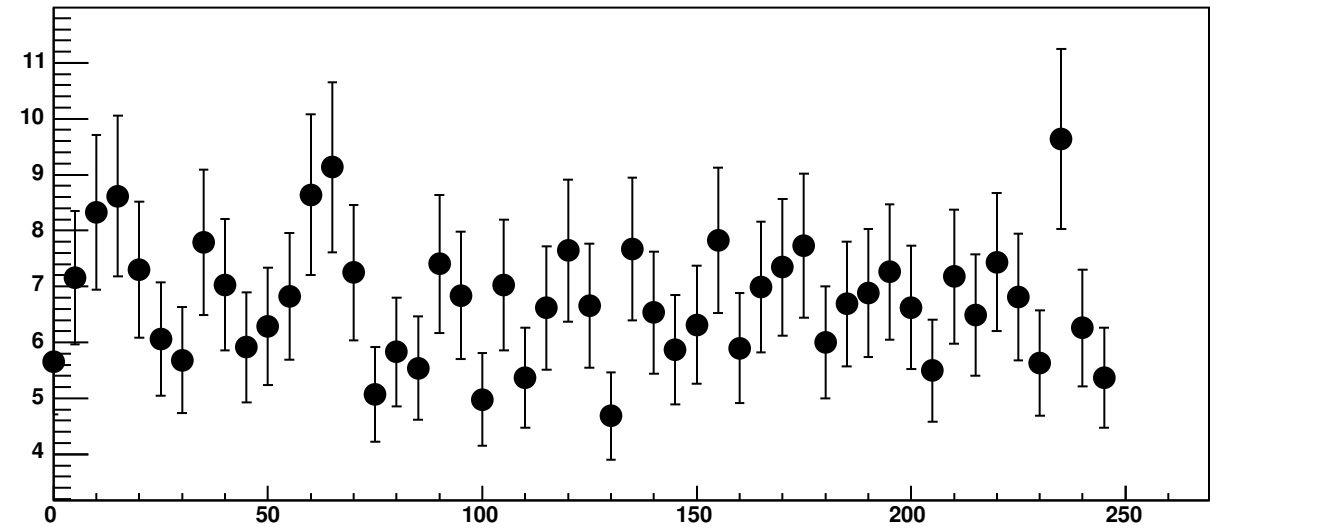

Chip 5, Channel 7, Enable 5, DAC=4000, ADC Noise vs Hold


Chip 5, Channel 7, Enable 5, DAC=4000, ADC Residuals vs Hold

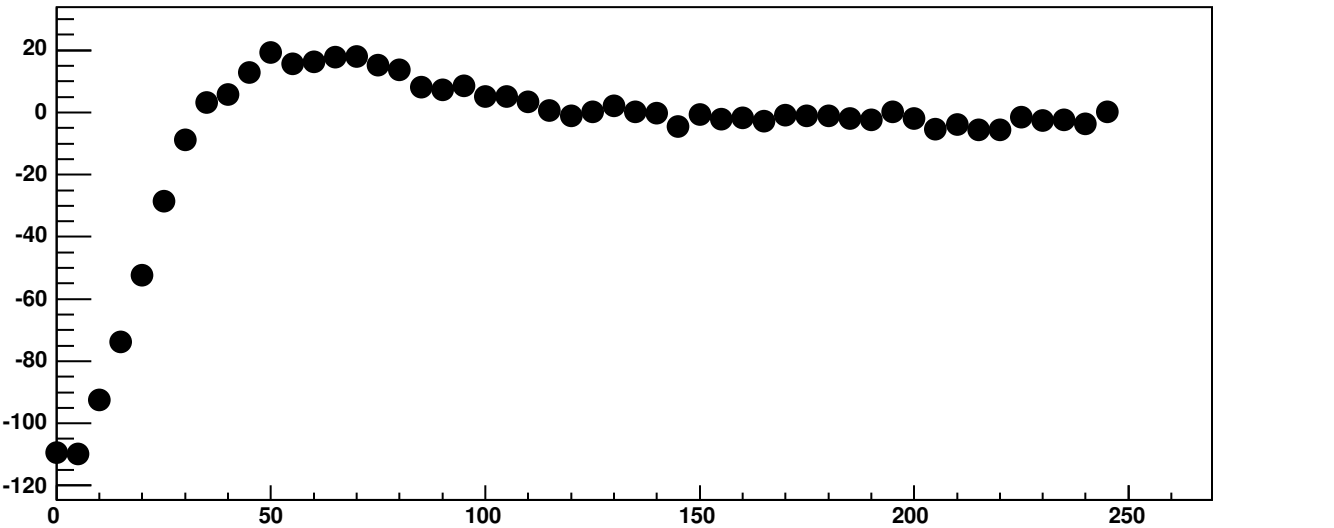
Chip 5, Channel 8, Enable 0, DAC=4000, ADC Mean vs Hold



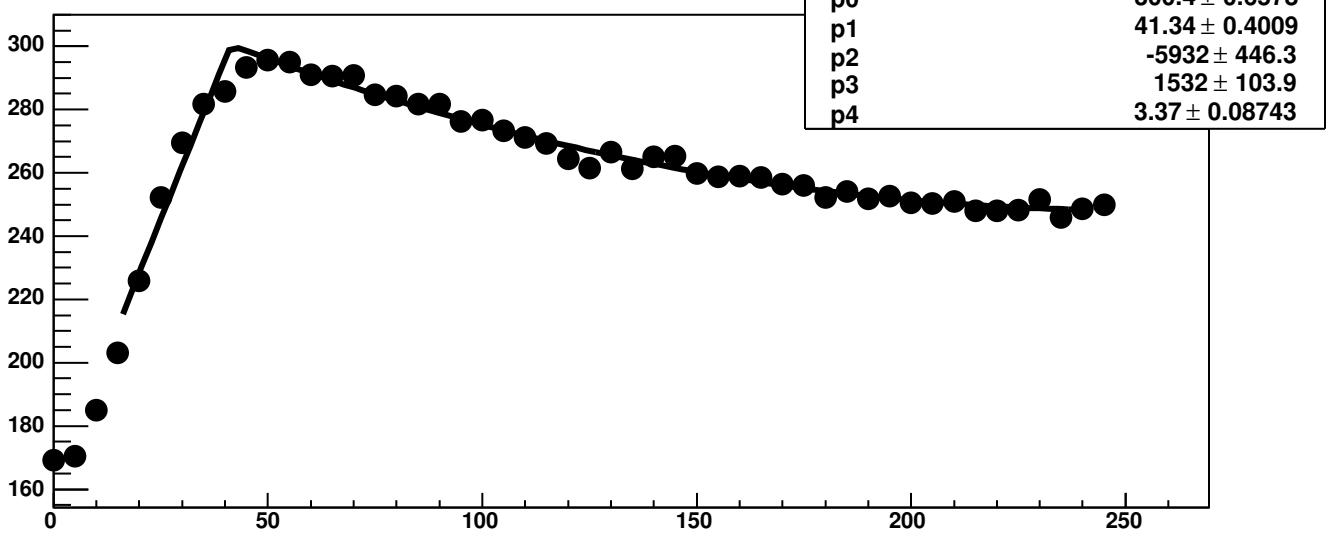
Chip 5, Channel 8, Enable 0, DAC=4000, ADC Noise vs Hold



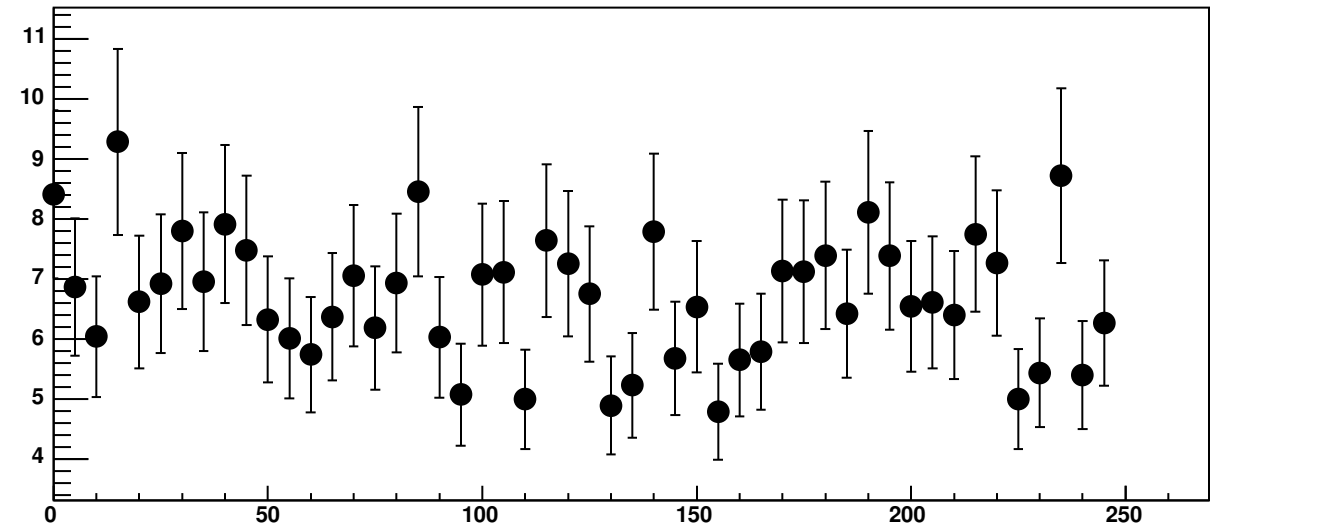
Chip 5, Channel 8, Enable 0, DAC=4000, ADC Residuals vs Hold



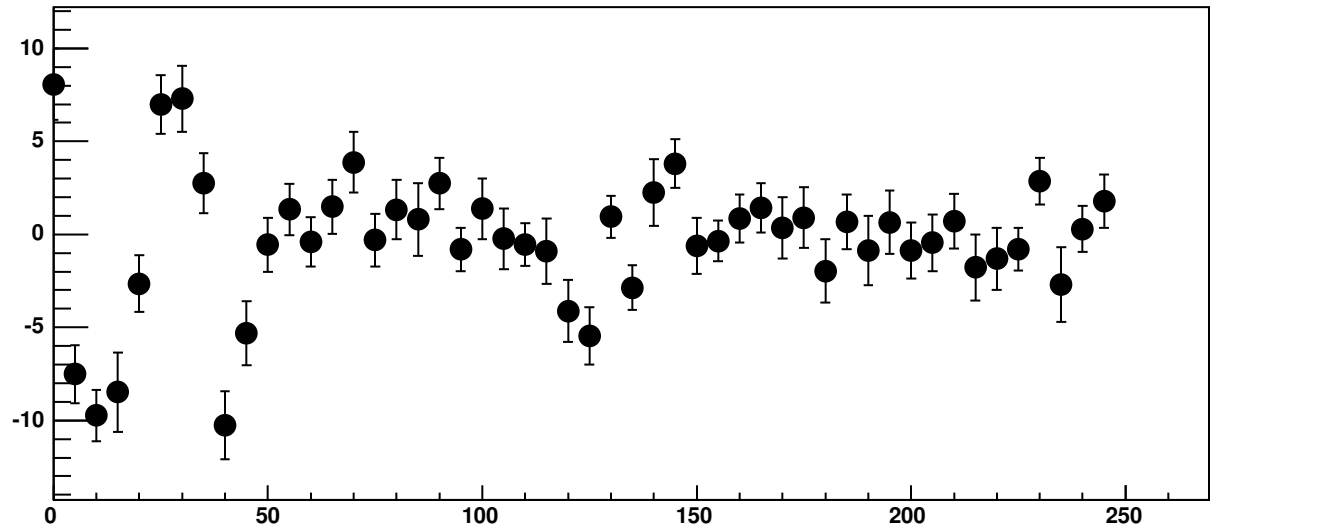
Chip 5, Channel 8, Enable 1, DAC=4000, ADC Mean vs Hold

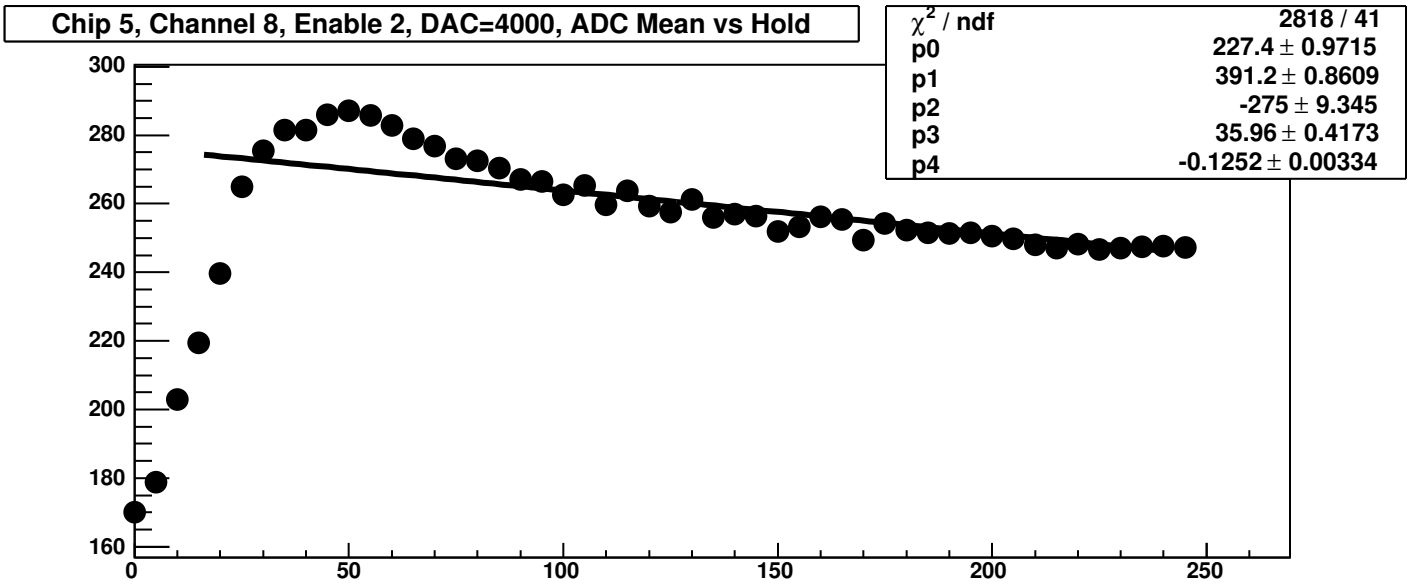


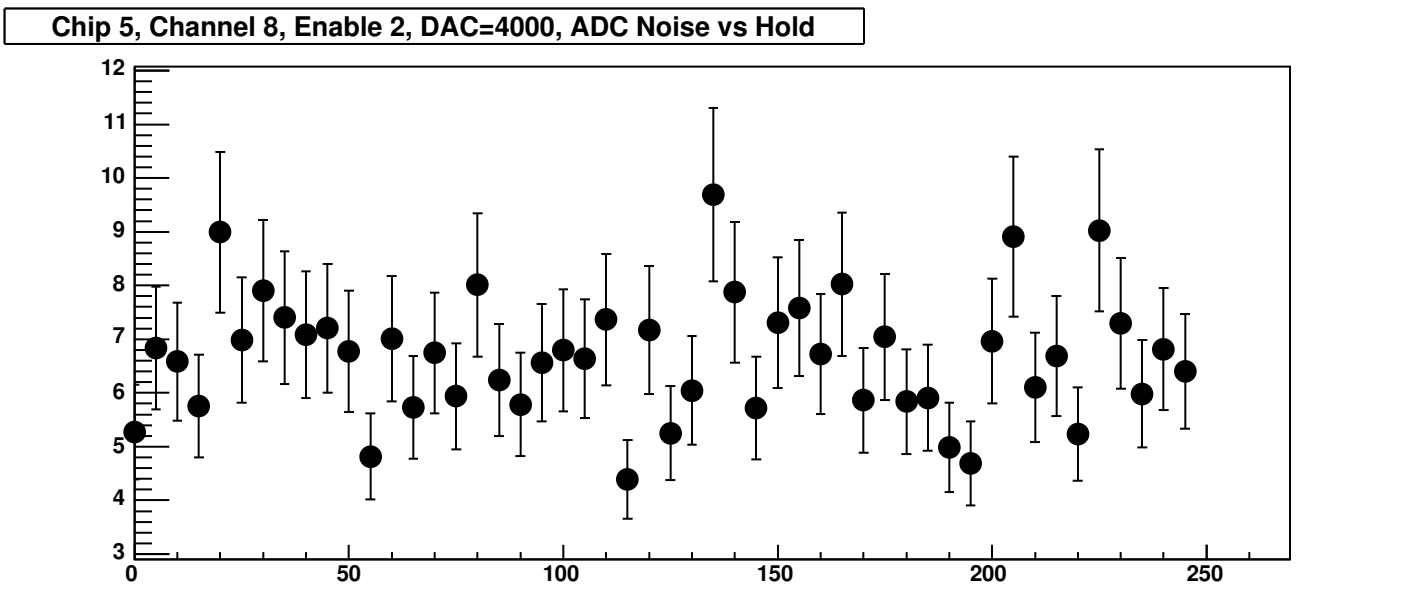
Chip 5, Channel 8, Enable 1, DAC=4000, ADC Noise vs Hold

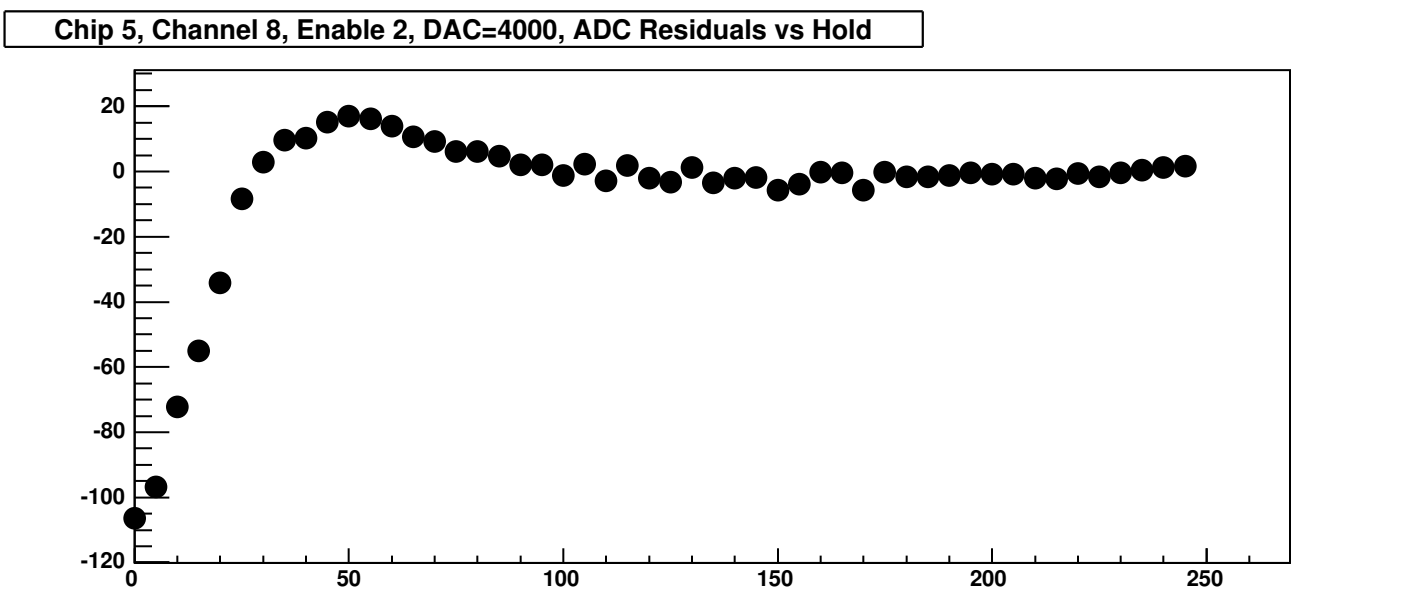


Chip 5, Channel 8, Enable 1, DAC=4000, ADC Residuals vs Hold

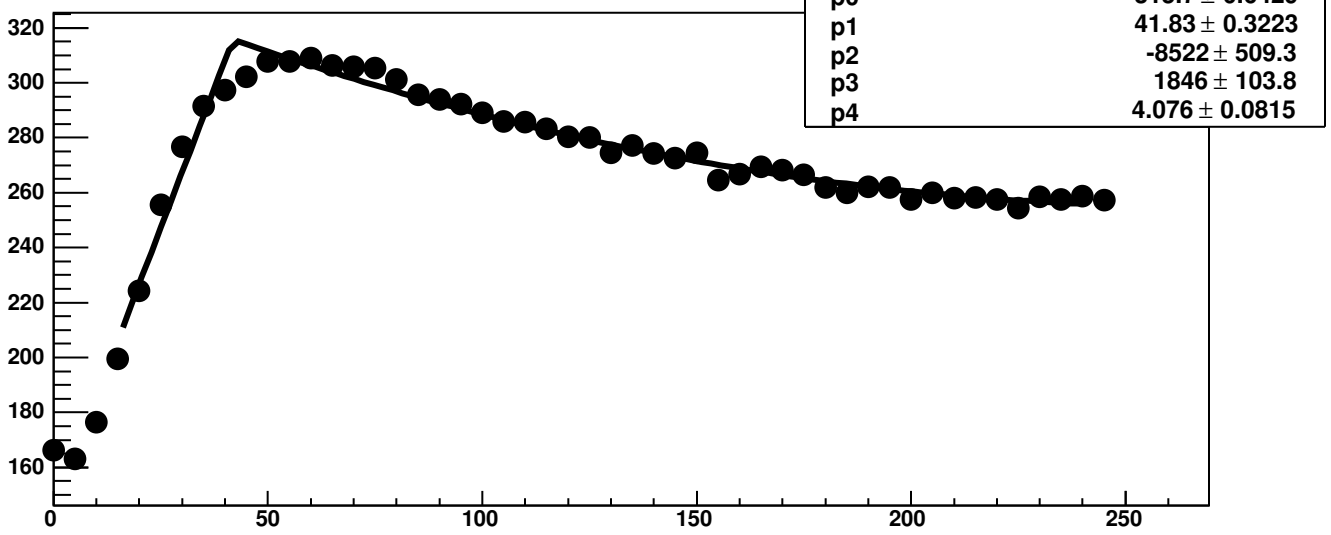



Chip 5, Channel 8, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 2818 / 41
p0 227.4 ± 0.9715
p1 391.2 ± 0.8609
p2 -275 ± 9.345
p3 35.96 ± 0.4173
p4 -0.1252 ± 0.00334
300
280
260
240
220
200
180
160
0
50
100
150
200
250

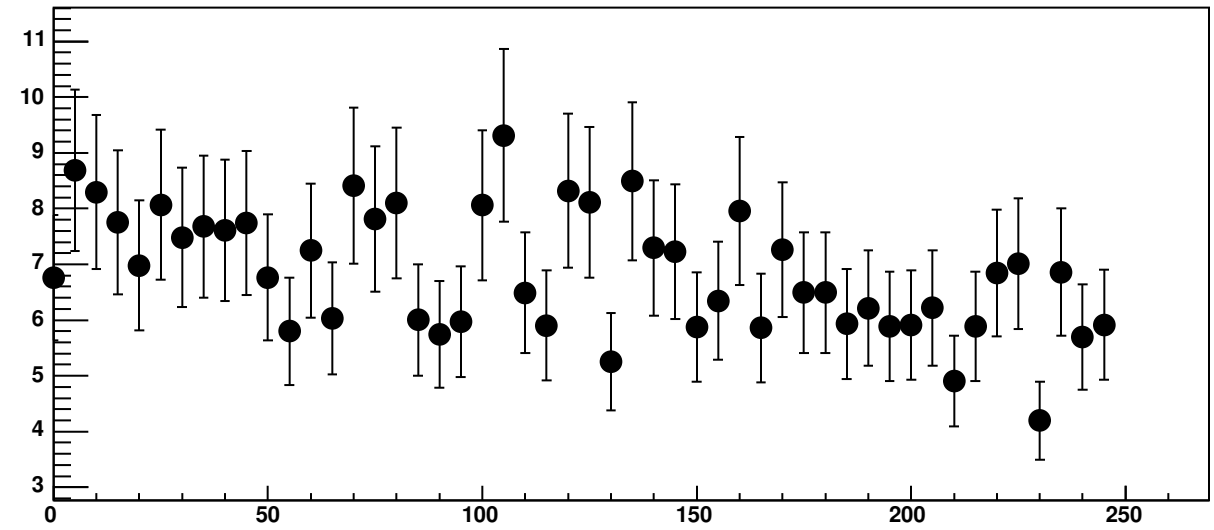

Chip 5, Channel 8, Enable 2, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 5, Channel 8, Enable 2, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
-80
-100
-120
0
50
100
150
200
250

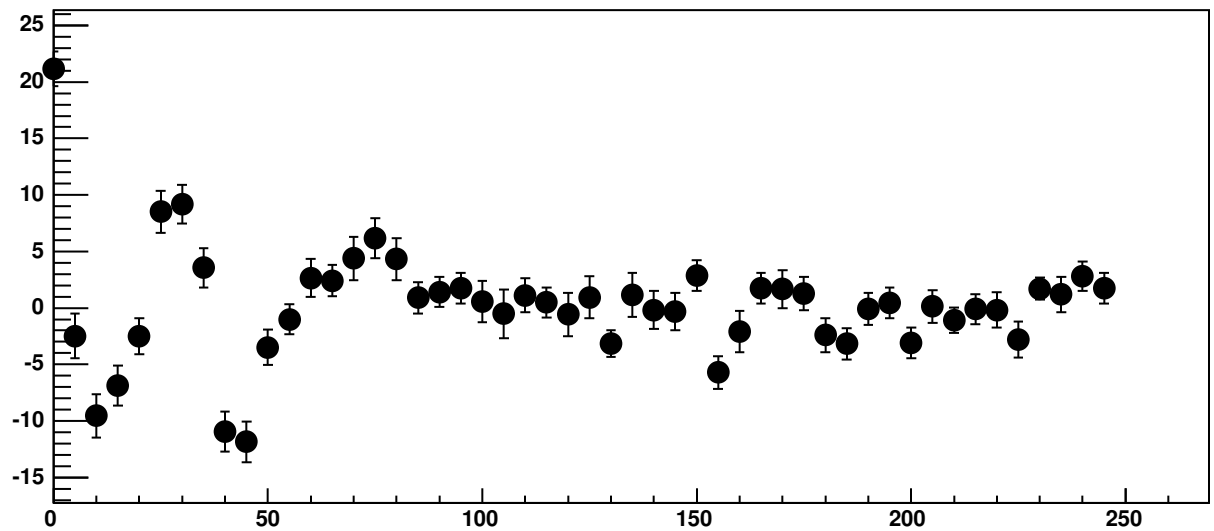
Chip 5, Channel 8, Enable 3, DAC=4000, ADC Mean vs Hold



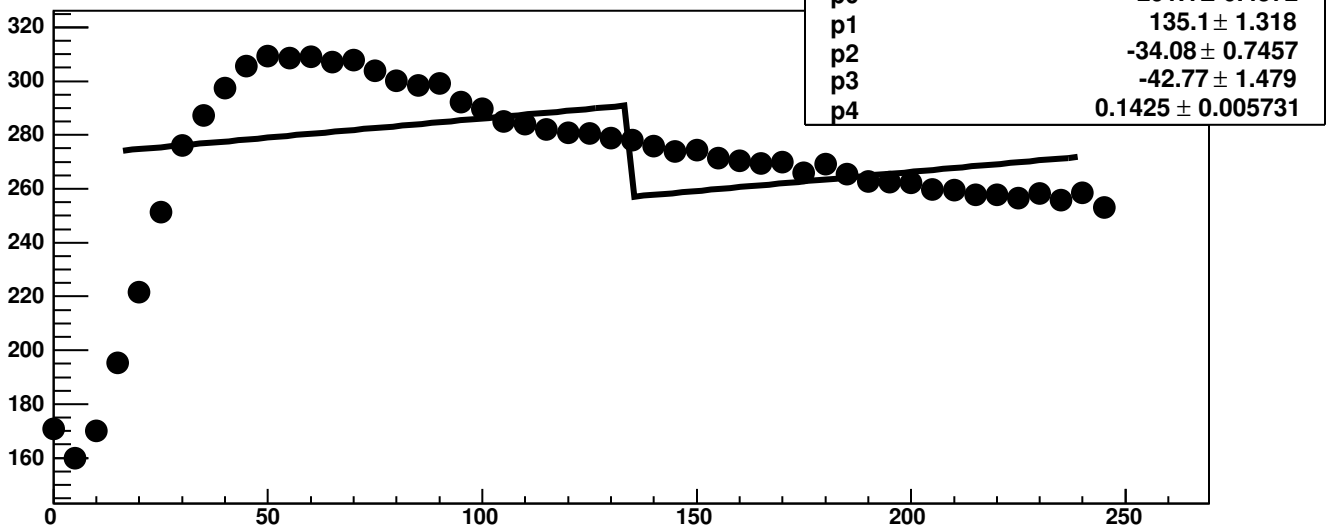
Chip 5, Channel 8, Enable 3, DAC=4000, ADC Noise vs Hold



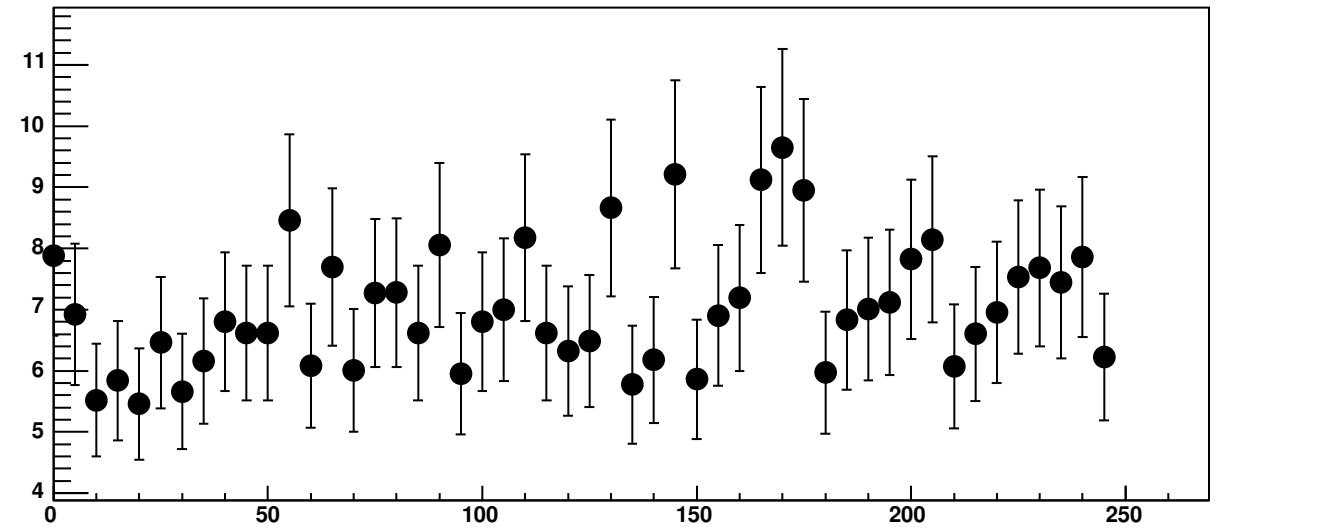
Chip 5, Channel 8, Enable 3, DAC=4000, ADC Residuals vs Hold



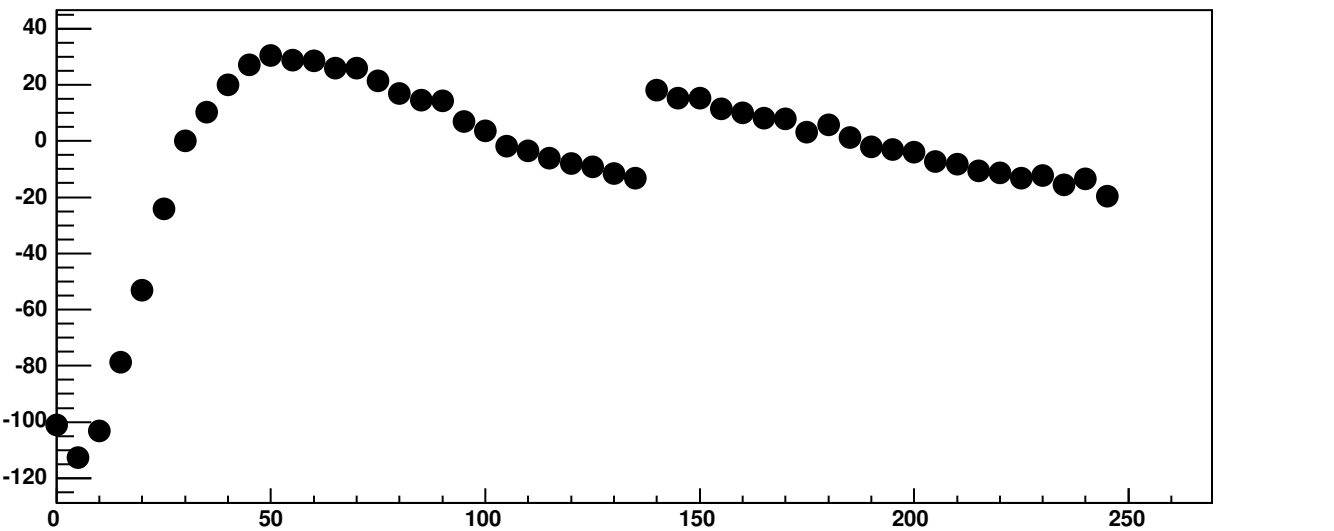
Chip 5, Channel 8, Enable 4, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 8, Enable 4, DAC=4000, ADC Noise vs Hold

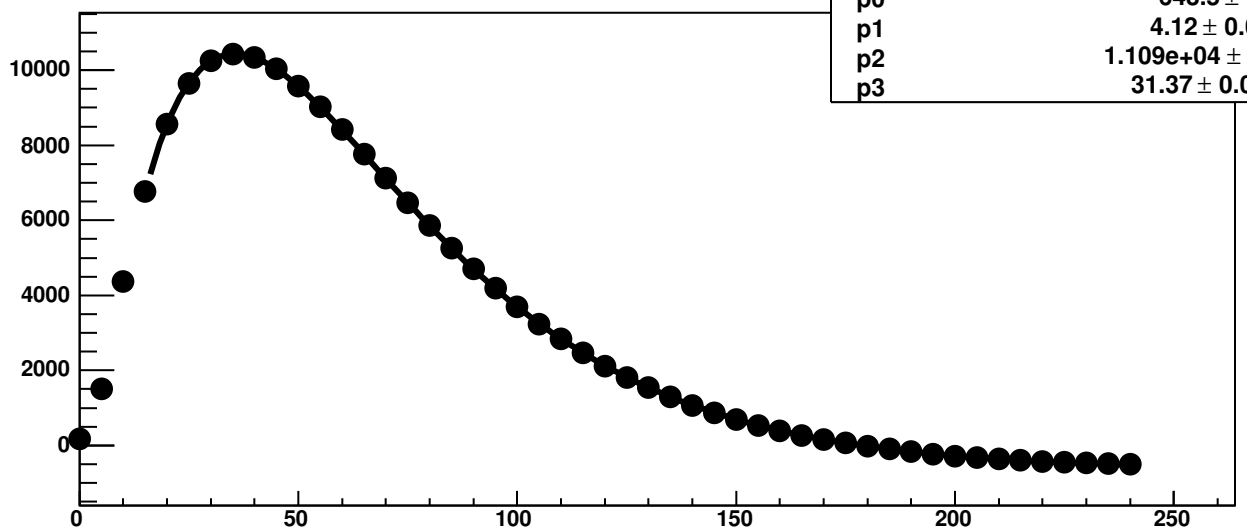


Chip 5, Channel 8, Enable 4, DAC=4000, ADC Residuals vs Hold

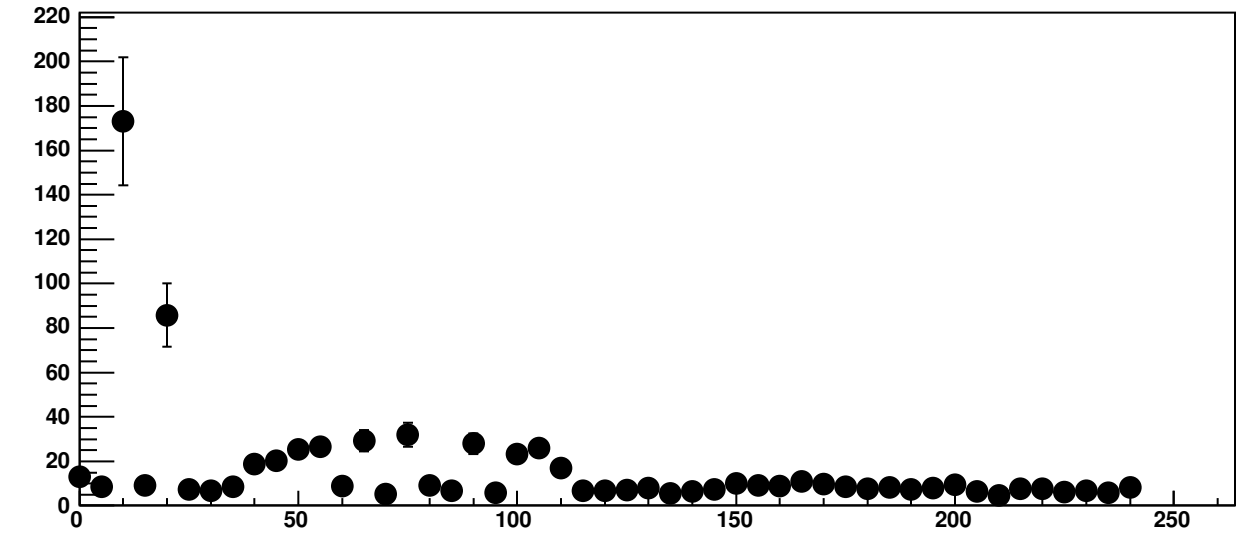


Chip 5, Channel 8, Enable 5!, DAC=4000, ADC Mean vs Hold

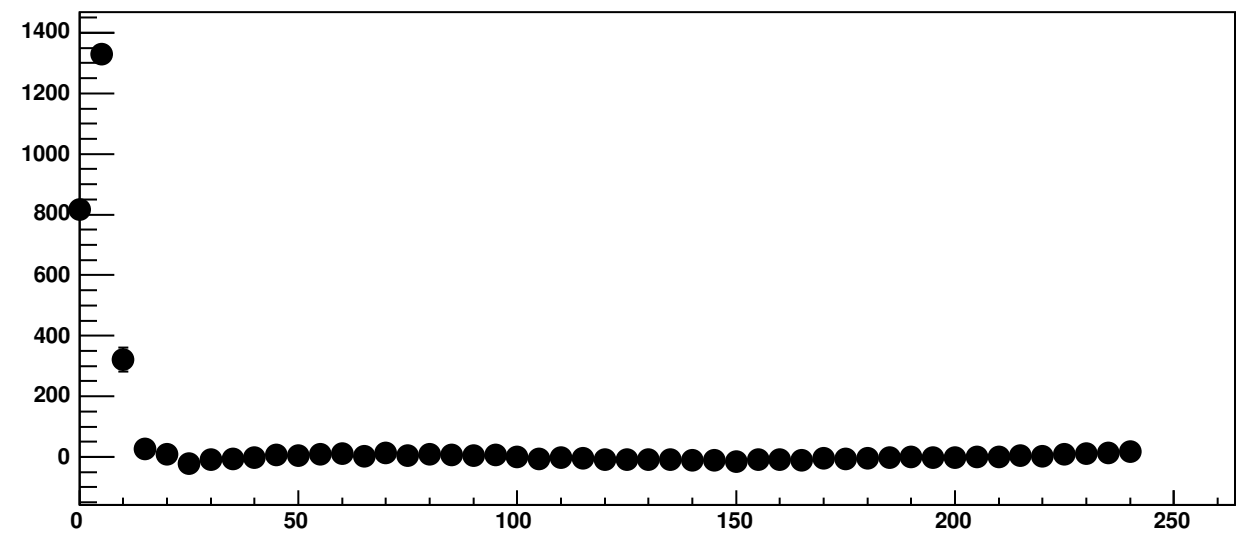
χ^2 / ndf	1196 / 42
p0	-643.5 ± 0.5174
p1	4.12 ± 0.005901
p2	1.109e+04 ± 0.9093
p3	31.37 ± 0.004367

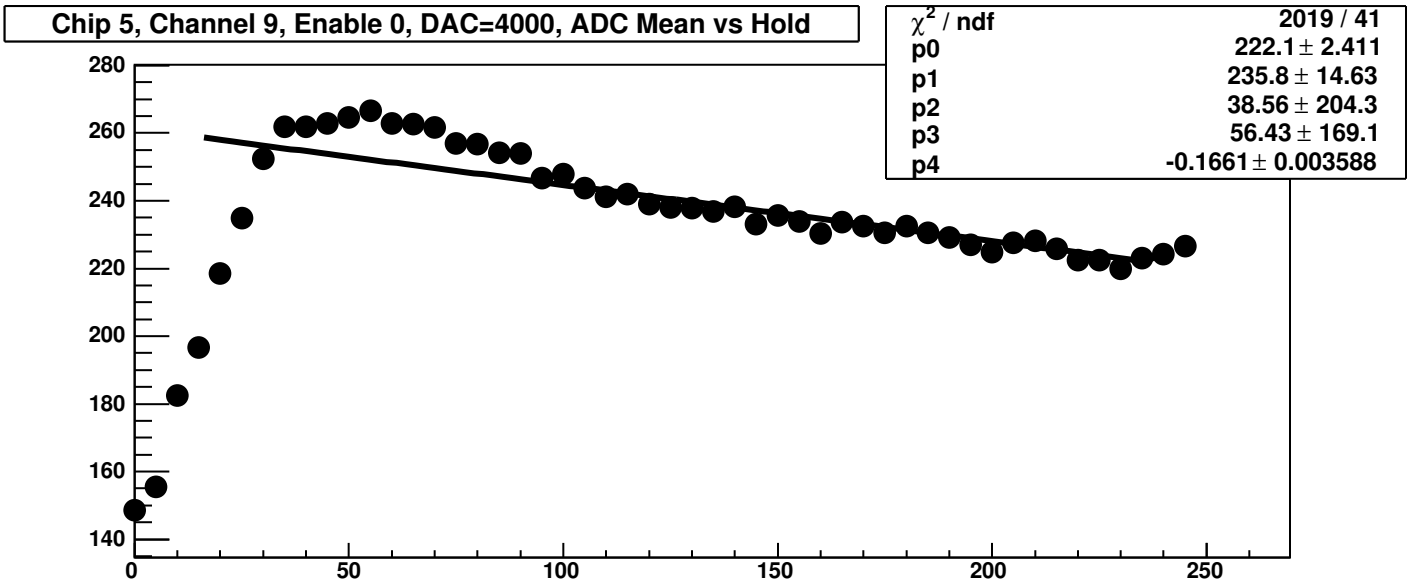


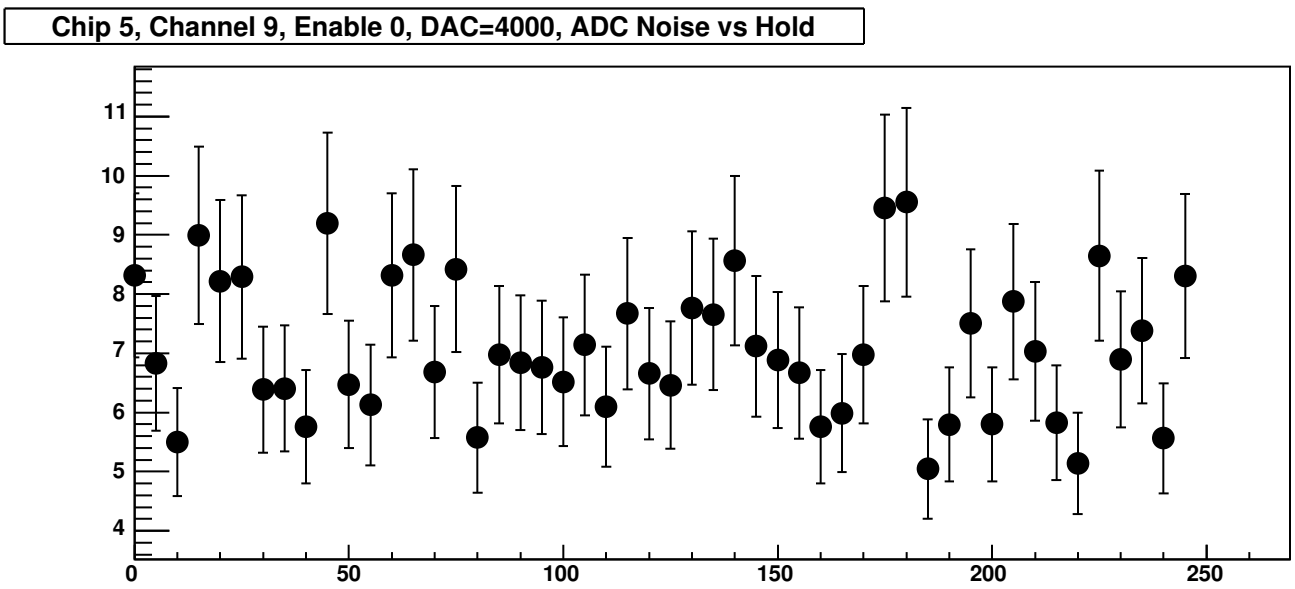
Chip 5, Channel 8, Enable 5!, DAC=4000, ADC Noise vs Hold

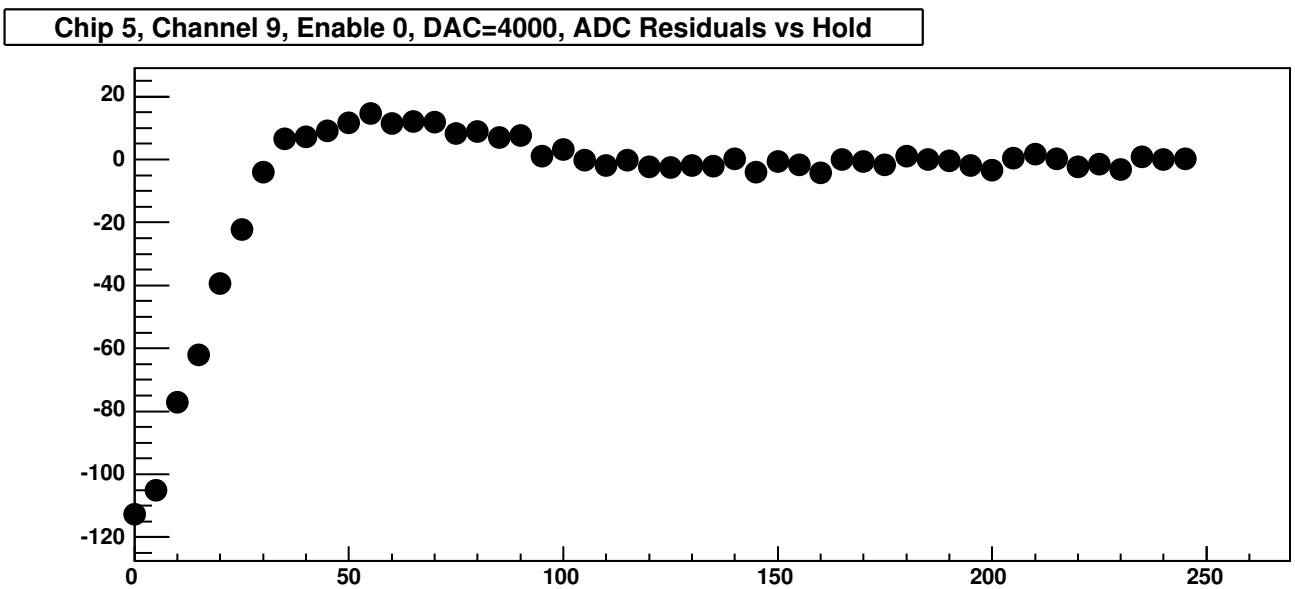


Chip 5, Channel 8, Enable 5!, DAC=4000, ADC Residuals vs Hold

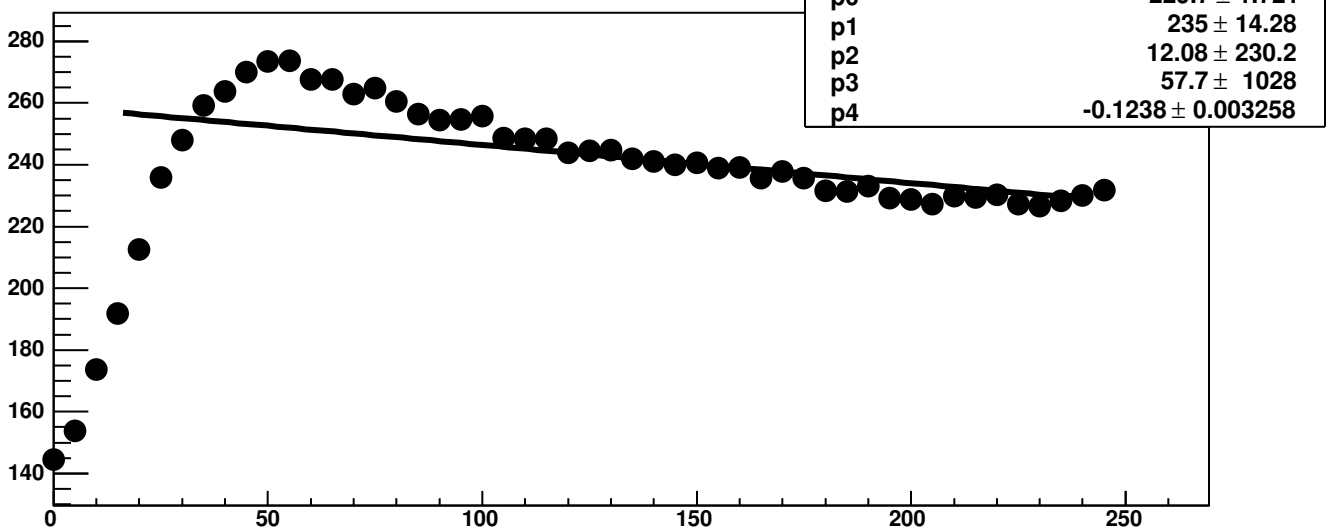



Chip 5, Channel 9, Enable 0, DAC=4000, ADC Mean vs Hold
χ² / ndf 2019 / 41
p0 222.1 ± 2.411
p1 235.8 ± 14.63
p2 38.56 ± 204.3
p3 56.43 ± 169.1
p4 -0.1661 ± 0.003588
280
260
240
220
200
180
160
140
0
50
100
150
200
250

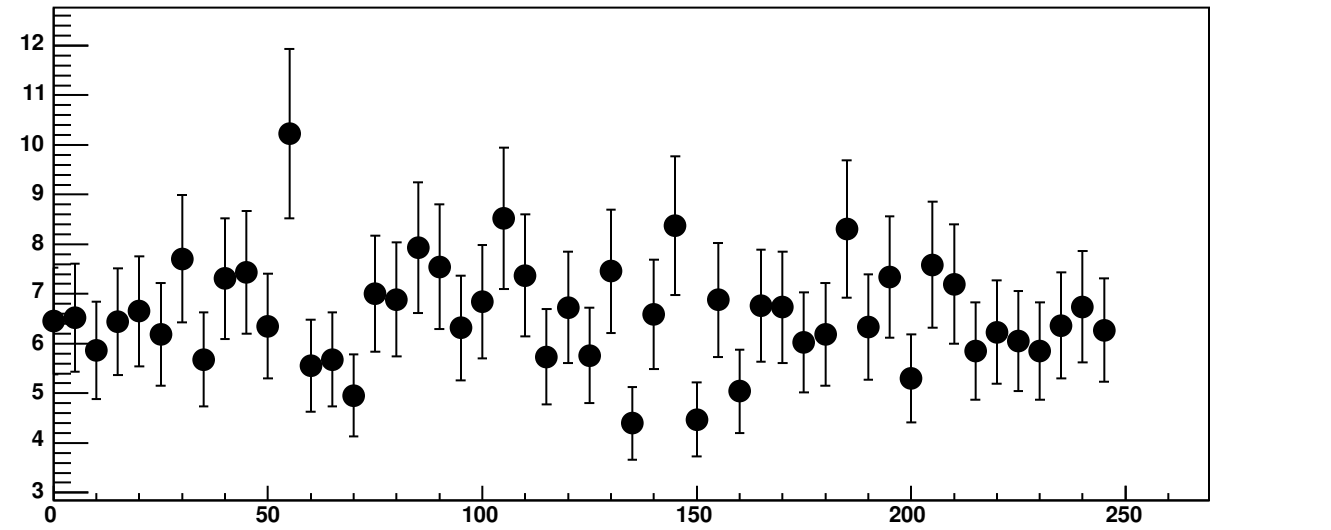

Chip 5, Channel 9, Enable 0, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 5, Channel 9, Enable 0, DAC=4000, ADC Residuals vs Hold
20
0
-20
-40
-60
-80
-100
-120
0
50
100
150
200
250

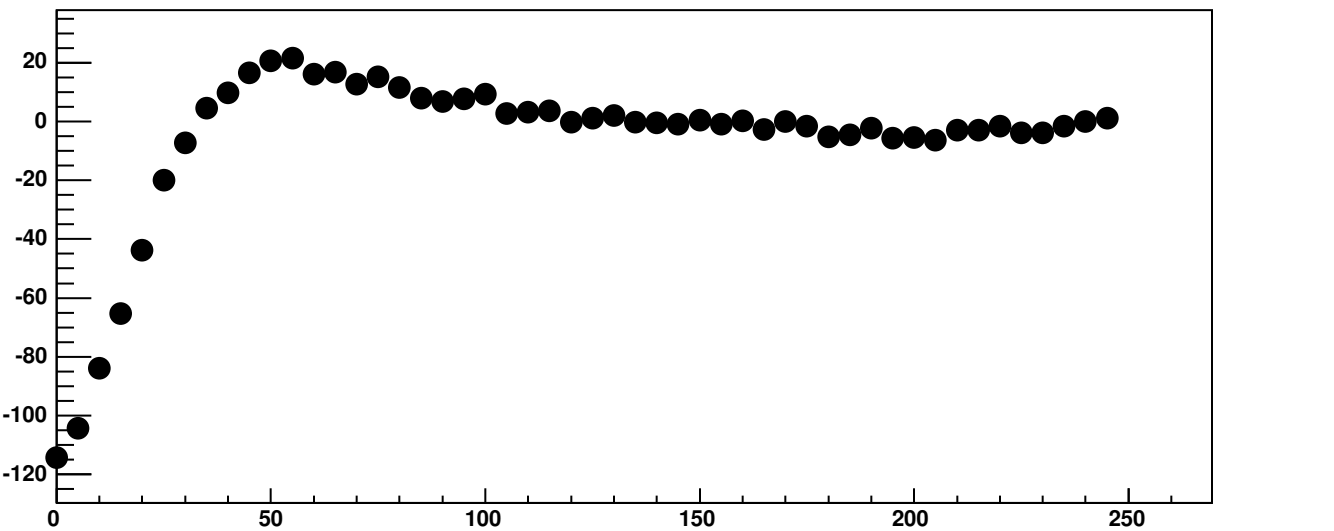
Chip 5, Channel 9, Enable 1, DAC=4000, ADC Mean vs Hold



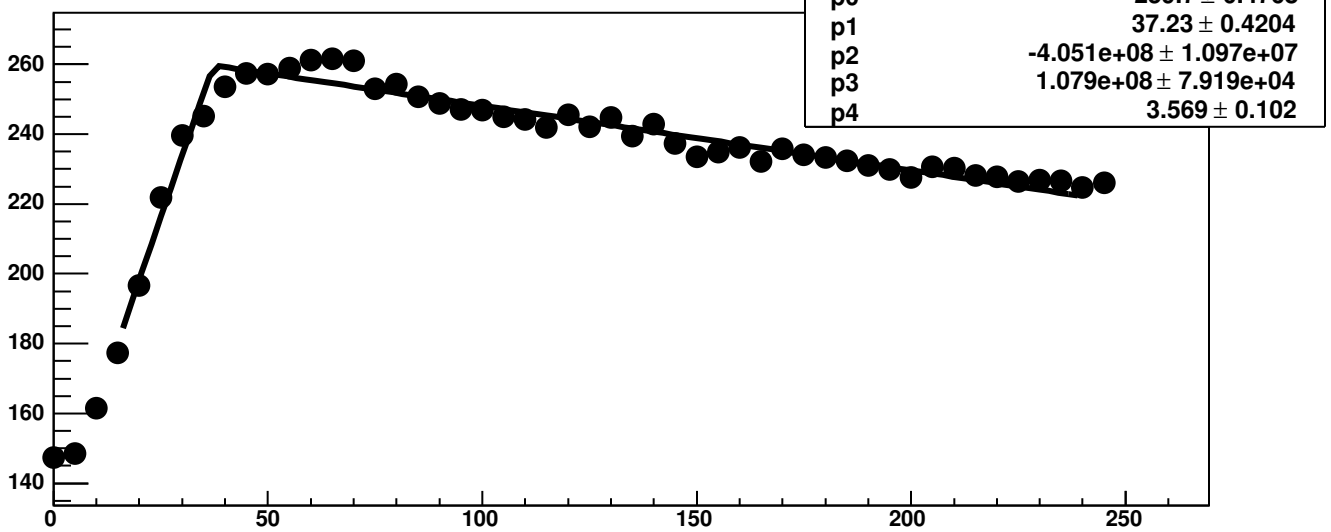
Chip 5, Channel 9, Enable 1, DAC=4000, ADC Noise vs Hold



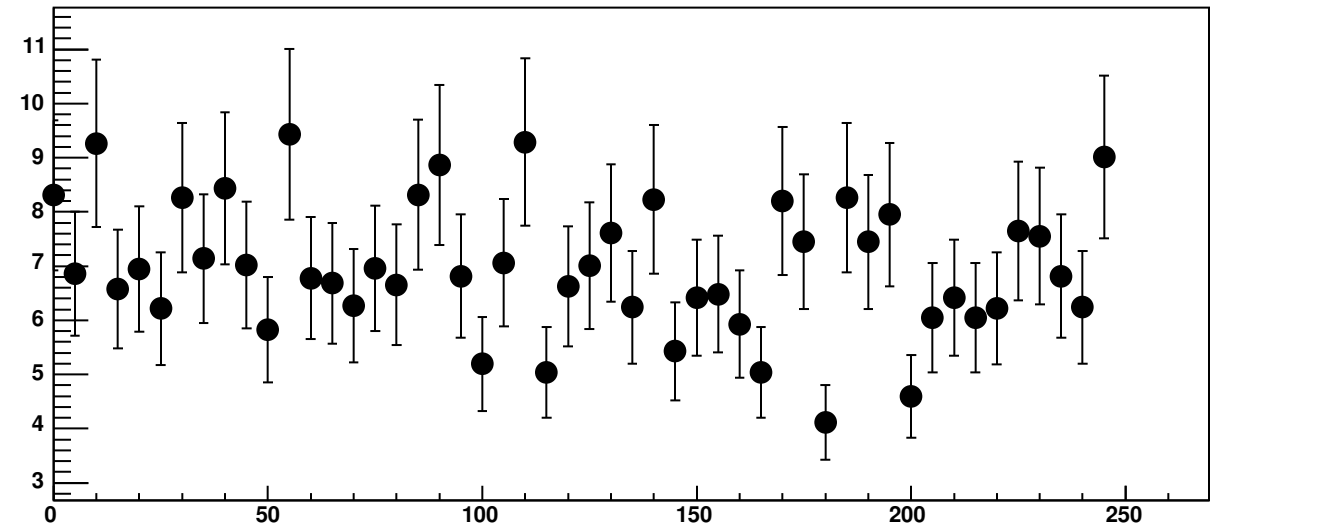
Chip 5, Channel 9, Enable 1, DAC=4000, ADC Residuals vs Hold



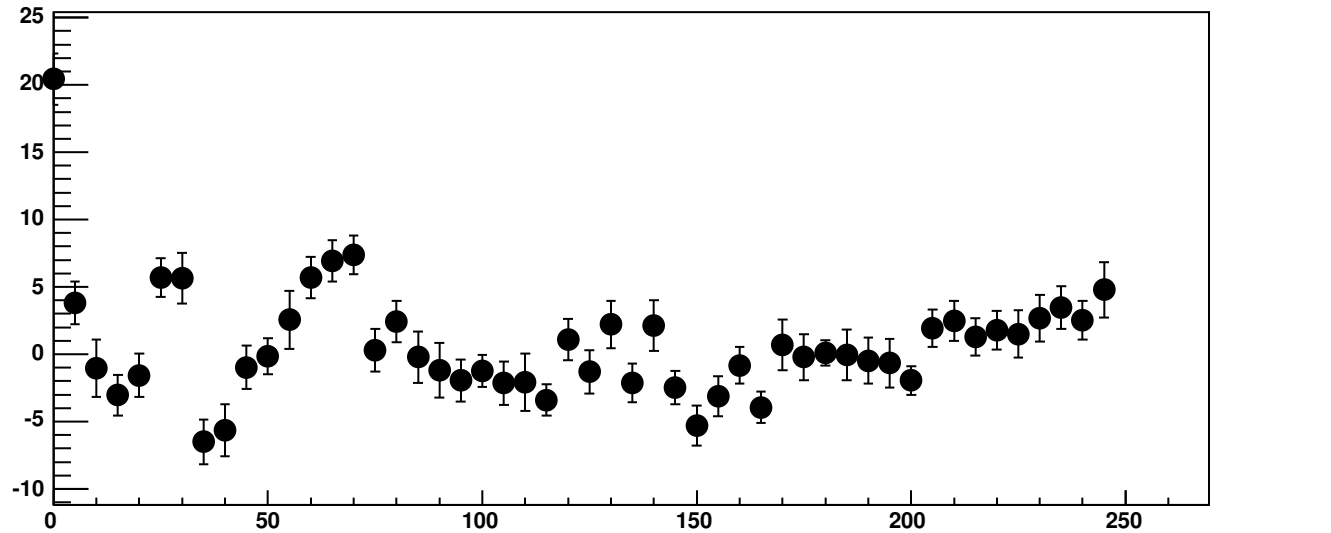
Chip 5, Channel 9, Enable 2, DAC=4000, ADC Mean vs Hold



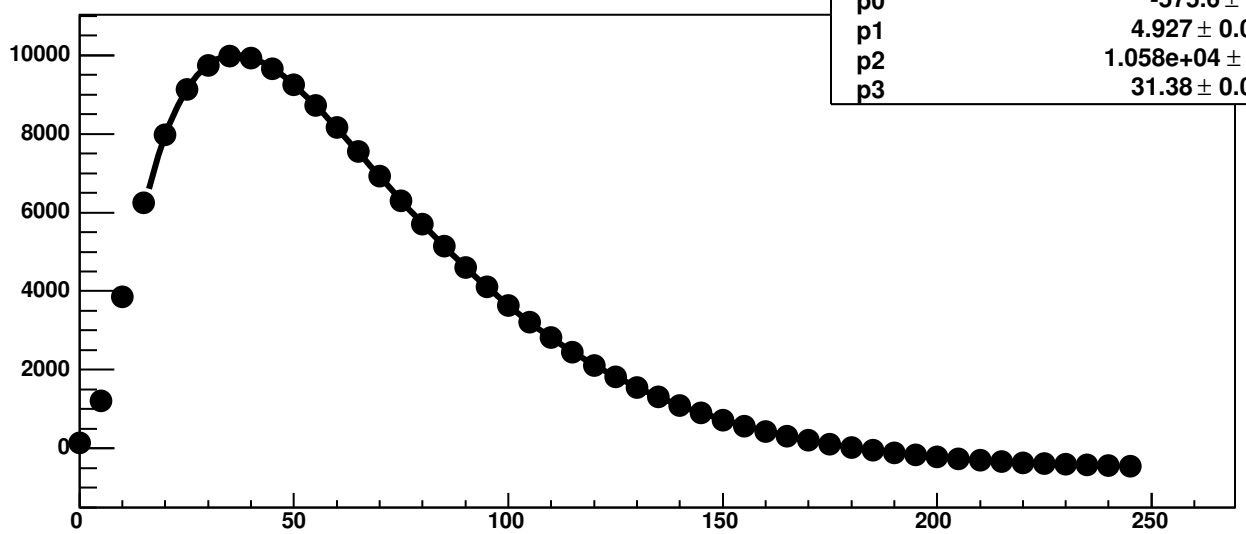
Chip 5, Channel 9, Enable 2, DAC=4000, ADC Noise vs Hold



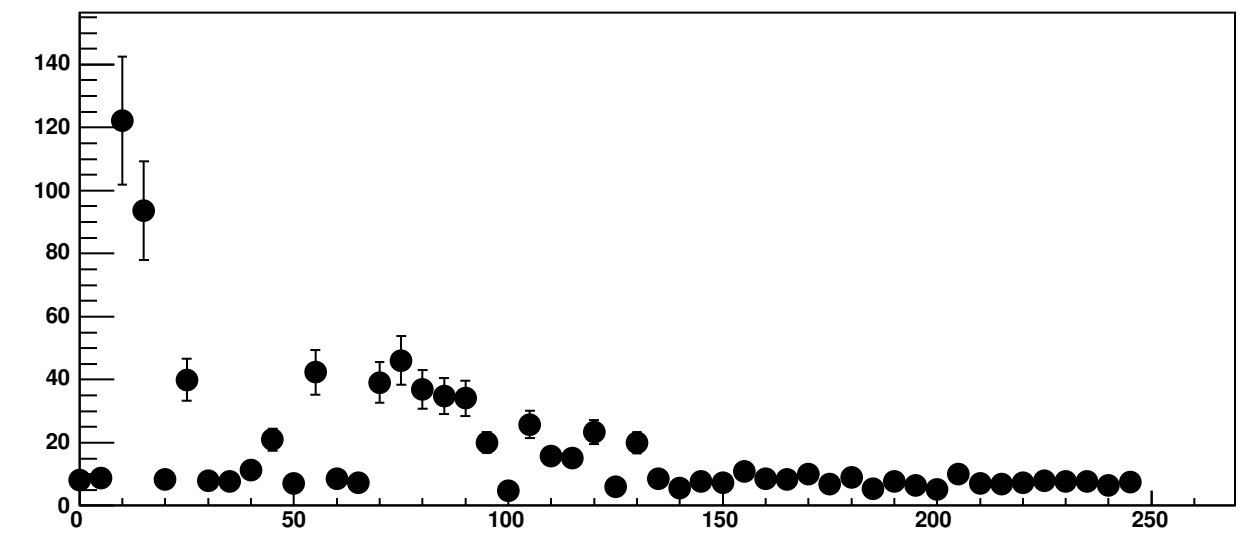
Chip 5, Channel 9, Enable 2, DAC=4000, ADC Residuals vs Hold



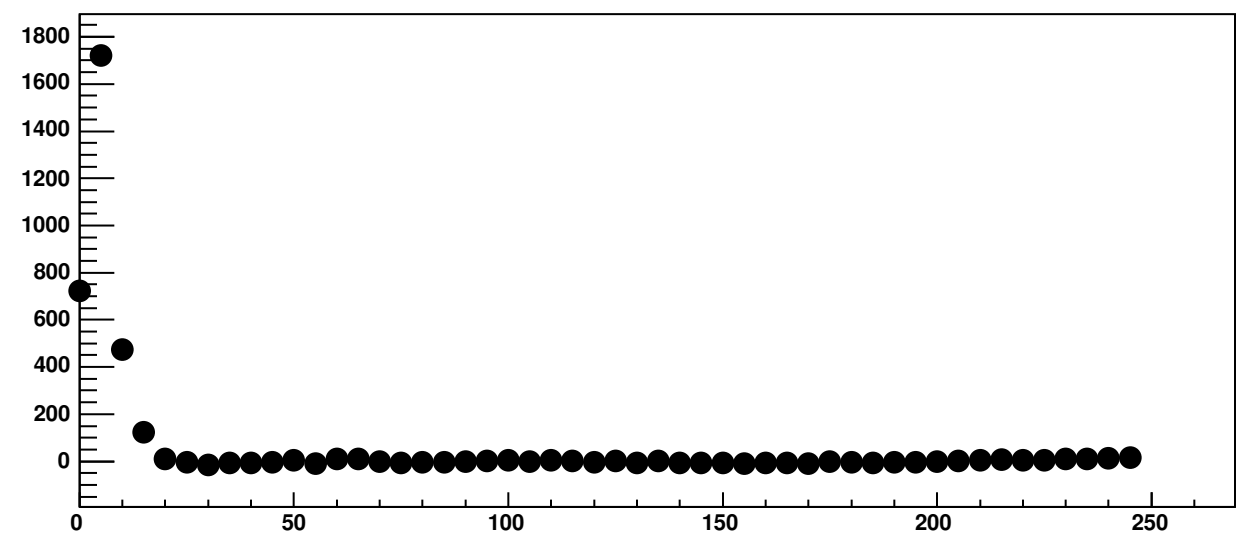
Chip 5, Channel 9, Enable 3!, DAC=4000, ADC Mean vs Hold



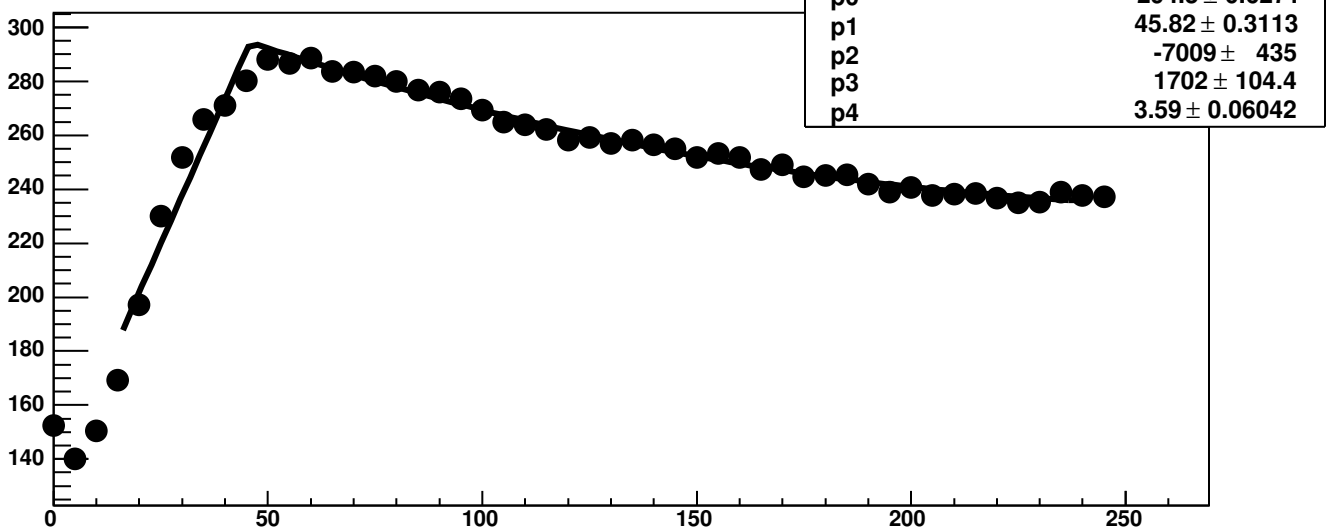
Chip 5, Channel 9, Enable 3!, DAC=4000, ADC Noise vs Hold



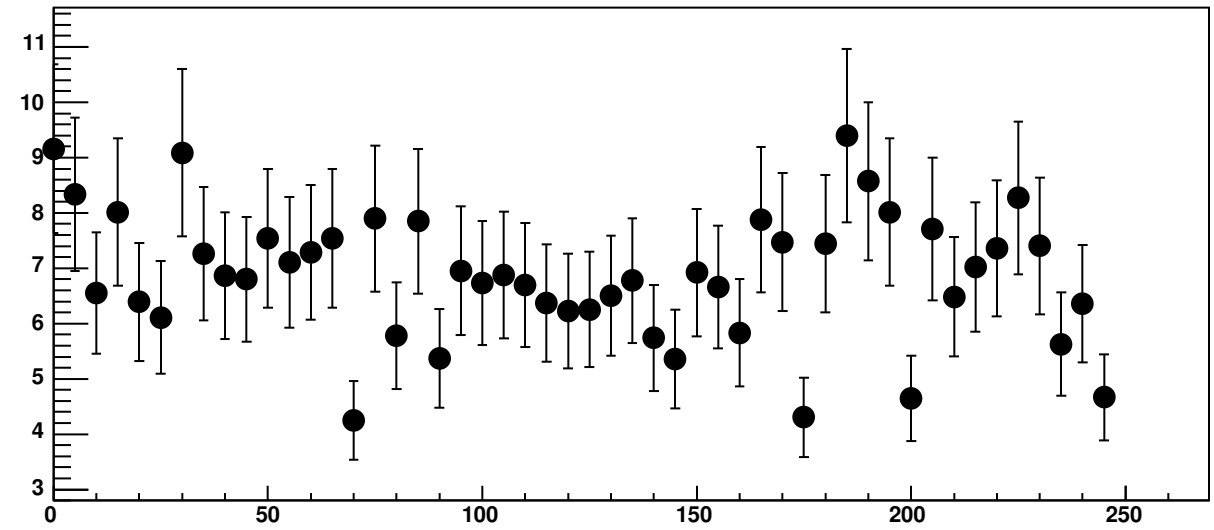
Chip 5, Channel 9, Enable 3!, DAC=4000, ADC Residuals vs Hold



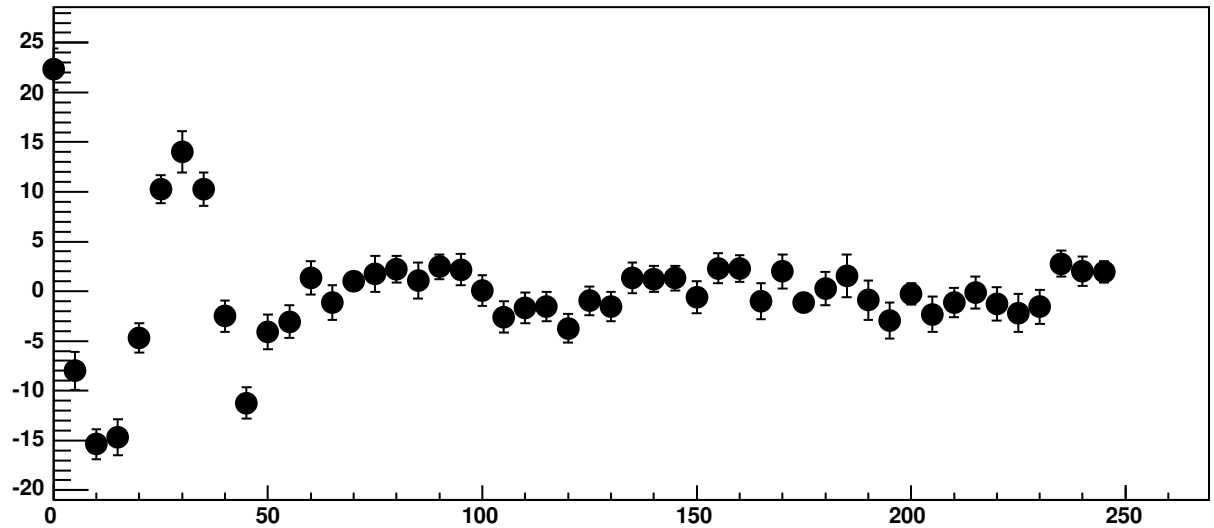
Chip 5, Channel 9, Enable 4, DAC=4000, ADC Mean vs Hold



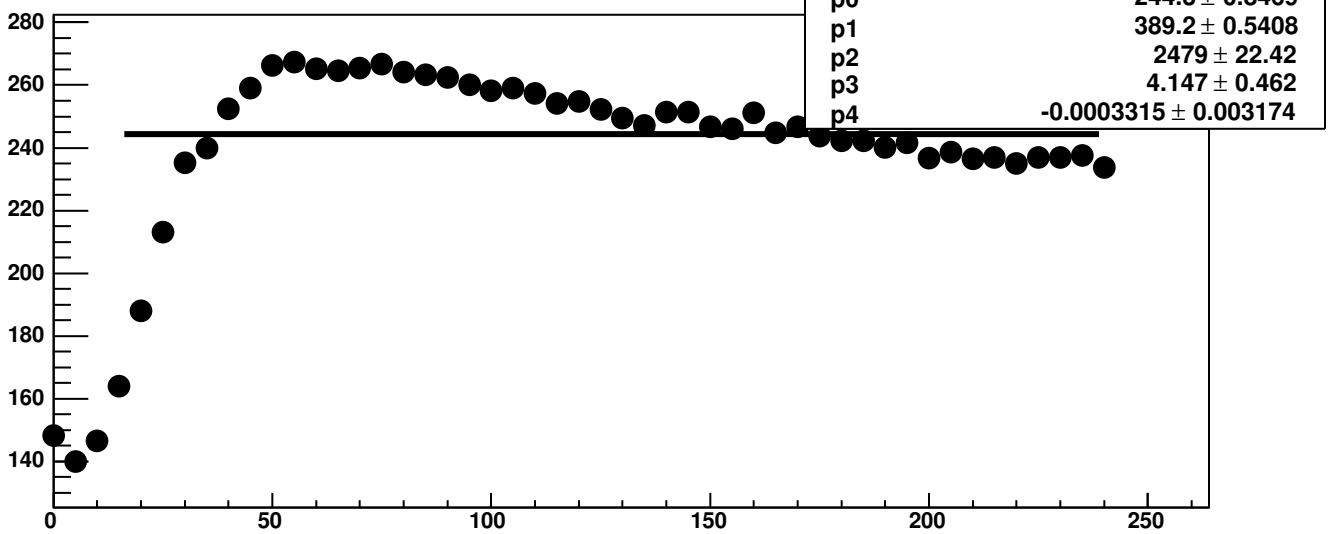
Chip 5, Channel 9, Enable 4, DAC=4000, ADC Noise vs Hold



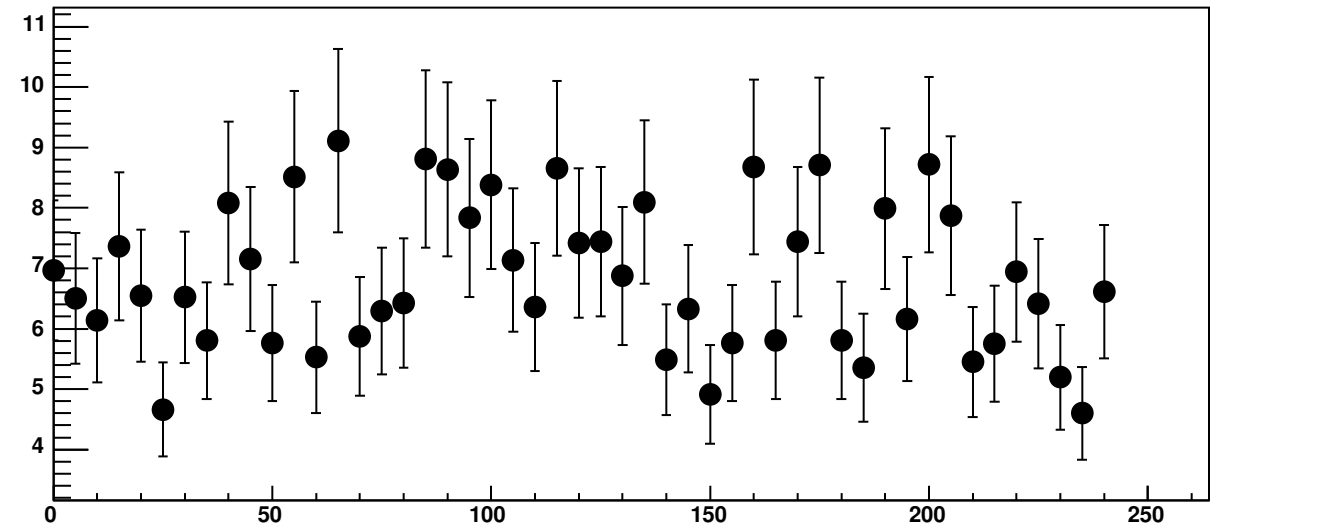
Chip 5, Channel 9, Enable 4, DAC=4000, ADC Residuals vs Hold



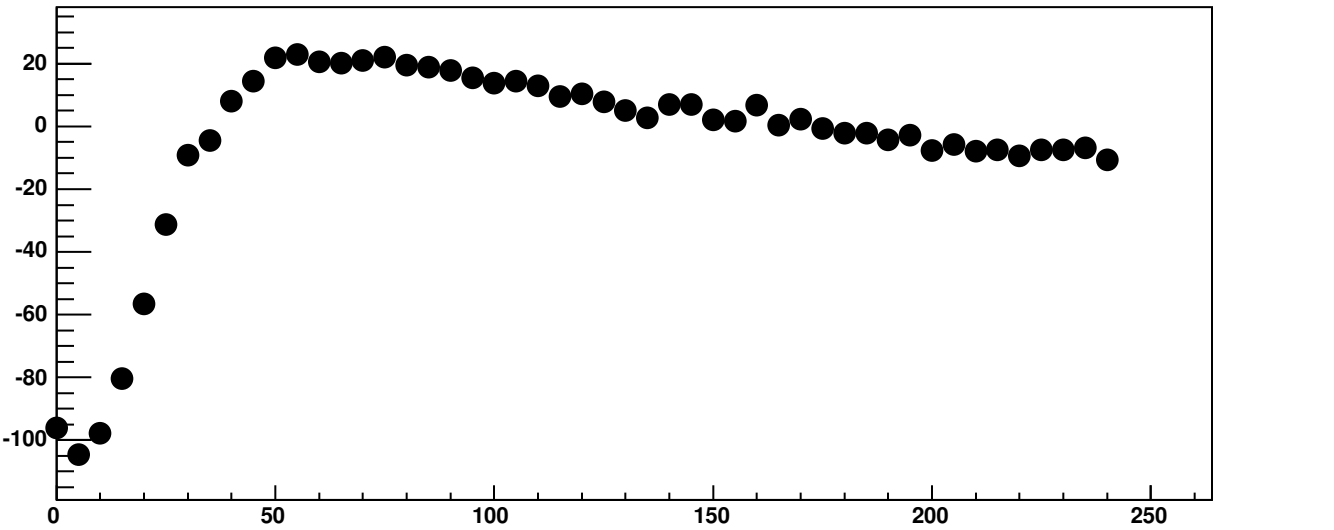
Chip 5, Channel 9, Enable 5, DAC=4000, ADC Mean vs Hold

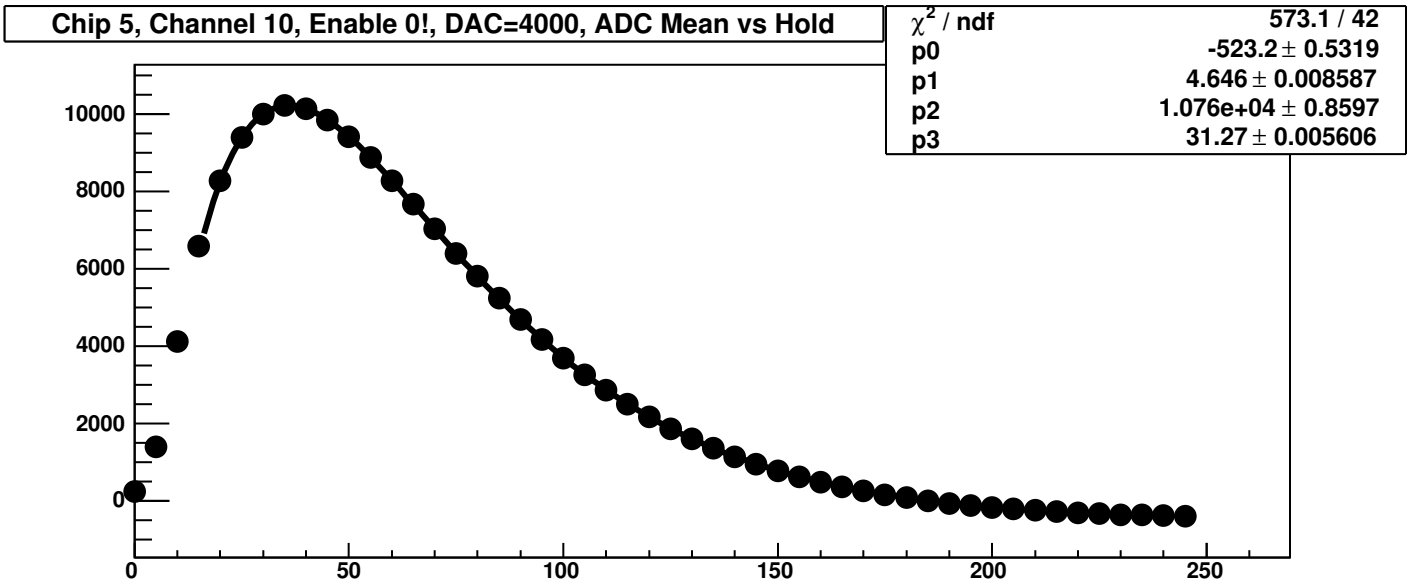


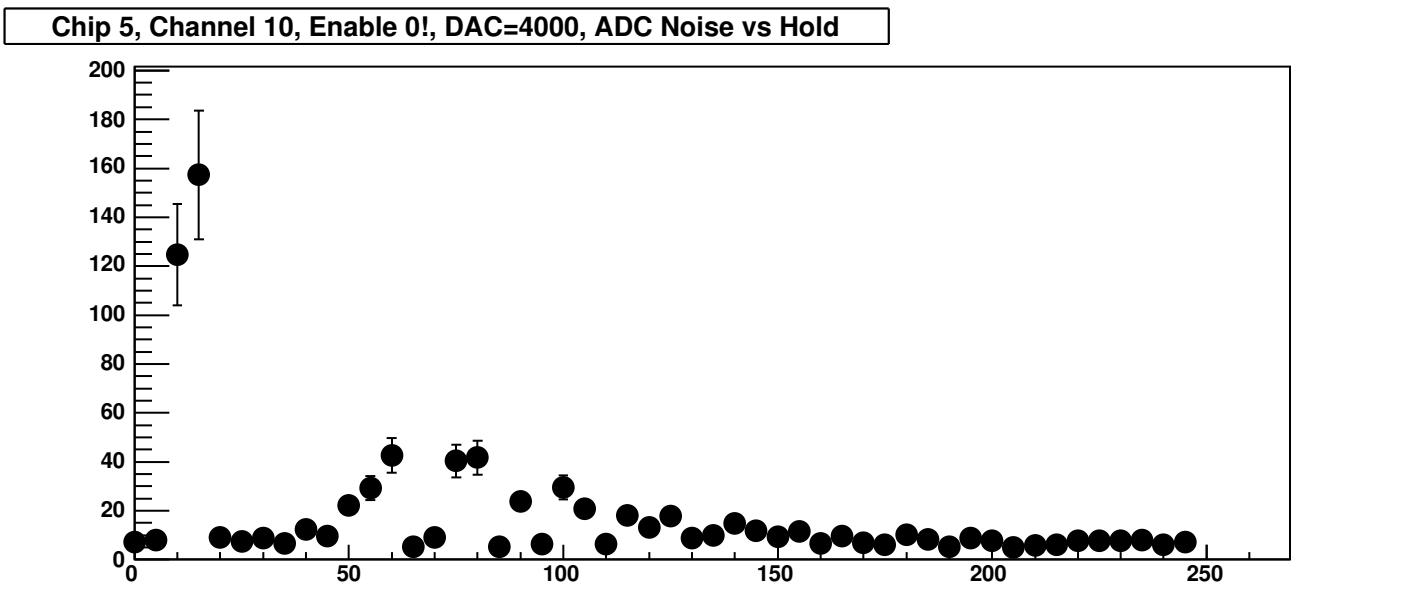
Chip 5, Channel 9, Enable 5, DAC=4000, ADC Noise vs Hold

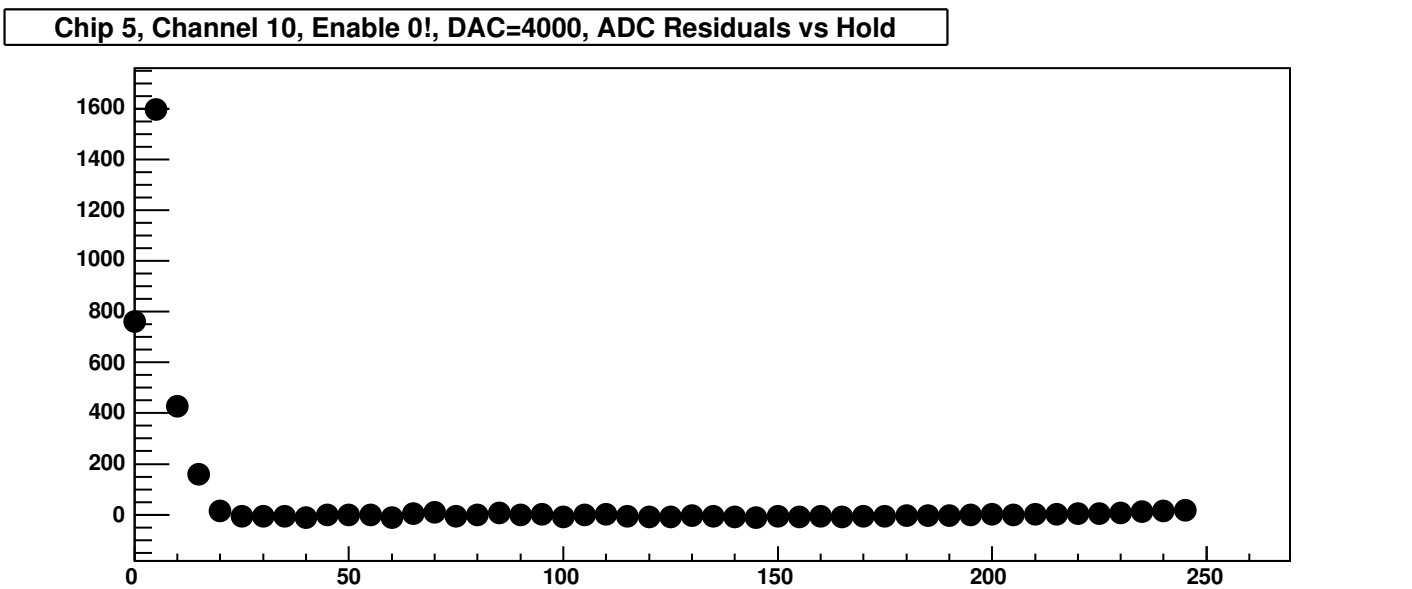


Chip 5, Channel 9, Enable 5, DAC=4000, ADC Residuals vs Hold

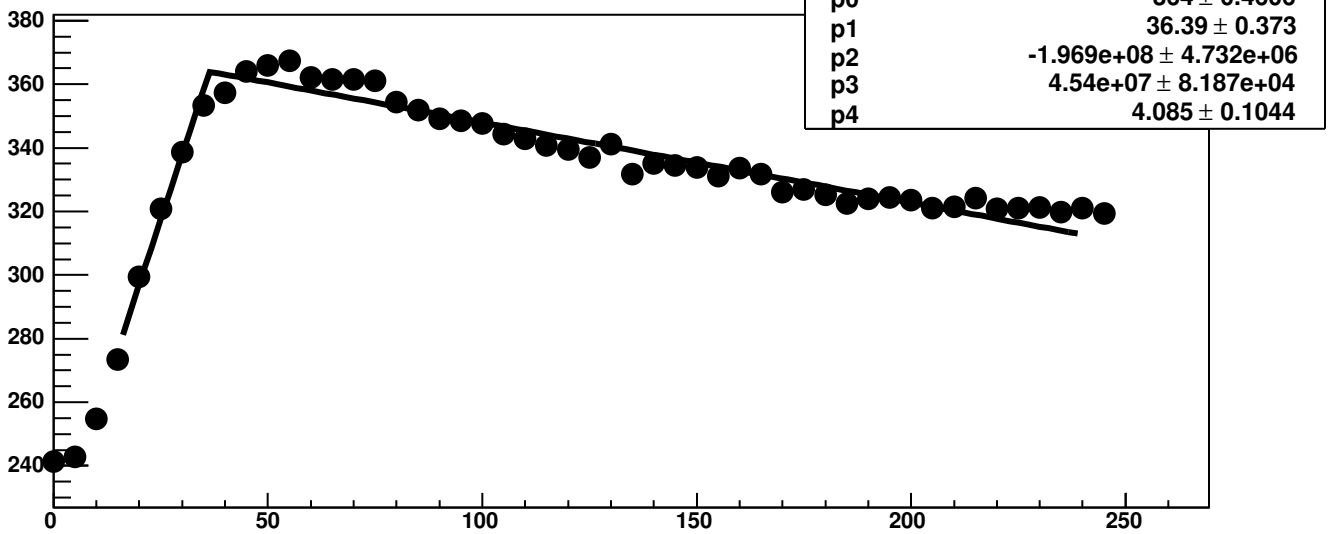



Chip 5, Channel 10, Enable 0!, DAC=4000, ADC Mean vs Hold
χ² / ndf 573.1 / 42
p0 -523.2 ± 0.5319
p1 4.646 ± 0.008587
p2 1.076e+04 ± 0.8597
p3 31.27 ± 0.005606
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

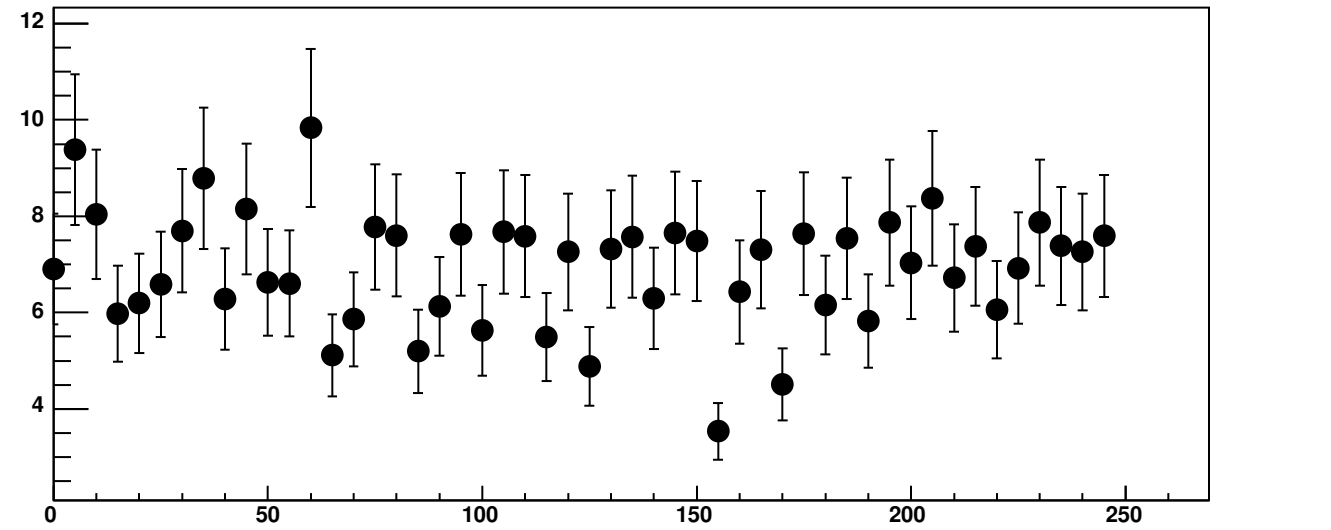

Chip 5, Channel 10, Enable 0!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 5, Channel 10, Enable 0!, DAC=4000, ADC Residuals vs Hold
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

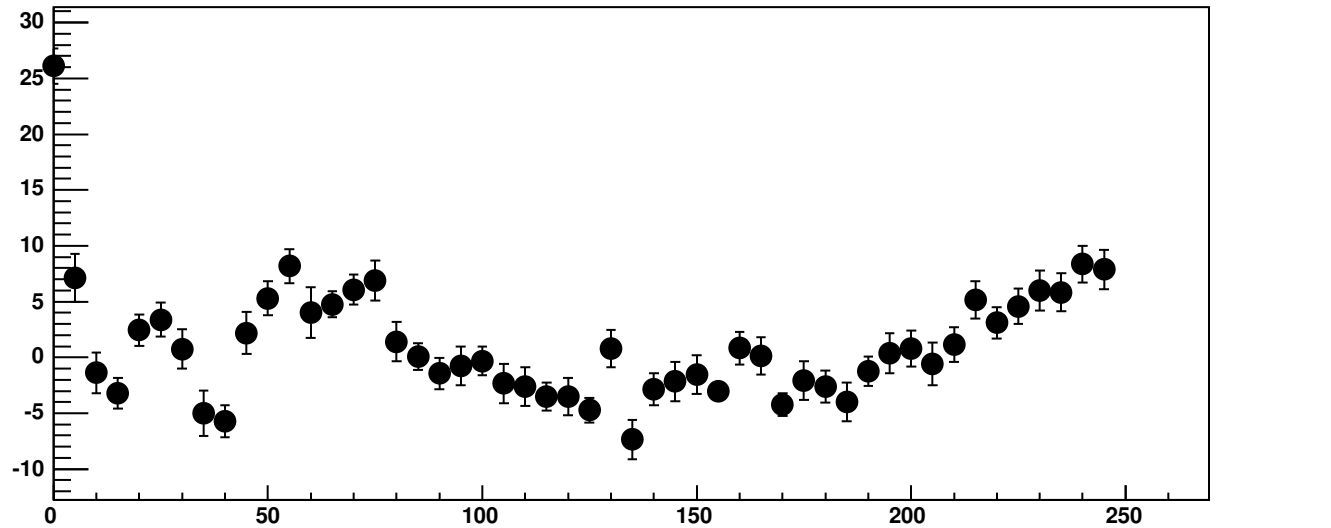
Chip 5, Channel 10, Enable 1, DAC=4000, ADC Mean vs Hold



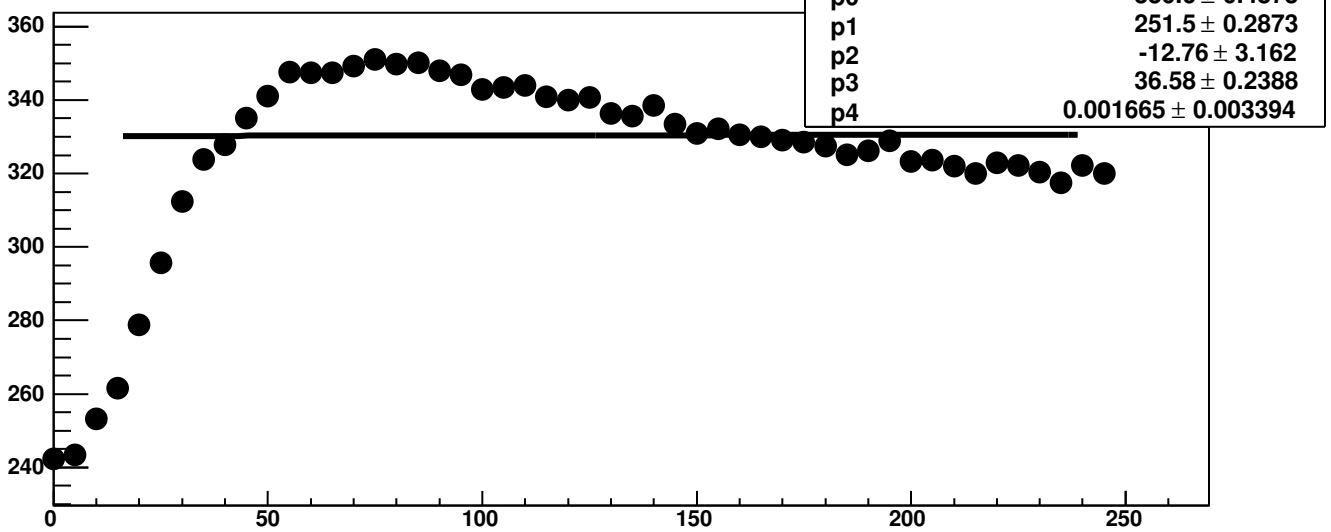
Chip 5, Channel 10, Enable 1, DAC=4000, ADC Noise vs Hold



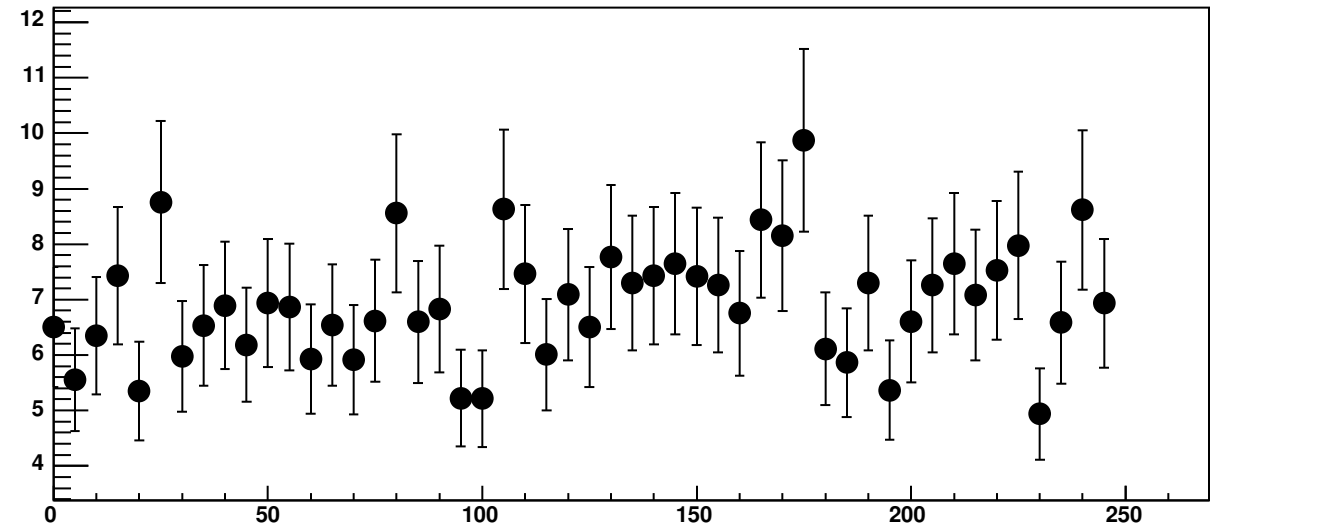
Chip 5, Channel 10, Enable 1, DAC=4000, ADC Residuals vs Hold



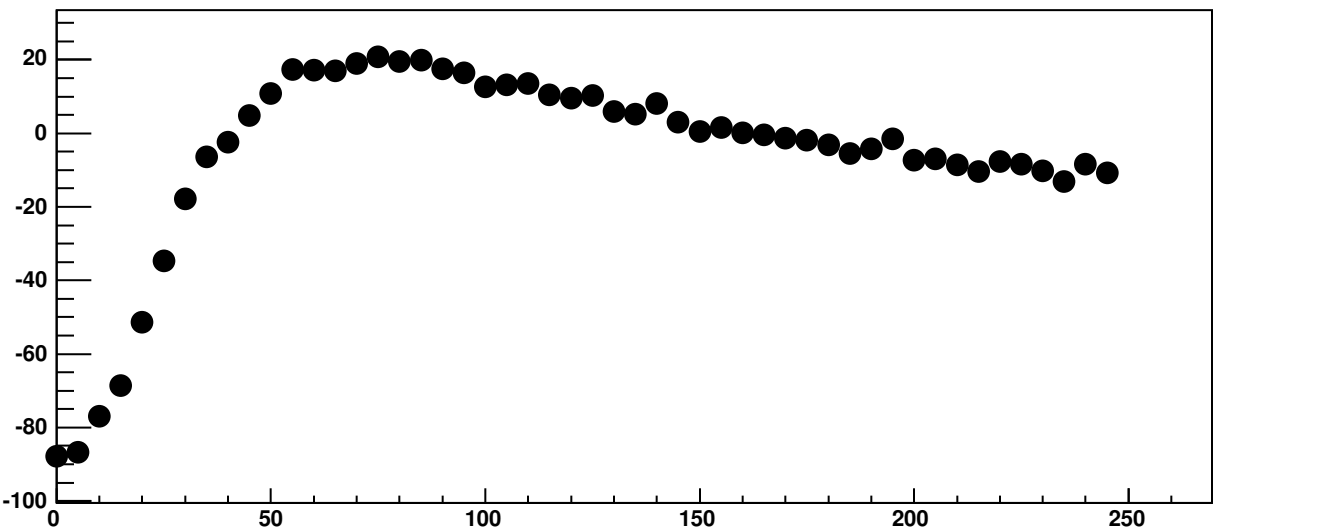
Chip 5, Channel 10, Enable 2, DAC=4000, ADC Mean vs Hold



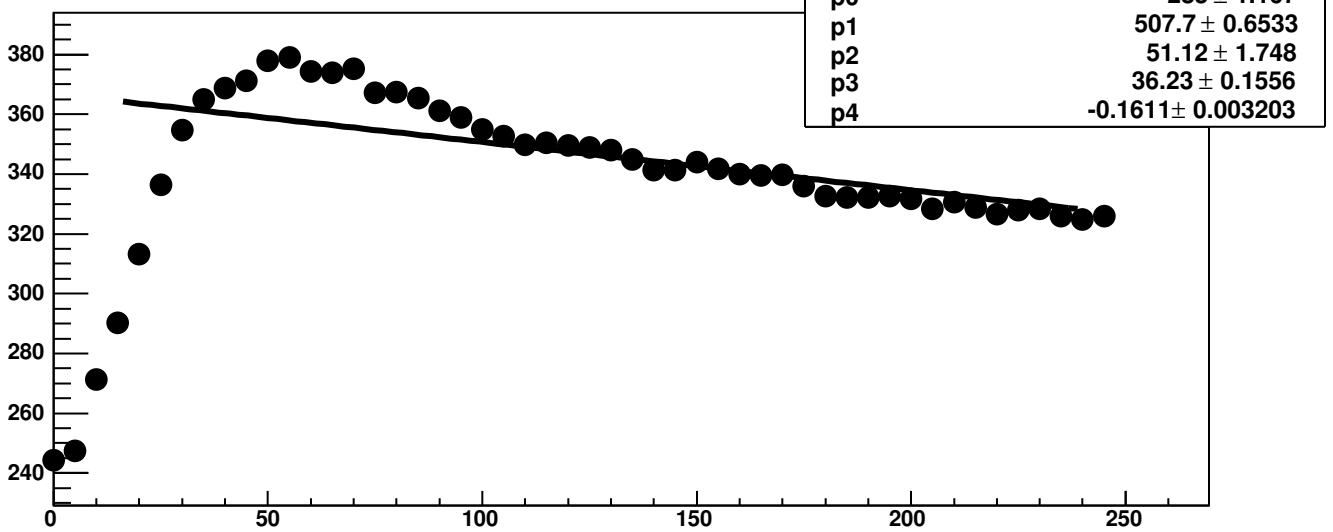
Chip 5, Channel 10, Enable 2, DAC=4000, ADC Noise vs Hold



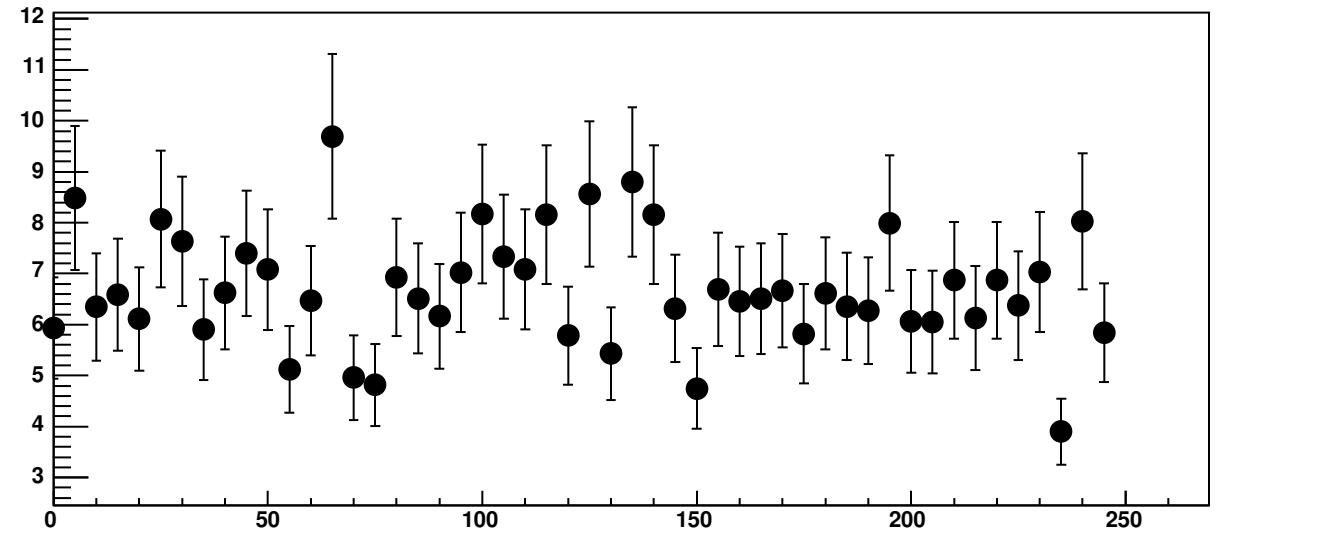
Chip 5, Channel 10, Enable 2, DAC=4000, ADC Residuals vs Hold



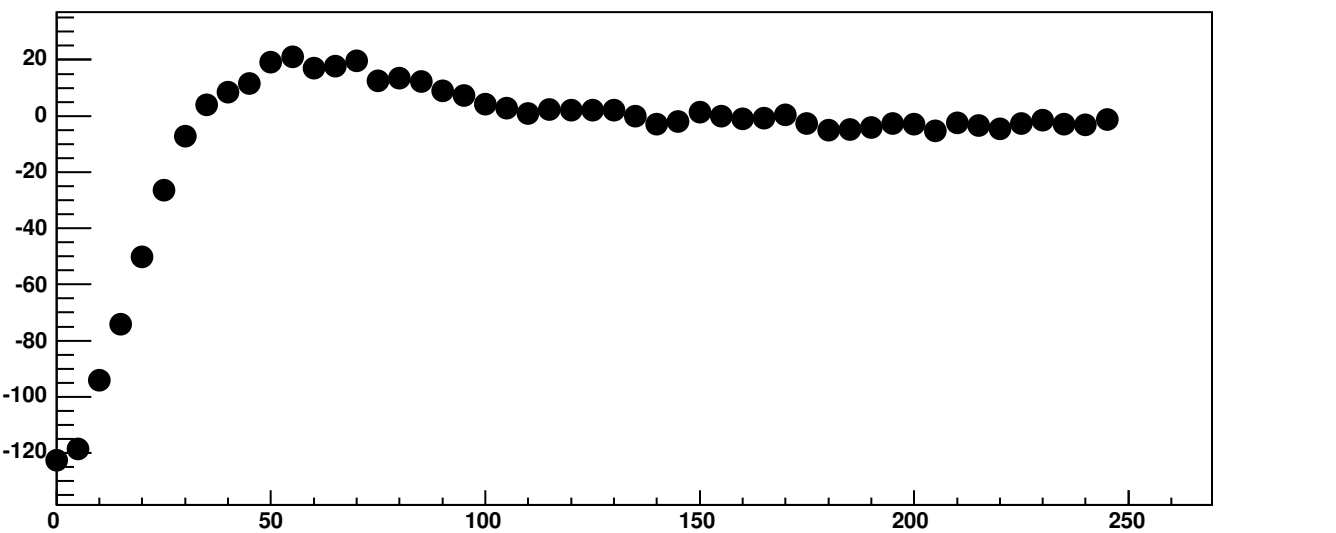
Chip 5, Channel 10, Enable 3, DAC=4000, ADC Mean vs Hold

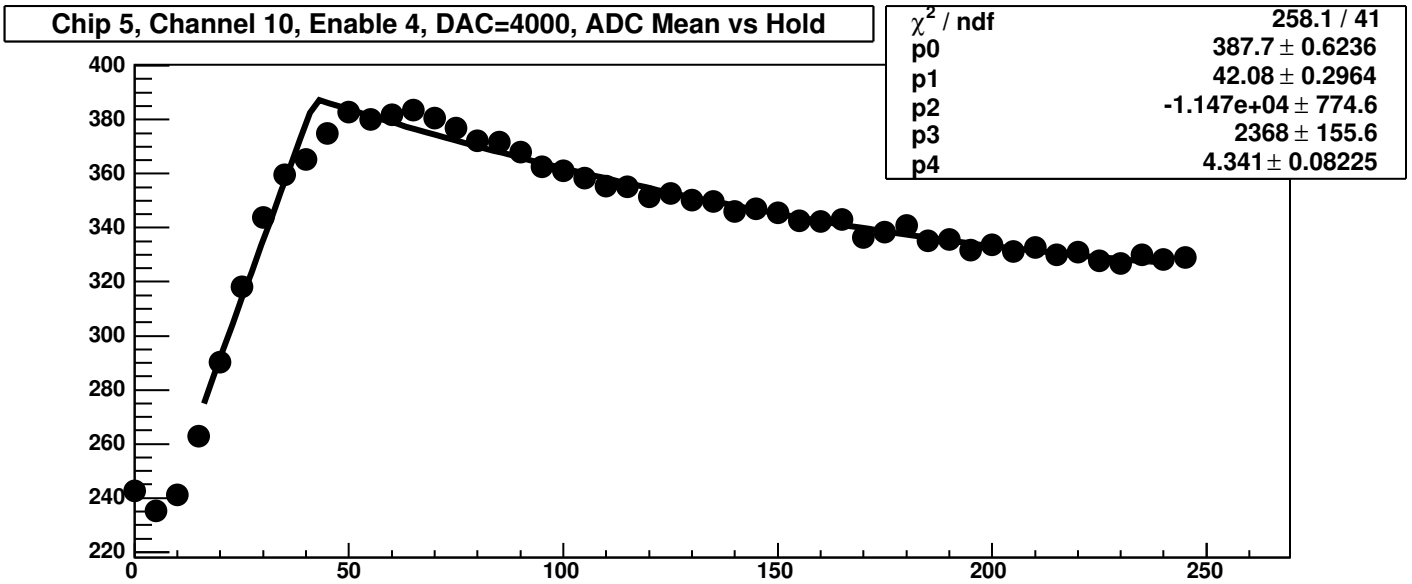


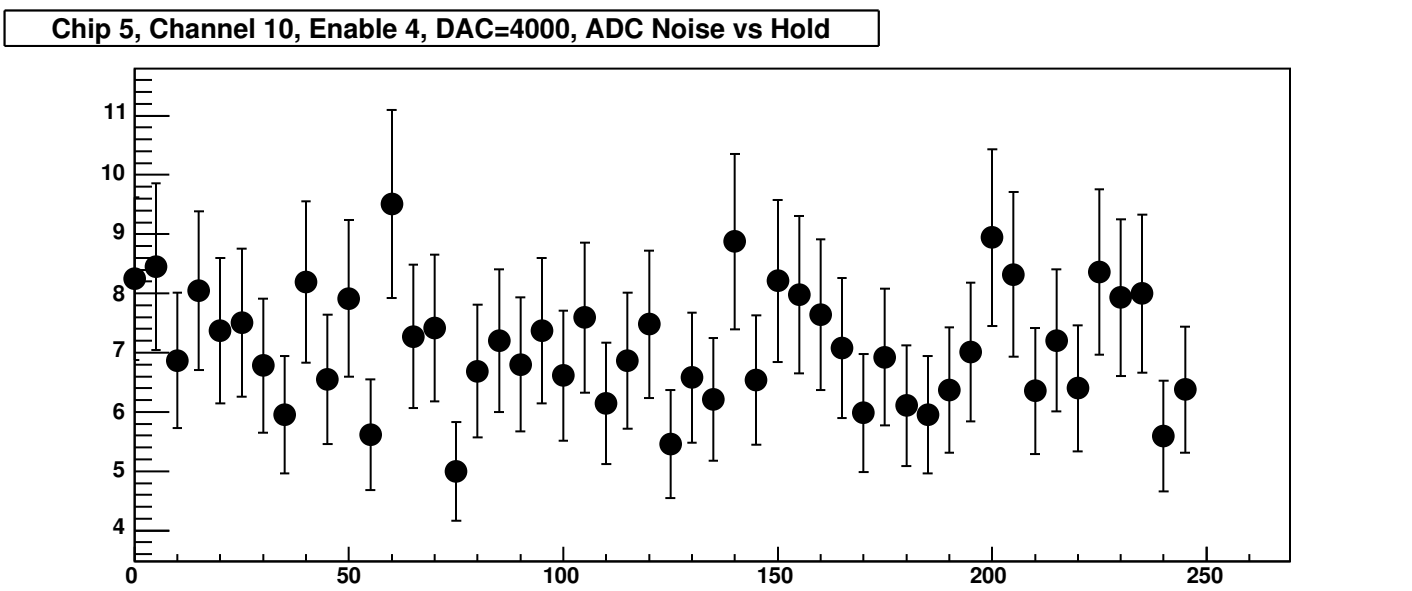
Chip 5, Channel 10, Enable 3, DAC=4000, ADC Noise vs Hold

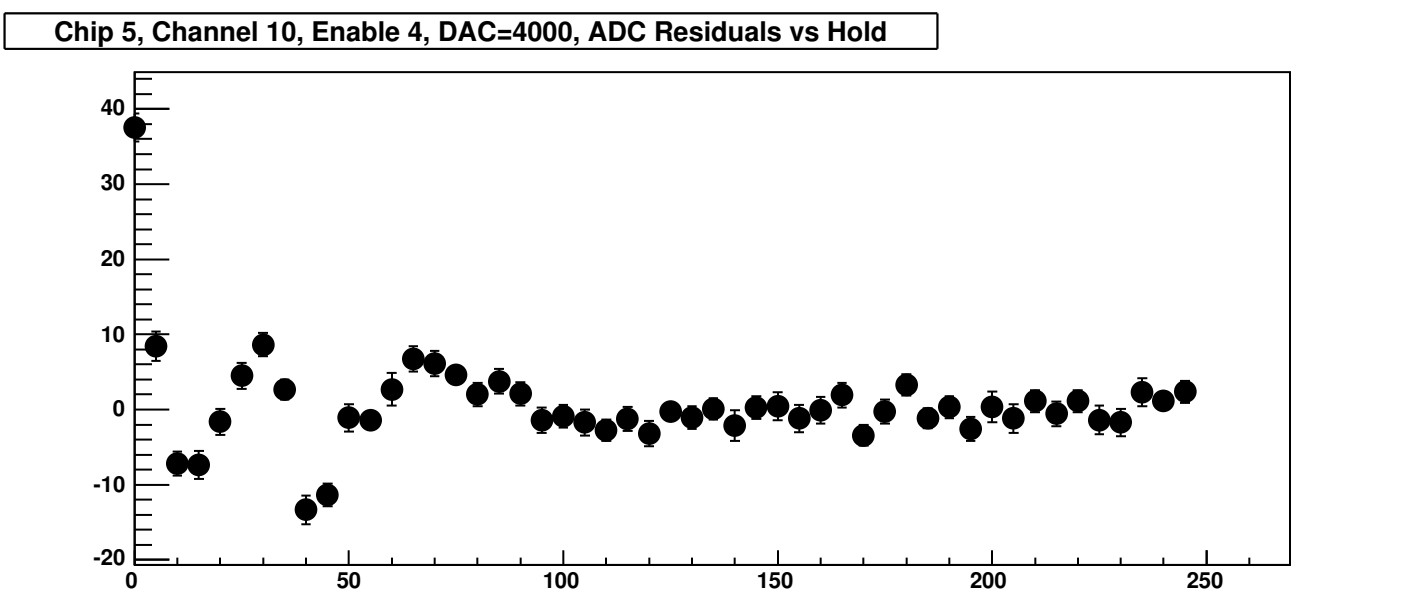


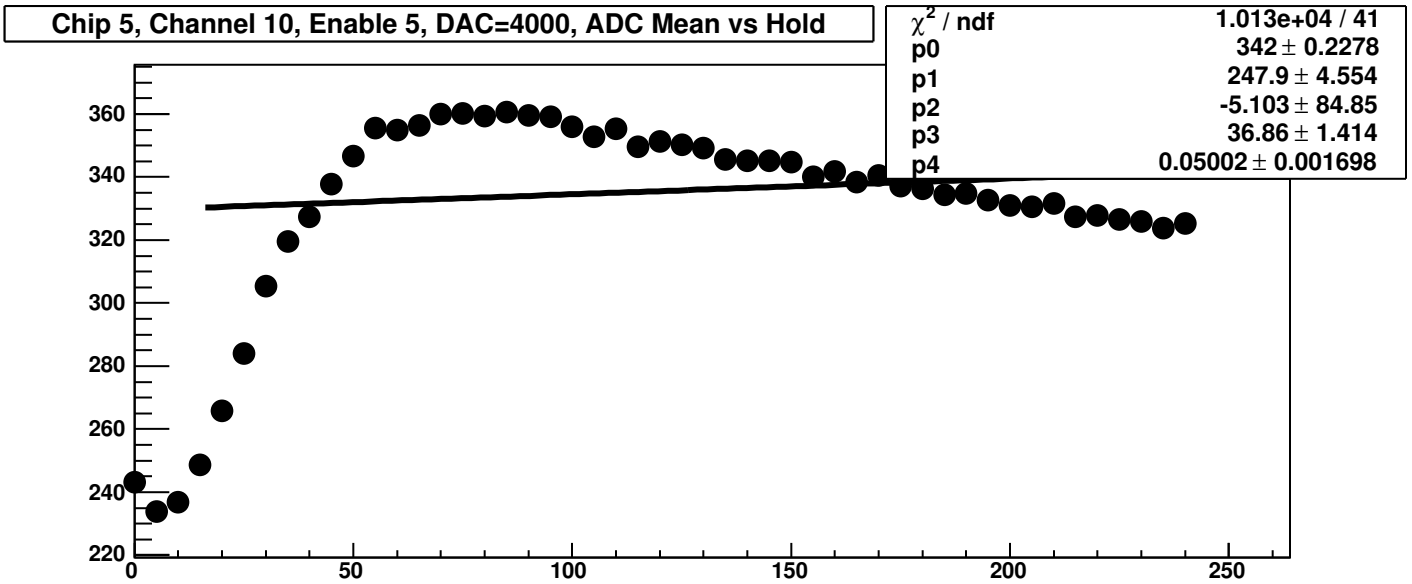
Chip 5, Channel 10, Enable 3, DAC=4000, ADC Residuals vs Hold

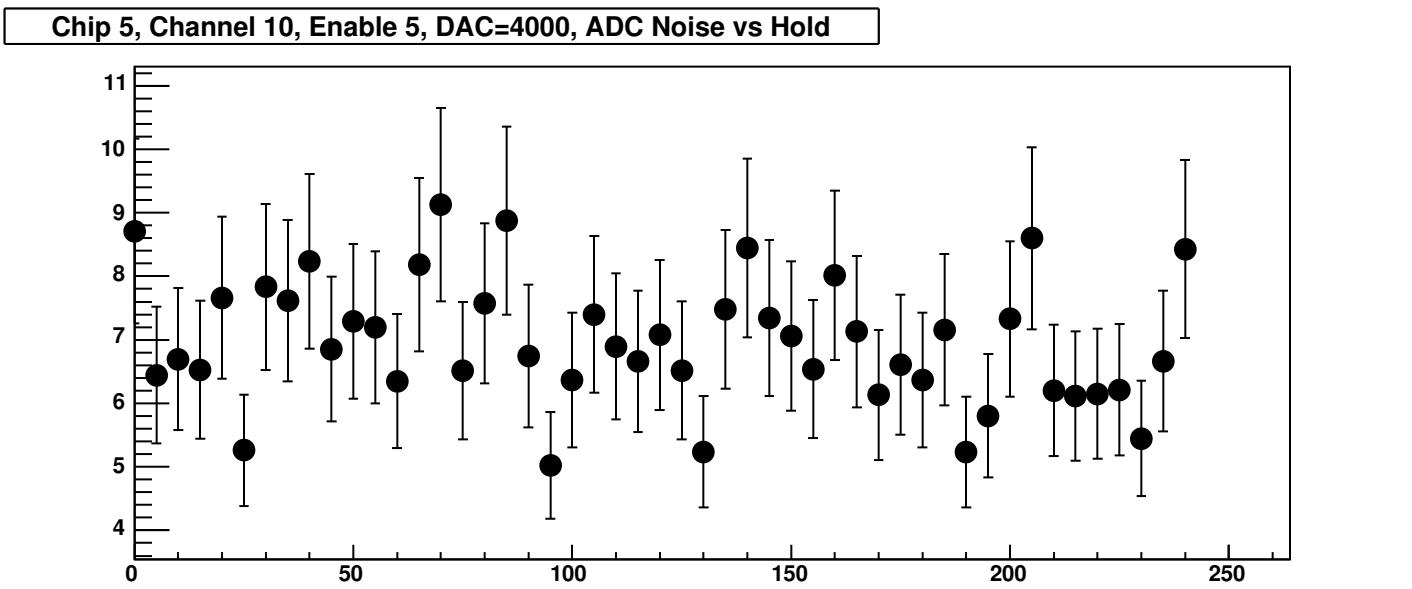


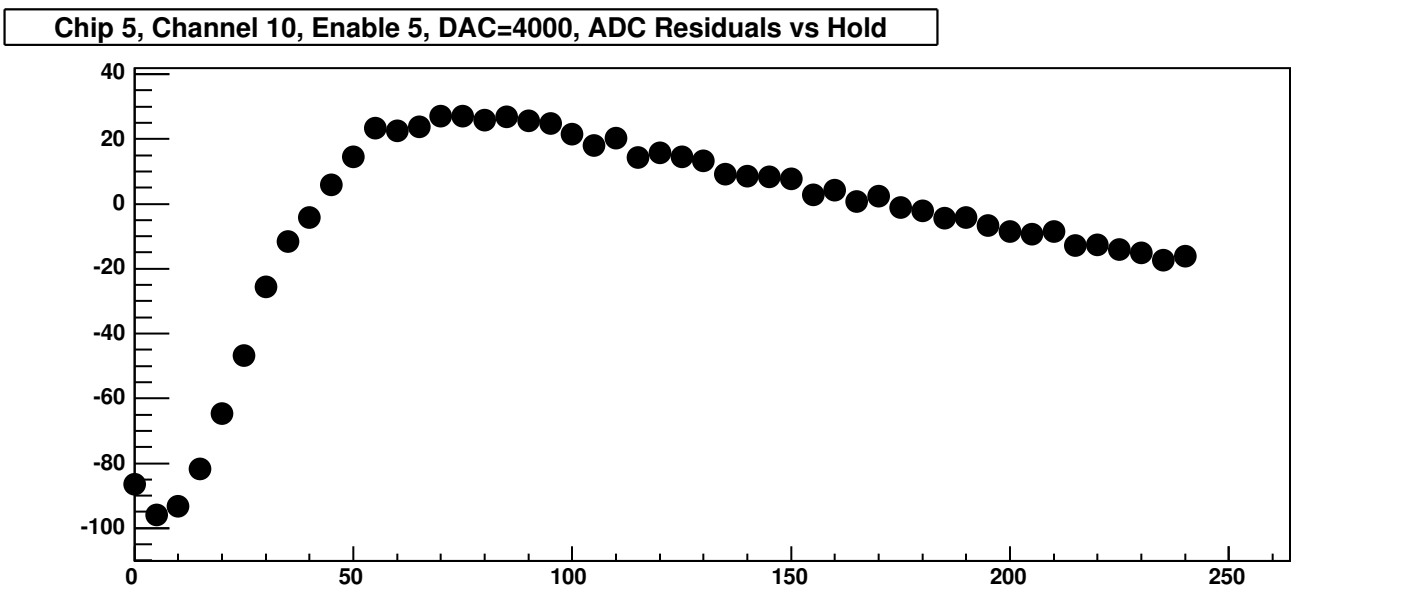

Chip 5, Channel 10, Enable 4, DAC=4000, ADC Mean vs Hold
χ² / ndf 258.1 / 41
p0 387.7 ± 0.6236
p1 42.08 ± 0.2964
p2 -1.147e+04 ± 774.6
p3 2368 ± 155.6
p4 4.341 ± 0.08225
400
380
360
340
320
300
280
260
240
220
0
50
100
150
200
250


Chip 5, Channel 10, Enable 4, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250

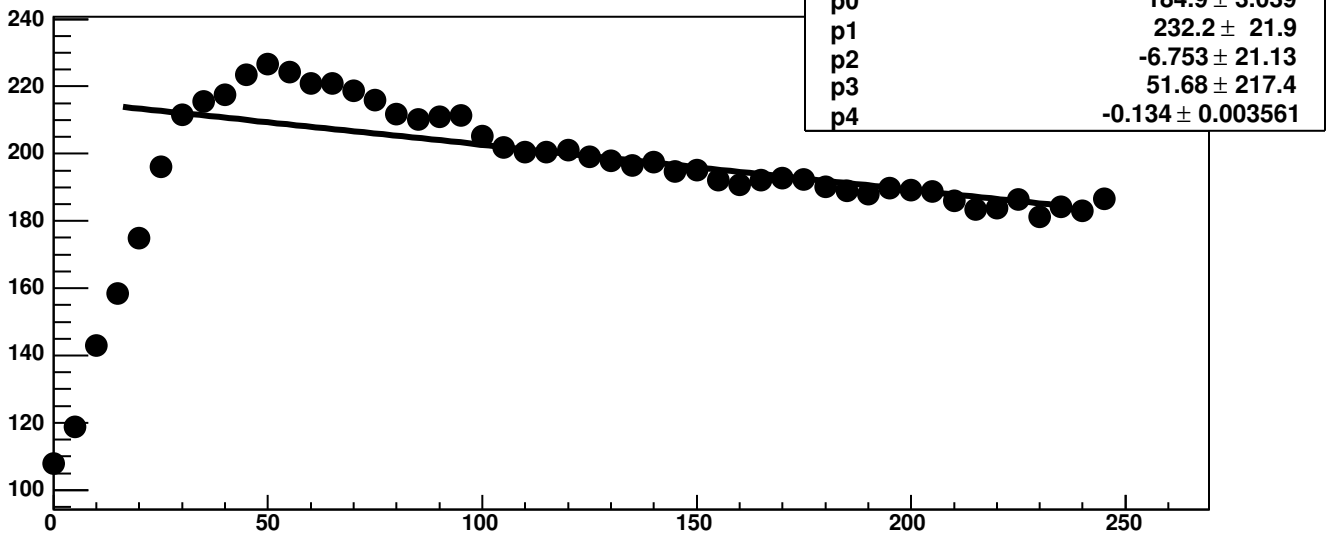

Chip 5, Channel 10, Enable 4, DAC=4000, ADC Residuals vs Hold
40
30
20
10
0
-10
-20
0
50
100
150
200
250


Chip 5, Channel 10, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.013e+04 / 41
p0 342 ± 0.2278
p1 247.9 ± 4.554
p2 -5.103 ± 84.85
p3 36.86 ± 1.414
p4 0.05002 ± 0.001698
360
340
320
300
280
260
240
220
0
50
100
150
200
250

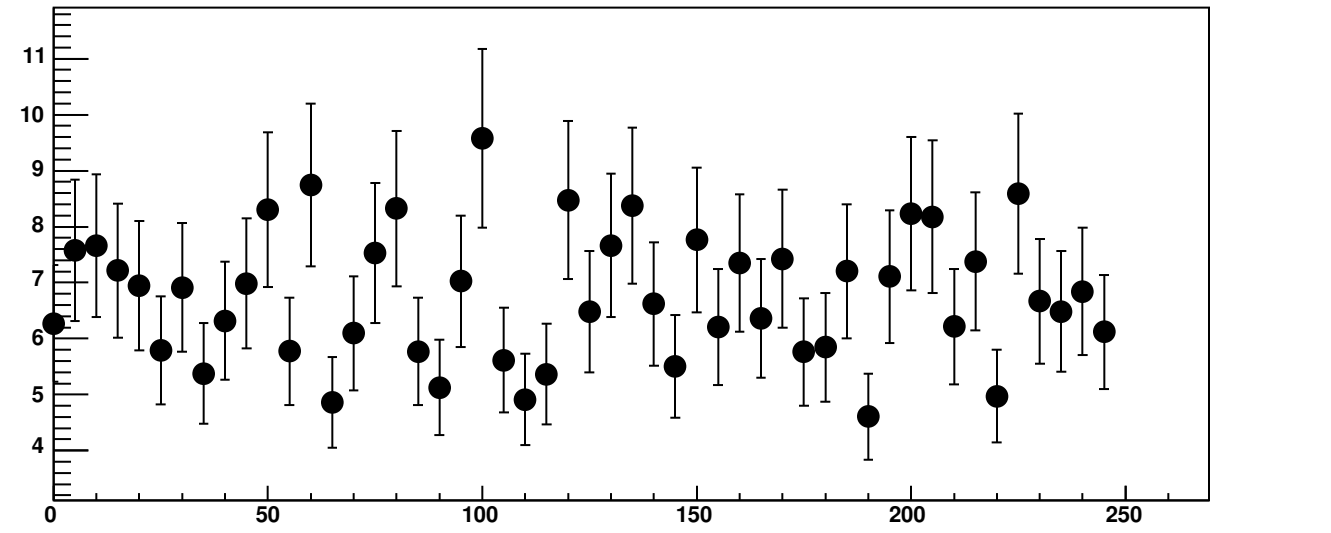

Chip 5, Channel 10, Enable 5, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 5, Channel 10, Enable 5, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
-100
0
50
100
150
200
250

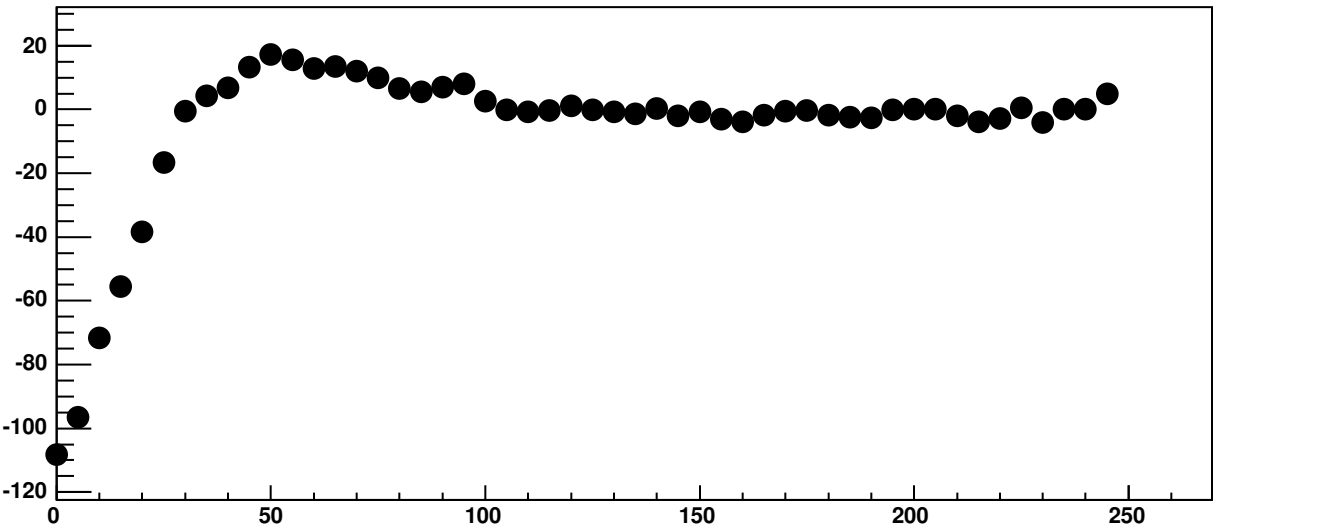
Chip 5, Channel 11, Enable 0, DAC=4000, ADC Mean vs Hold



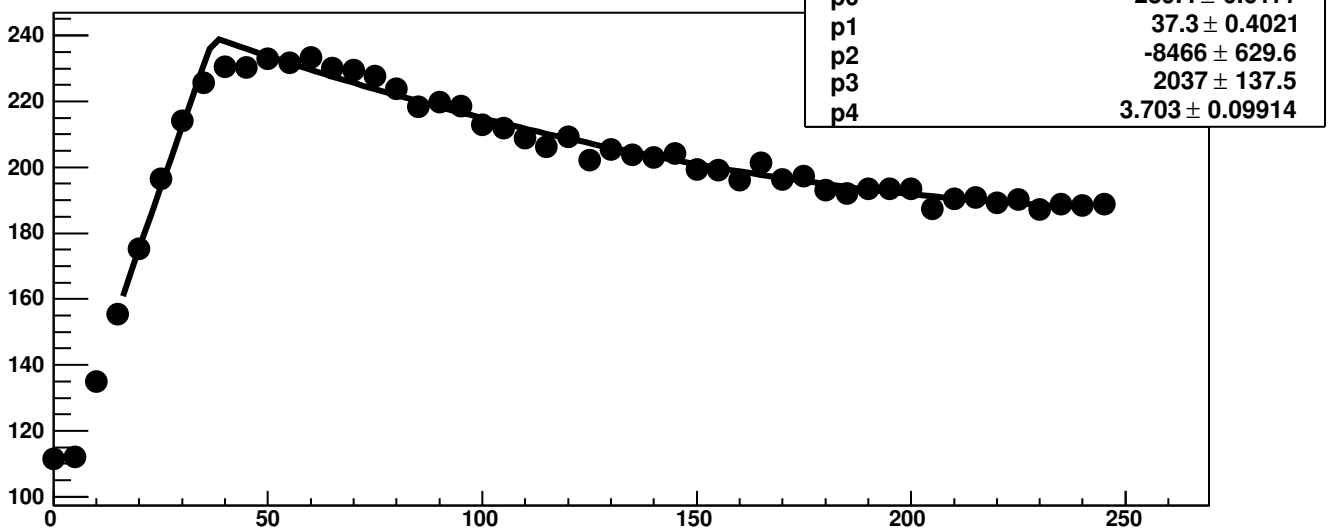
Chip 5, Channel 11, Enable 0, DAC=4000, ADC Noise vs Hold



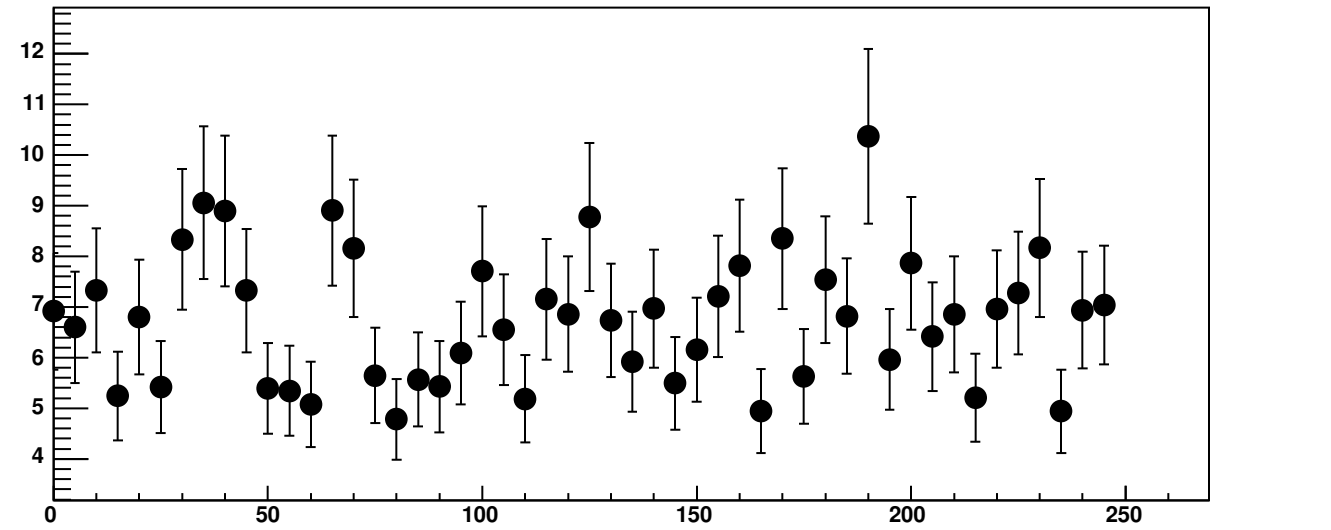
Chip 5, Channel 11, Enable 0, DAC=4000, ADC Residuals vs Hold



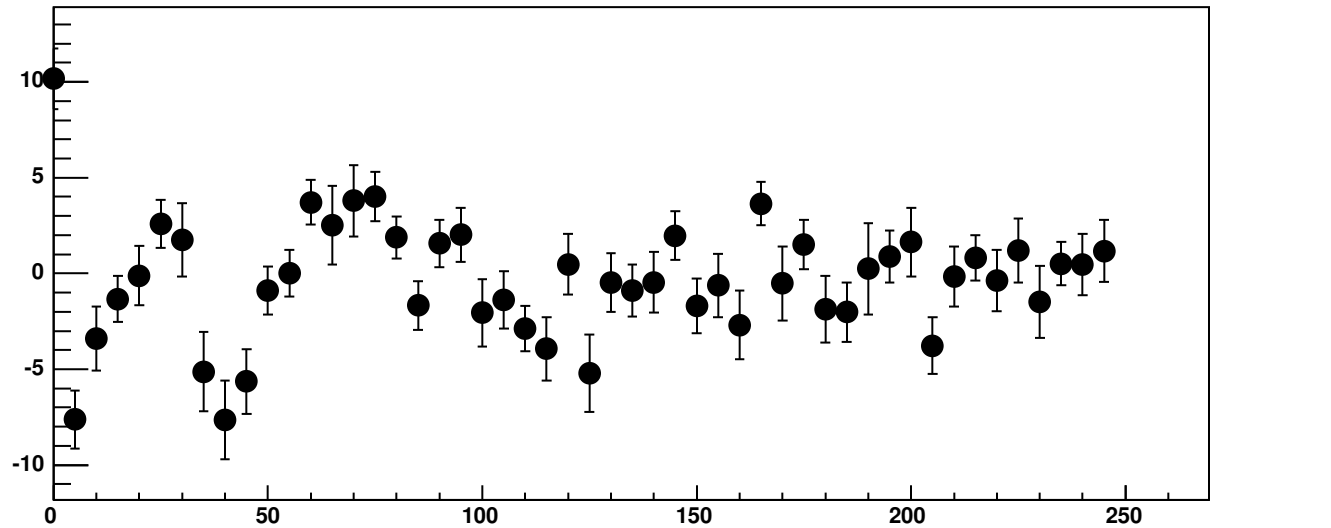
Chip 5, Channel 11, Enable 1, DAC=4000, ADC Mean vs Hold



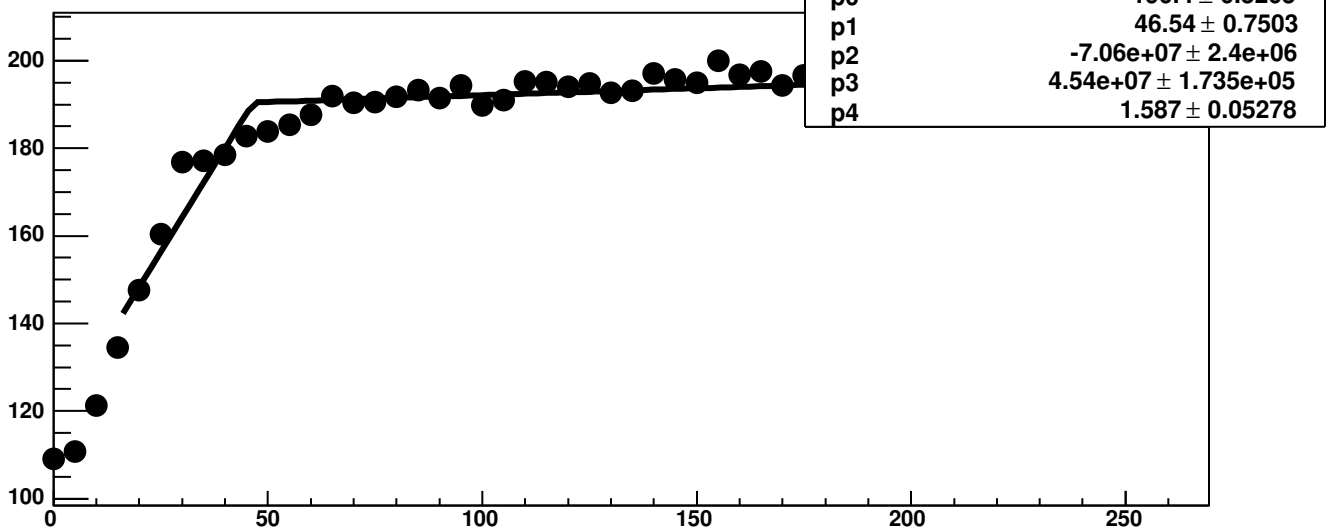
Chip 5, Channel 11, Enable 1, DAC=4000, ADC Noise vs Hold



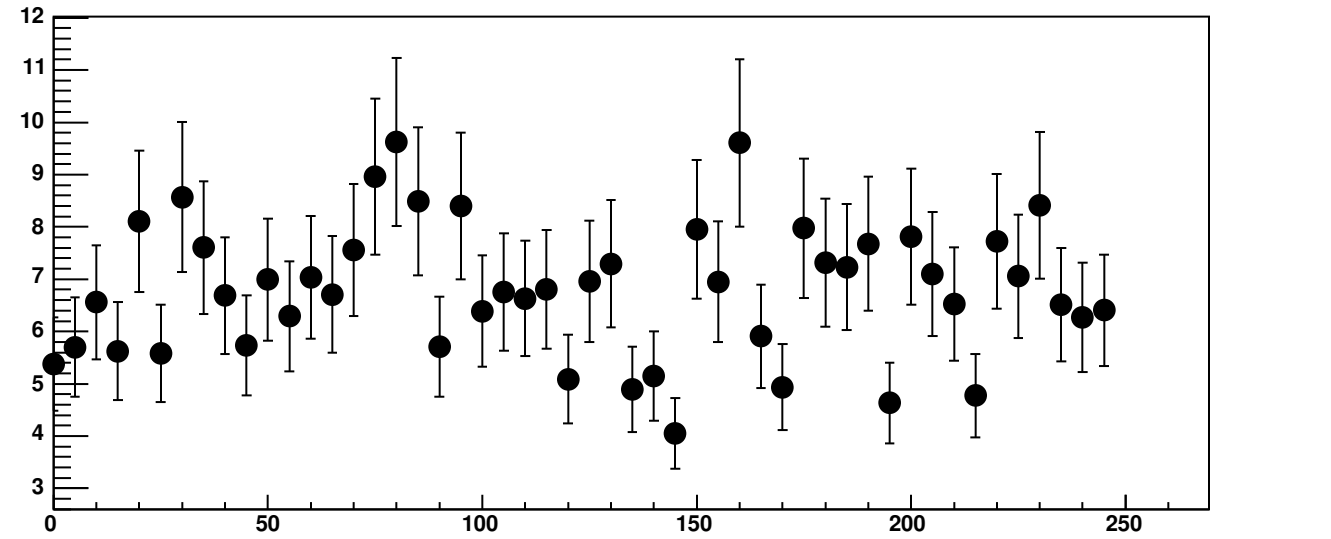
Chip 5, Channel 11, Enable 1, DAC=4000, ADC Residuals vs Hold



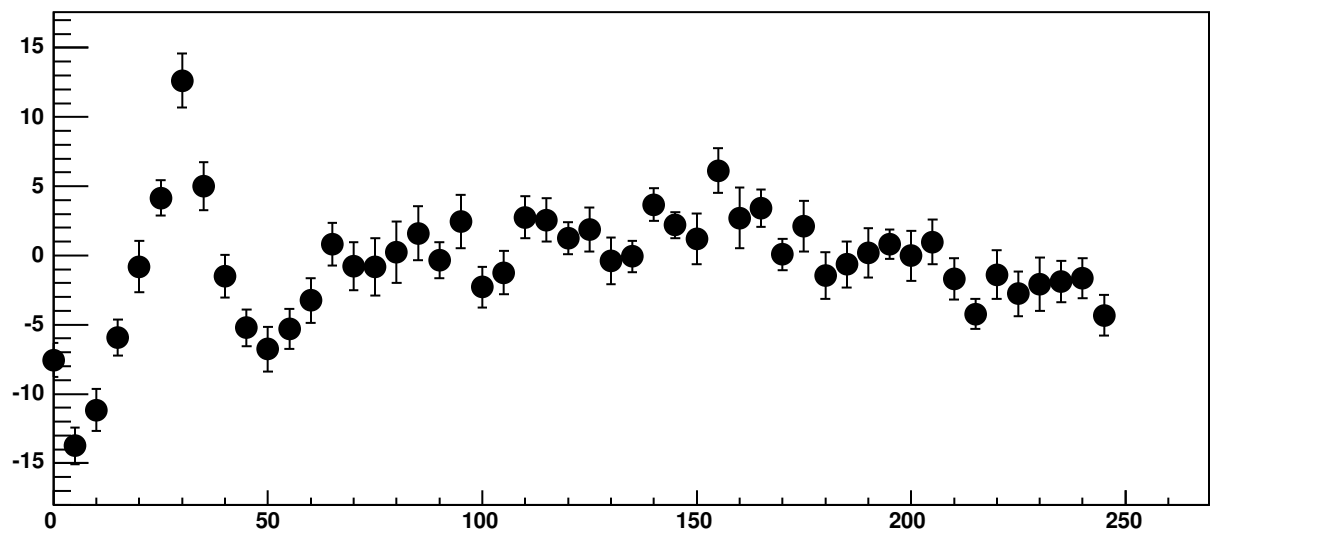
Chip 5, Channel 11, Enable 2, DAC=4000, ADC Mean vs Hold

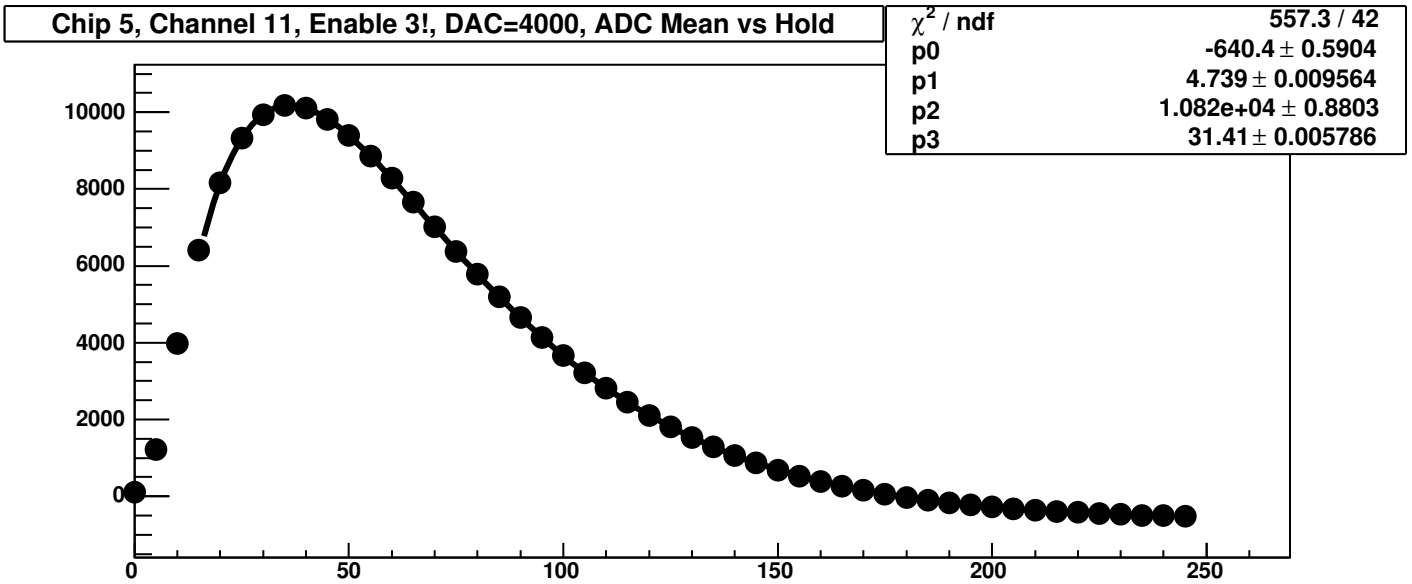


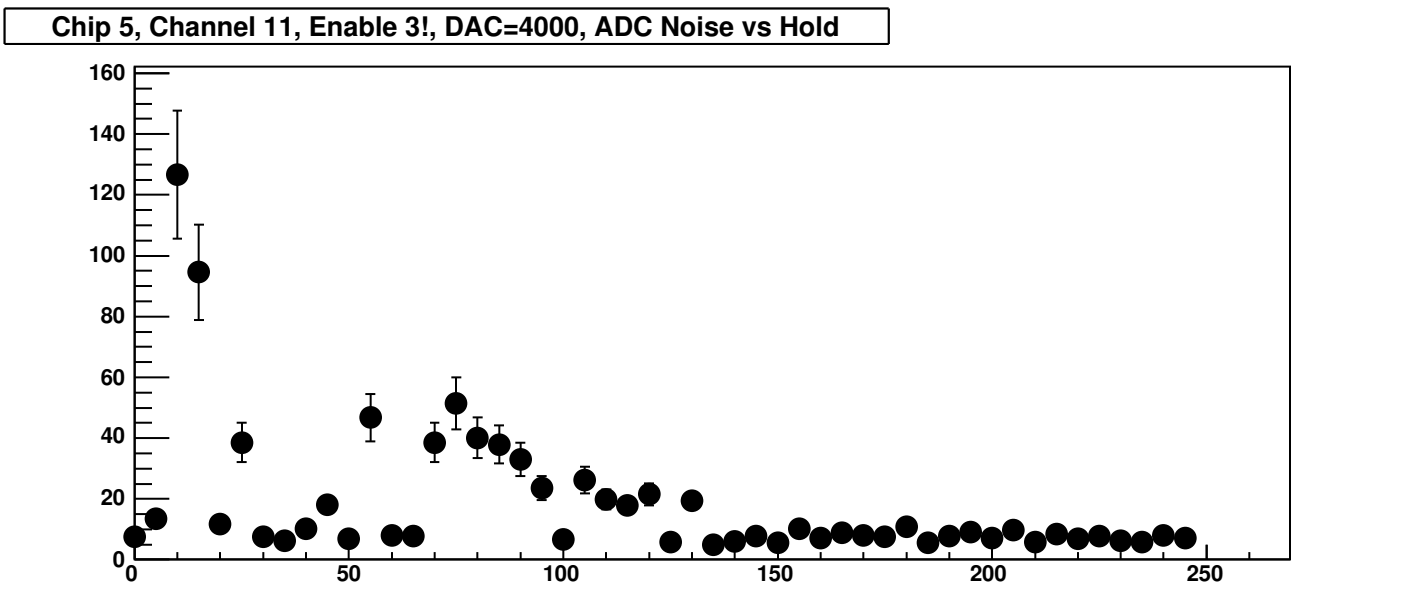
Chip 5, Channel 11, Enable 2, DAC=4000, ADC Noise vs Hold

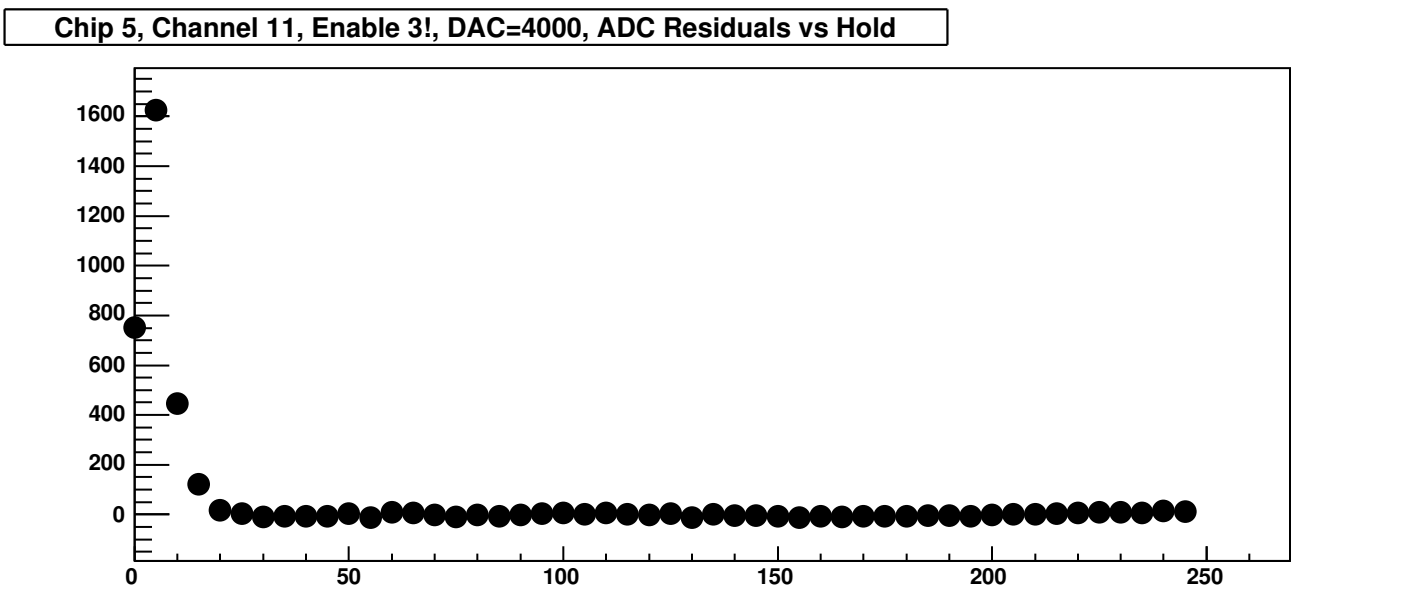


Chip 5, Channel 11, Enable 2, DAC=4000, ADC Residuals vs Hold

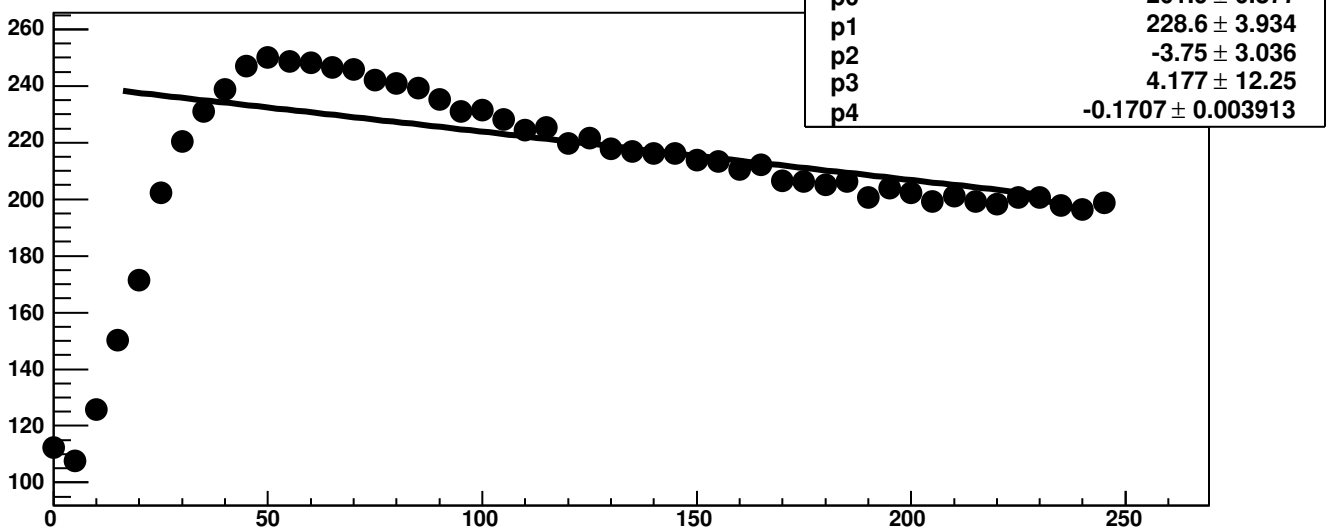



Chip 5, Channel 11, Enable 3!, DAC=4000, ADC Mean vs Hold
χ² / ndf 557.3 / 42
p0 -640.4 ± 0.5904
p1 4.739 ± 0.009564
p2 1.082e+04 ± 0.8803
p3 31.41 ± 0.005786
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

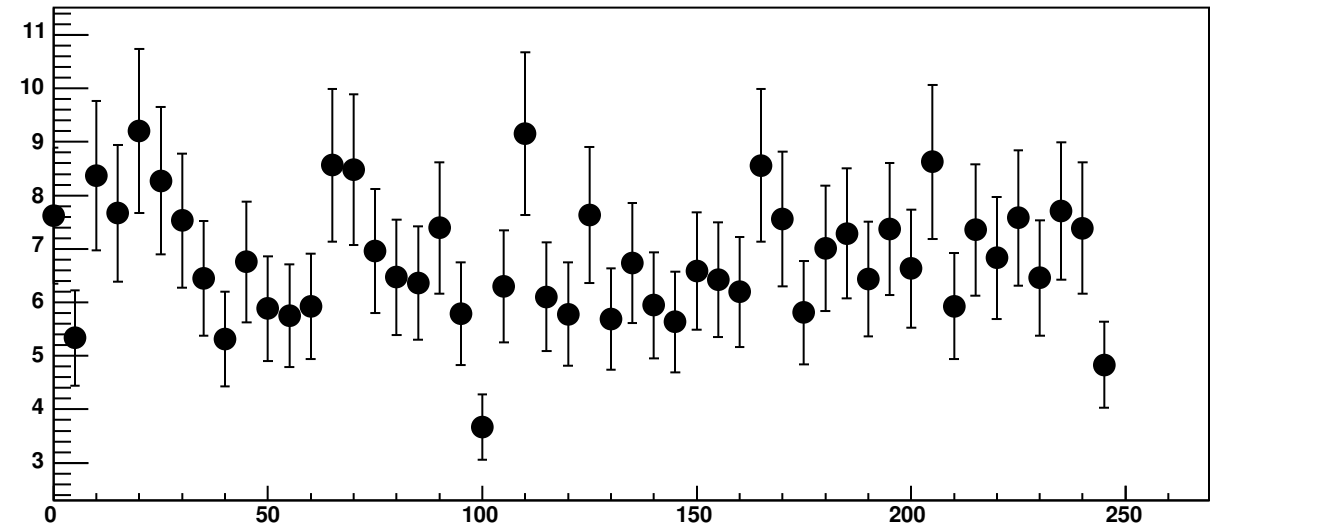

Chip 5, Channel 11, Enable 3!, DAC=4000, ADC Noise vs Hold
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250


Chip 5, Channel 11, Enable 3!, DAC=4000, ADC Residuals vs Hold
1600
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

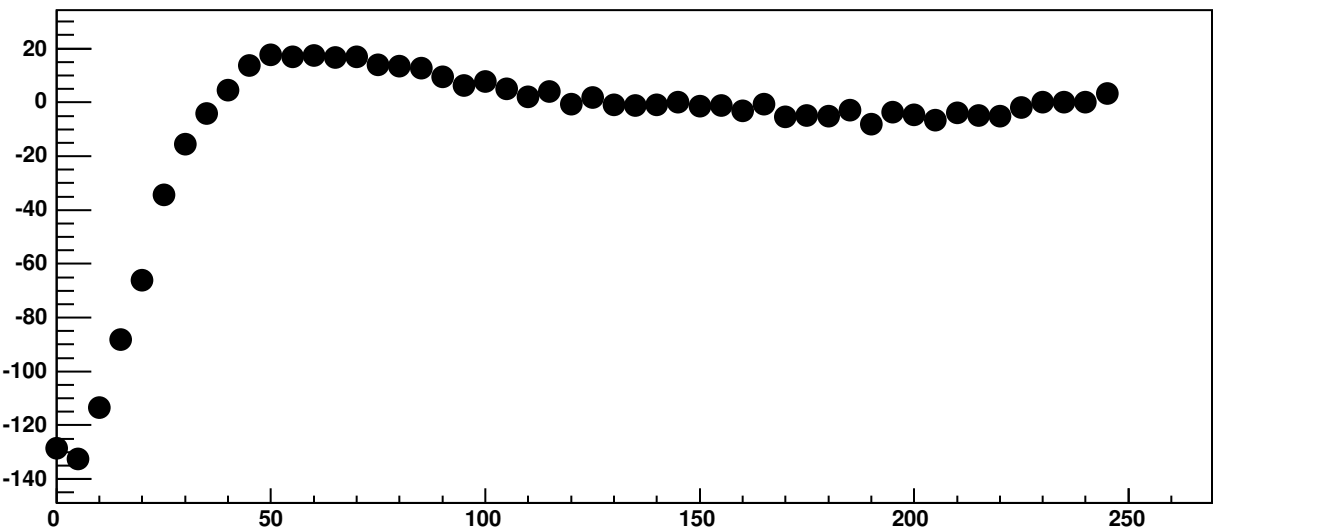
Chip 5, Channel 11, Enable 4, DAC=4000, ADC Mean vs Hold

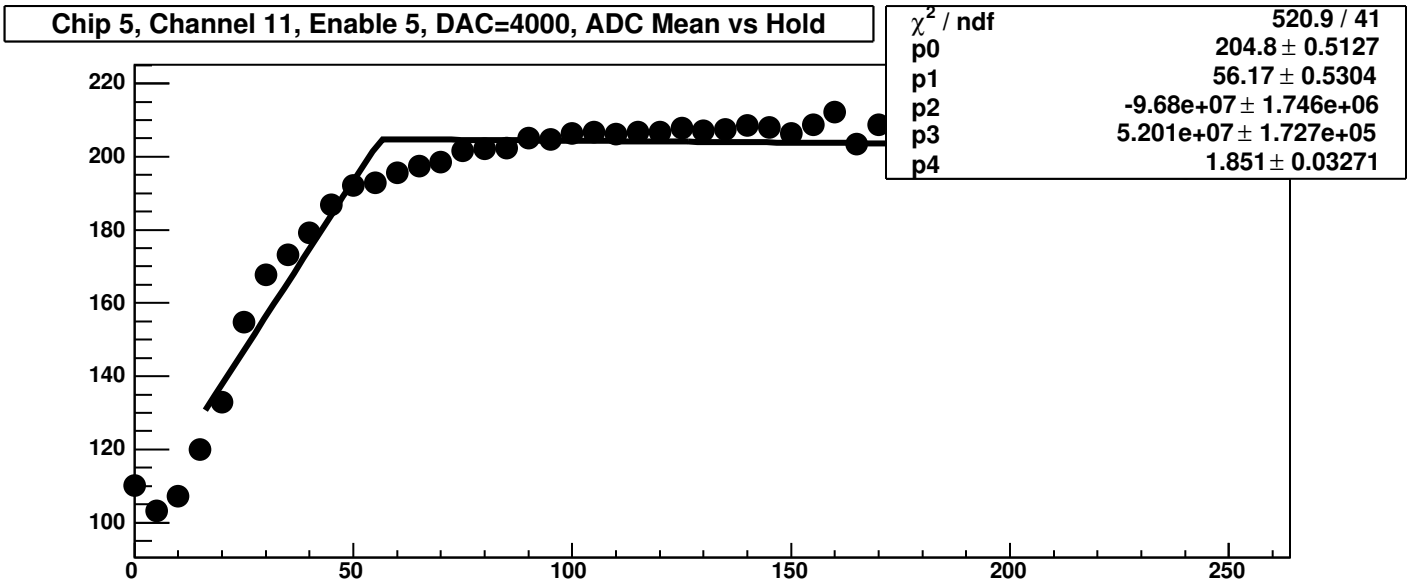


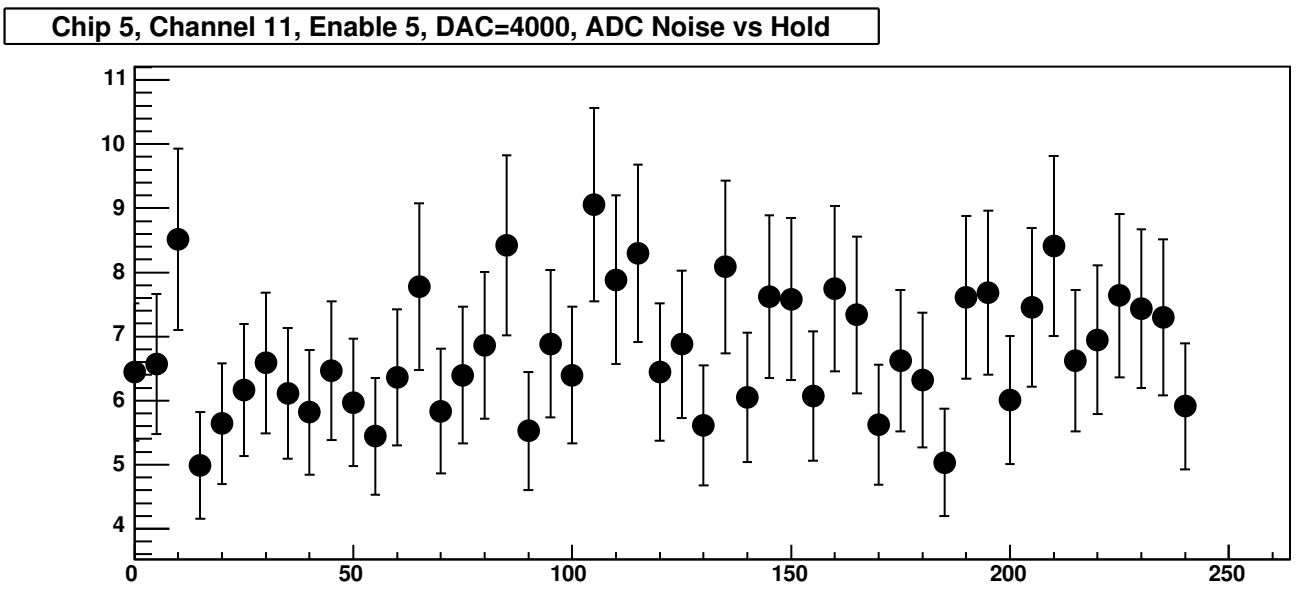
Chip 5, Channel 11, Enable 4, DAC=4000, ADC Noise vs Hold

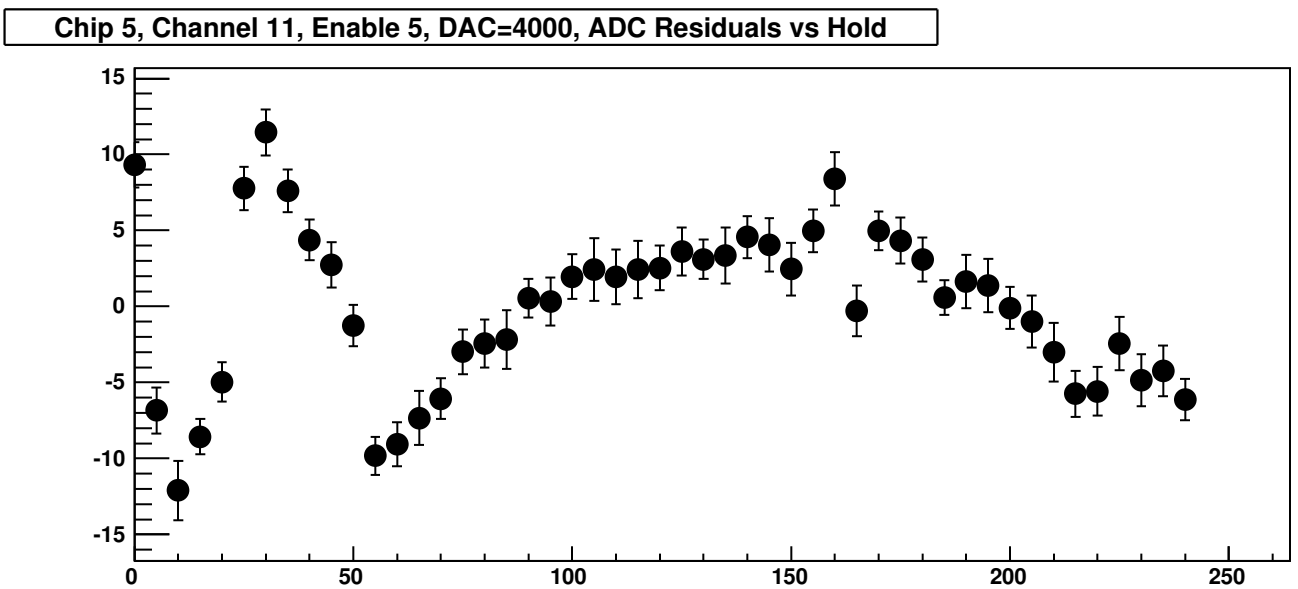


Chip 5, Channel 11, Enable 4, DAC=4000, ADC Residuals vs Hold

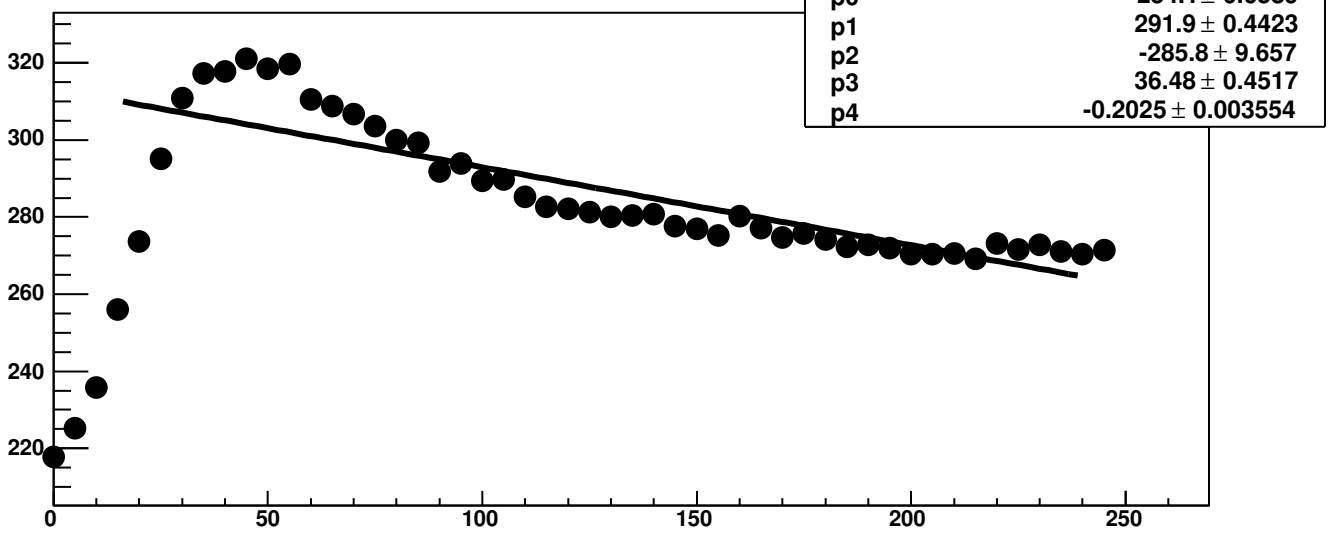



Chip 5, Channel 11, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 520.9 / 41
p0 204.8 ± 0.5127
p1 56.17 ± 0.5304
p2 -9.68e+07 ± 1.746e+06
p3 5.201e+07 ± 1.727e+05
p4 1.851 ± 0.03271
220
200
180
160
140
120
100
0
50
100
150
200
250

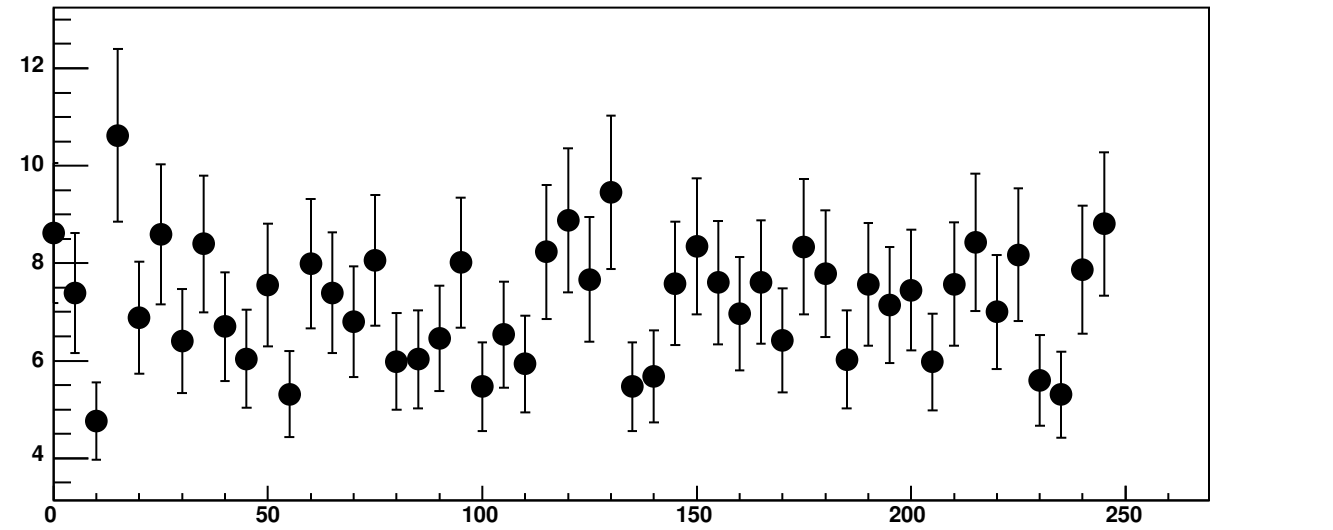

Chip 5, Channel 11, Enable 5, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 5, Channel 11, Enable 5, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
-15
0
50
100
150
200
250

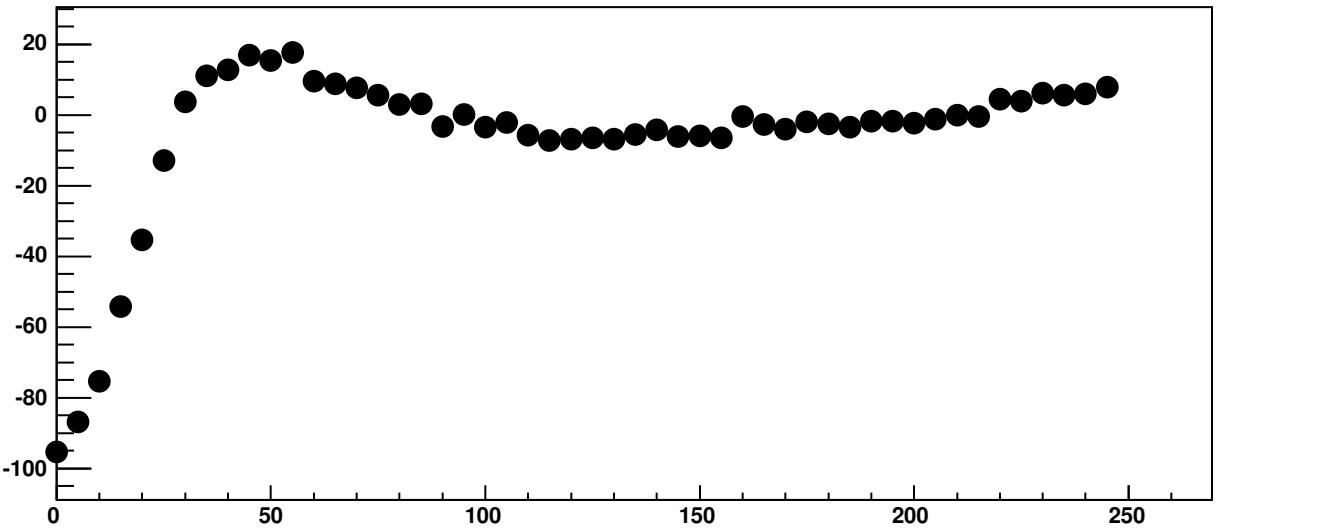
Chip 5, Channel 12, Enable 0, DAC=4000, ADC Mean vs Hold



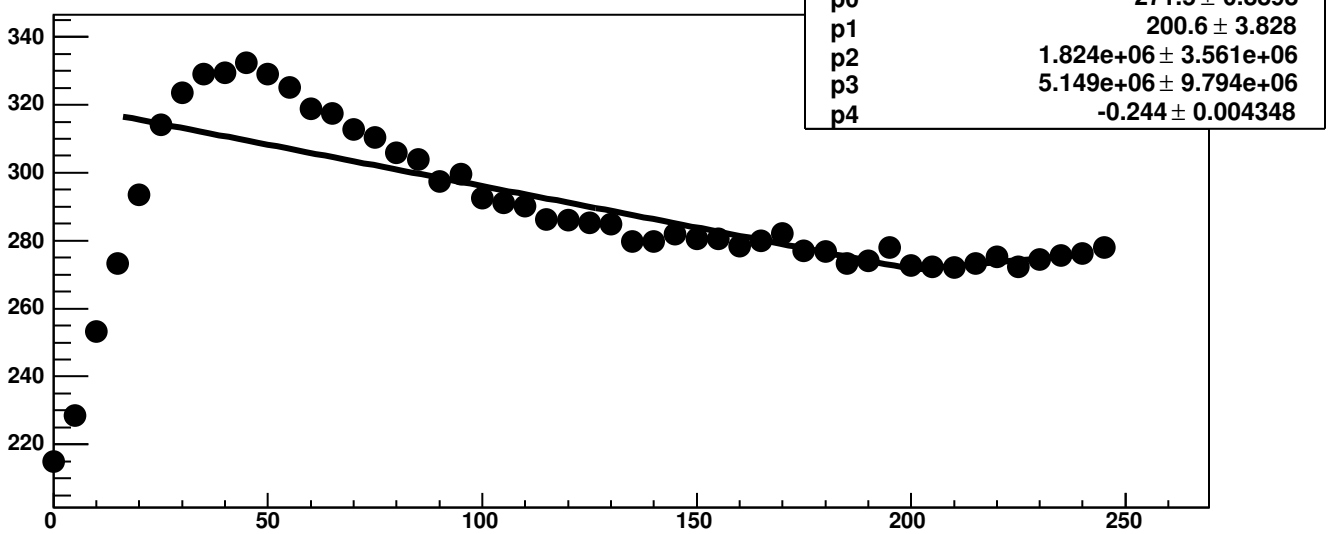
Chip 5, Channel 12, Enable 0, DAC=4000, ADC Noise vs Hold



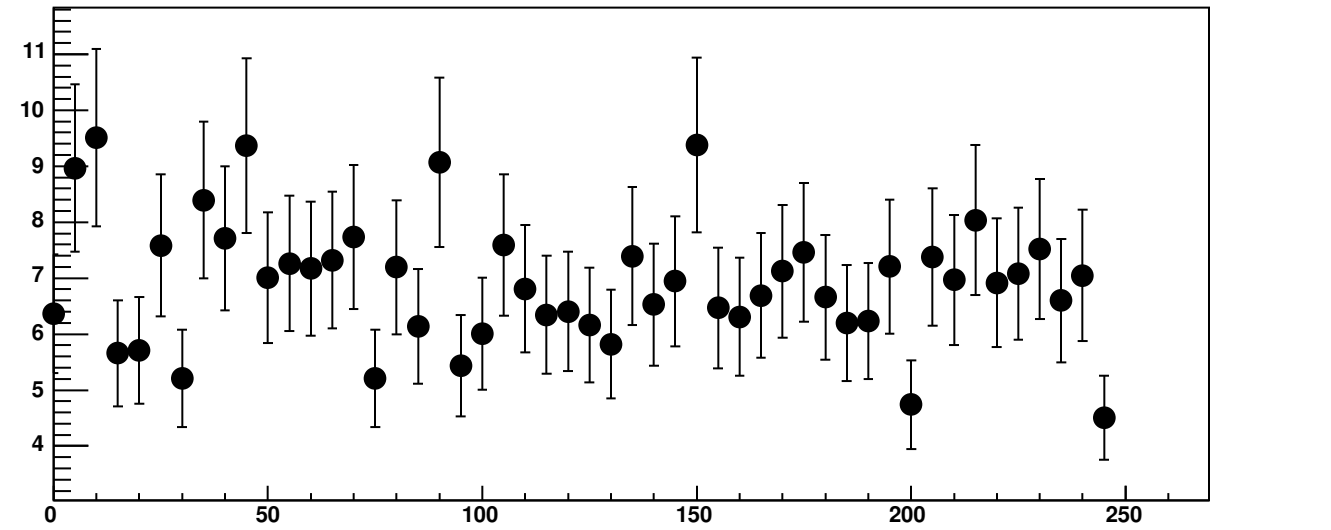
Chip 5, Channel 12, Enable 0, DAC=4000, ADC Residuals vs Hold



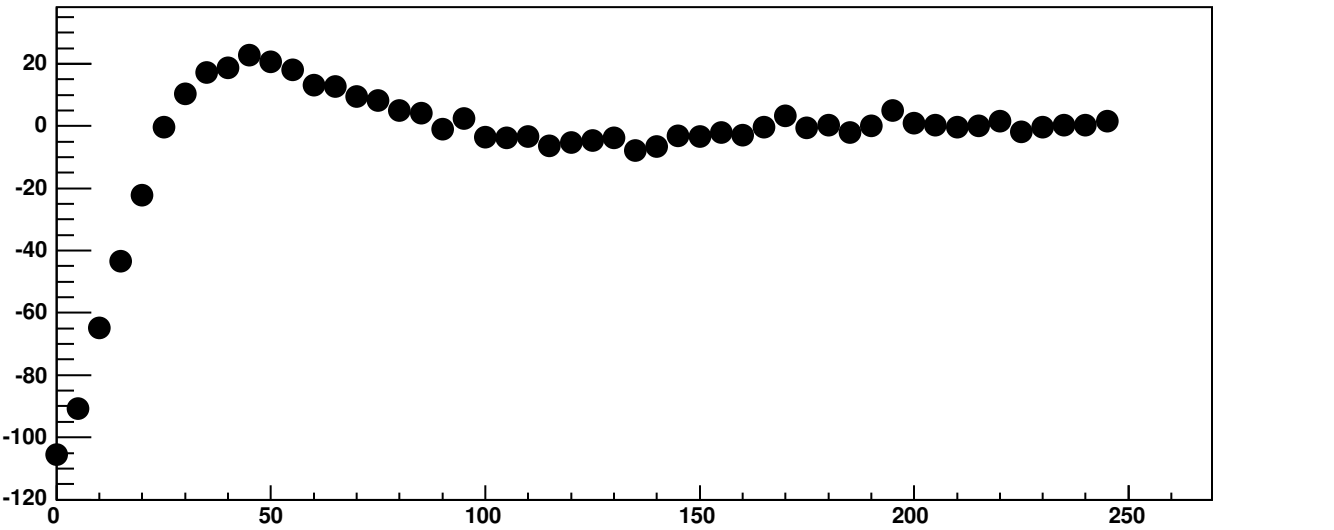
Chip 5, Channel 12, Enable 1, DAC=4000, ADC Mean vs Hold



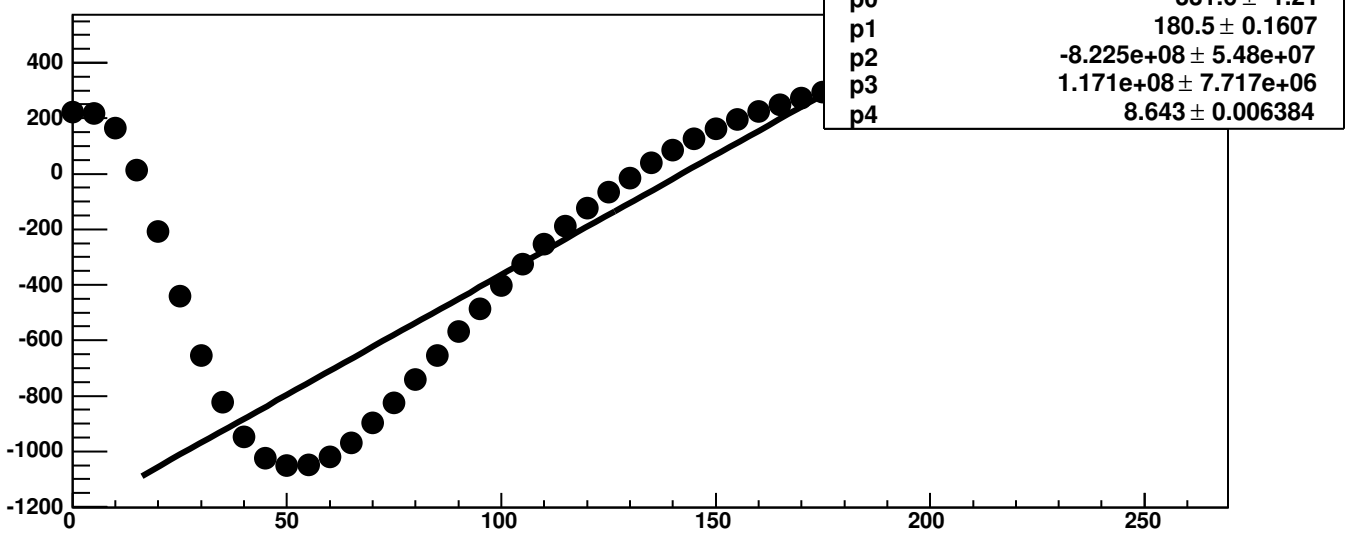
Chip 5, Channel 12, Enable 1, DAC=4000, ADC Noise vs Hold



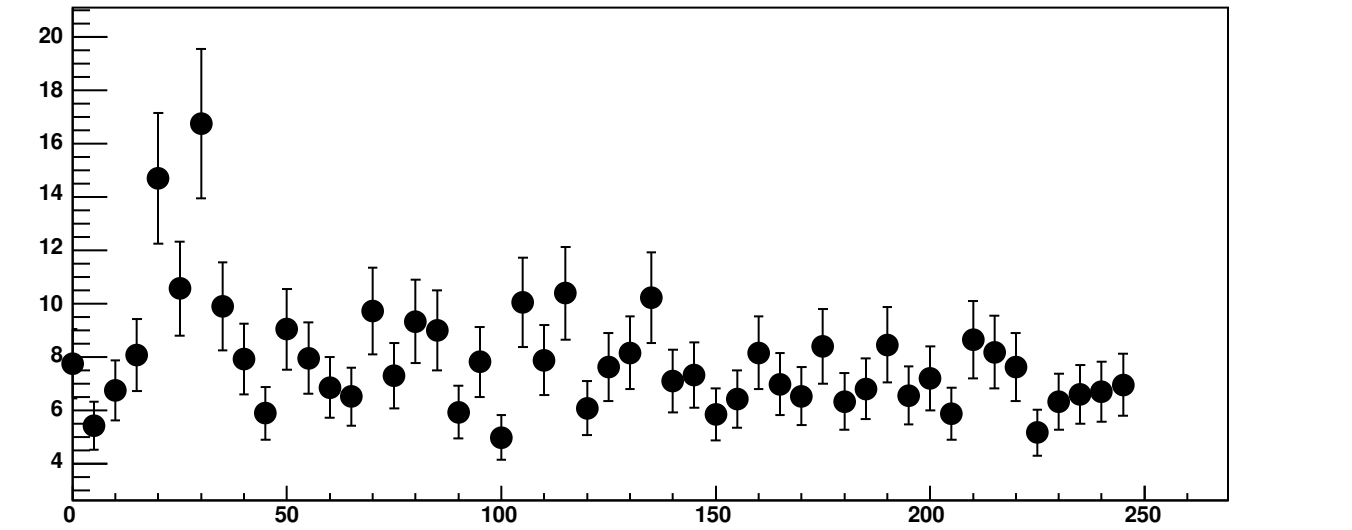
Chip 5, Channel 12, Enable 1, DAC=4000, ADC Residuals vs Hold



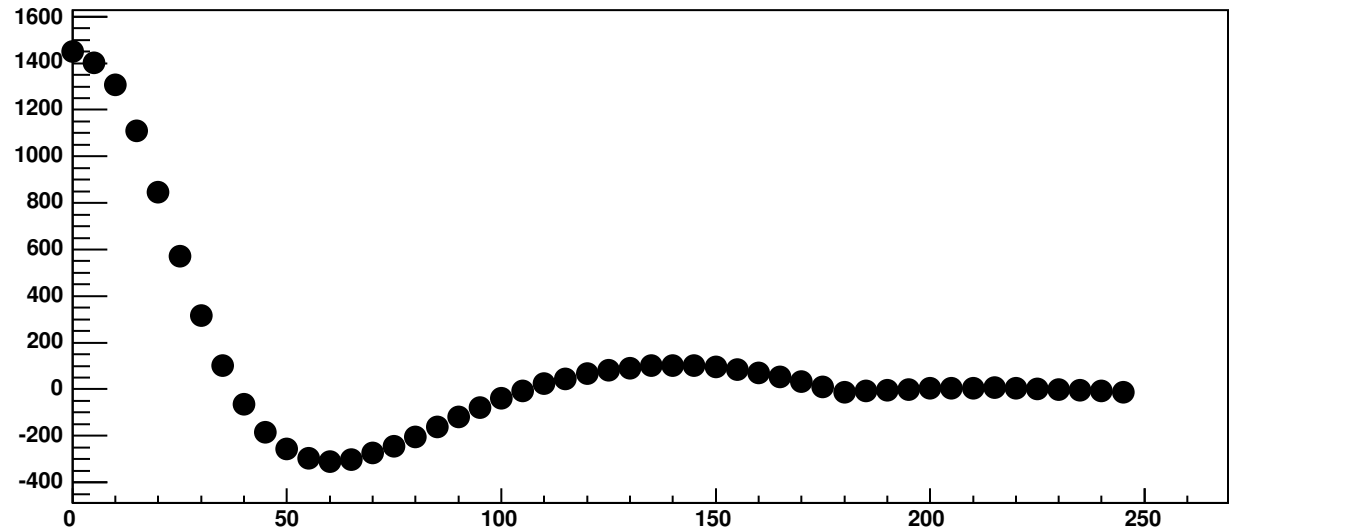
Chip 5, Channel 12, Enable 2, DAC=4000, ADC Mean vs Hold



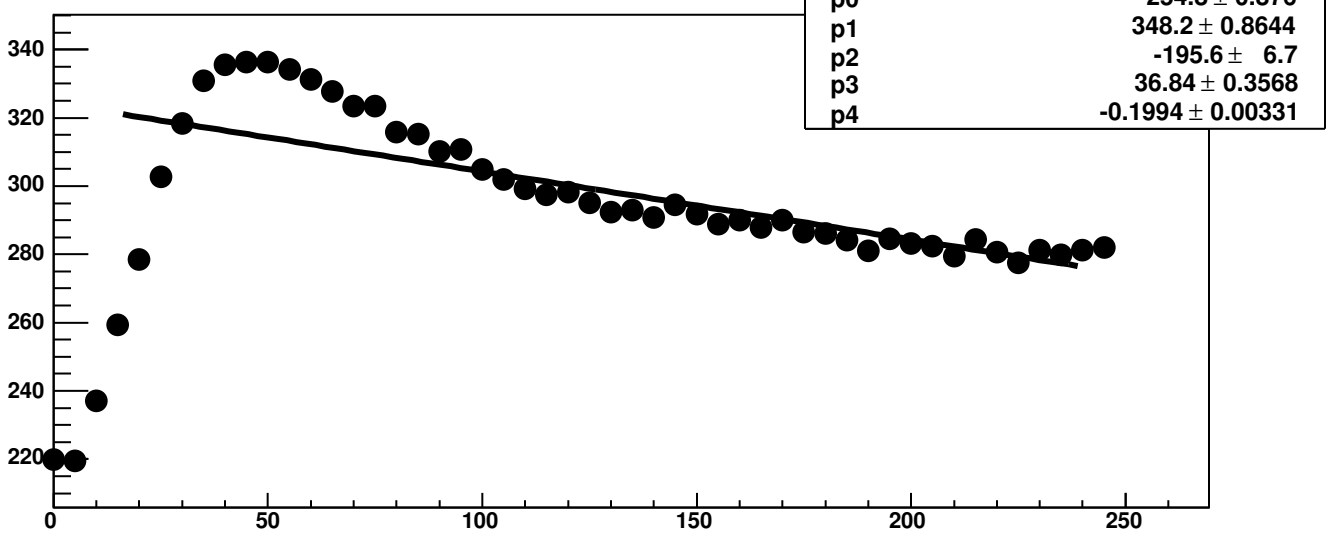
Chip 5, Channel 12, Enable 2, DAC=4000, ADC Noise vs Hold



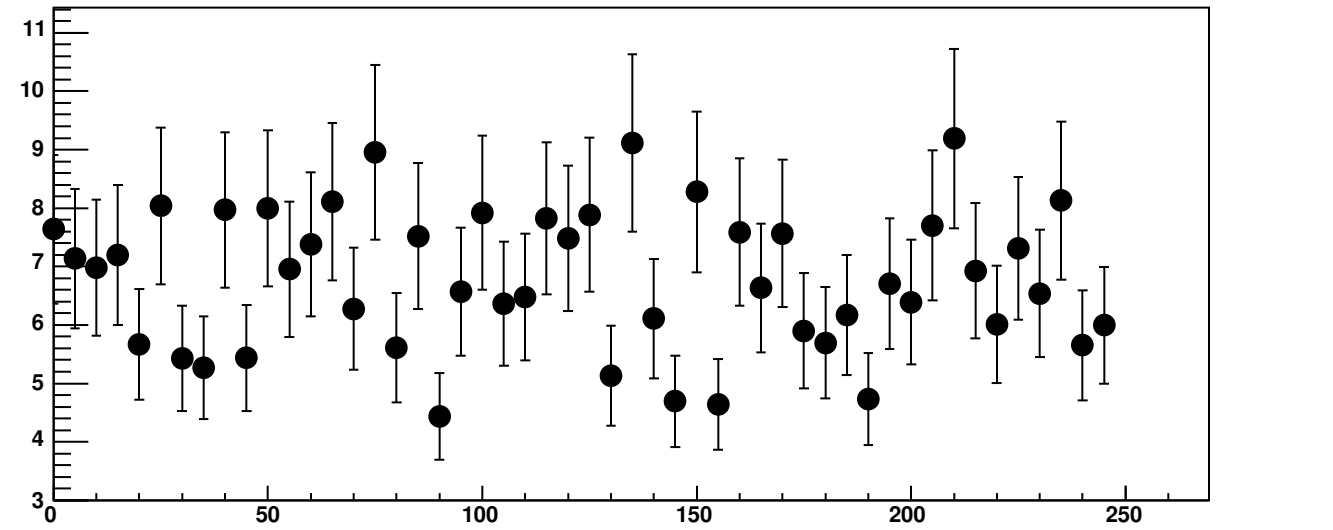
Chip 5, Channel 12, Enable 2, DAC=4000, ADC Residuals vs Hold



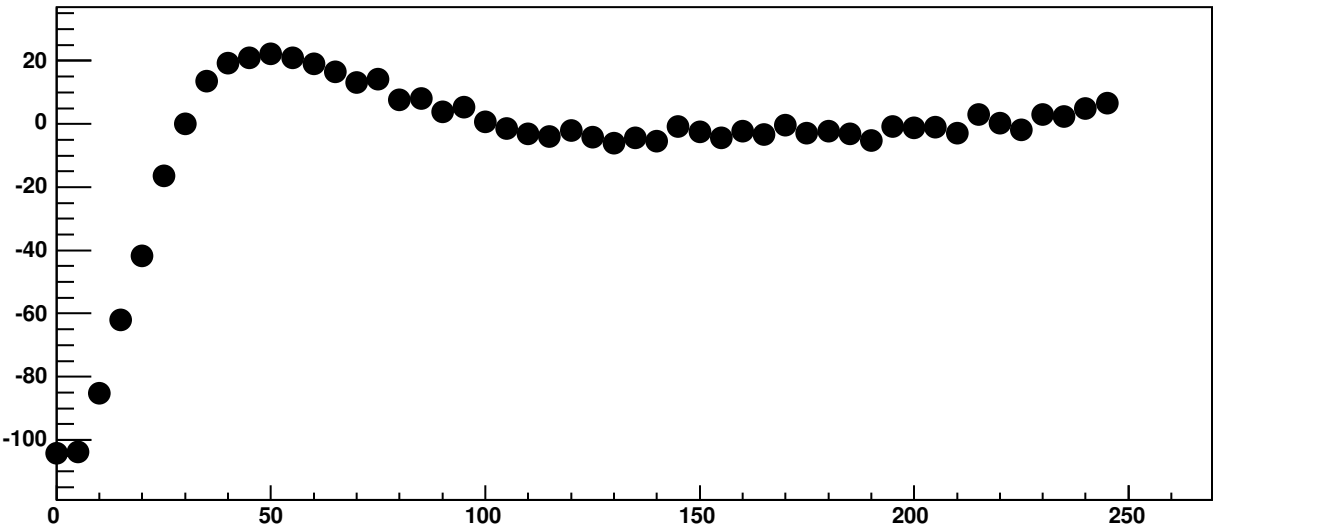
Chip 5, Channel 12, Enable 3, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 12, Enable 3, DAC=4000, ADC Noise vs Hold

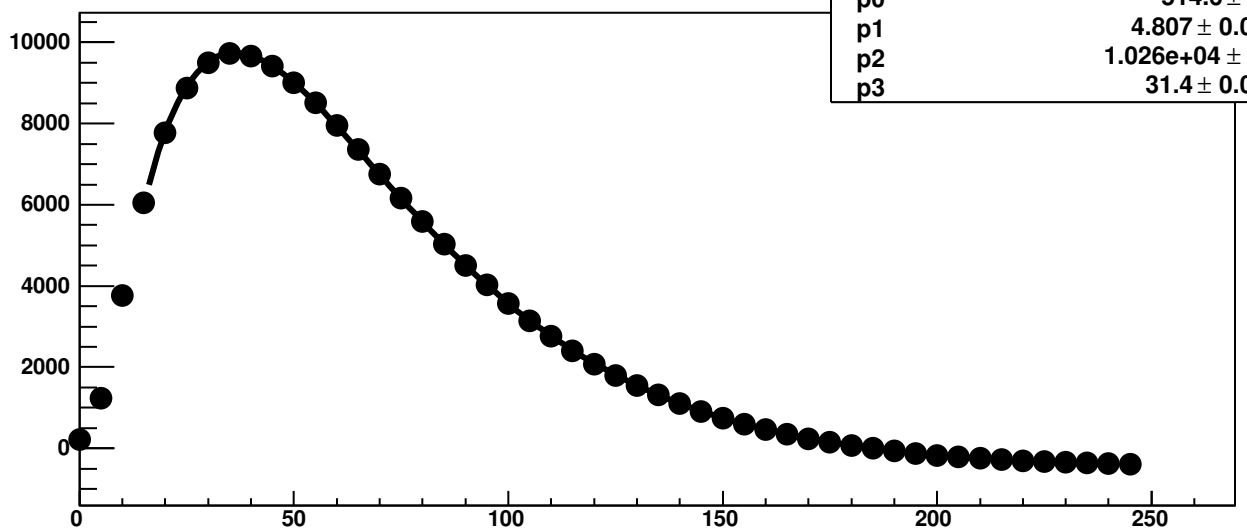


Chip 5, Channel 12, Enable 3, DAC=4000, ADC Residuals vs Hold

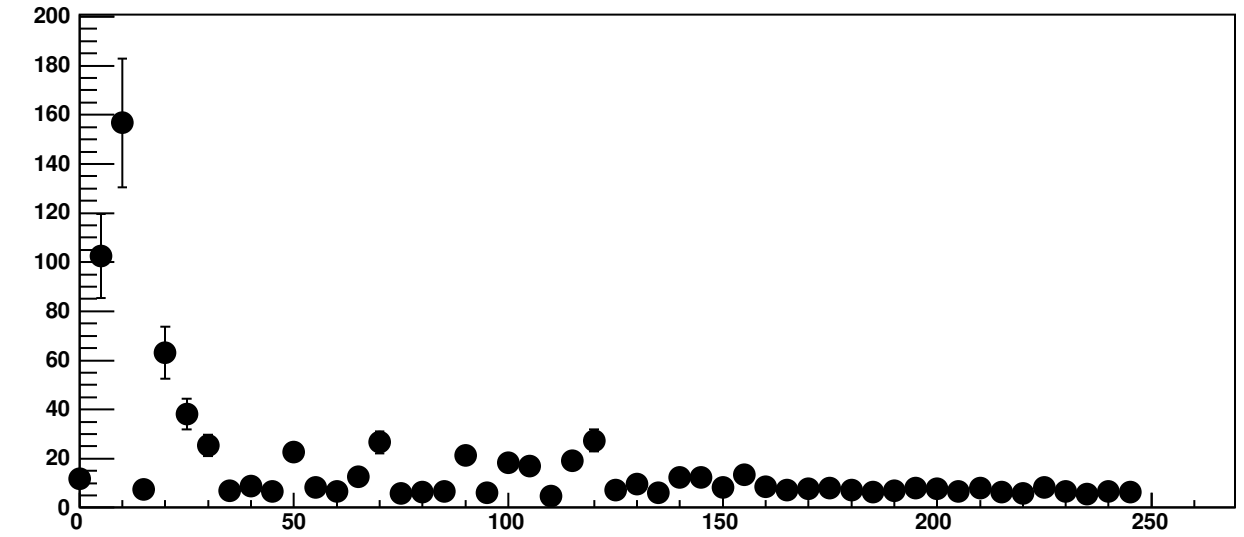


Chip 5, Channel 12, Enable 4!, DAC=4000, ADC Mean vs Hold

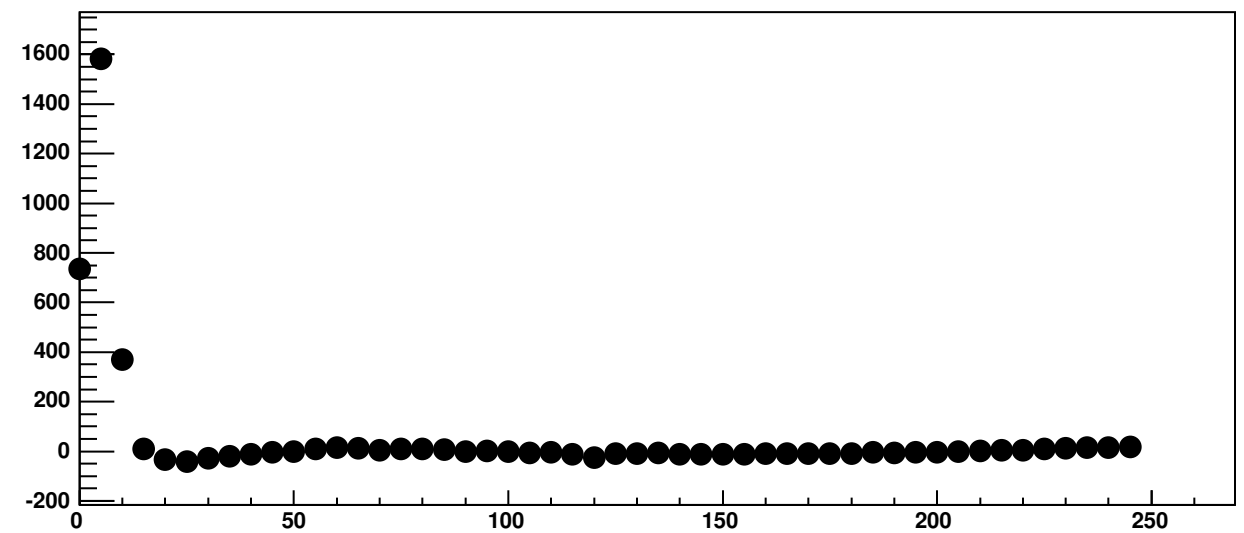
χ^2 / ndf	1335 / 42
p0	-514.6 ± 0.5049
p1	4.807 ± 0.004946
p2	1.026e+04 ± 0.8562
p3	31.4 ± 0.004285



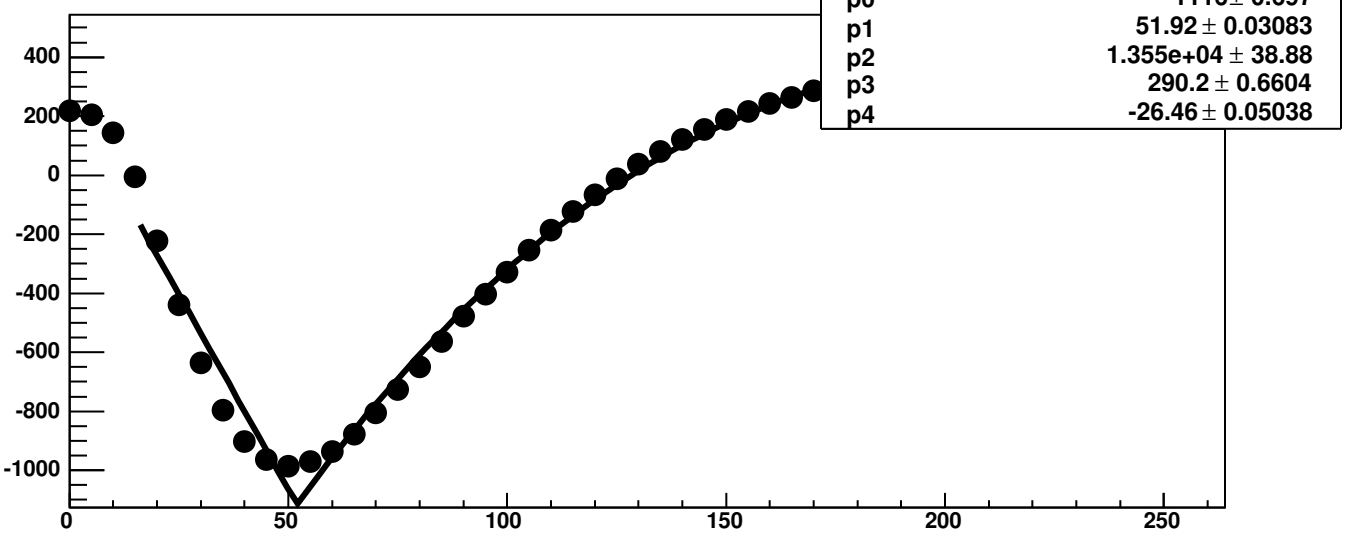
Chip 5, Channel 12, Enable 4!, DAC=4000, ADC Noise vs Hold



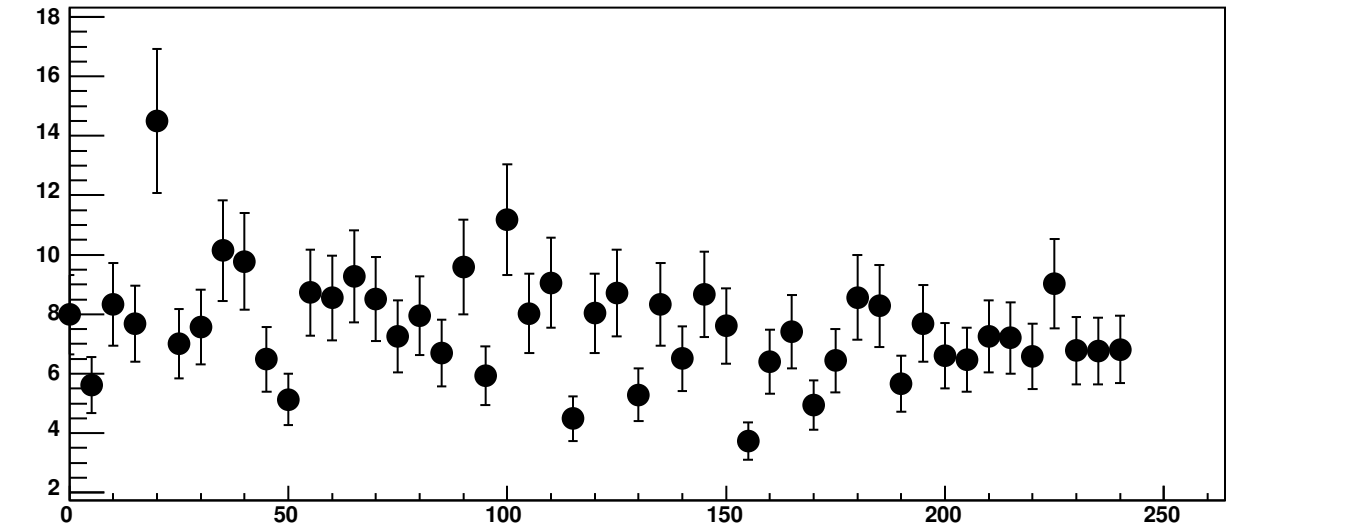
Chip 5, Channel 12, Enable 4!, DAC=4000, ADC Residuals vs Hold



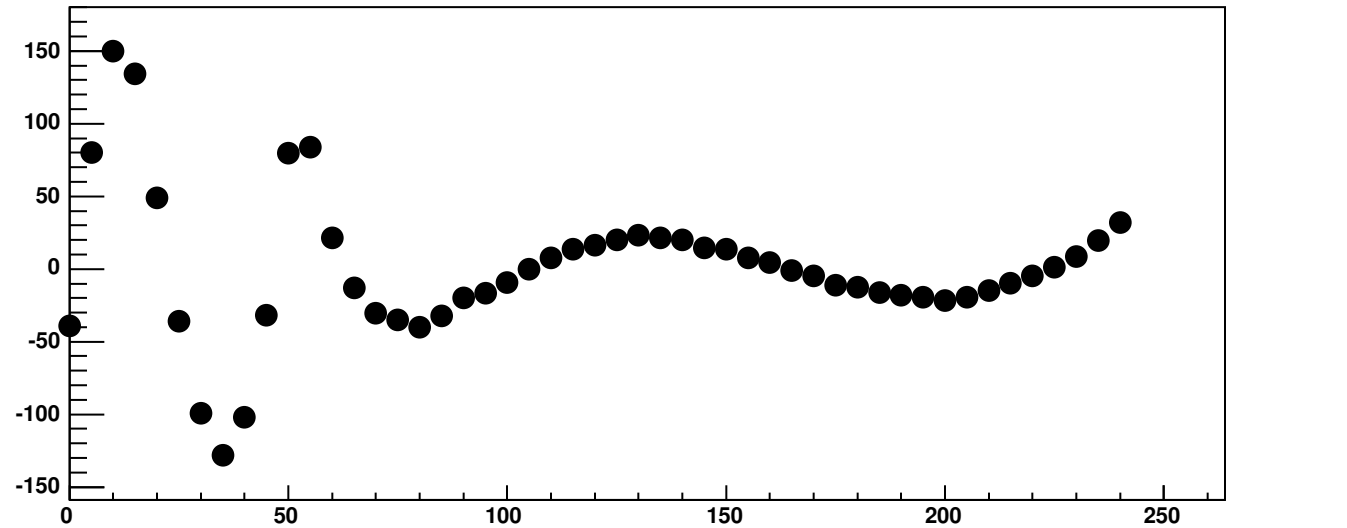
Chip 5, Channel 12, Enable 5, DAC=4000, ADC Mean vs Hold



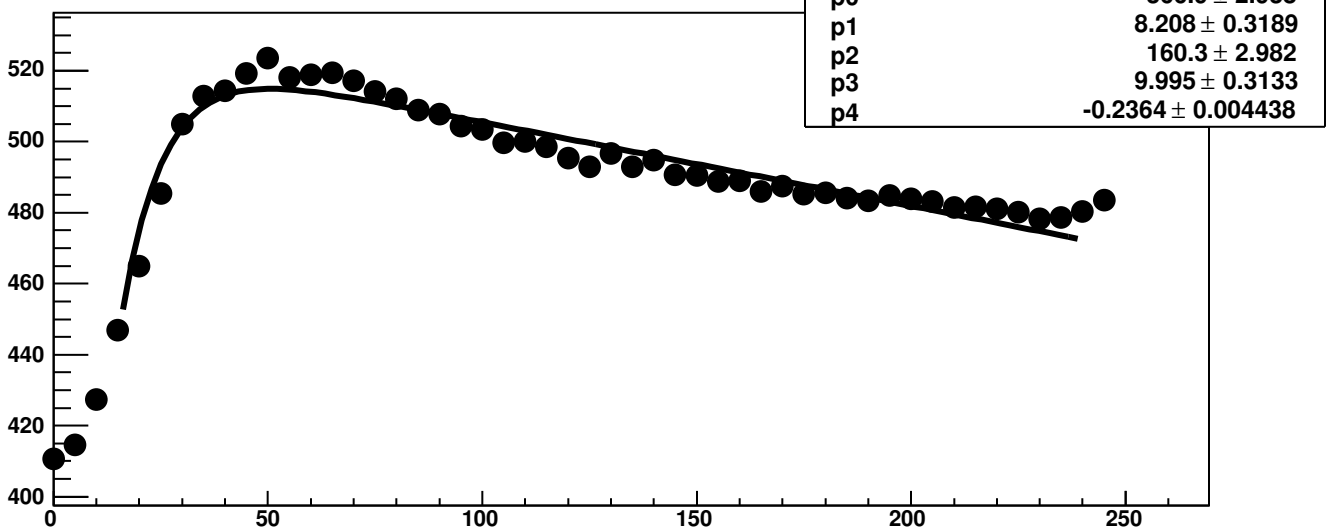
Chip 5, Channel 12, Enable 5, DAC=4000, ADC Noise vs Hold



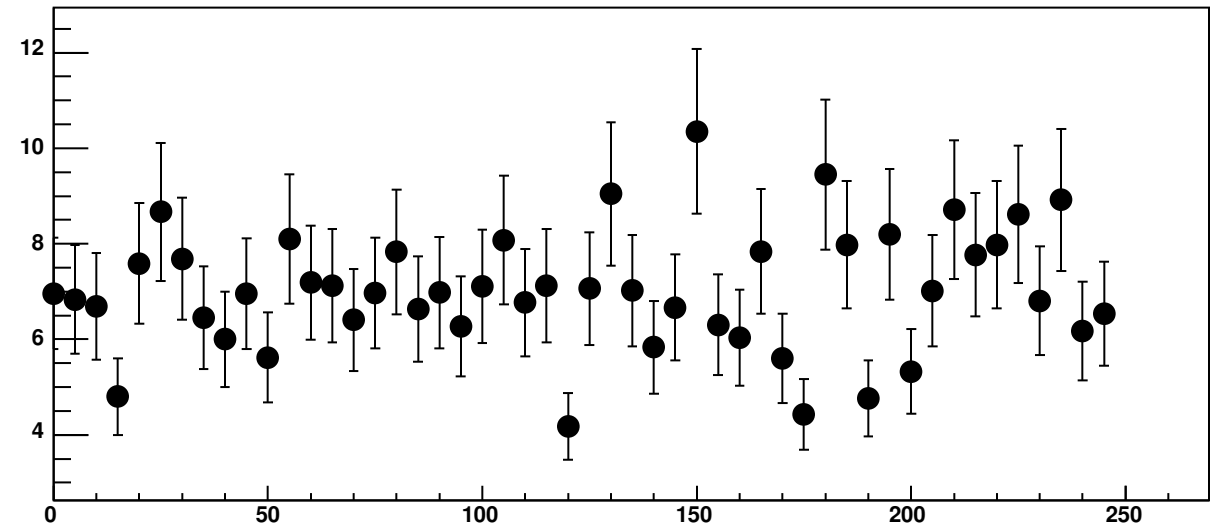
Chip 5, Channel 12, Enable 5, DAC=4000, ADC Residuals vs Hold



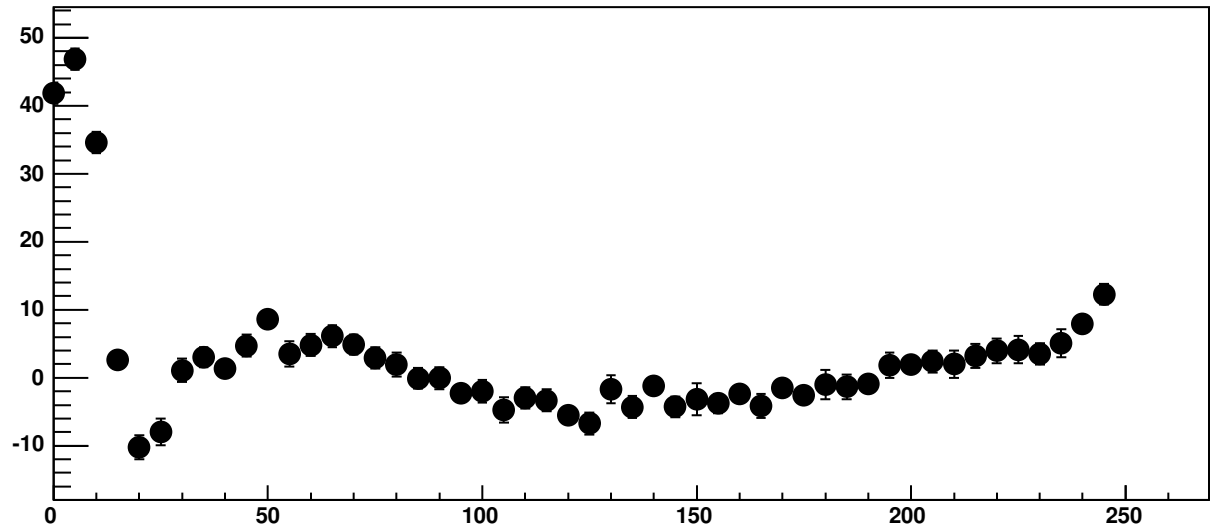
Chip 5, Channel 13, Enable 0, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 13, Enable 0, DAC=4000, ADC Noise vs Hold

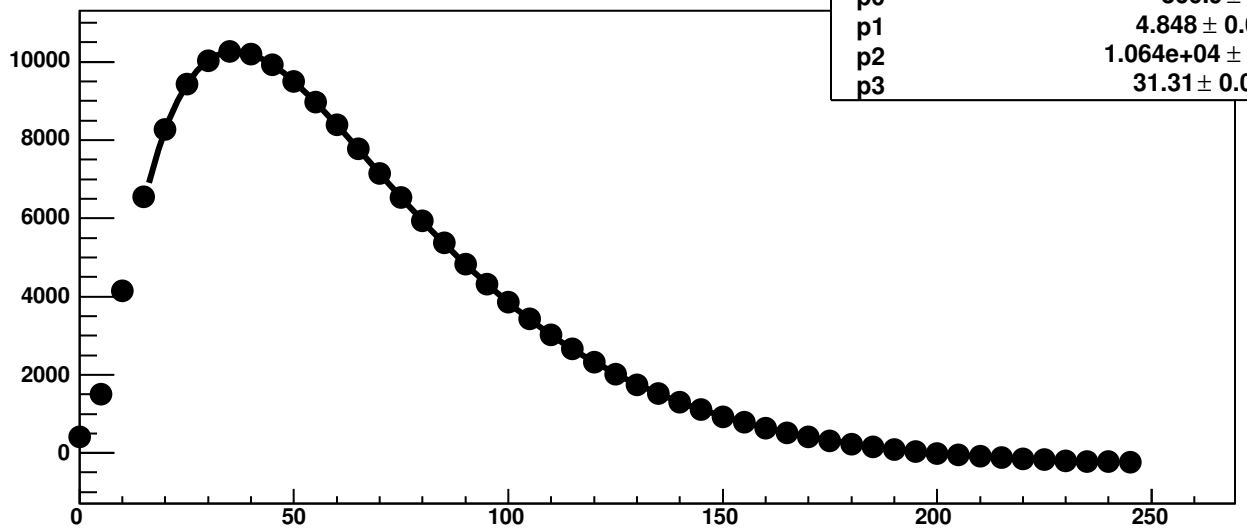


Chip 5, Channel 13, Enable 0, DAC=4000, ADC Residuals vs Hold

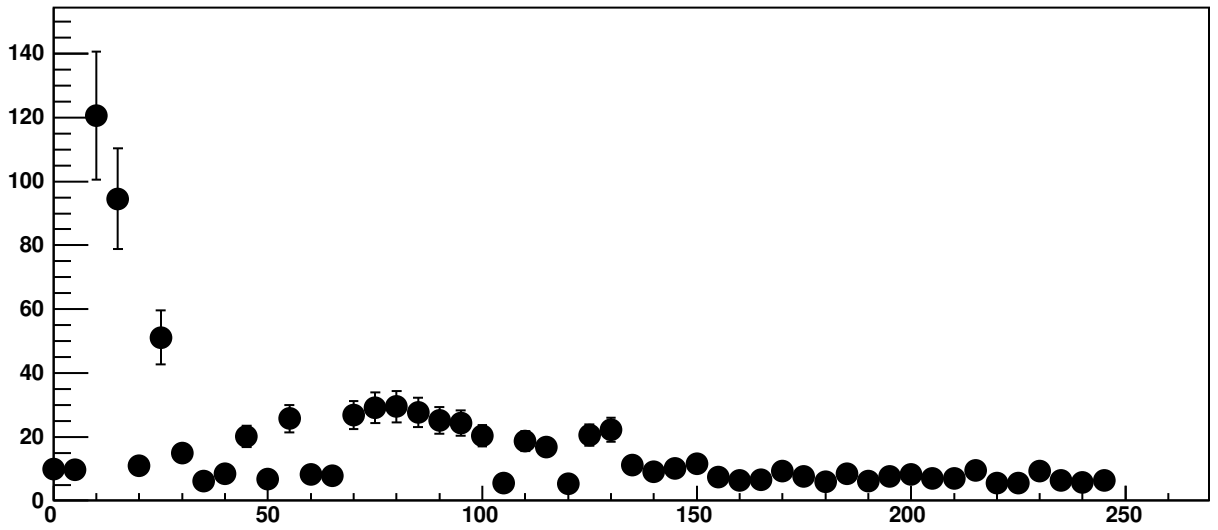


Chip 5, Channel 13, Enable 1!, DAC=4000, ADC Mean vs Hold

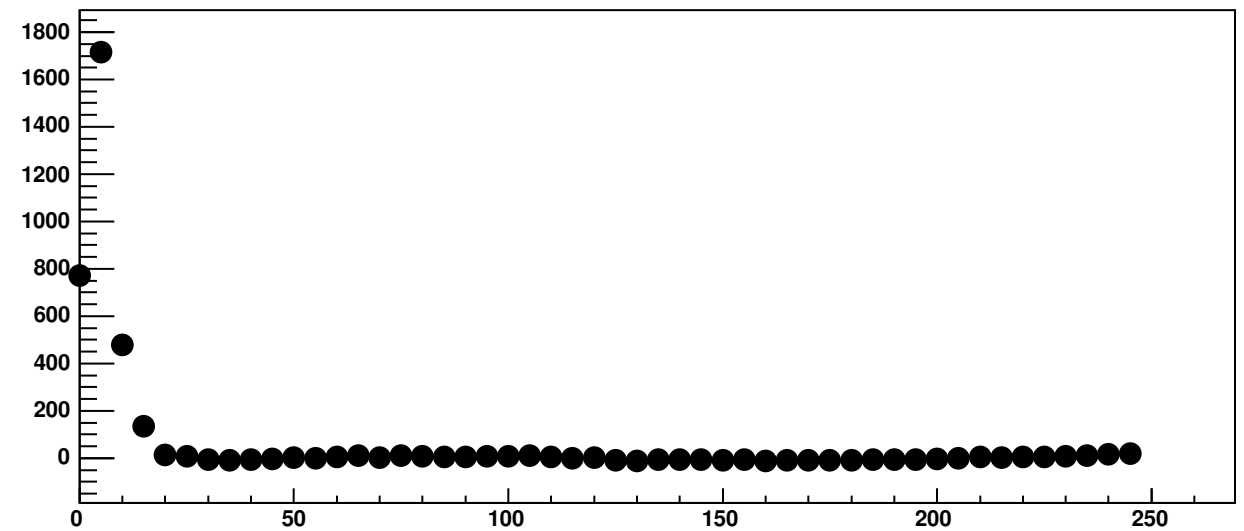
χ^2 / ndf	806 / 42
p0	-360.9 ± 0.5489
p1	4.848 ± 0.009481
p2	1.064e+04 ± 0.9002
p3	31.31 ± 0.005723



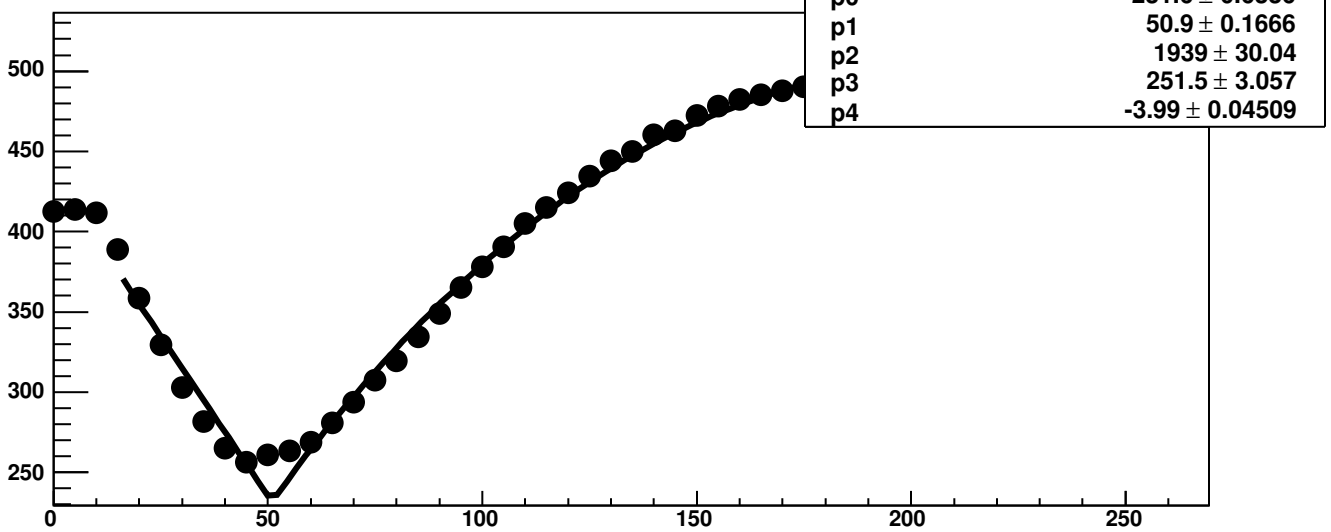
Chip 5, Channel 13, Enable 1!, DAC=4000, ADC Noise vs Hold



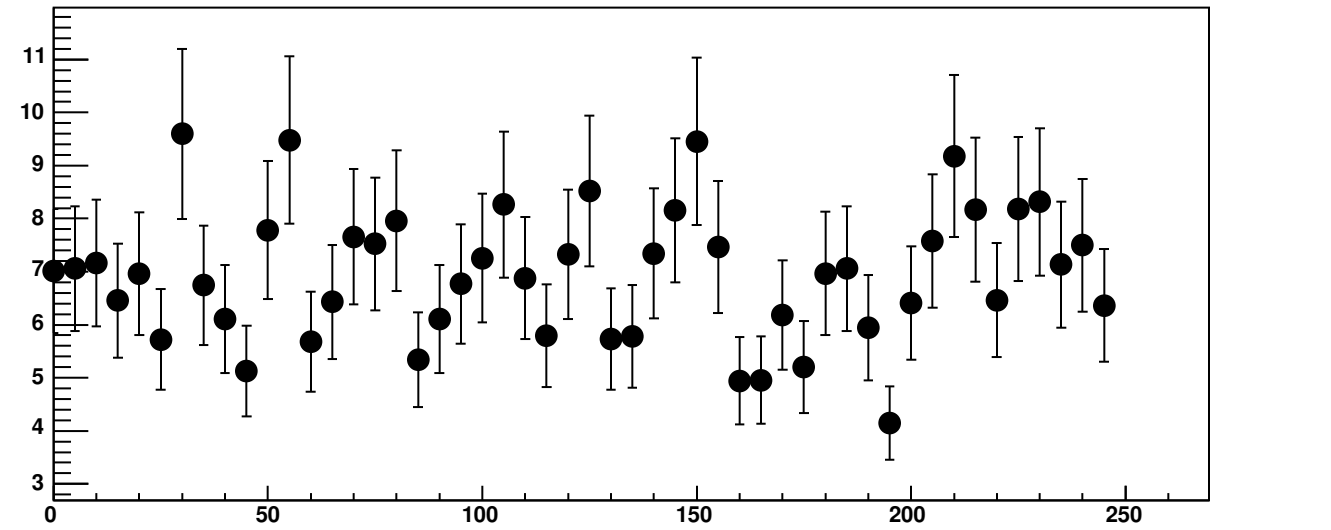
Chip 5, Channel 13, Enable 1!, DAC=4000, ADC Residuals vs Hold



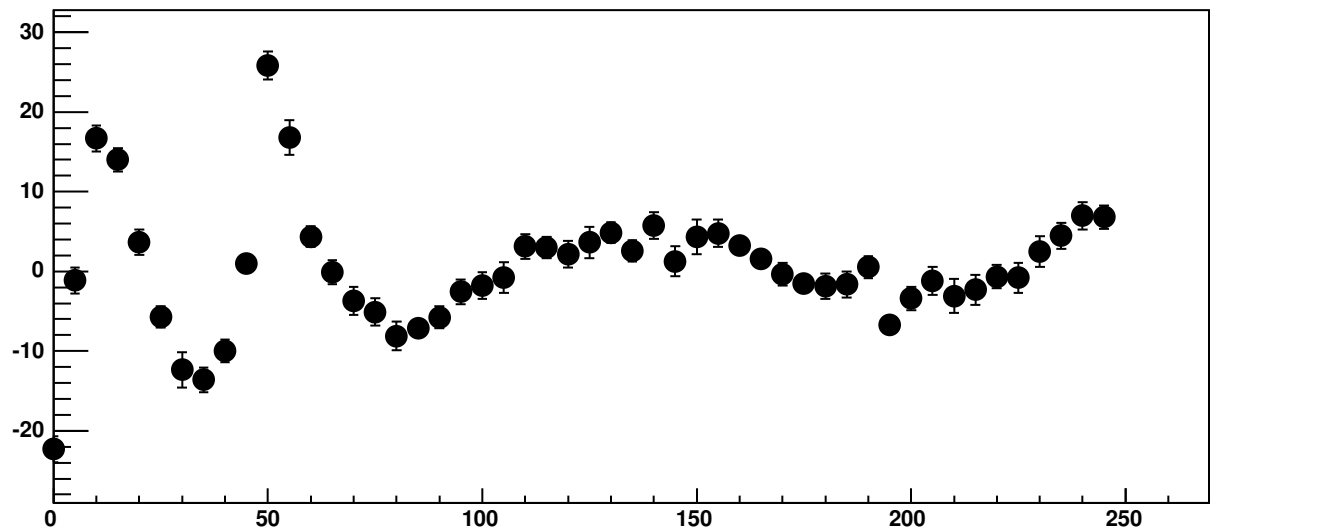
Chip 5, Channel 13, Enable 2, DAC=4000, ADC Mean vs Hold



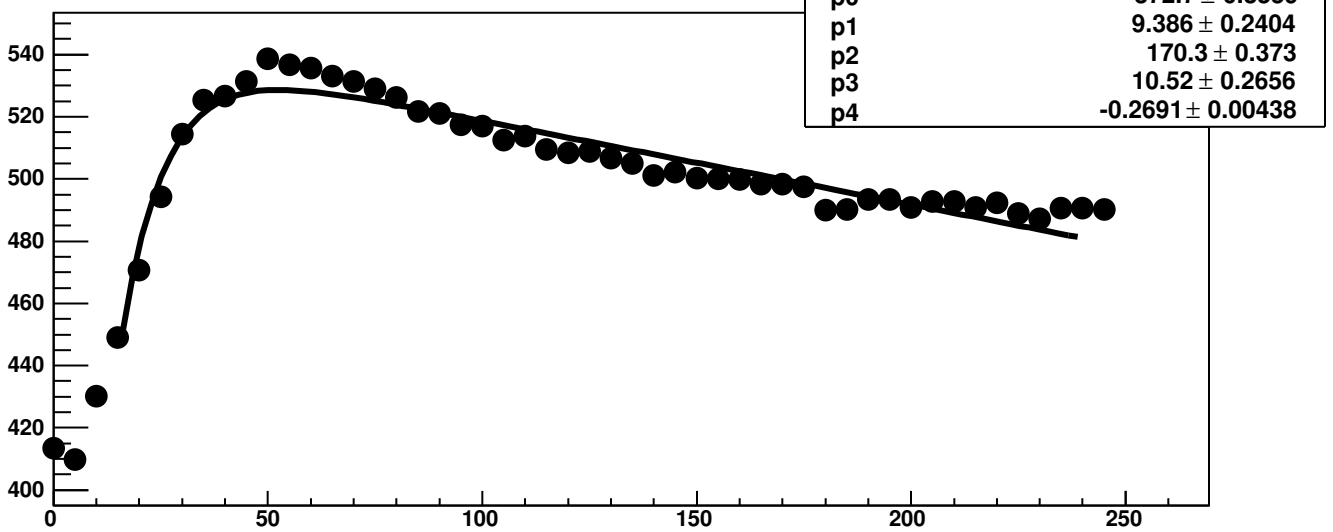
Chip 5, Channel 13, Enable 2, DAC=4000, ADC Noise vs Hold



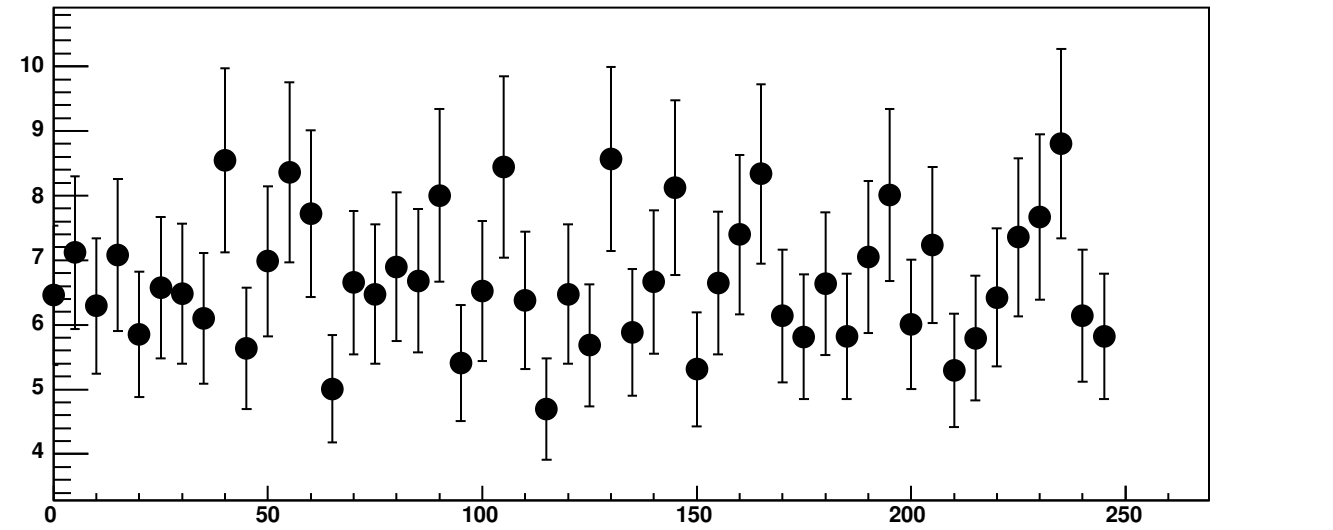
Chip 5, Channel 13, Enable 2, DAC=4000, ADC Residuals vs Hold



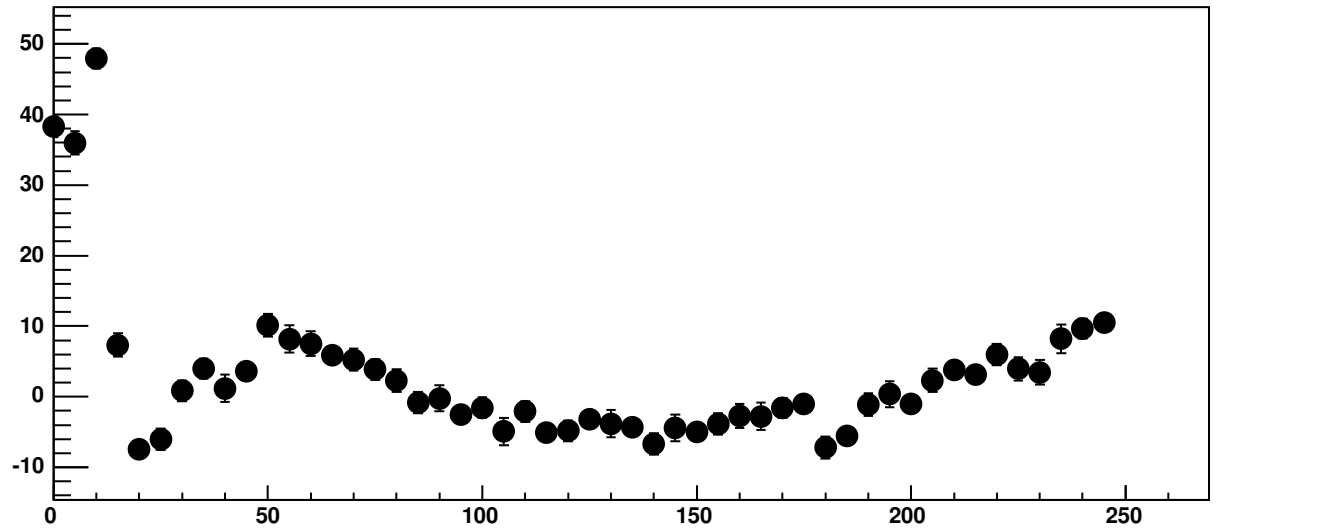
Chip 5, Channel 13, Enable 3, DAC=4000, ADC Mean vs Hold



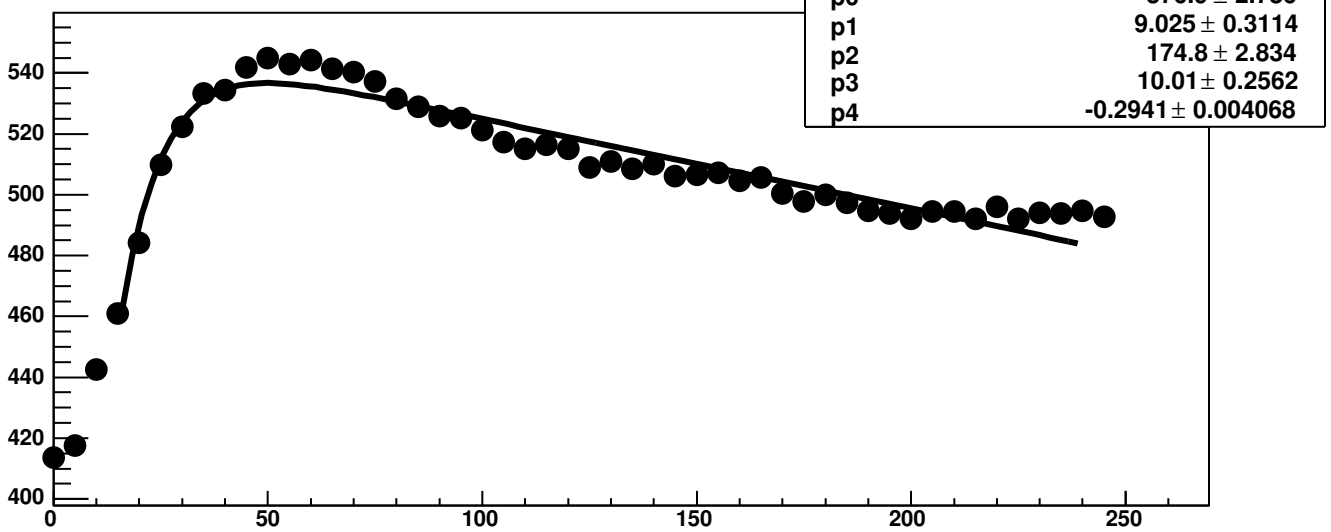
Chip 5, Channel 13, Enable 3, DAC=4000, ADC Noise vs Hold



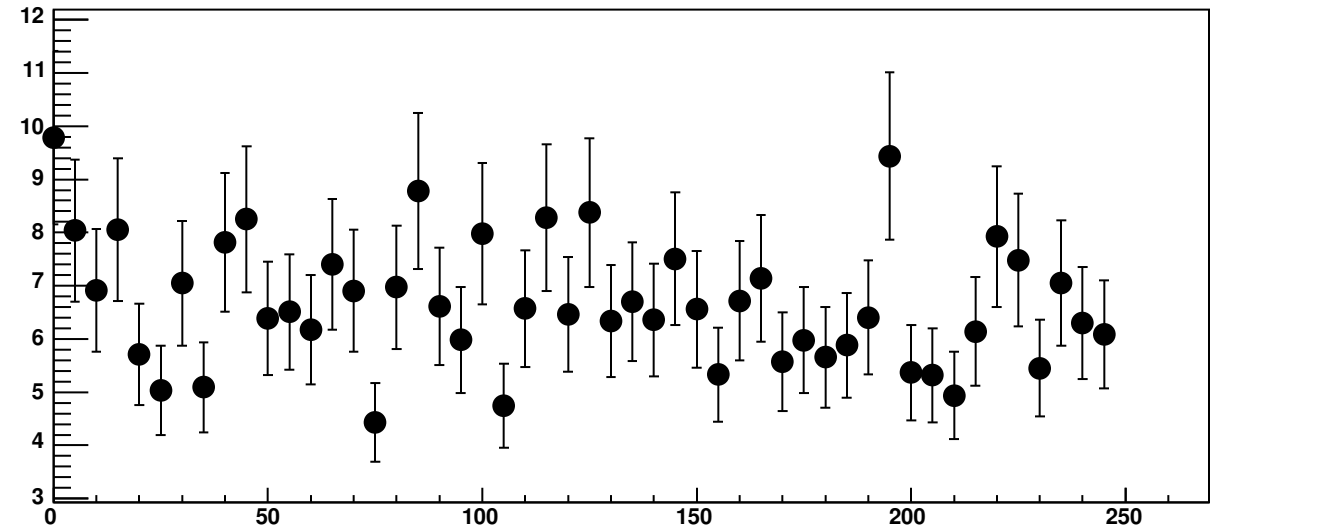
Chip 5, Channel 13, Enable 3, DAC=4000, ADC Residuals vs Hold



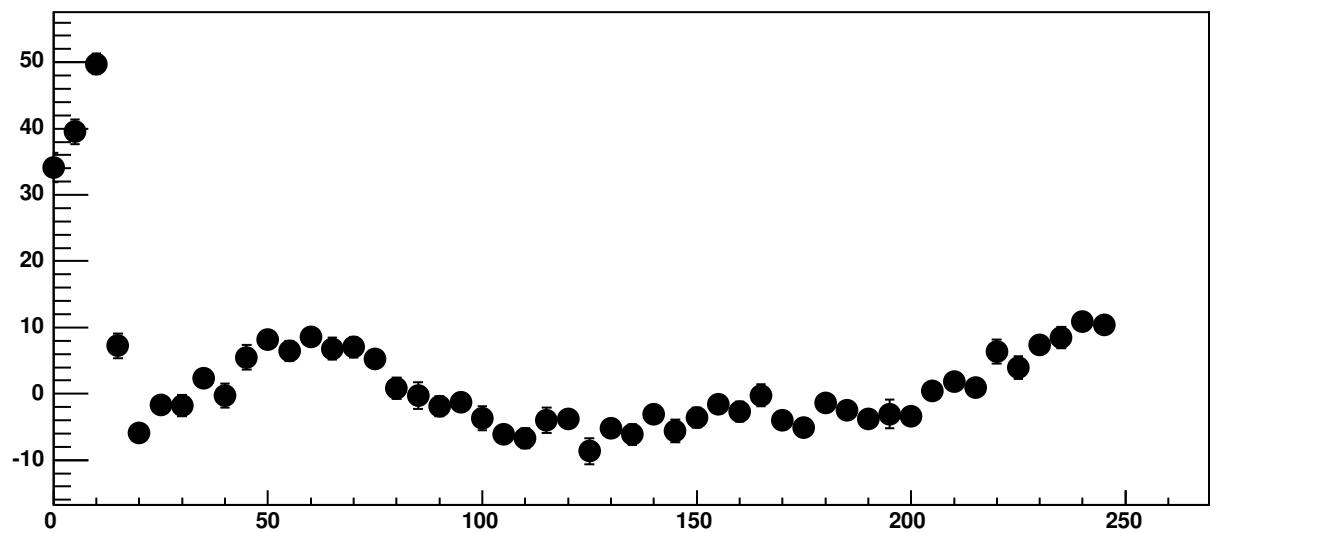
Chip 5, Channel 13, Enable 4, DAC=4000, ADC Mean vs Hold



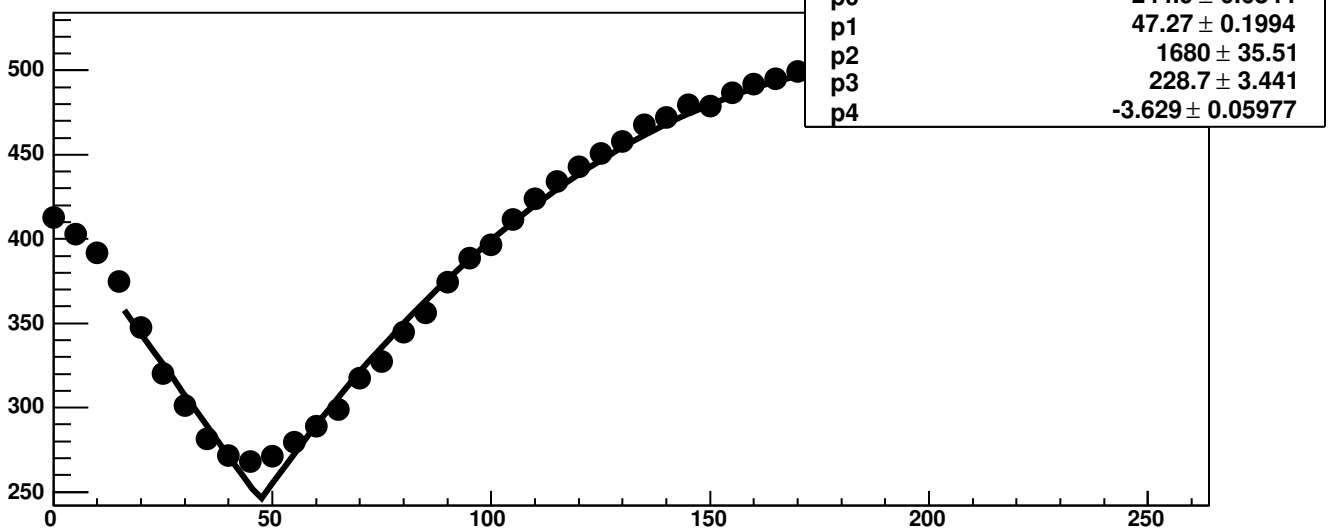
Chip 5, Channel 13, Enable 4, DAC=4000, ADC Noise vs Hold



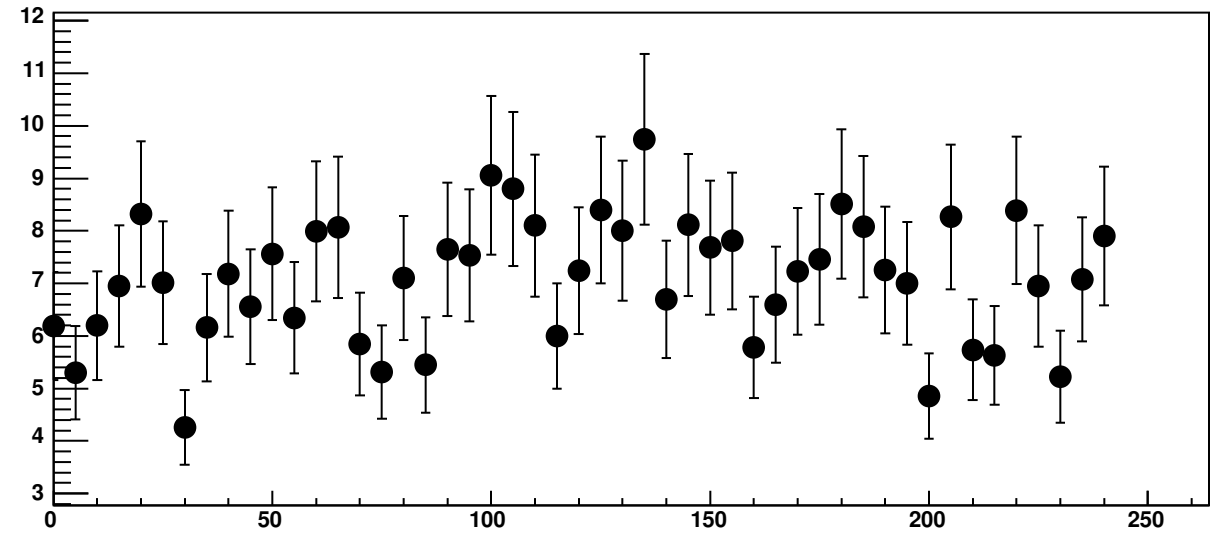
Chip 5, Channel 13, Enable 4, DAC=4000, ADC Residuals vs Hold



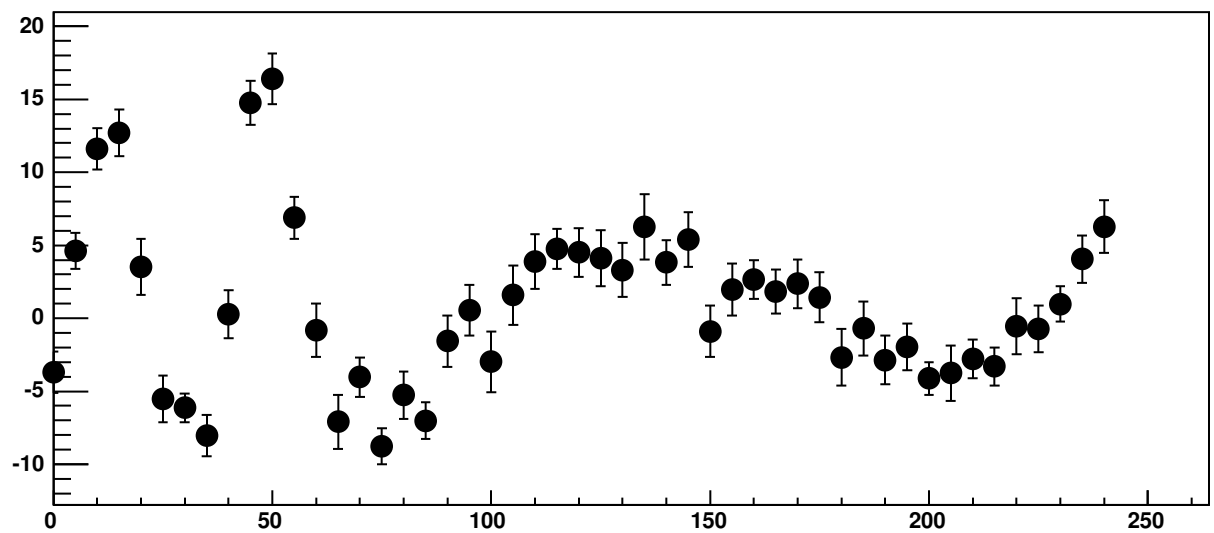
Chip 5, Channel 13, Enable 5, DAC=4000, ADC Mean vs Hold



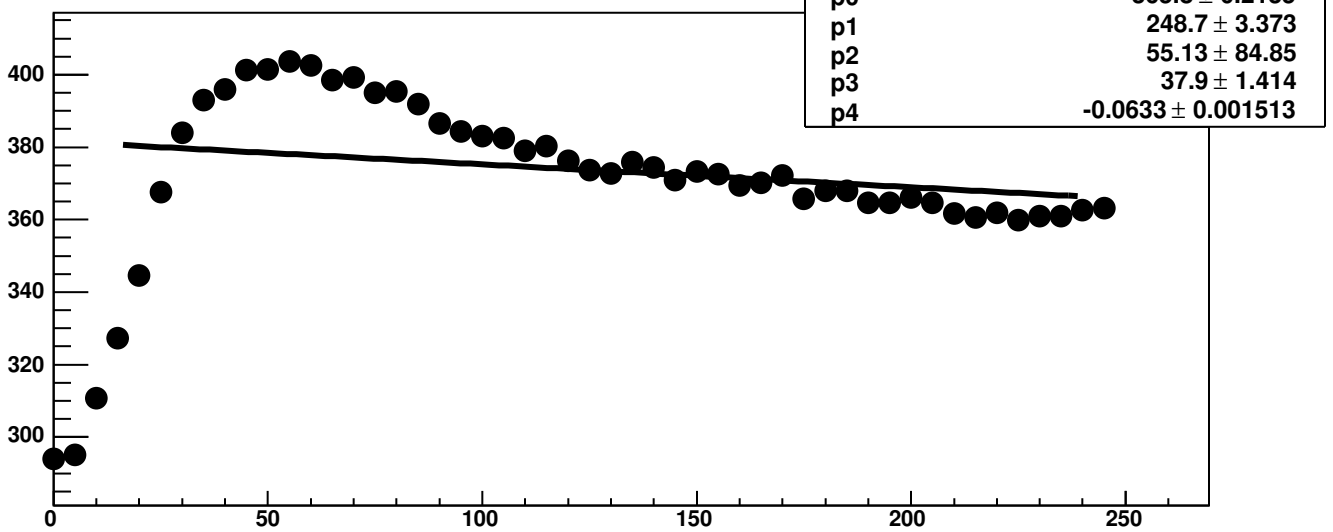
Chip 5, Channel 13, Enable 5, DAC=4000, ADC Noise vs Hold



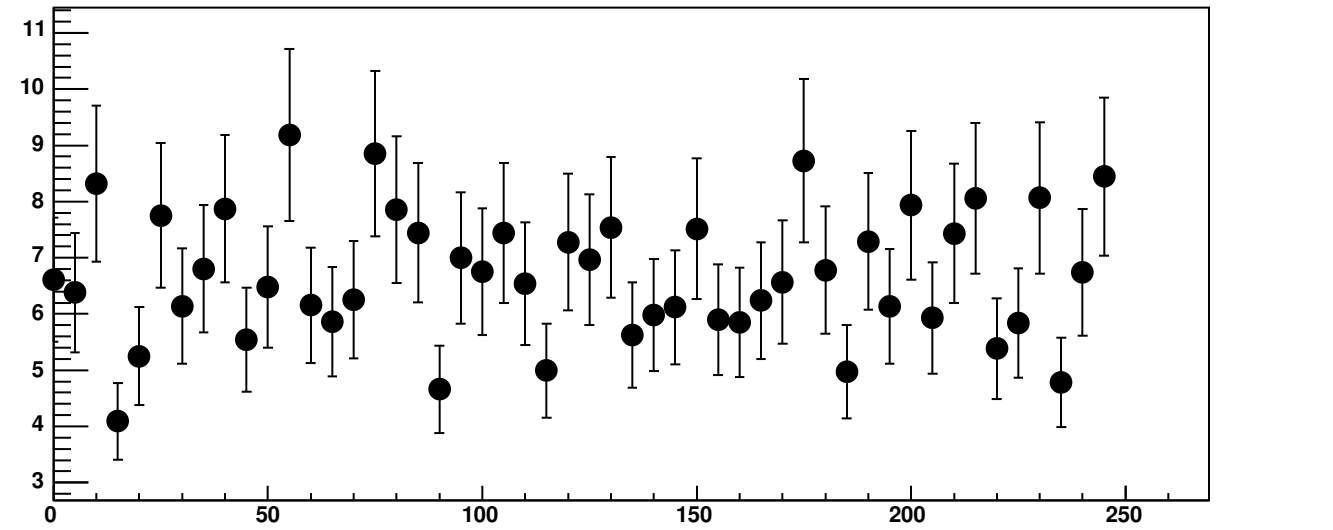
Chip 5, Channel 13, Enable 5, DAC=4000, ADC Residuals vs Hold



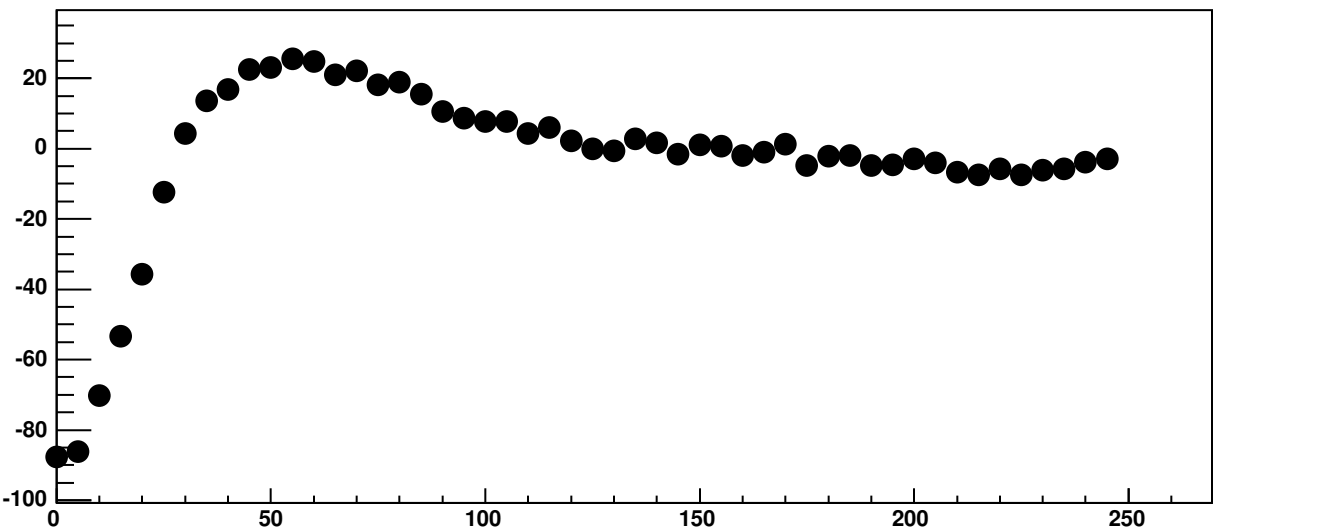
Chip 5, Channel 14, Enable 0, DAC=4000, ADC Mean vs Hold



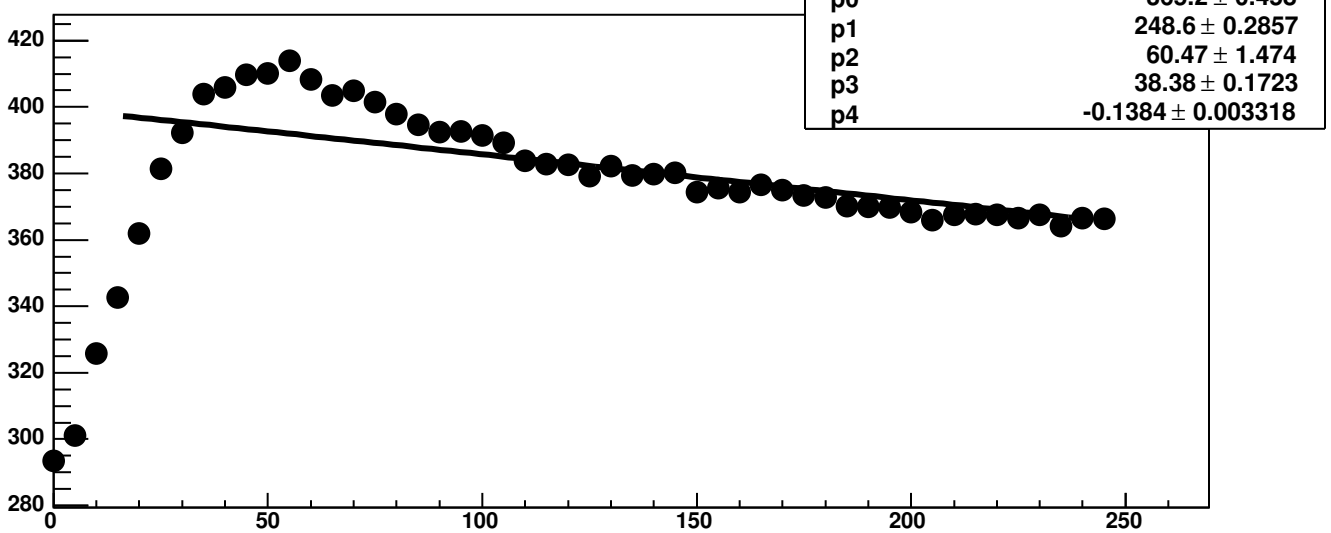
Chip 5, Channel 14, Enable 0, DAC=4000, ADC Noise vs Hold



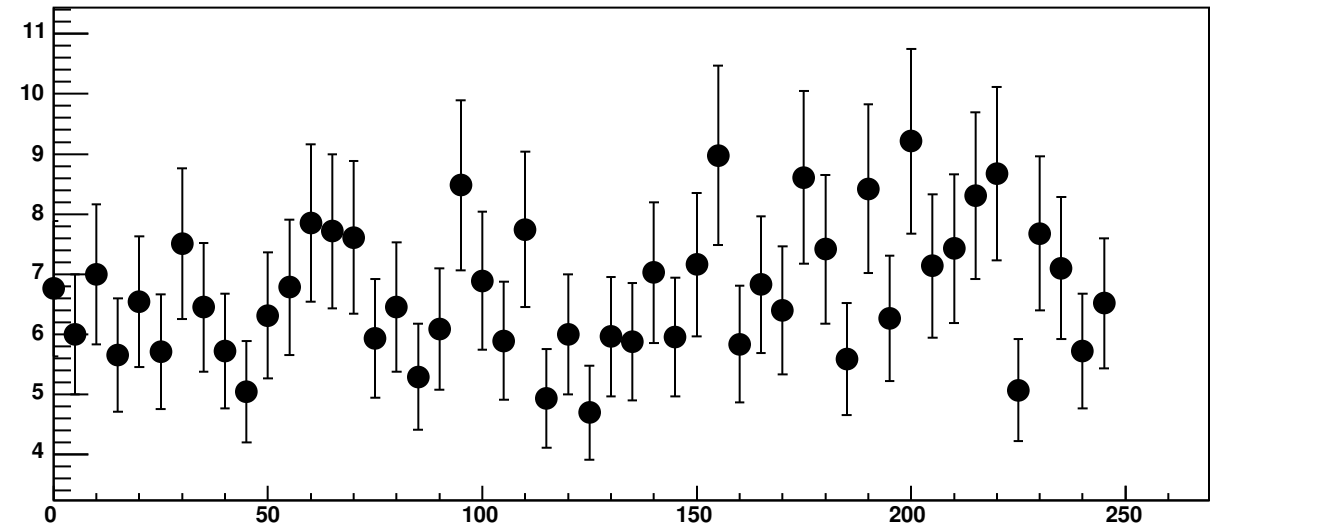
Chip 5, Channel 14, Enable 0, DAC=4000, ADC Residuals vs Hold



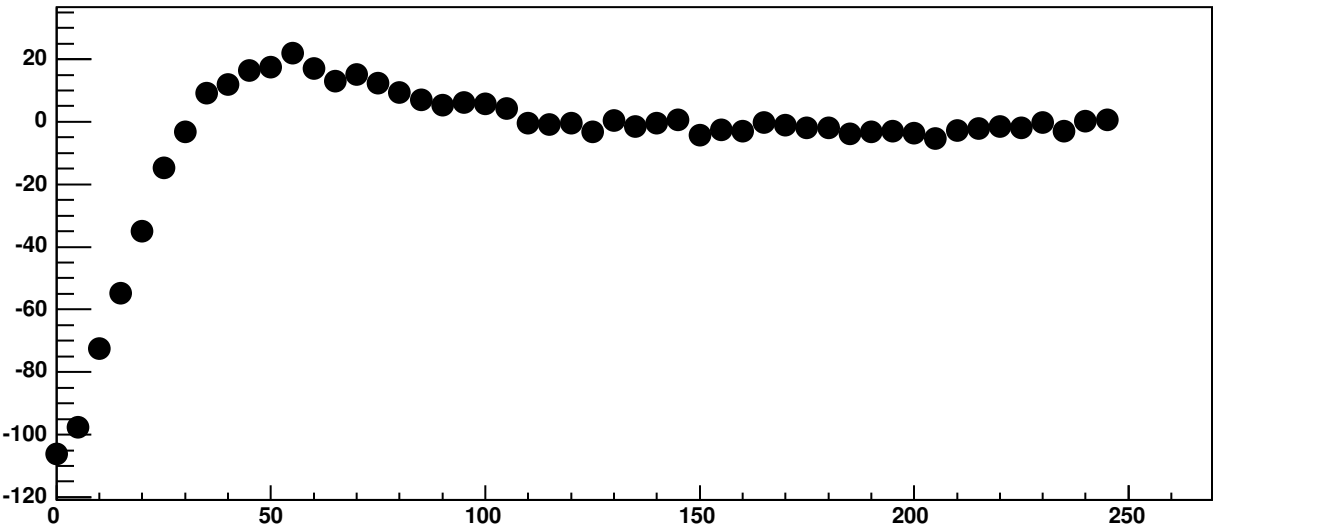
Chip 5, Channel 14, Enable 1, DAC=4000, ADC Mean vs Hold

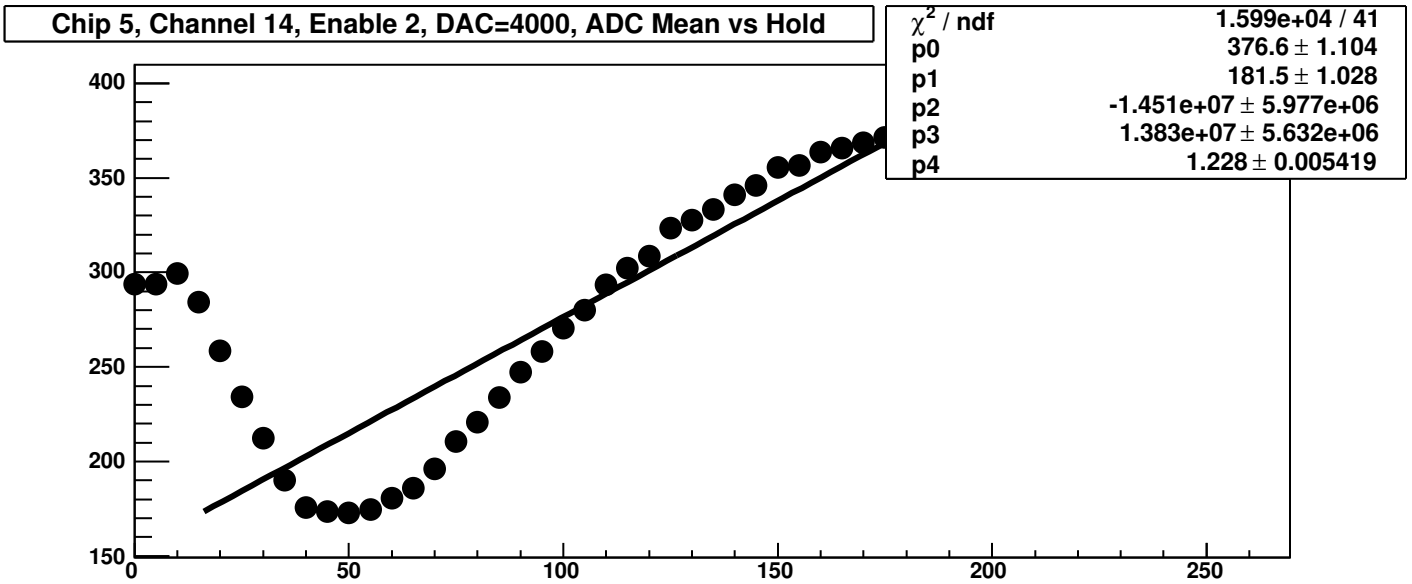


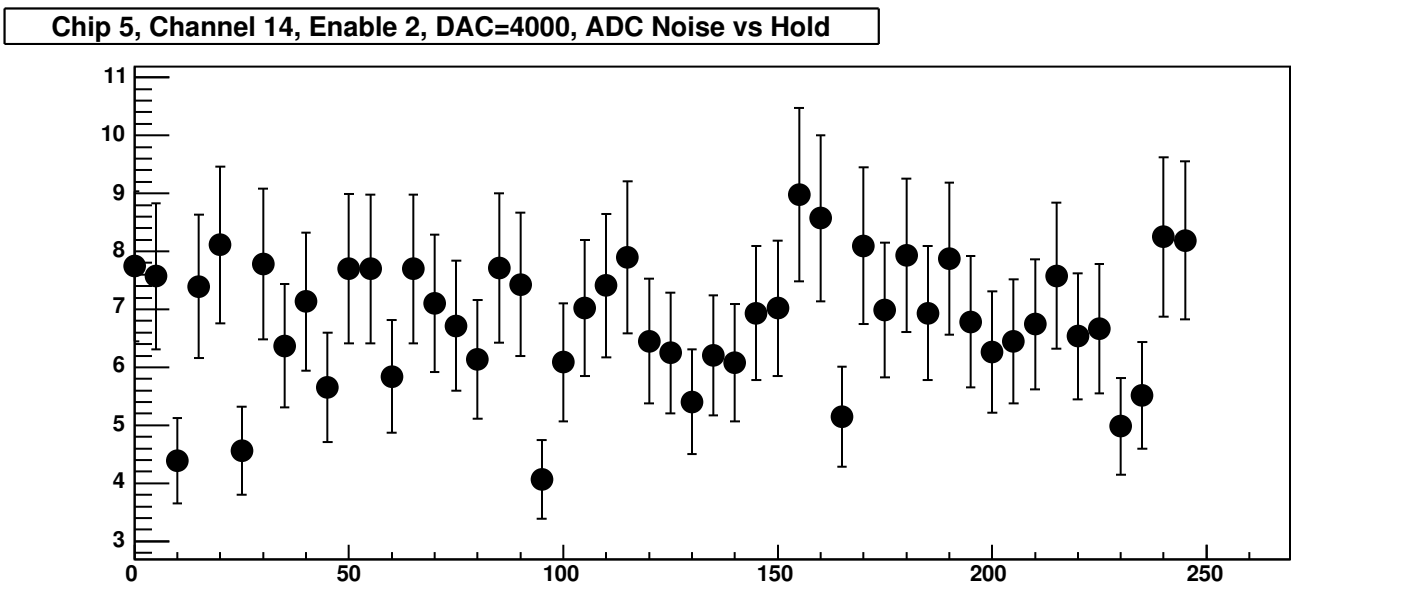
Chip 5, Channel 14, Enable 1, DAC=4000, ADC Noise vs Hold

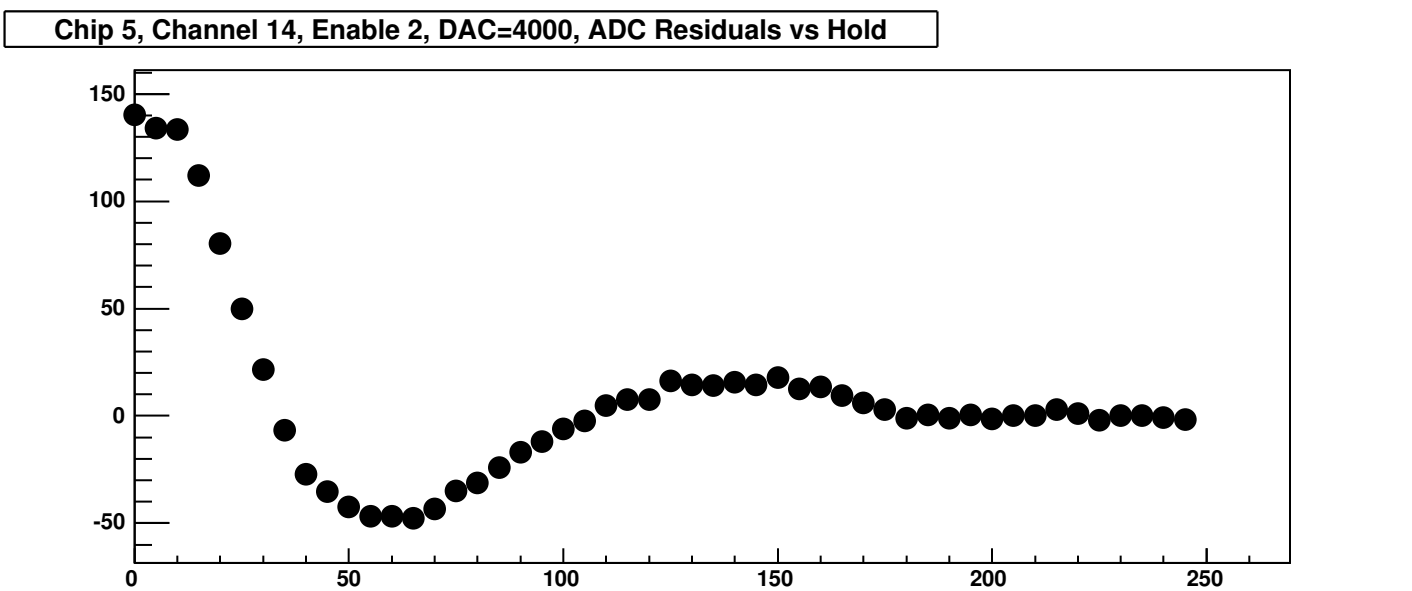


Chip 5, Channel 14, Enable 1, DAC=4000, ADC Residuals vs Hold

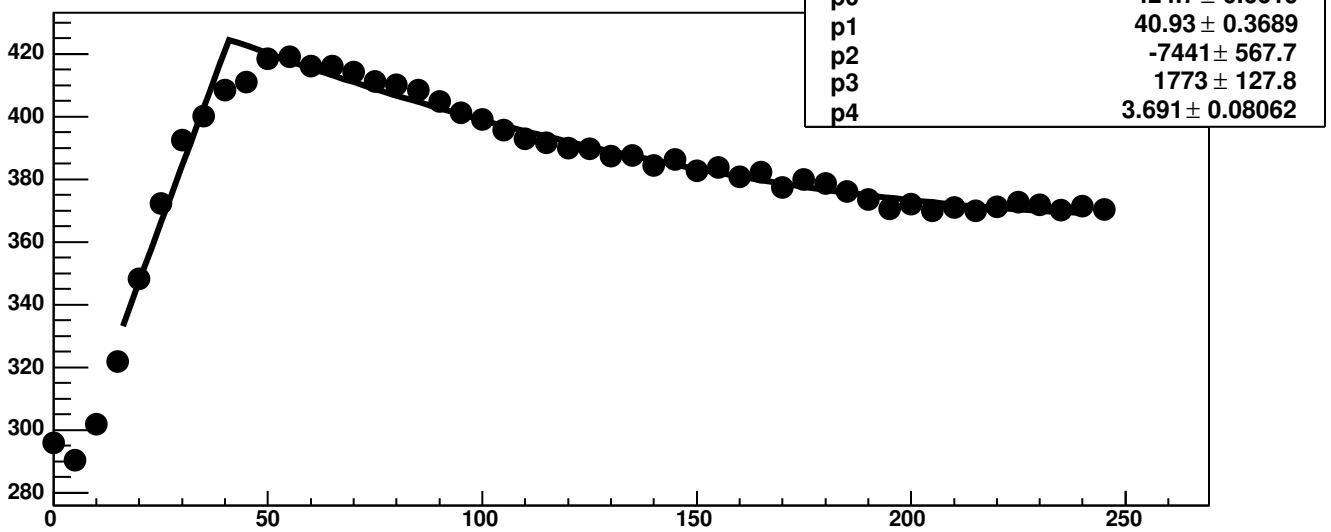



Chip 5, Channel 14, Enable 2, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.599e+04 / 41
p0 376.6 ± 1.104
p1 181.5 ± 1.028
p2 -1.451e+07 ± 5.977e+06
p3 1.383e+07 ± 5.632e+06
p4 1.228 ± 0.005419
400
350
300
250
200
150
0
50
100
150
200
250

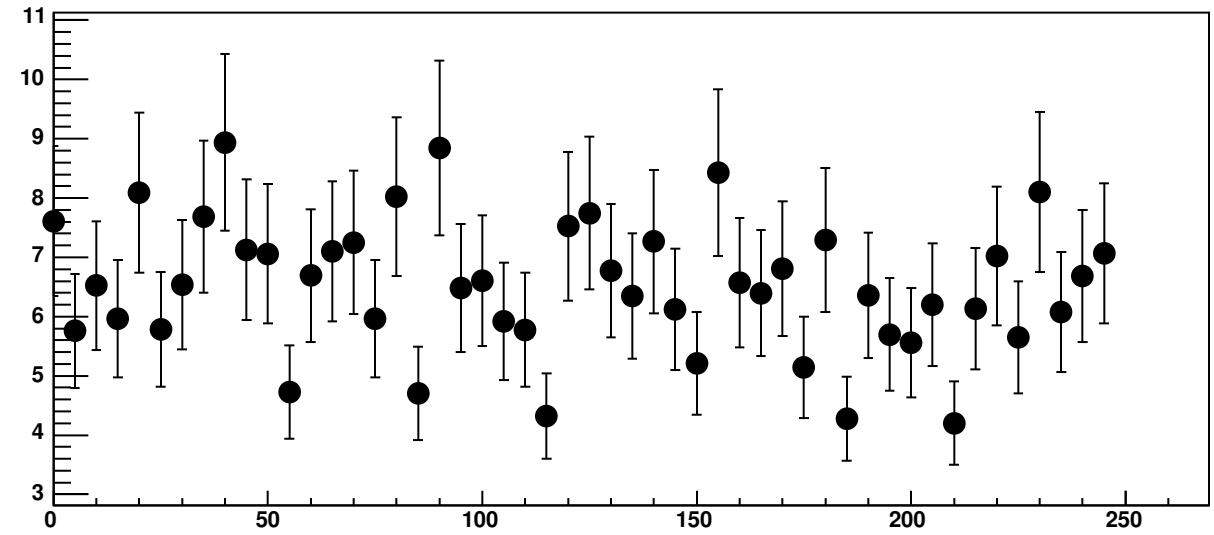

Chip 5, Channel 14, Enable 2, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250


Chip 5, Channel 14, Enable 2, DAC=4000, ADC Residuals vs Hold
150
100
50
0
-50
0
50
100
150
200
250

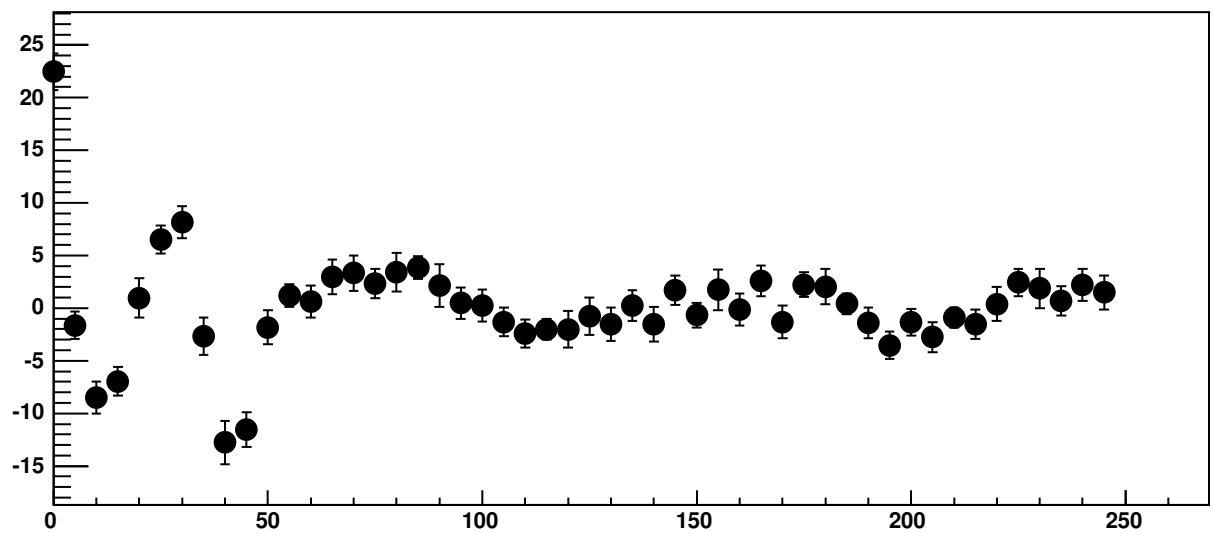
Chip 5, Channel 14, Enable 3, DAC=4000, ADC Mean vs Hold

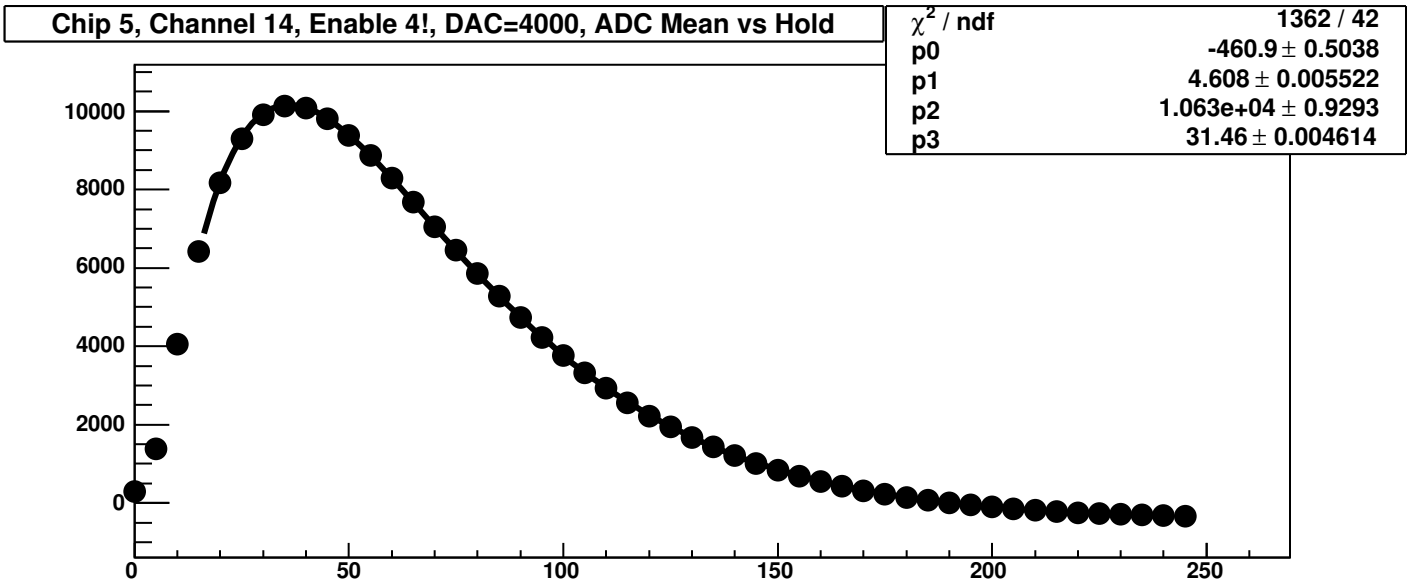


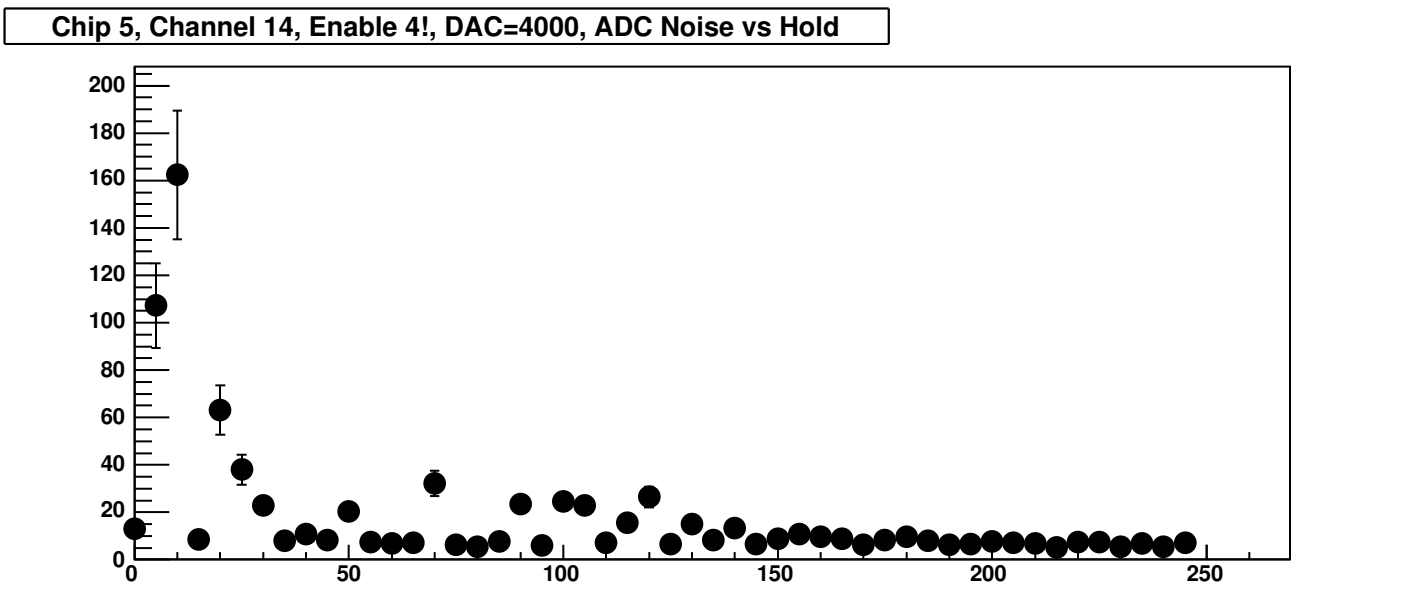
Chip 5, Channel 14, Enable 3, DAC=4000, ADC Noise vs Hold

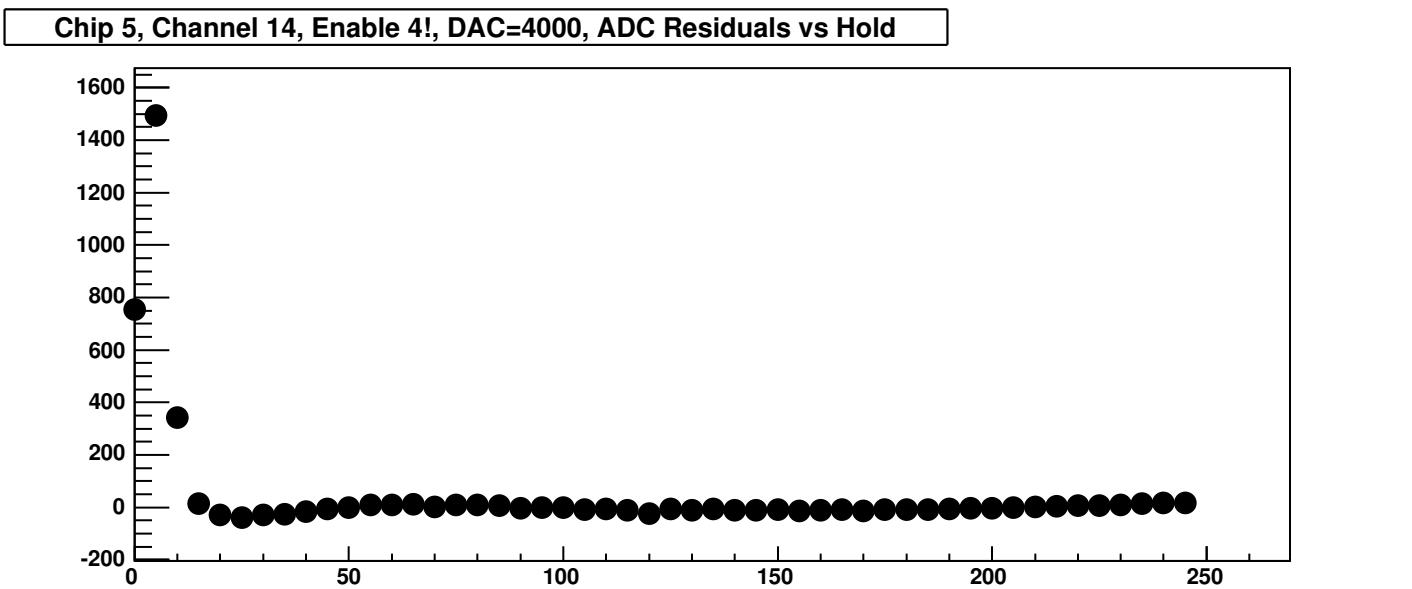


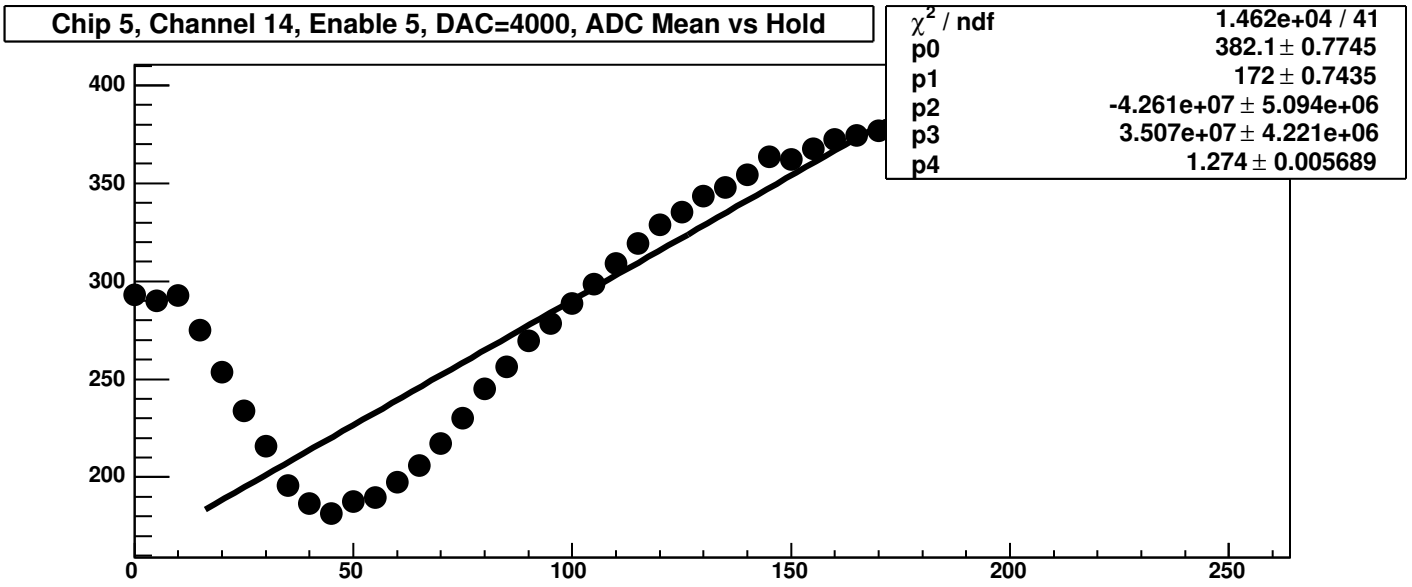
Chip 5, Channel 14, Enable 3, DAC=4000, ADC Residuals vs Hold

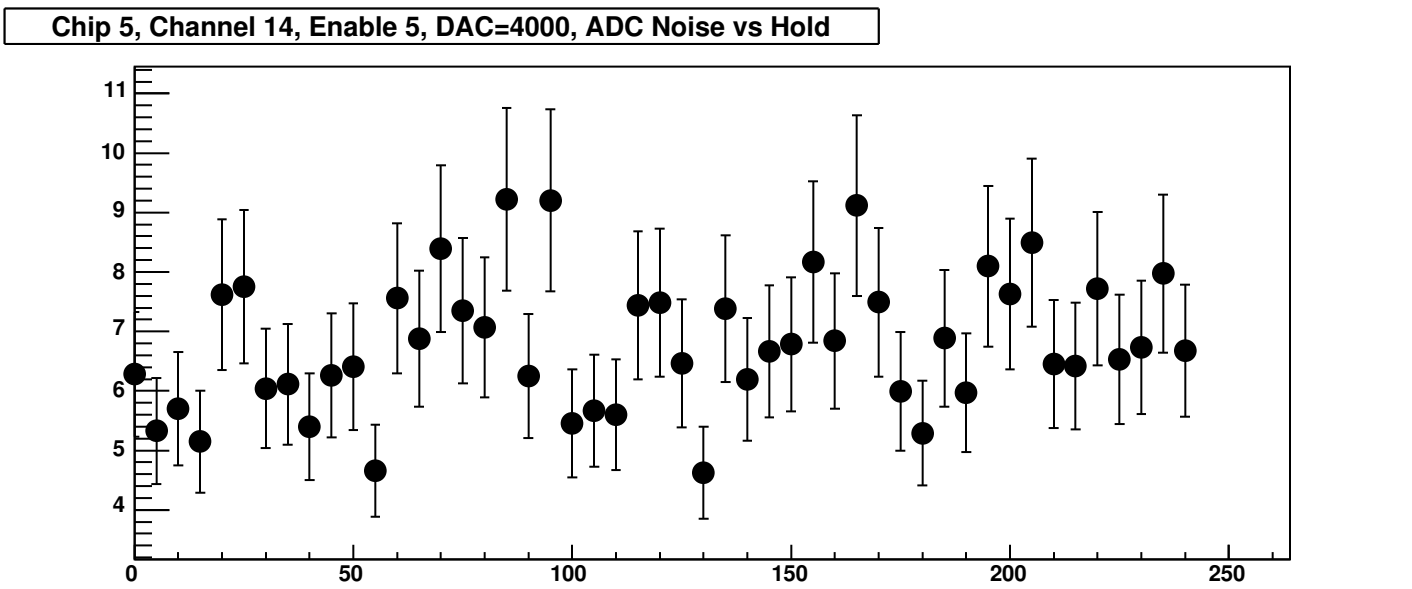


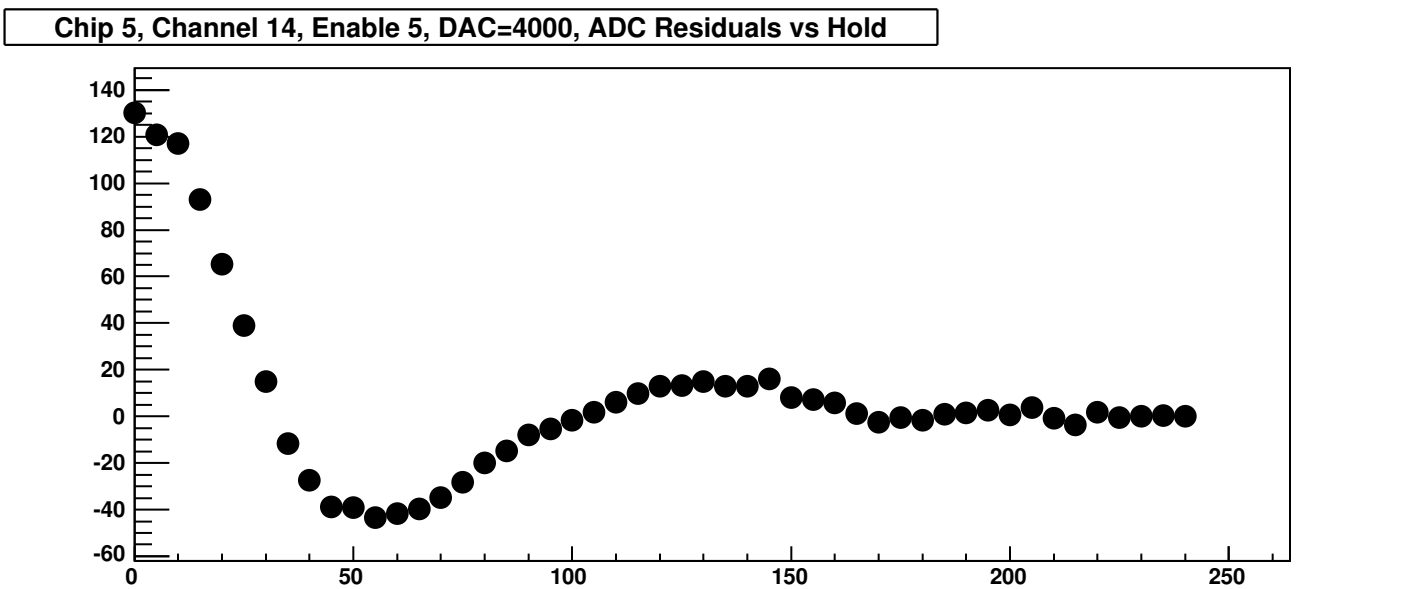

Chip 5, Channel 14, Enable 4!, DAC=4000, ADC Mean vs Hold
χ² / ndf 1362 / 42
p0 -460.9 ± 0.5038
p1 4.608 ± 0.005522
p2 1.063e+04 ± 0.9293
p3 31.46 ± 0.004614
10000
8000
6000
4000
2000
0
0
50
100
150
200
250


Chip 5, Channel 14, Enable 4!, DAC=4000, ADC Noise vs Hold
200
180
160
140
120
100
80
60
40
20
0
0
50
100
150
200
250

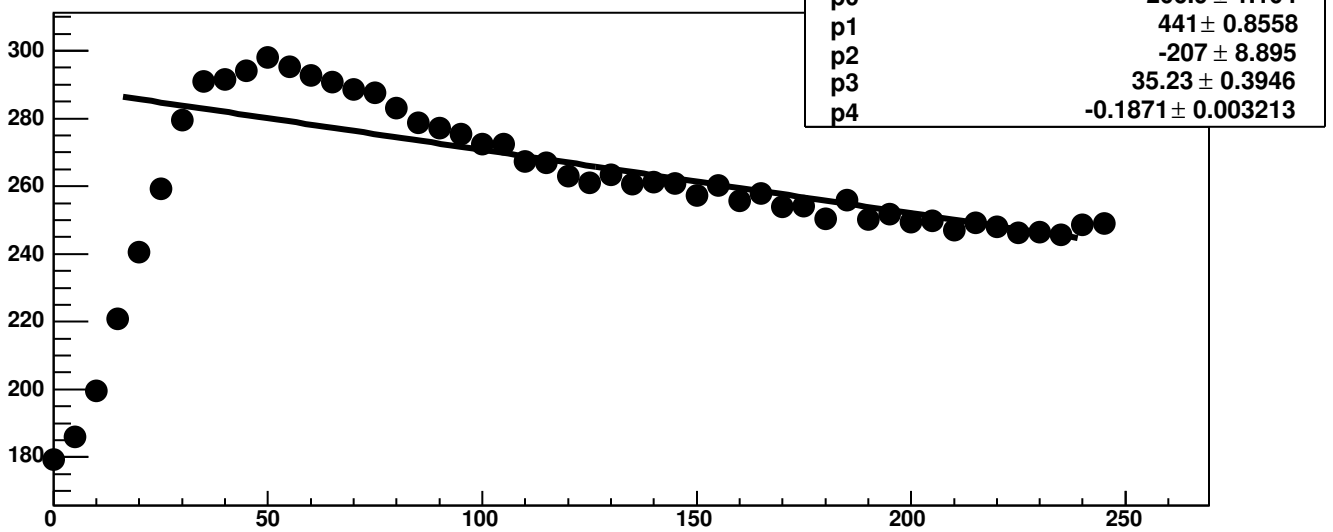

Chip 5, Channel 14, Enable 4!, DAC=4000, ADC Residuals vs Hold
1600
1400
1200
1000
800
600
400
200
0
-200
0
50
100
150
200
250


Chip 5, Channel 14, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 1.462e+04 / 41
p0 382.1 ± 0.7745
p1 172 ± 0.7435
p2 -4.261e+07 ± 5.094e+06
p3 3.507e+07 ± 4.221e+06
p4 1.274 ± 0.005689
400
350
300
250
200
0
50
100
150
200
250

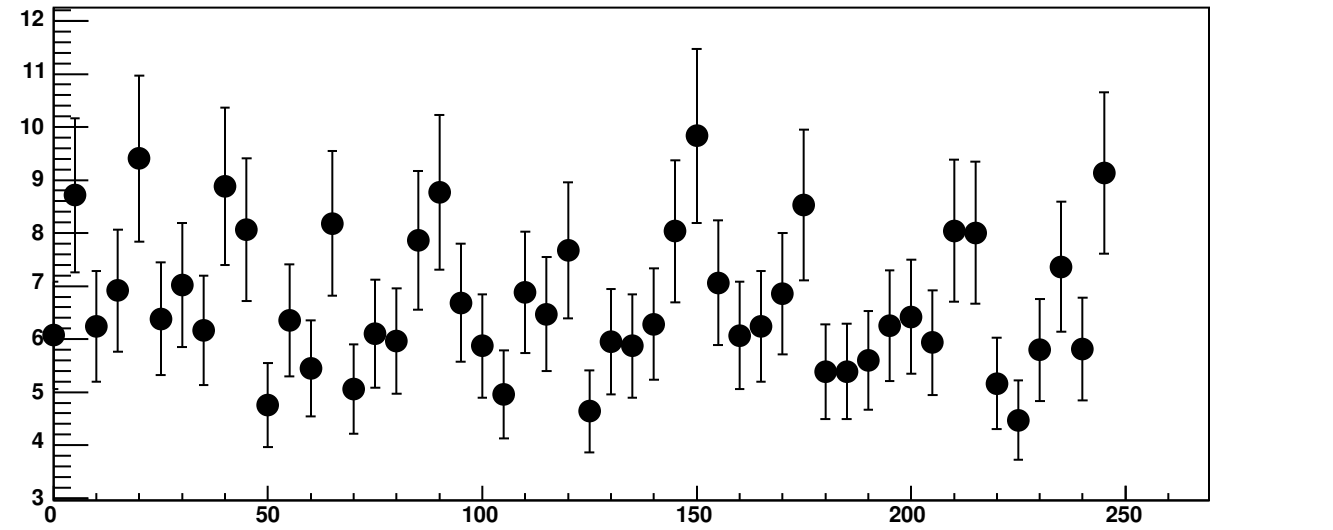

Chip 5, Channel 14, Enable 5, DAC=4000, ADC Noise vs Hold
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 5, Channel 14, Enable 5, DAC=4000, ADC Residuals vs Hold
140
120
100
80
60
40
20
0
-20
-40
-60
0
50
100
150
200
250

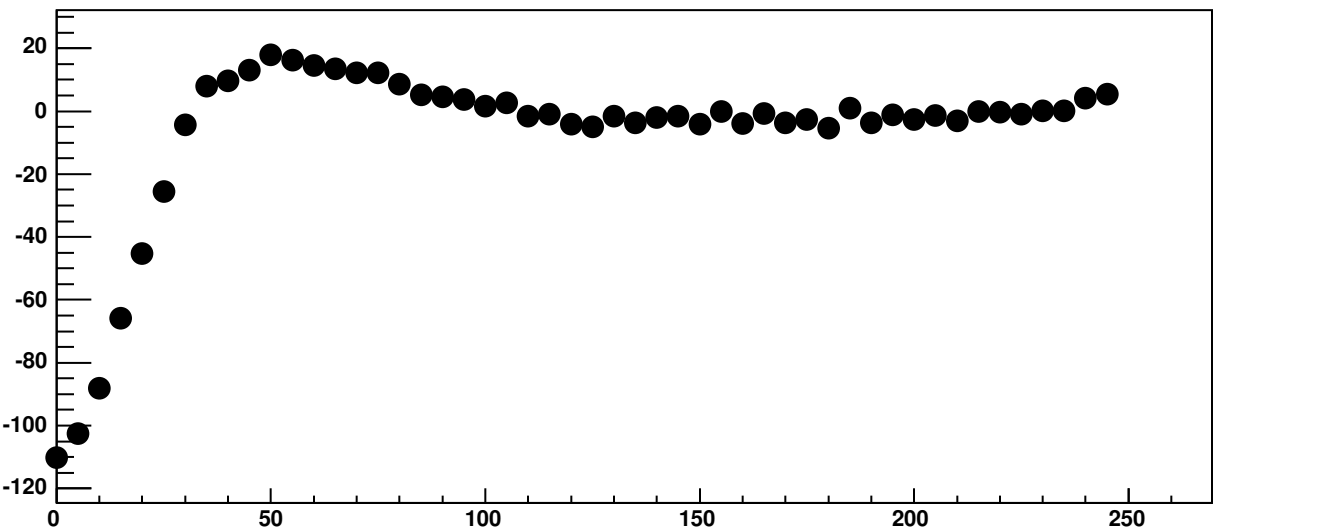
Chip 5, Channel 15, Enable 0, DAC=4000, ADC Mean vs Hold



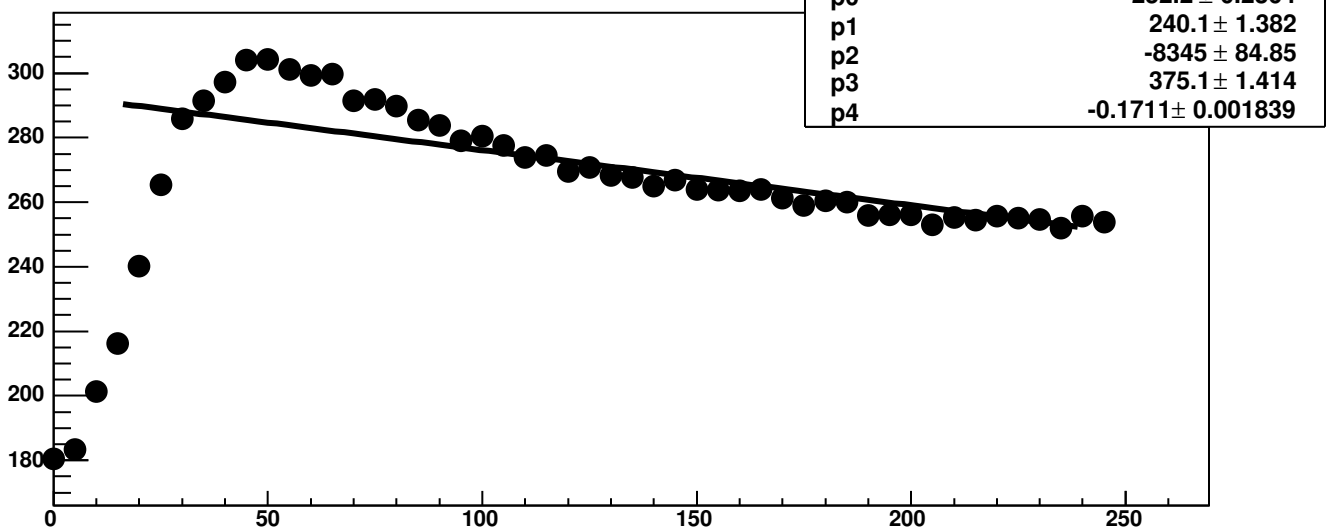
Chip 5, Channel 15, Enable 0, DAC=4000, ADC Noise vs Hold



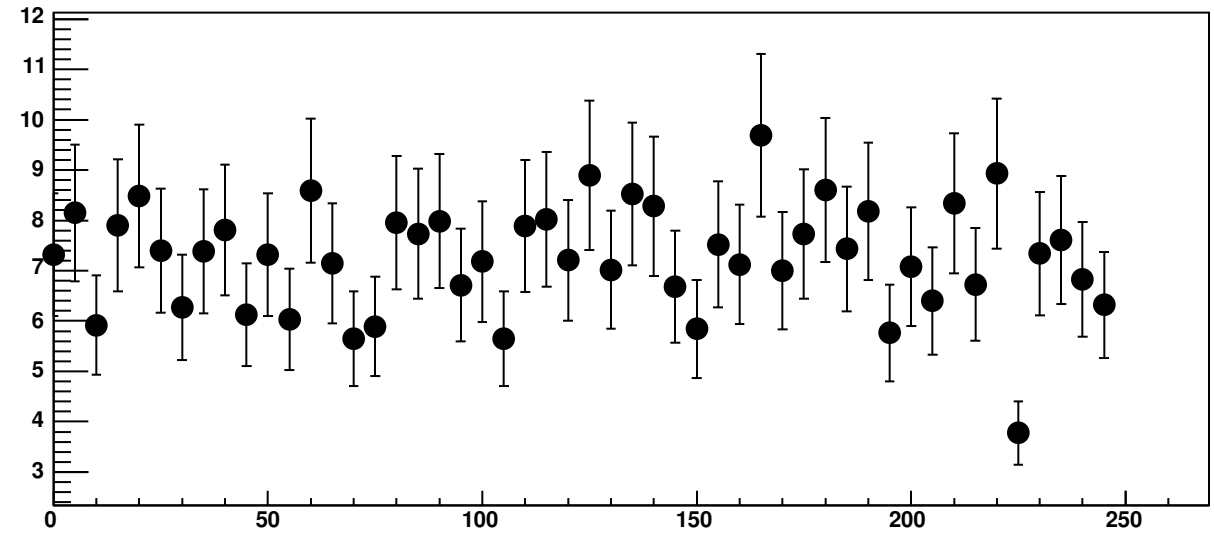
Chip 5, Channel 15, Enable 0, DAC=4000, ADC Residuals vs Hold



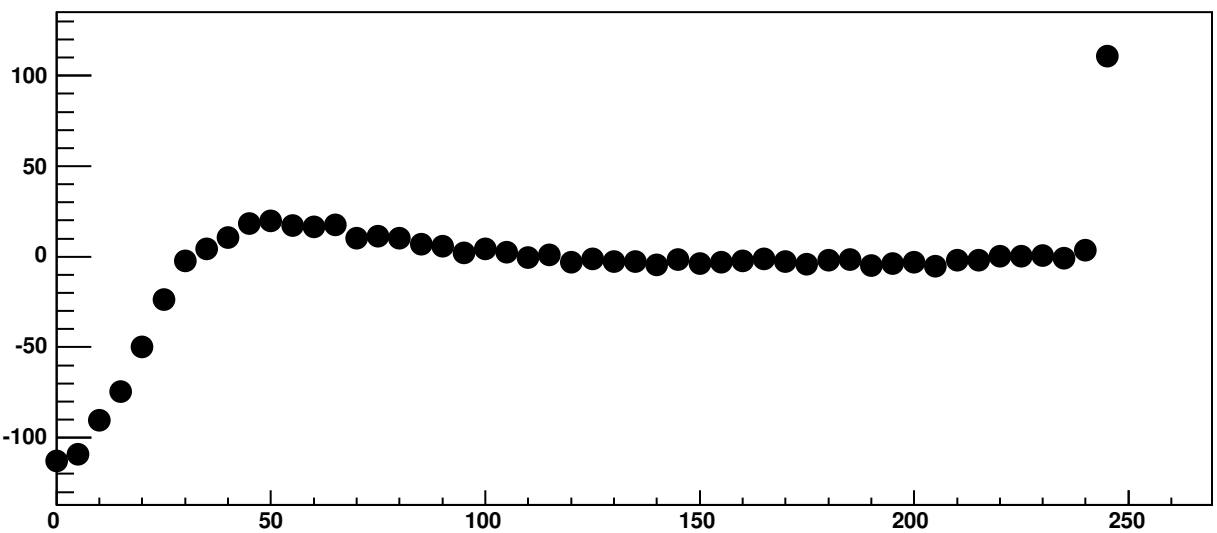
Chip 5, Channel 15, Enable 1, DAC=4000, ADC Mean vs Hold



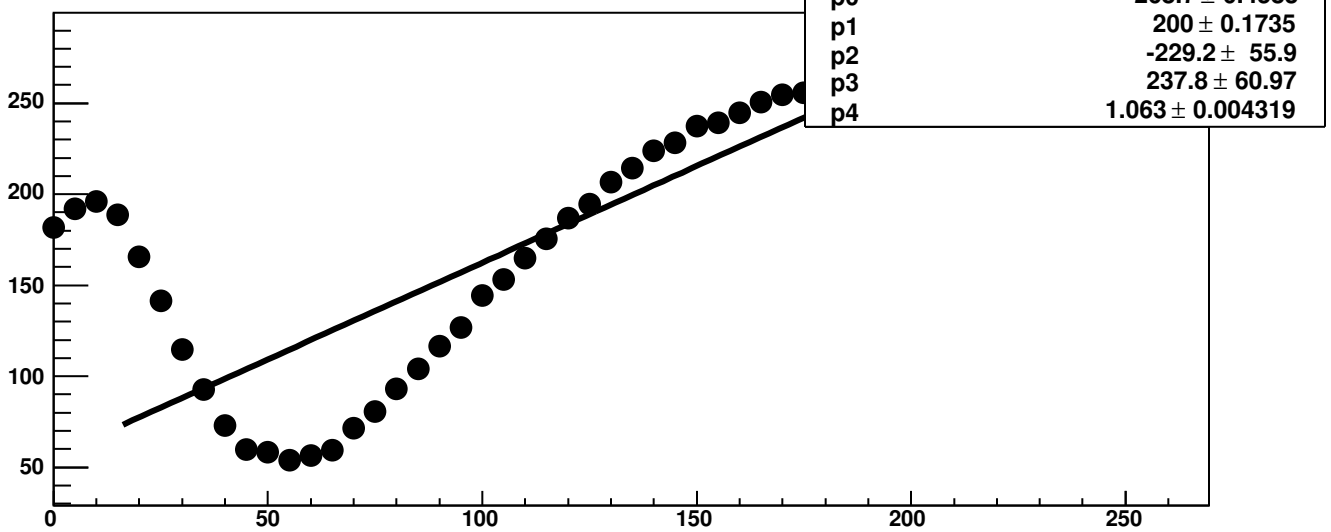
Chip 5, Channel 15, Enable 1, DAC=4000, ADC Noise vs Hold



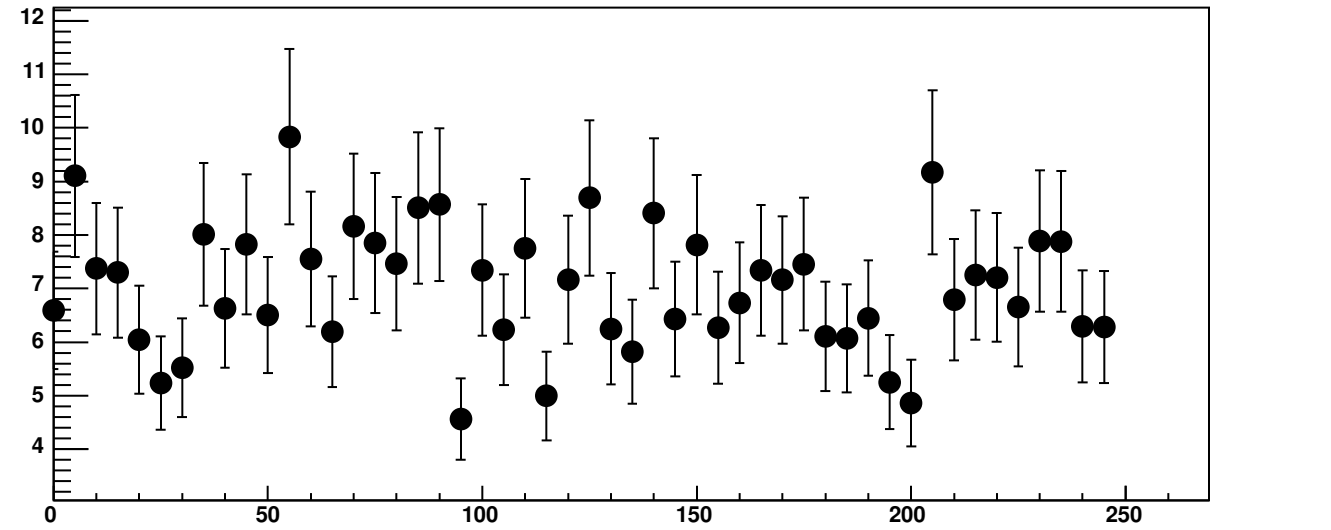
Chip 5, Channel 15, Enable 1, DAC=4000, ADC Residuals vs Hold



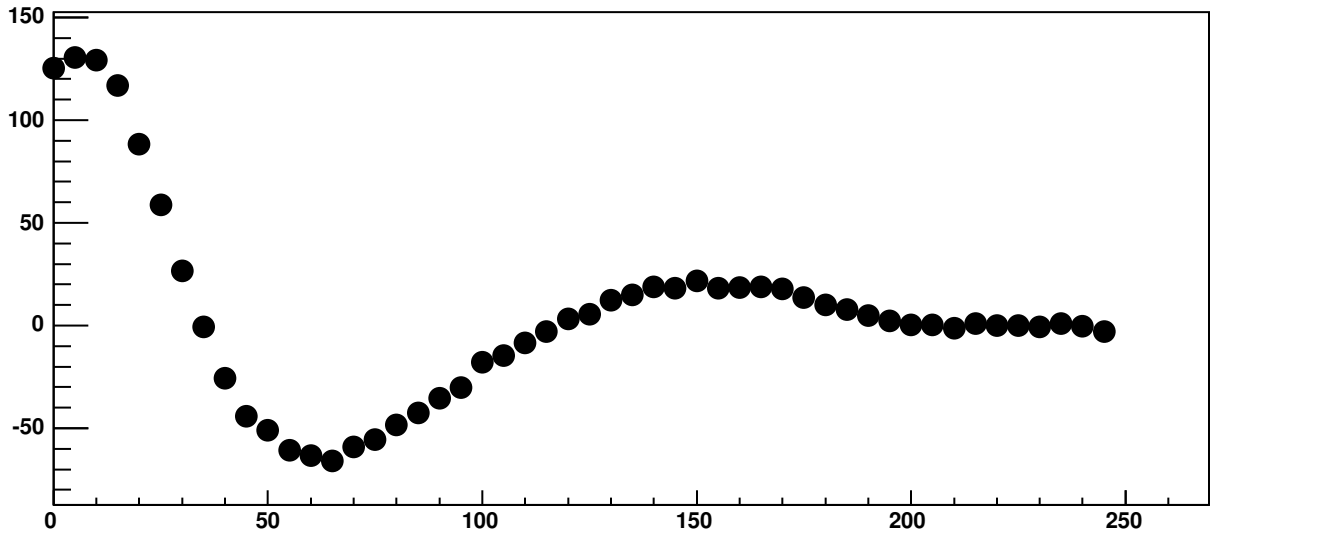
Chip 5, Channel 15, Enable 2, DAC=4000, ADC Mean vs Hold



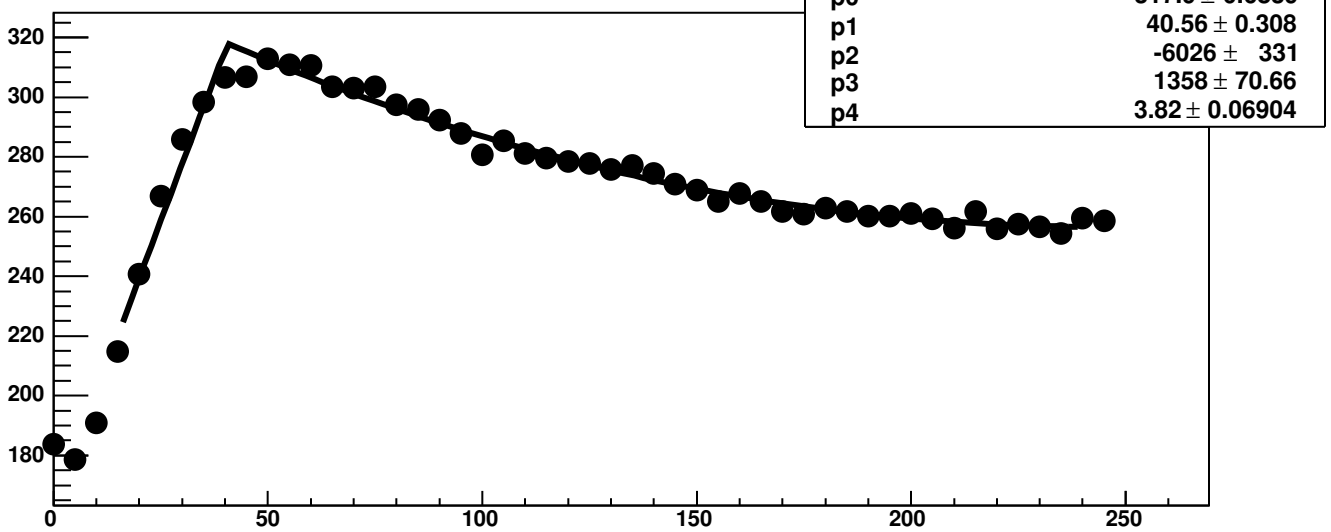
Chip 5, Channel 15, Enable 2, DAC=4000, ADC Noise vs Hold



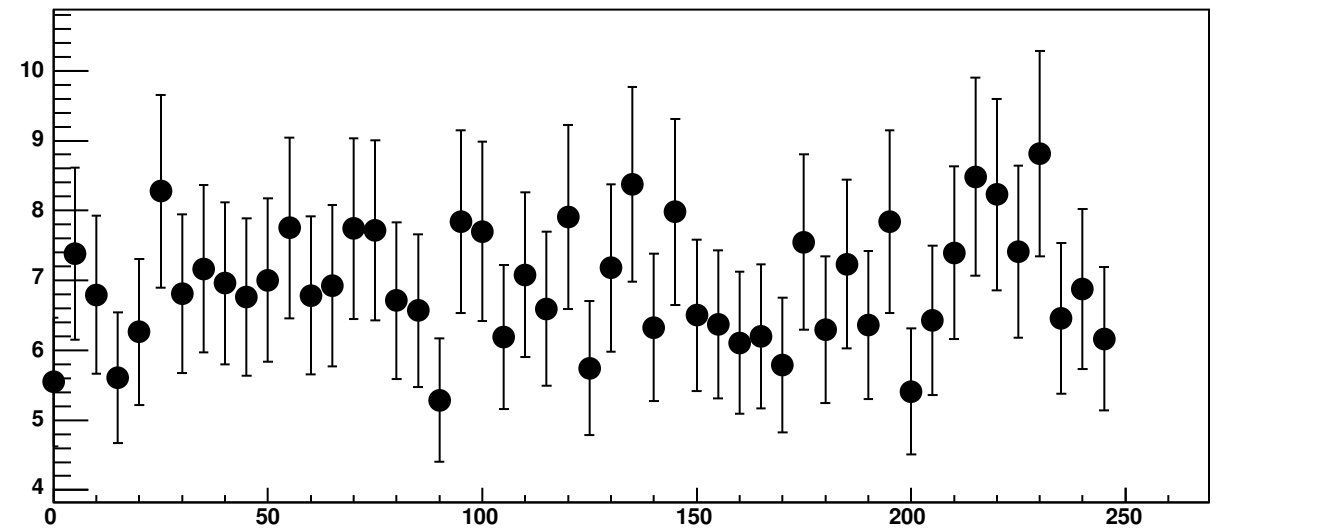
Chip 5, Channel 15, Enable 2, DAC=4000, ADC Residuals vs Hold



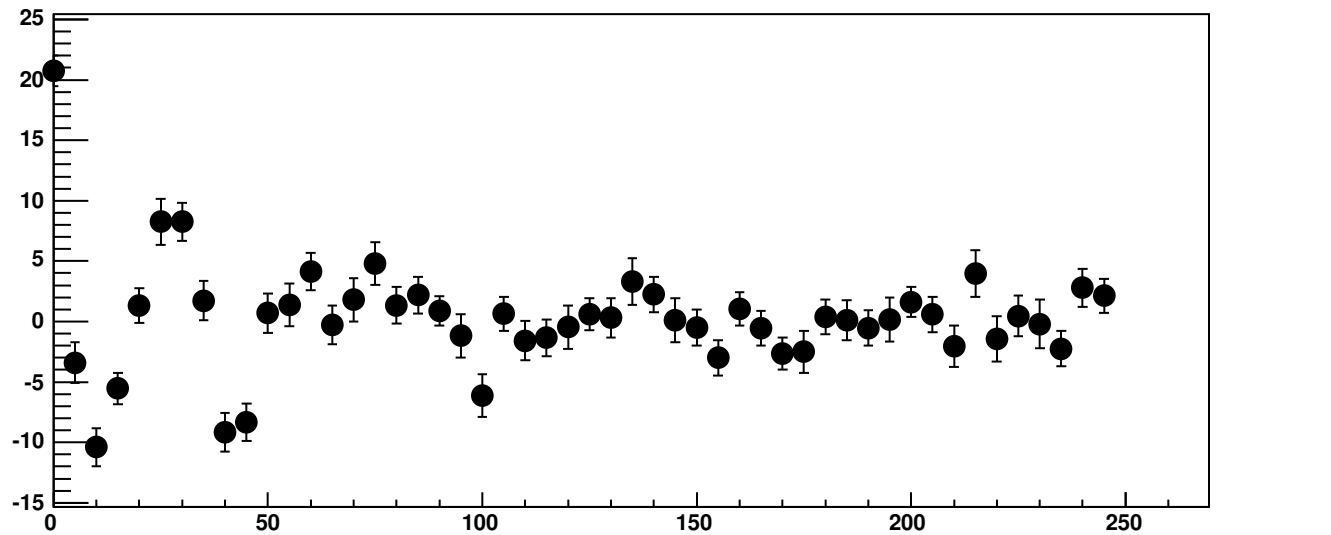
Chip 5, Channel 15, Enable 3, DAC=4000, ADC Mean vs Hold



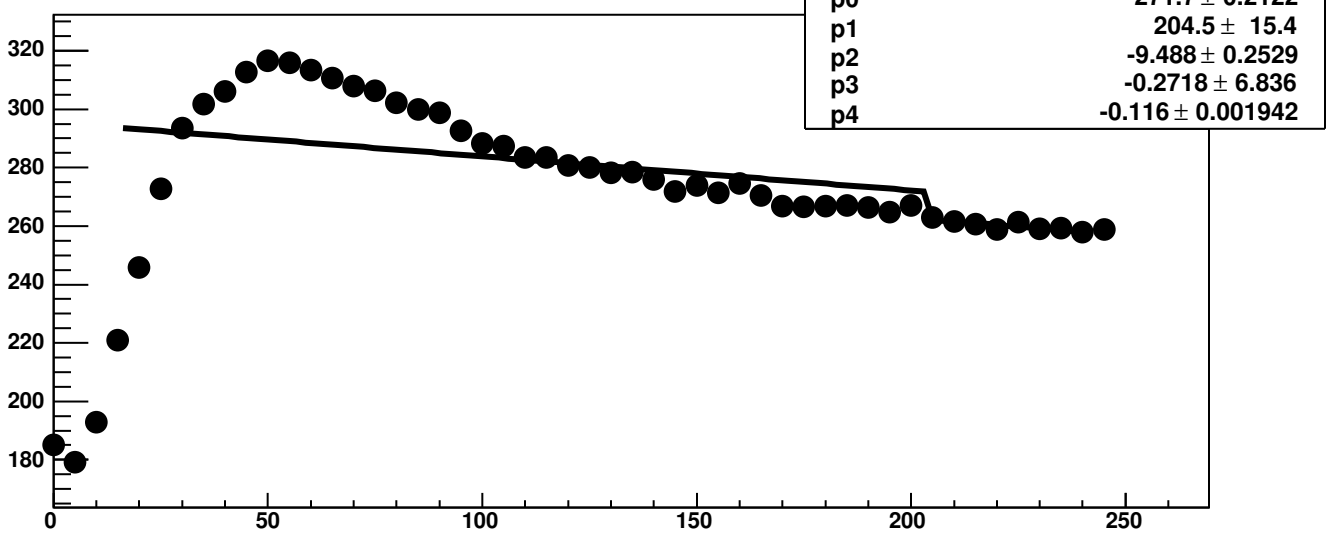
Chip 5, Channel 15, Enable 3, DAC=4000, ADC Noise vs Hold



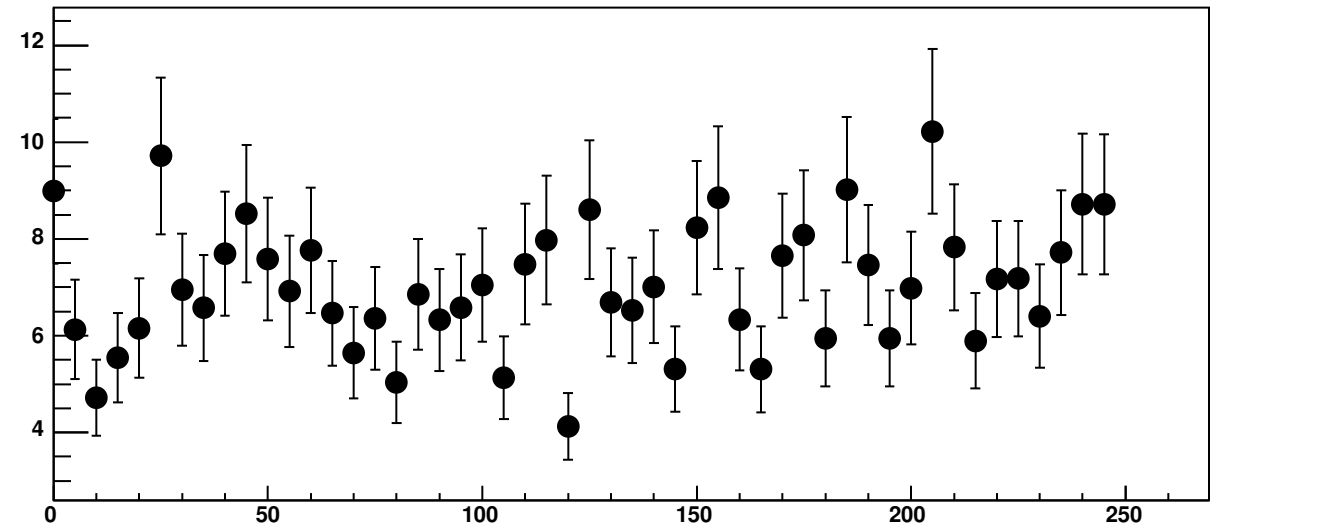
Chip 5, Channel 15, Enable 3, DAC=4000, ADC Residuals vs Hold



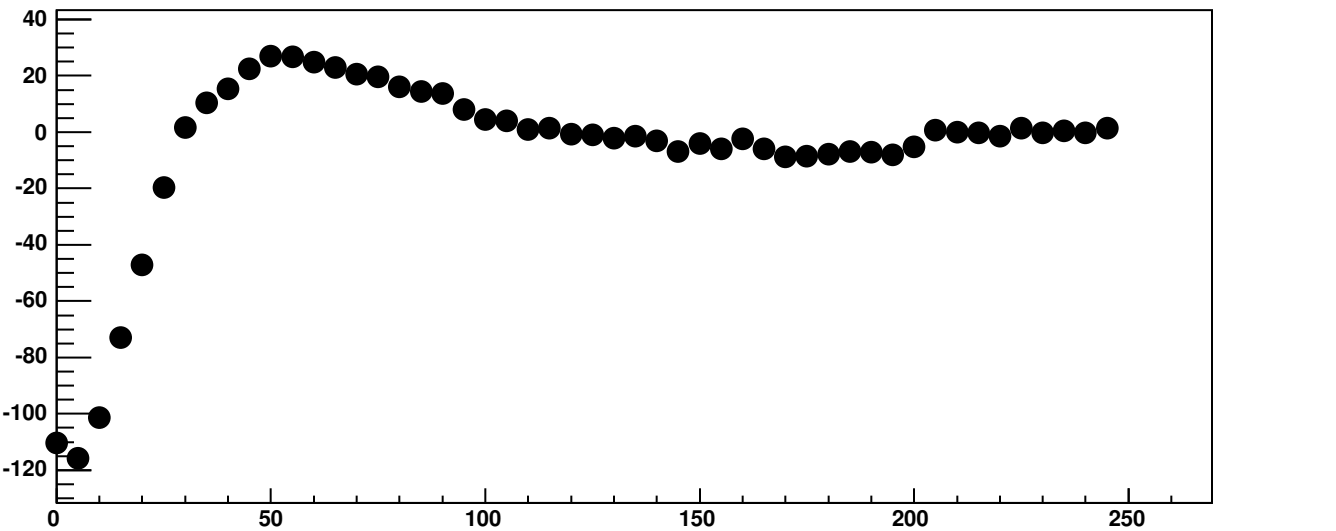
Chip 5, Channel 15, Enable 4, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 15, Enable 4, DAC=4000, ADC Noise vs Hold

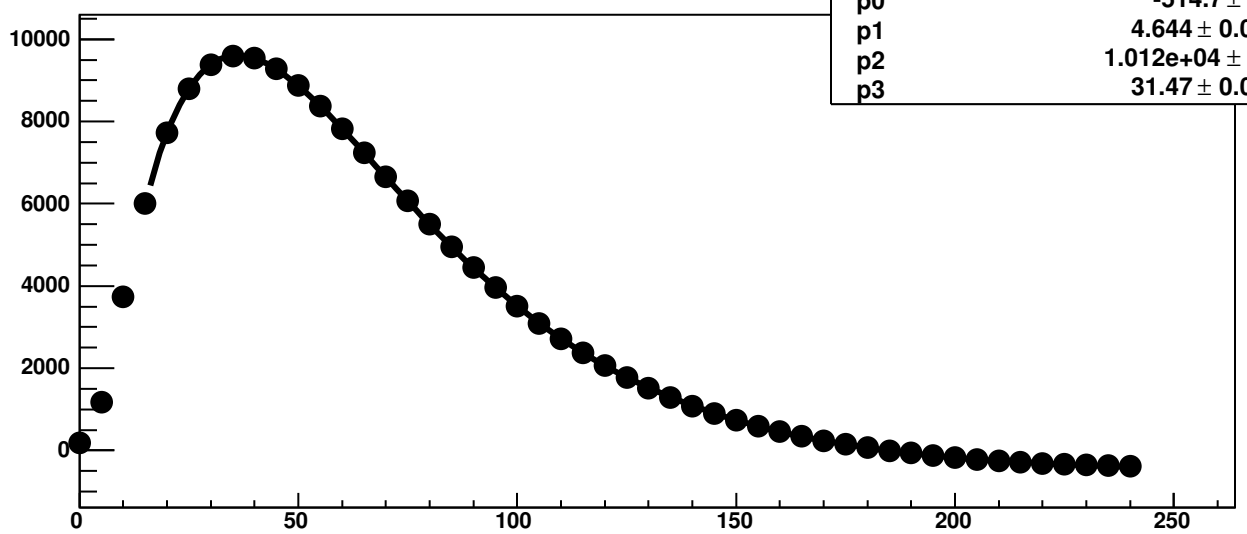


Chip 5, Channel 15, Enable 4, DAC=4000, ADC Residuals vs Hold

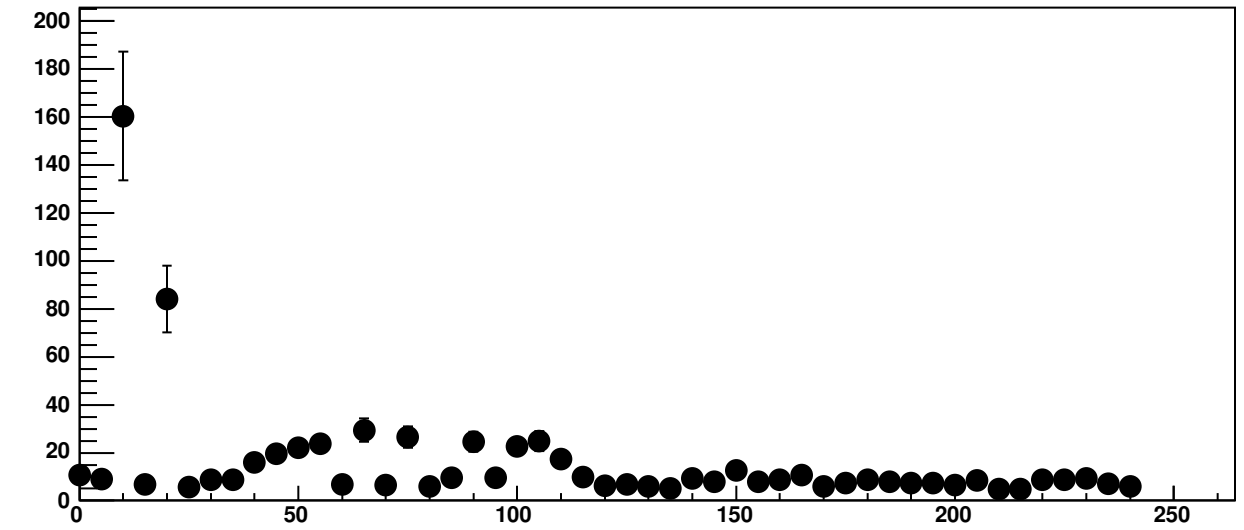


Chip 5, Channel 15, Enable 5!, DAC=4000, ADC Mean vs Hold

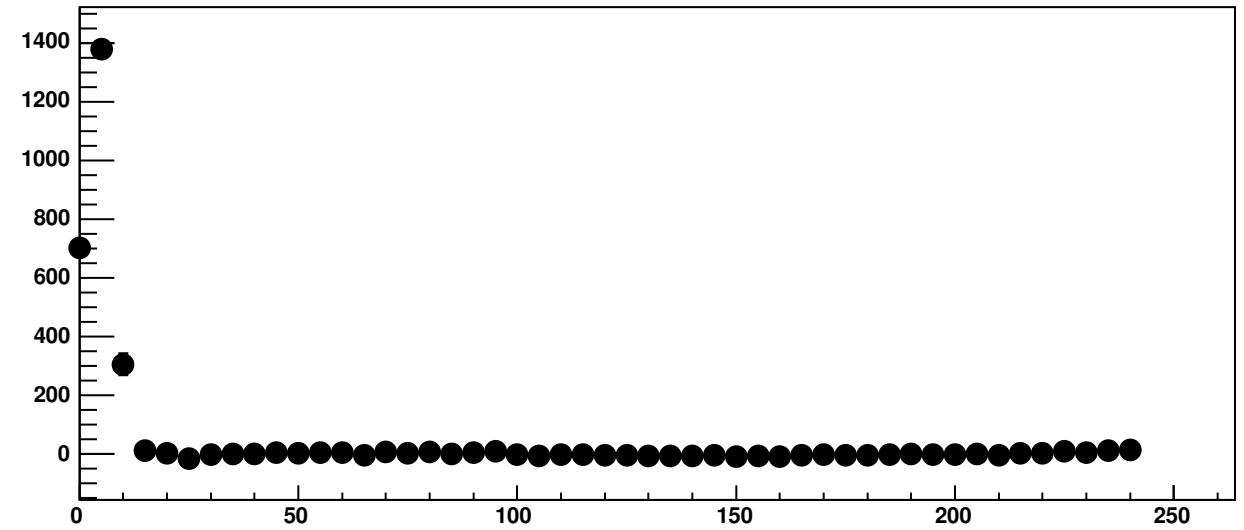
χ^2 / ndf	594.7 / 42
p0	-514.7 ± 0.5239
p1	4.644 ± 0.005109
p2	1.012e+04 ± 0.9027
p3	31.47 ± 0.004628



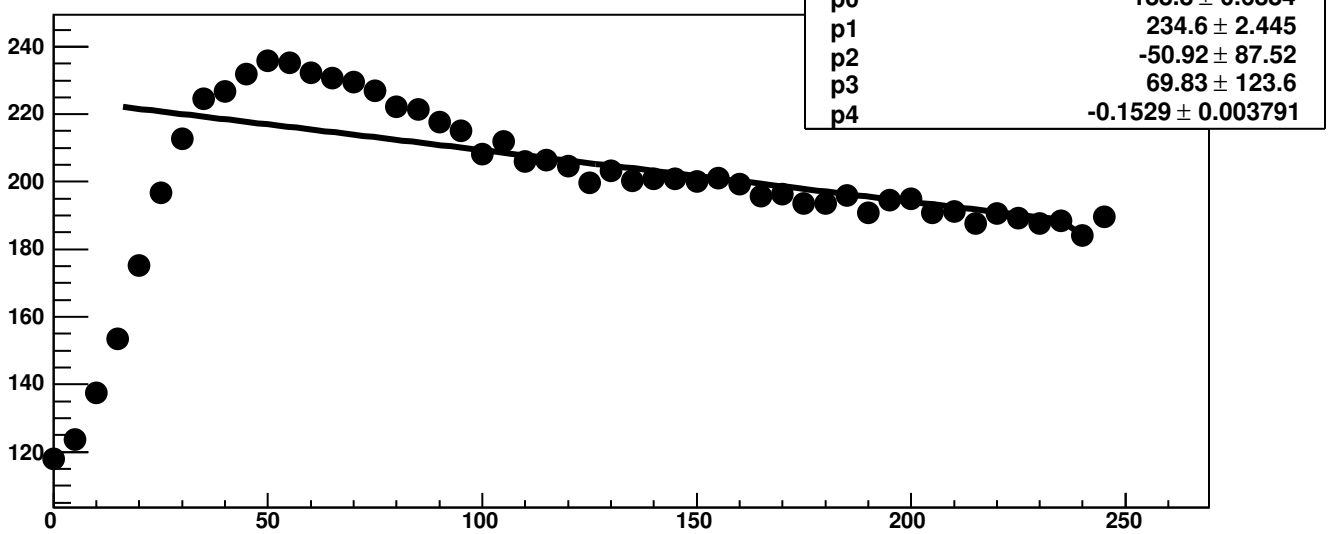
Chip 5, Channel 15, Enable 5!, DAC=4000, ADC Noise vs Hold



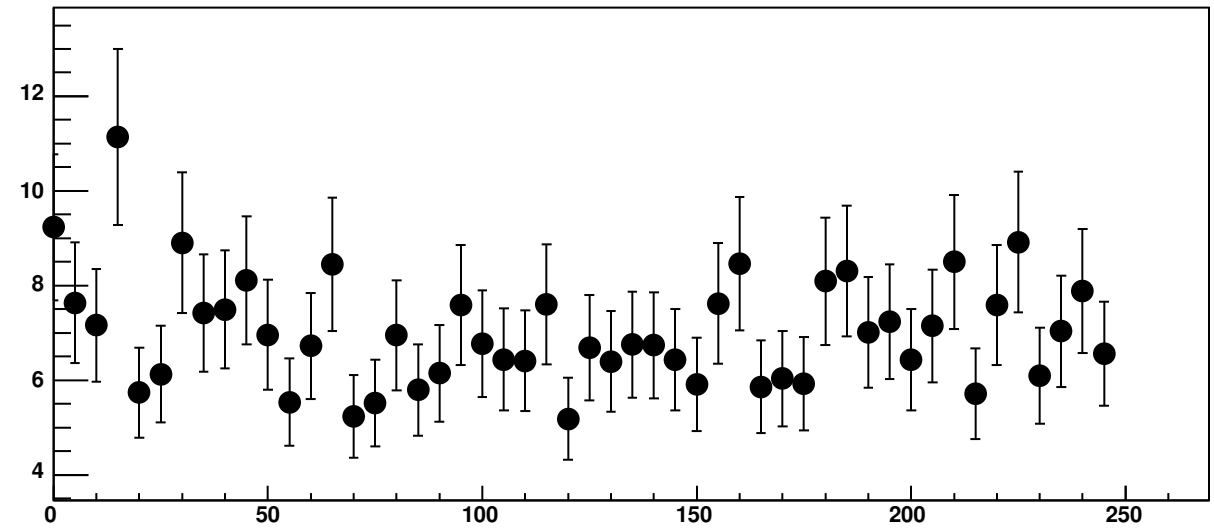
Chip 5, Channel 15, Enable 5!, DAC=4000, ADC Residuals vs Hold



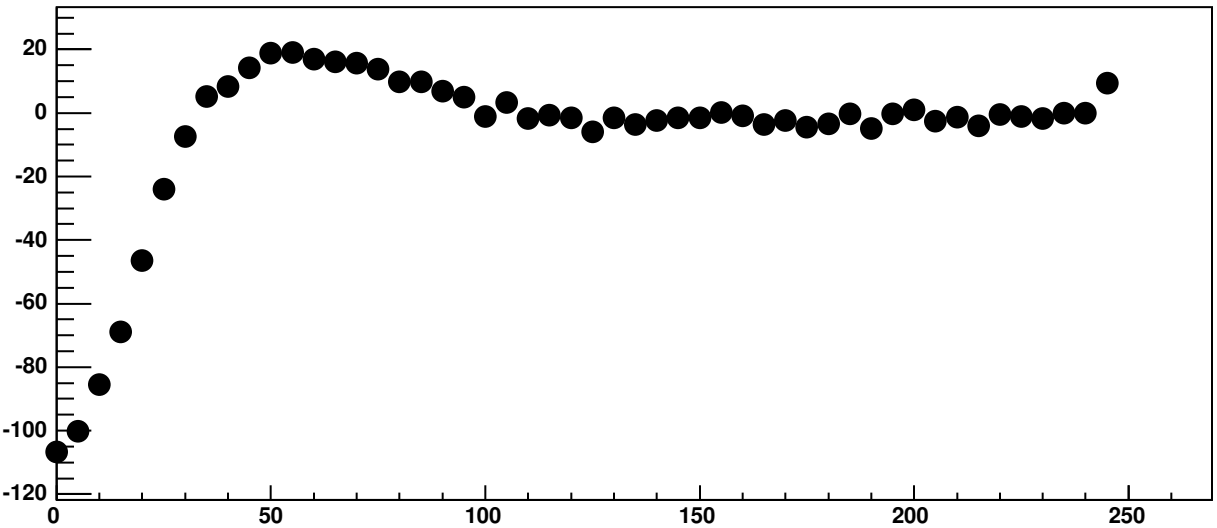
Chip 5, Channel 16, Enable 0, DAC=4000, ADC Mean vs Hold

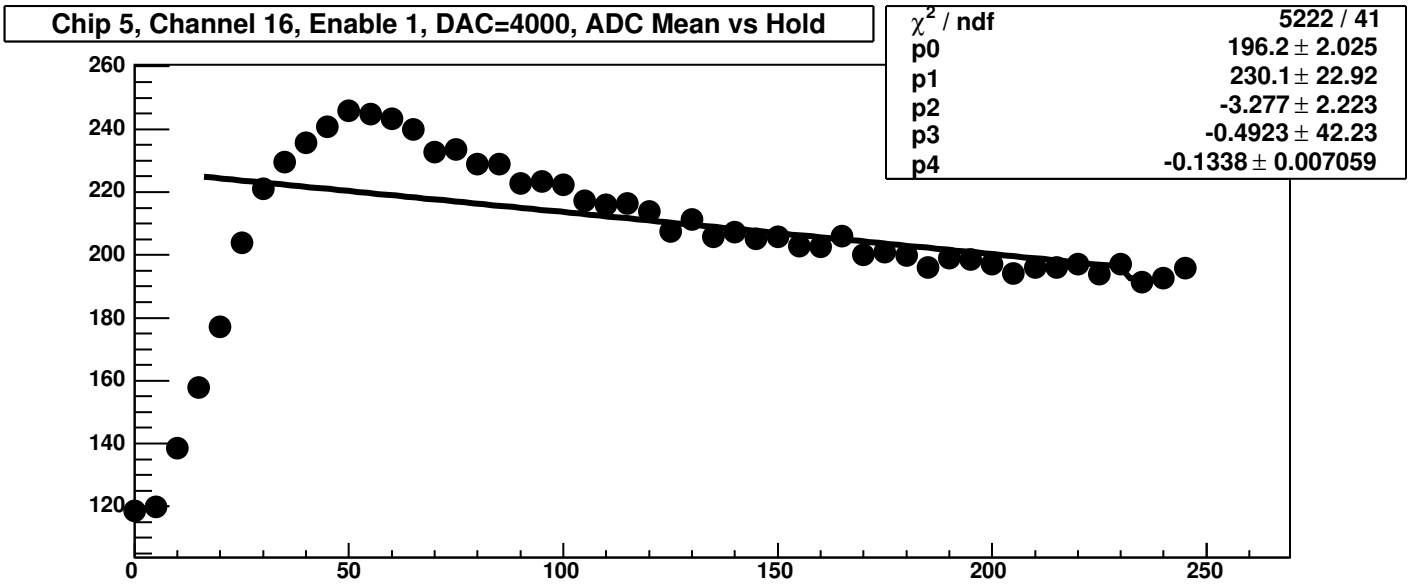


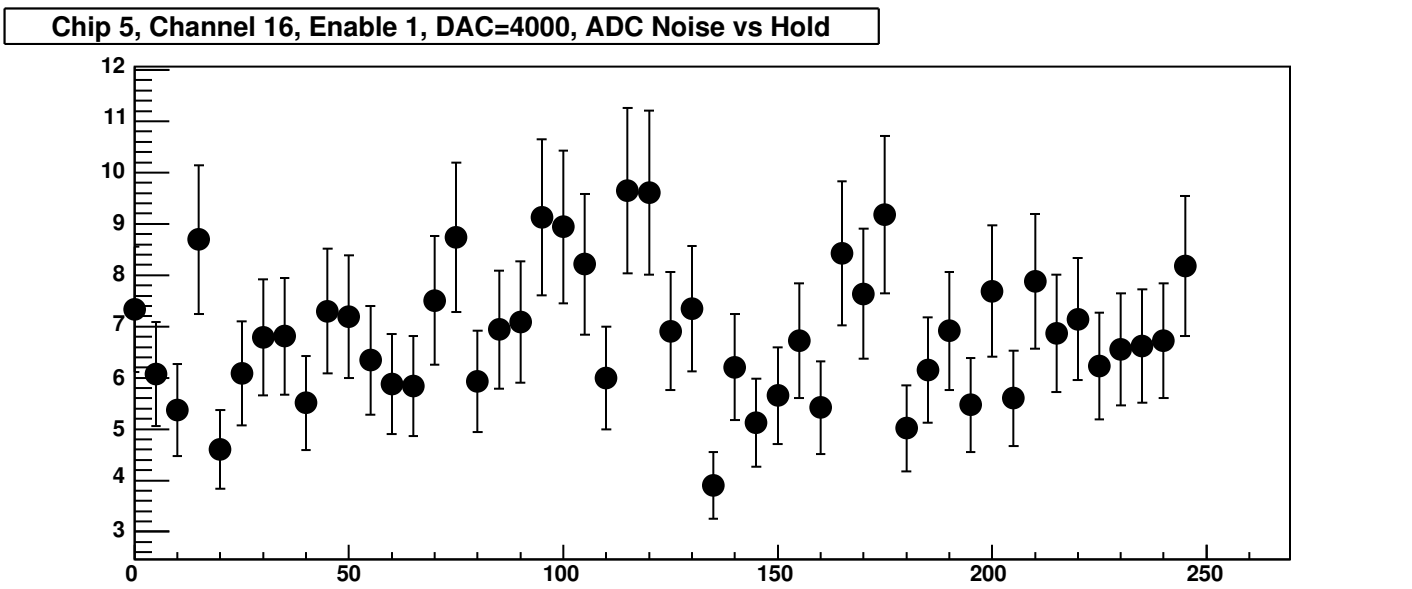
Chip 5, Channel 16, Enable 0, DAC=4000, ADC Noise vs Hold

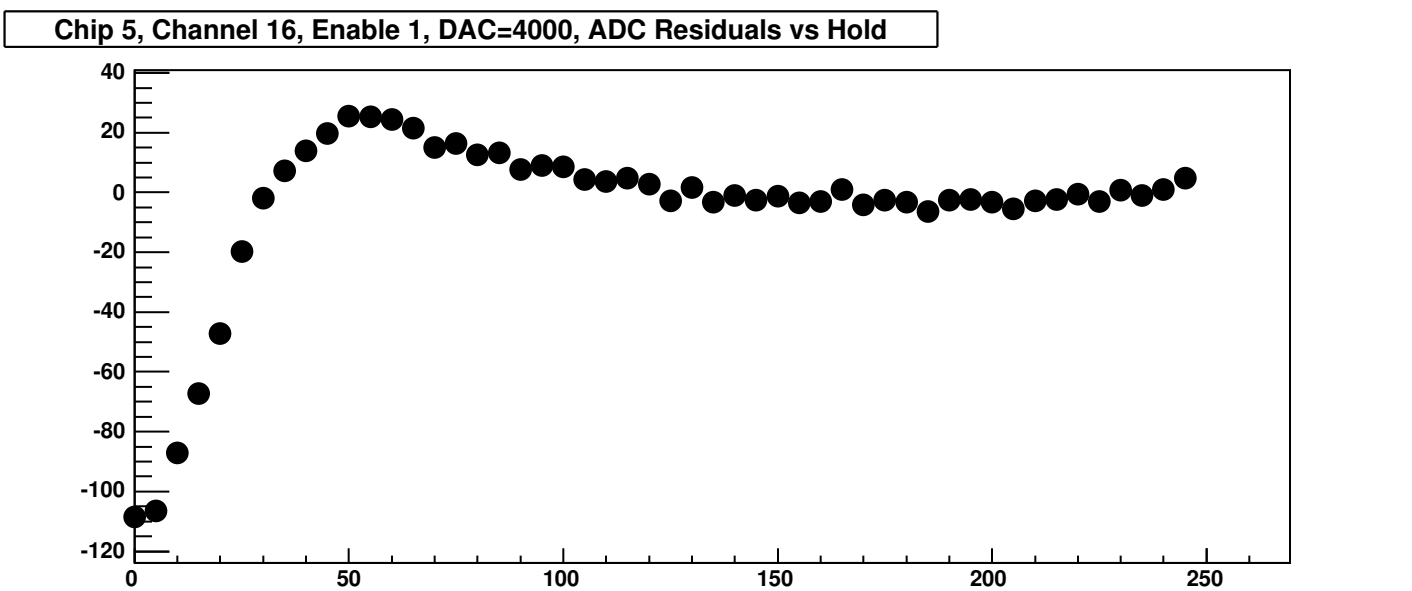


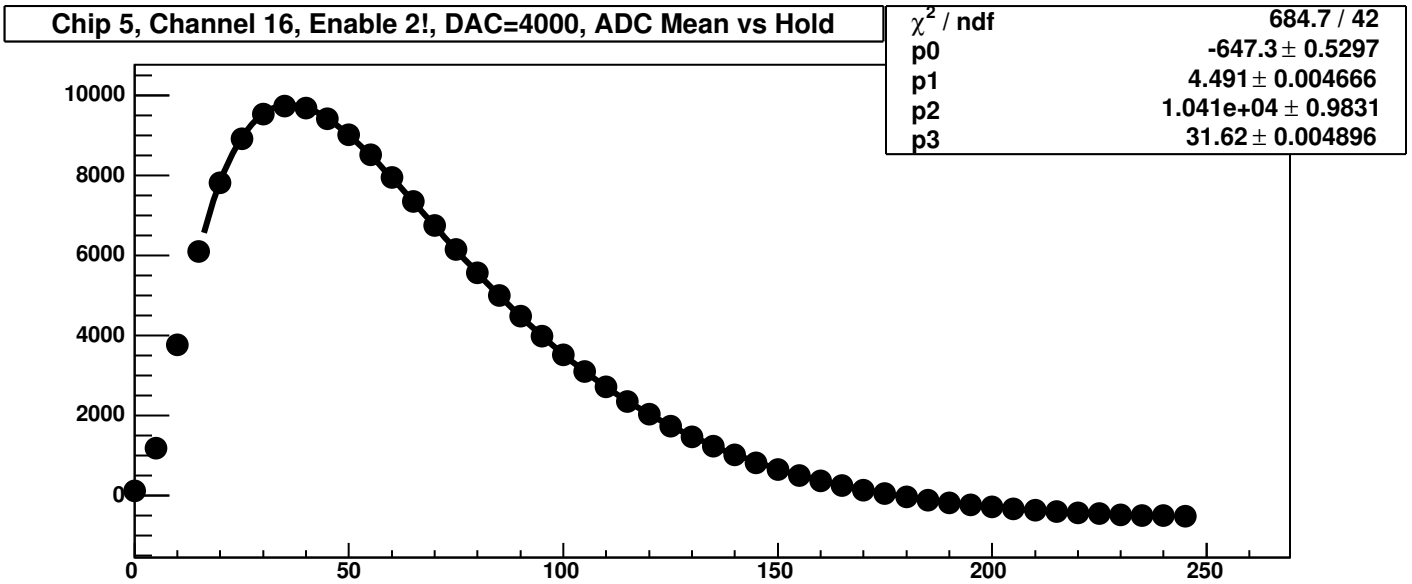
Chip 5, Channel 16, Enable 0, DAC=4000, ADC Residuals vs Hold

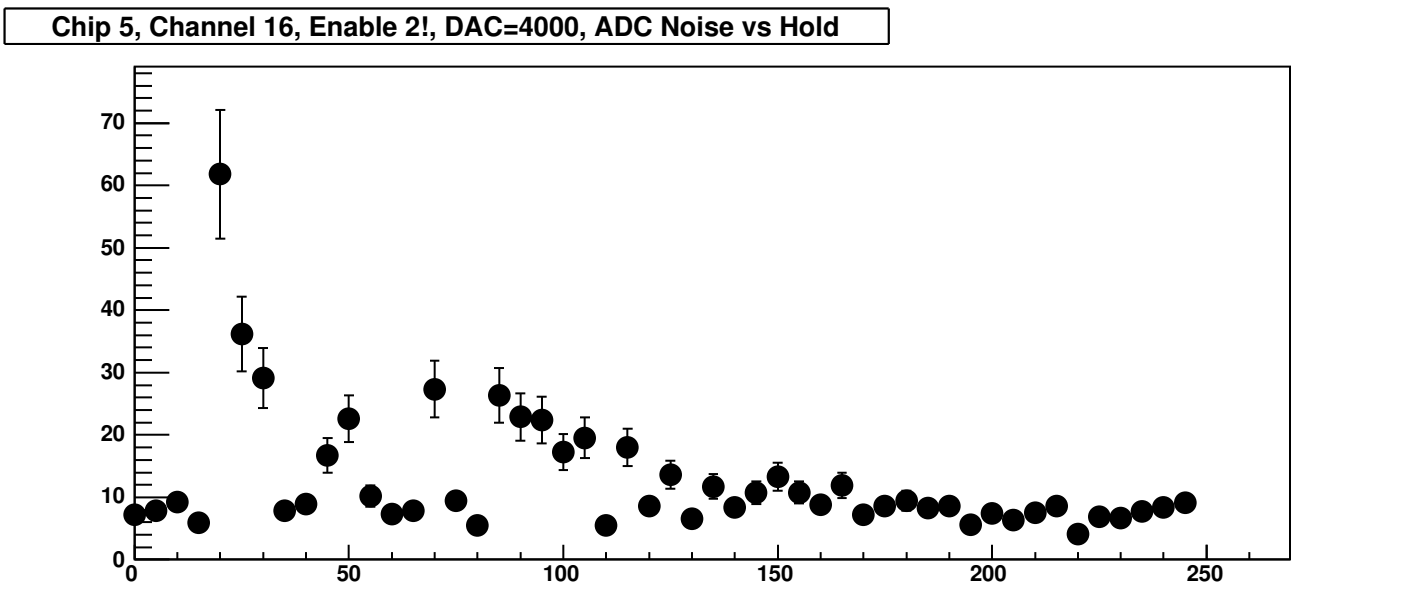


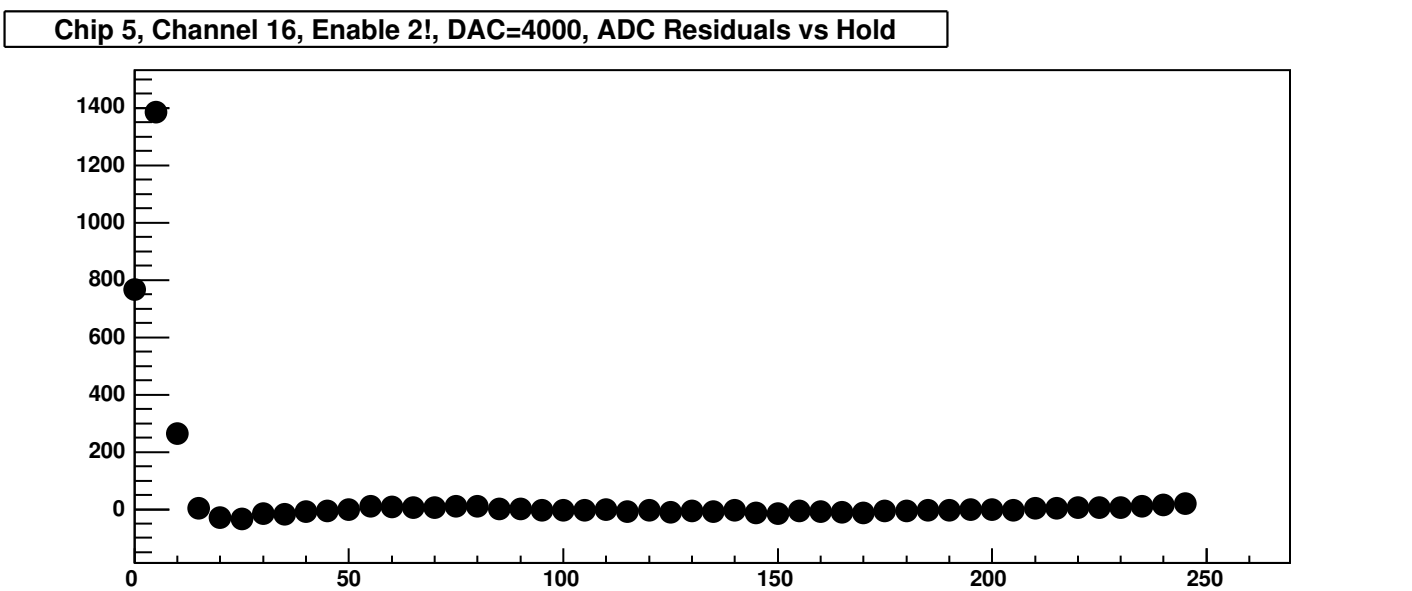

Chip 5, Channel 16, Enable 1, DAC=4000, ADC Mean vs Hold
χ² / ndf 5222 / 41
p0 196.2 ± 2.025
p1 230.1 ± 22.92
p2 -3.277 ± 2.223
p3 -0.4923 ± 42.23
p4 -0.1338 ± 0.007059
260
240
220
200
180
160
140
120
0
50
100
150
200
250


Chip 5, Channel 16, Enable 1, DAC=4000, ADC Noise vs Hold
12
11
10
9
8
7
6
5
4
3
0
50
100
150
200
250

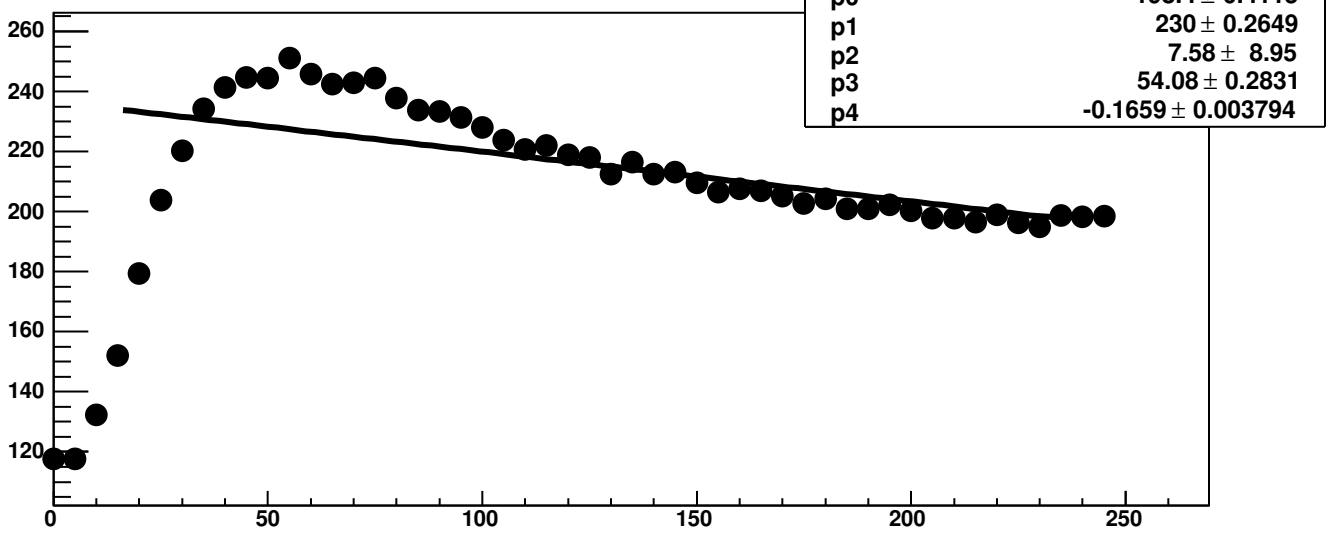

Chip 5, Channel 16, Enable 1, DAC=4000, ADC Residuals vs Hold
40
20
0
-20
-40
-60
-80
-100
-120
0
50
100
150
200
250


Chip 5, Channel 16, Enable 2!, DAC=4000, ADC Mean vs Hold
χ² / ndf 684.7 / 42
p0 -647.3 ± 0.5297
p1 4.491 ± 0.004666
p2 1.041e+04 ± 0.9831
p3 31.62 ± 0.004896
10000
8000
6000
4000
2000
0
0
50
100
150
200
250

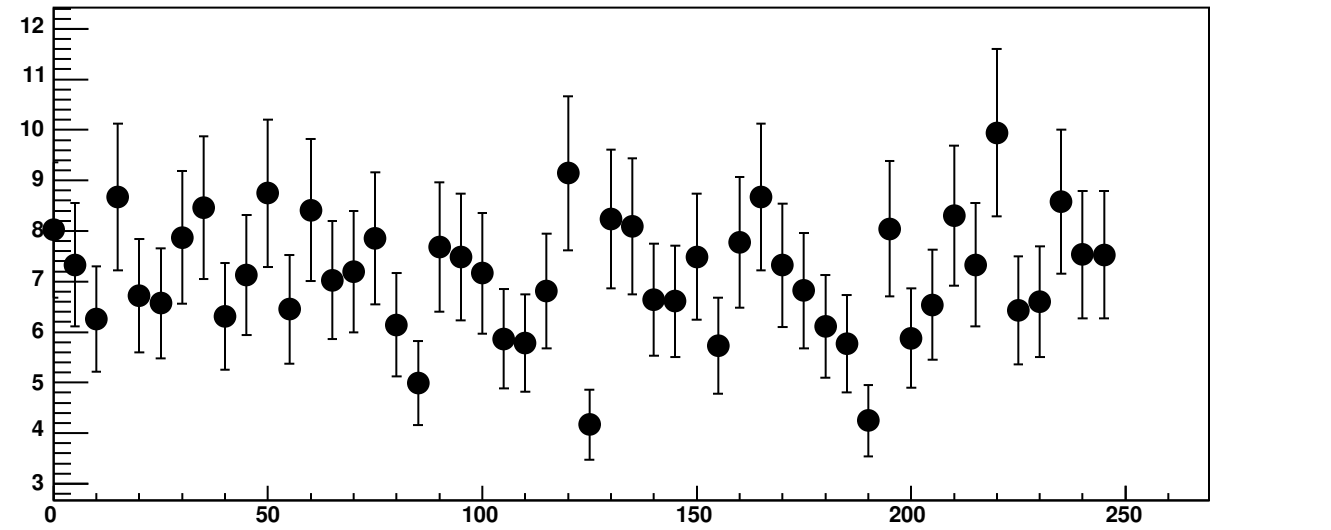

Chip 5, Channel 16, Enable 2!, DAC=4000, ADC Noise vs Hold
70
60
50
40
30
20
10
0
0
50
100
150
200
250


Chip 5, Channel 16, Enable 2!, DAC=4000, ADC Residuals vs Hold
1400
1200
1000
800
600
400
200
0
0
50
100
150
200
250

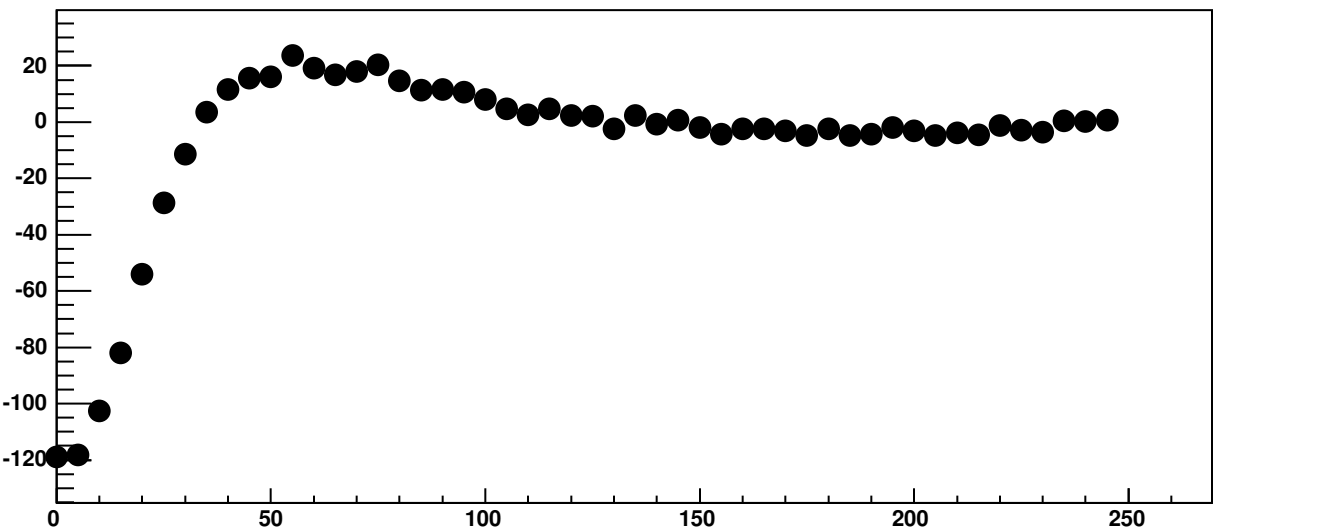
Chip 5, Channel 16, Enable 3, DAC=4000, ADC Mean vs Hold



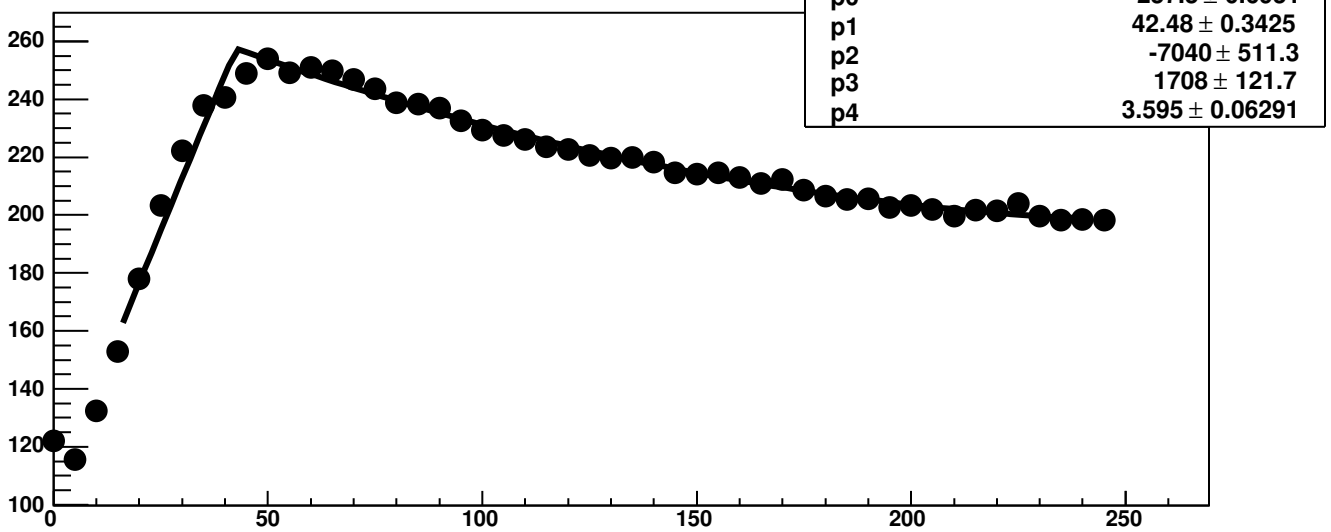
Chip 5, Channel 16, Enable 3, DAC=4000, ADC Noise vs Hold



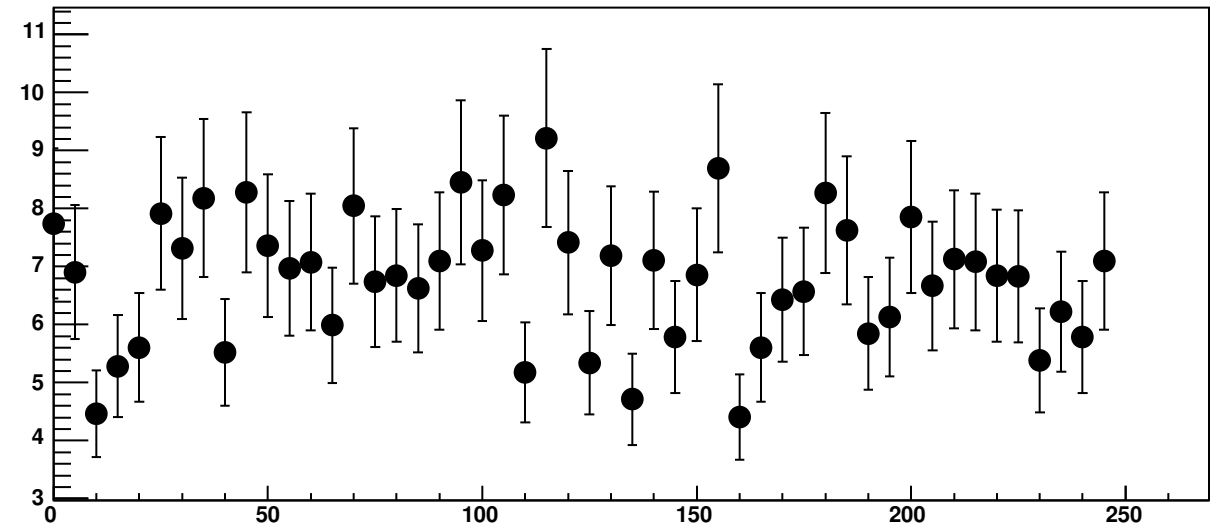
Chip 5, Channel 16, Enable 3, DAC=4000, ADC Residuals vs Hold



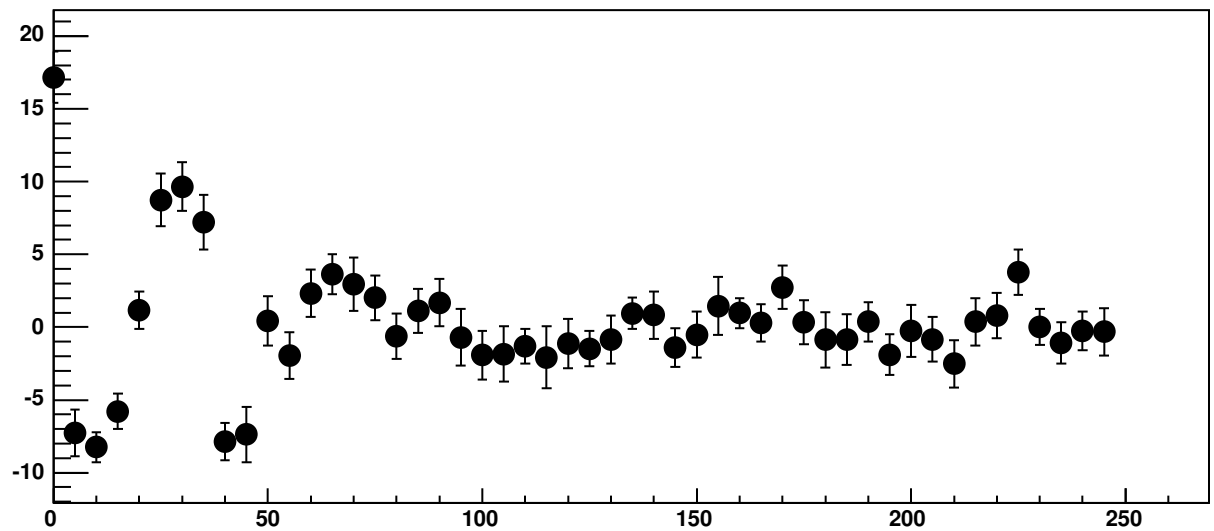
Chip 5, Channel 16, Enable 4, DAC=4000, ADC Mean vs Hold

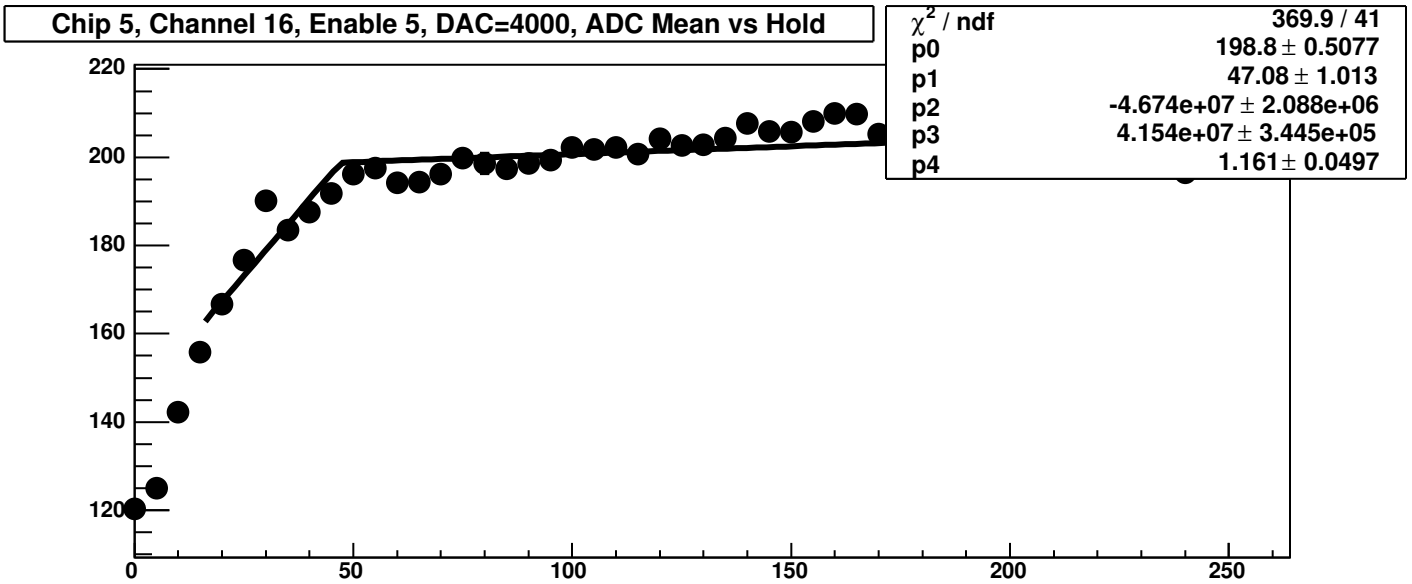


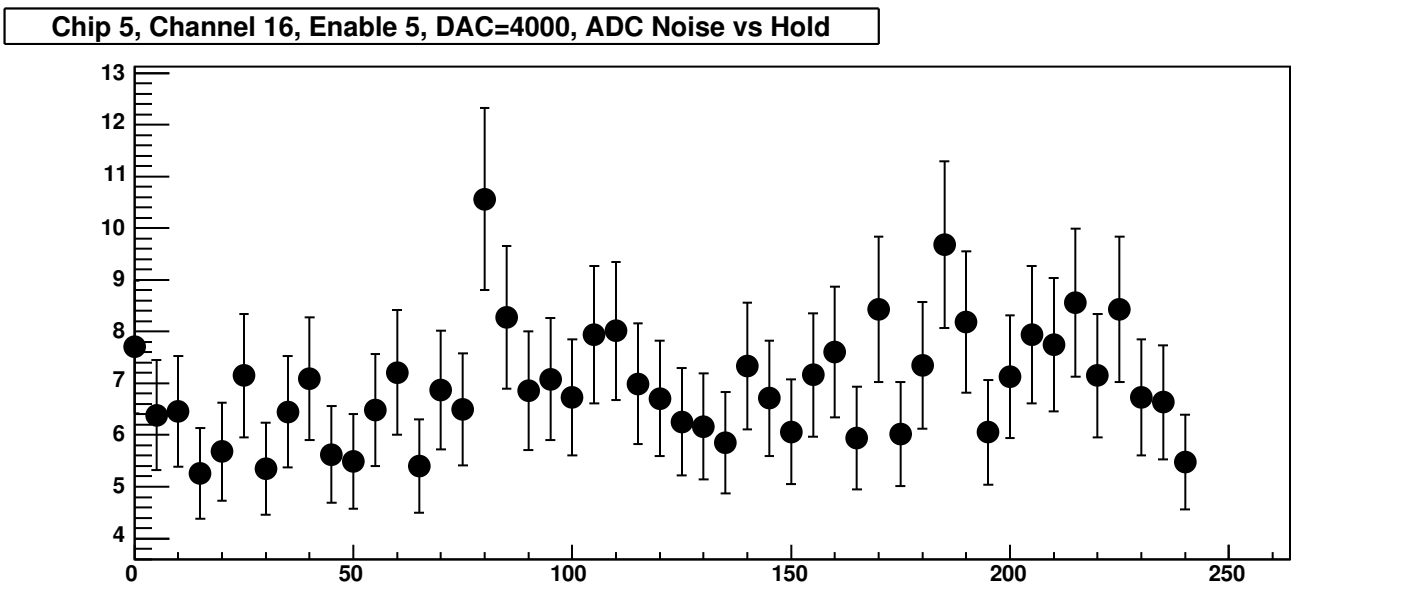
Chip 5, Channel 16, Enable 4, DAC=4000, ADC Noise vs Hold

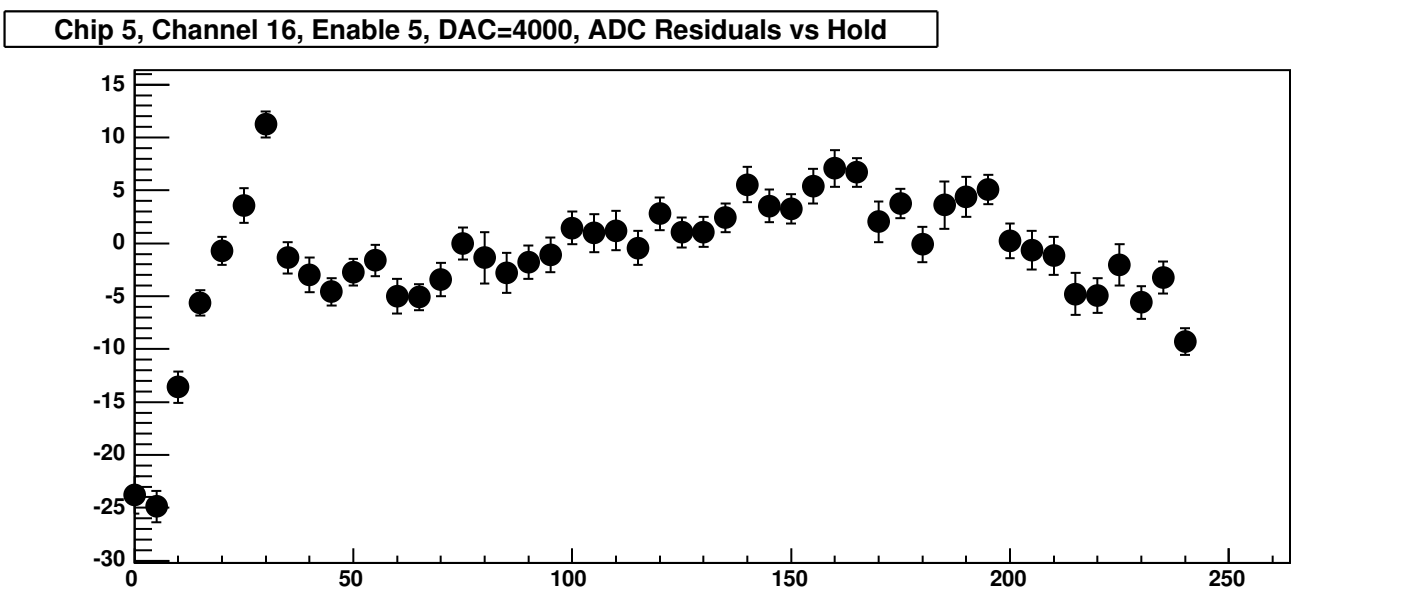


Chip 5, Channel 16, Enable 4, DAC=4000, ADC Residuals vs Hold

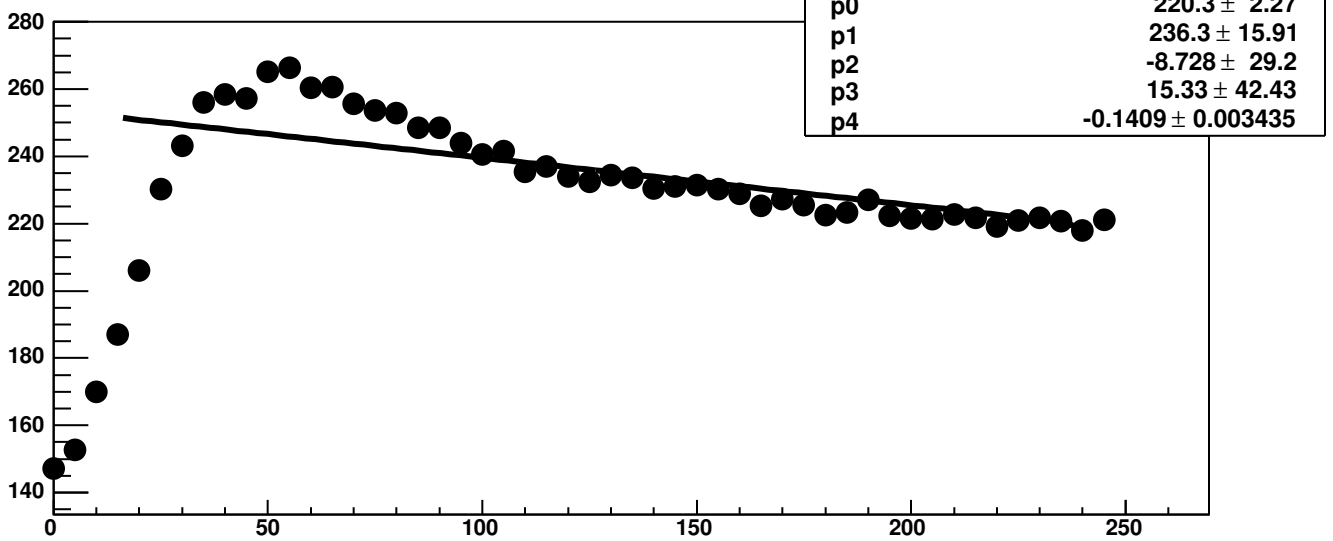



Chip 5, Channel 16, Enable 5, DAC=4000, ADC Mean vs Hold
χ² / ndf 369.9 / 41
p0 198.8 ± 0.5077
p1 47.08 ± 1.013
p2 -4.674e+07 ± 2.088e+06
p3 4.154e+07 ± 3.445e+05
p4 1.161 ± 0.0497
220
200
180
160
140
120
0
50
100
150
200
250

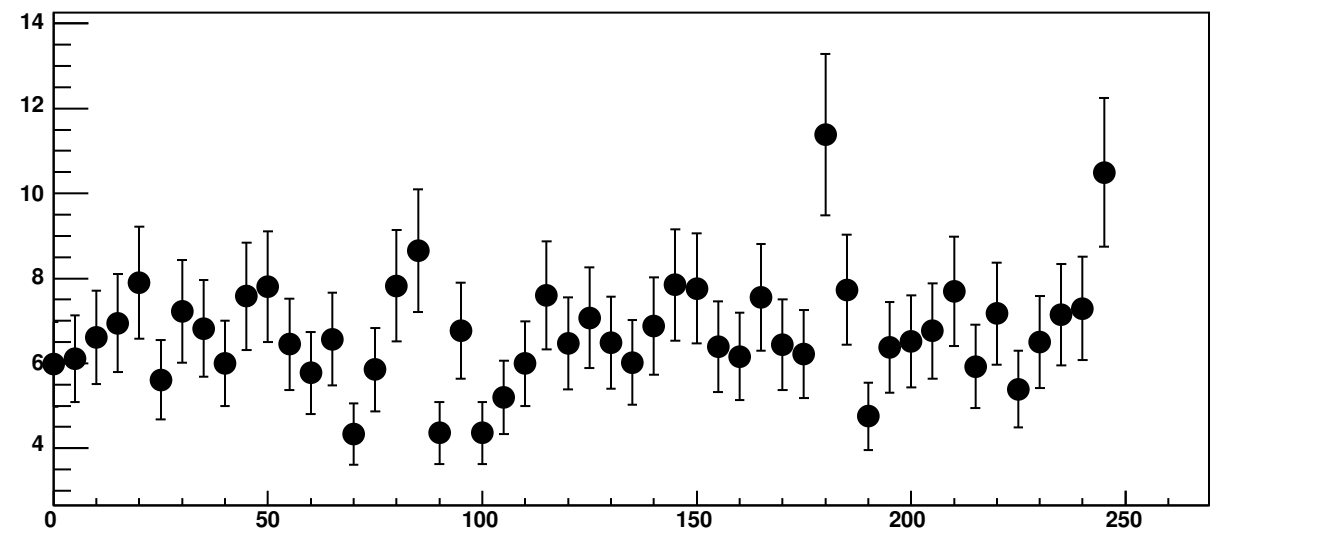

Chip 5, Channel 16, Enable 5, DAC=4000, ADC Noise vs Hold
13
12
11
10
9
8
7
6
5
4
0
50
100
150
200
250


Chip 5, Channel 16, Enable 5, DAC=4000, ADC Residuals vs Hold
15
10
5
0
-5
-10
-15
-20
-25
-30
0
50
100
150
200
250

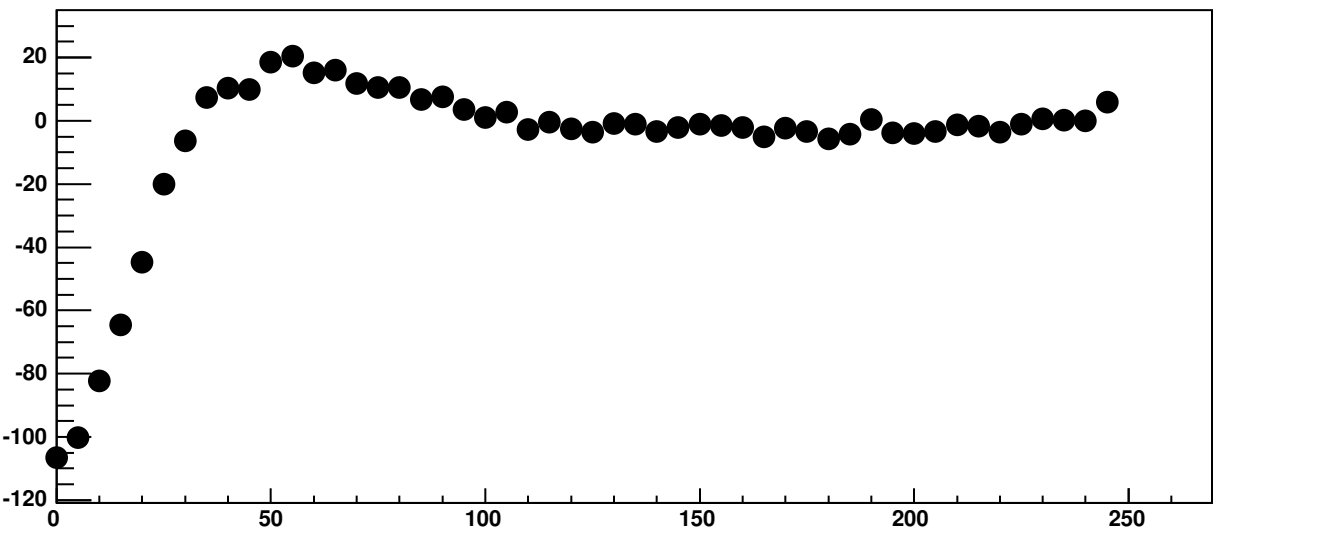
Chip 5, Channel 17, Enable 0, DAC=4000, ADC Mean vs Hold



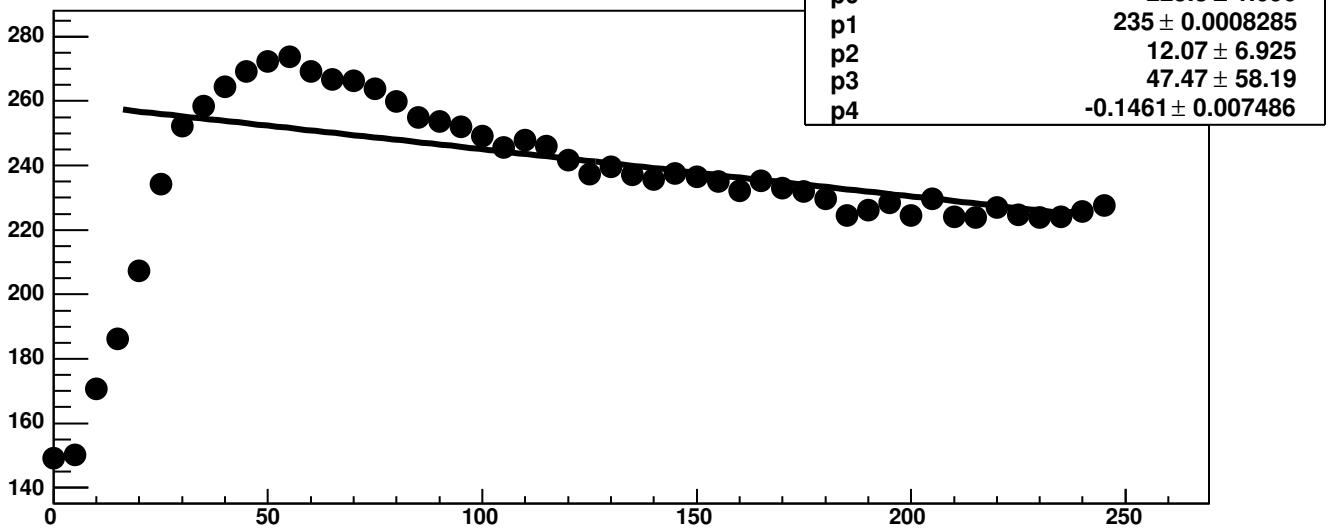
Chip 5, Channel 17, Enable 0, DAC=4000, ADC Noise vs Hold



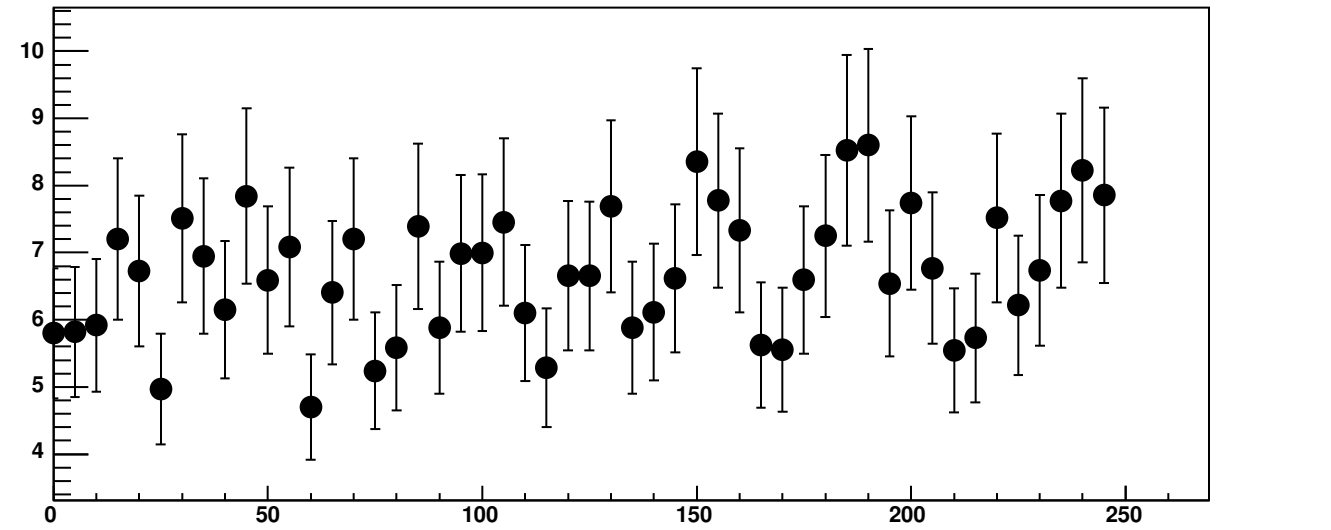
Chip 5, Channel 17, Enable 0, DAC=4000, ADC Residuals vs Hold



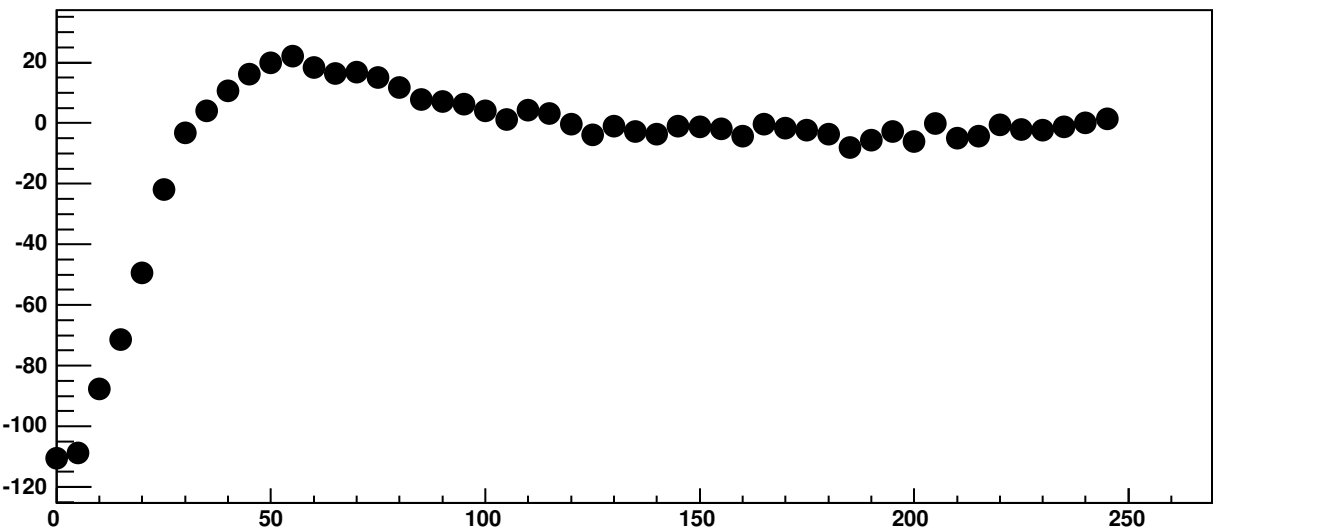
Chip 5, Channel 17, Enable 1, DAC=4000, ADC Mean vs Hold



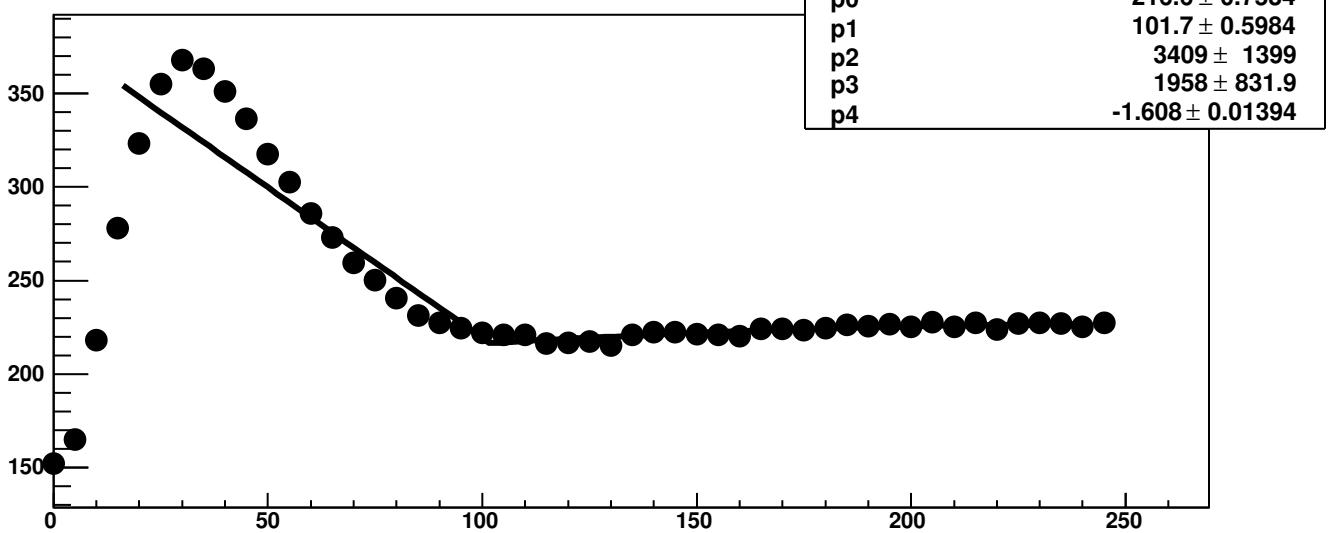
Chip 5, Channel 17, Enable 1, DAC=4000, ADC Noise vs Hold



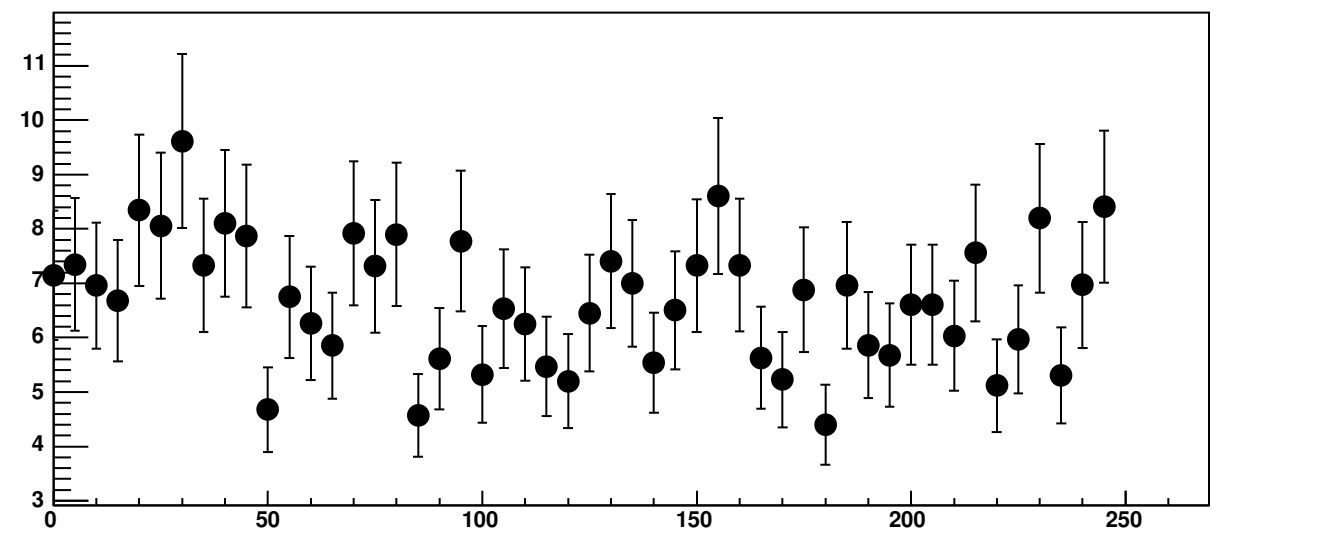
Chip 5, Channel 17, Enable 1, DAC=4000, ADC Residuals vs Hold



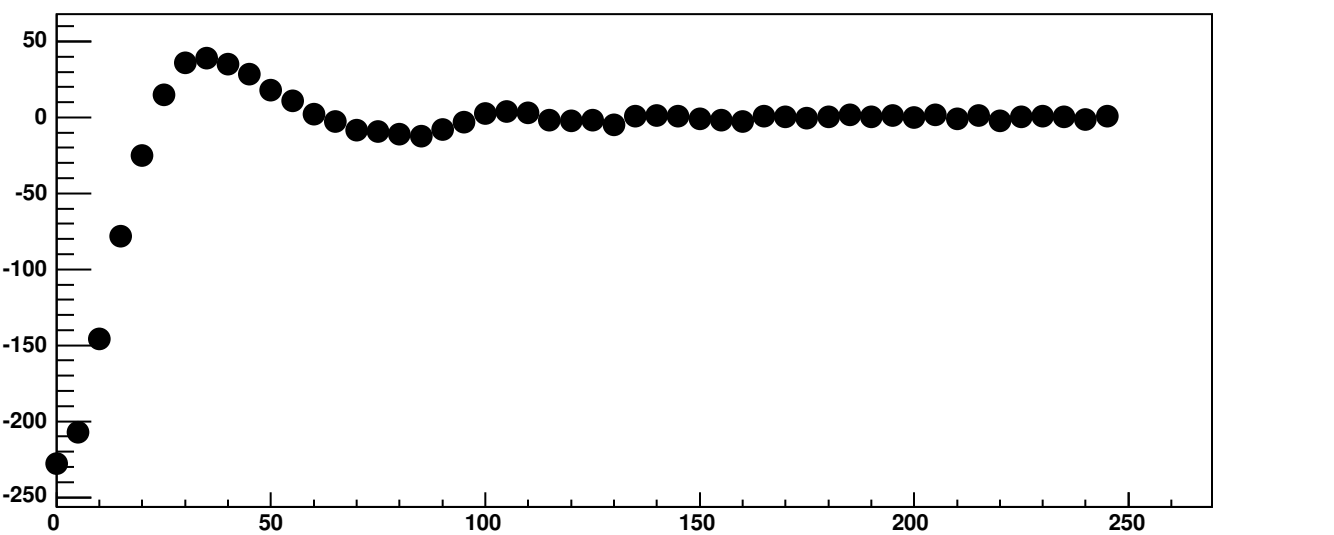
Chip 5, Channel 17, Enable 2, DAC=4000, ADC Mean vs Hold



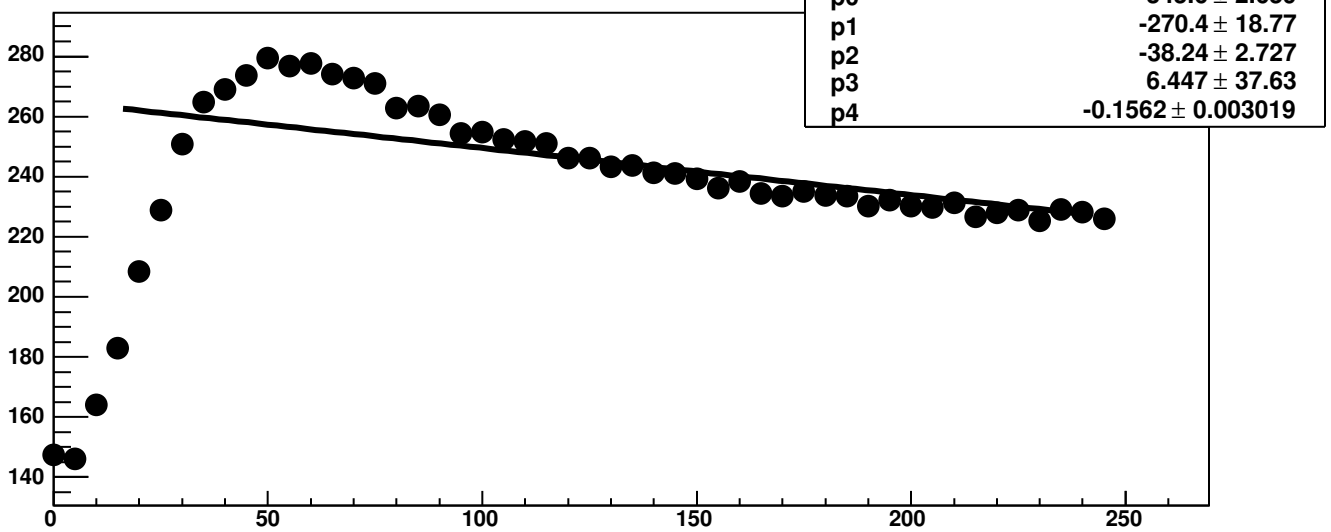
Chip 5, Channel 17, Enable 2, DAC=4000, ADC Noise vs Hold



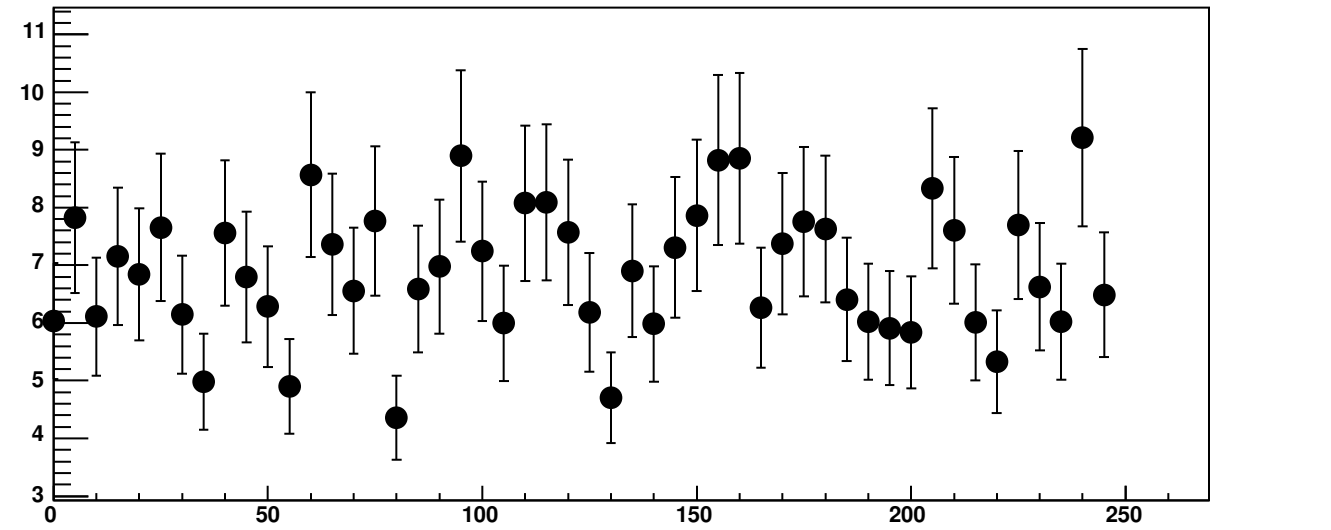
Chip 5, Channel 17, Enable 2, DAC=4000, ADC Residuals vs Hold



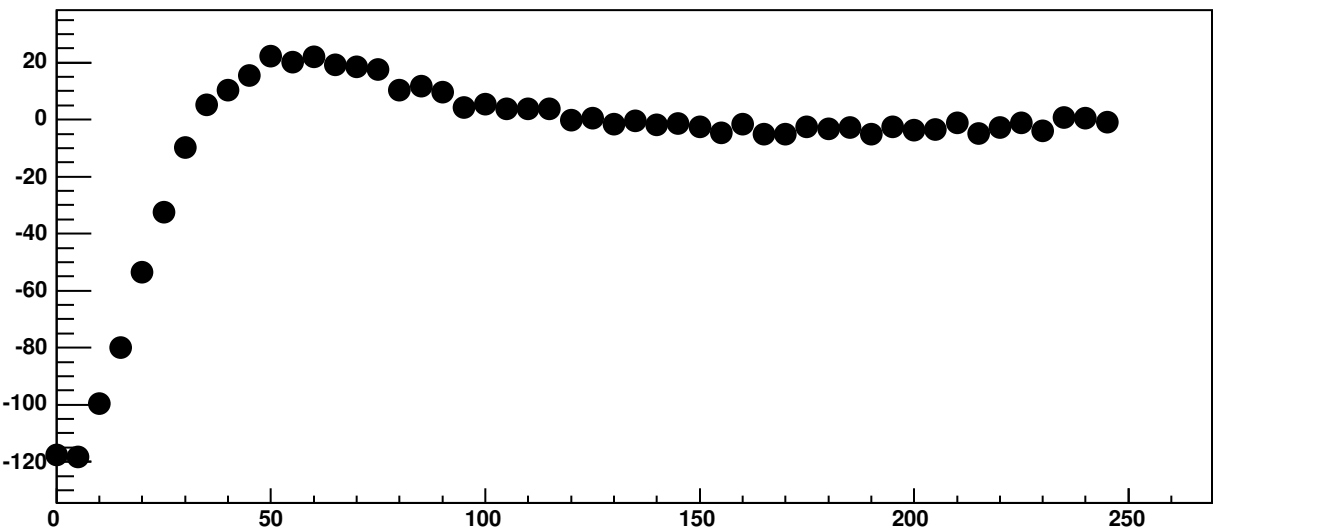
Chip 5, Channel 17, Enable 3, DAC=4000, ADC Mean vs Hold



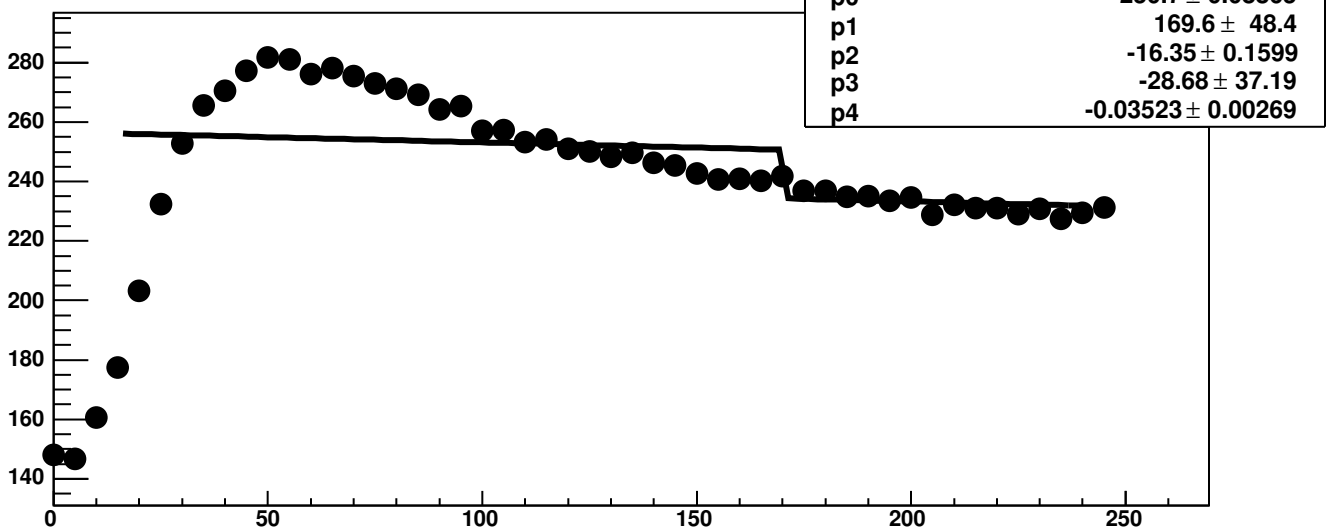
Chip 5, Channel 17, Enable 3, DAC=4000, ADC Noise vs Hold



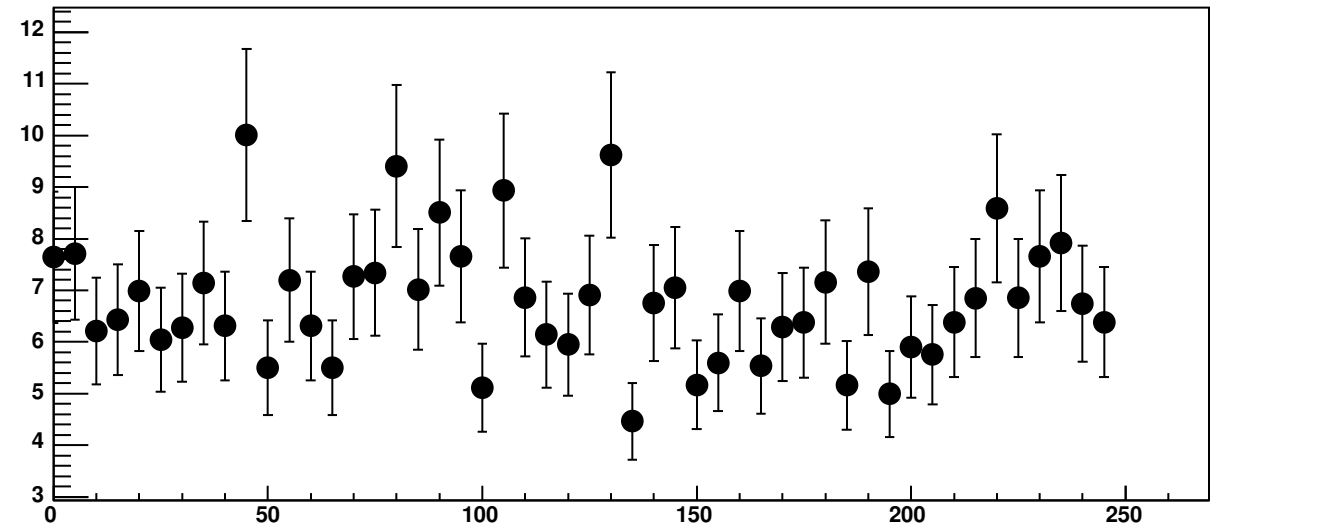
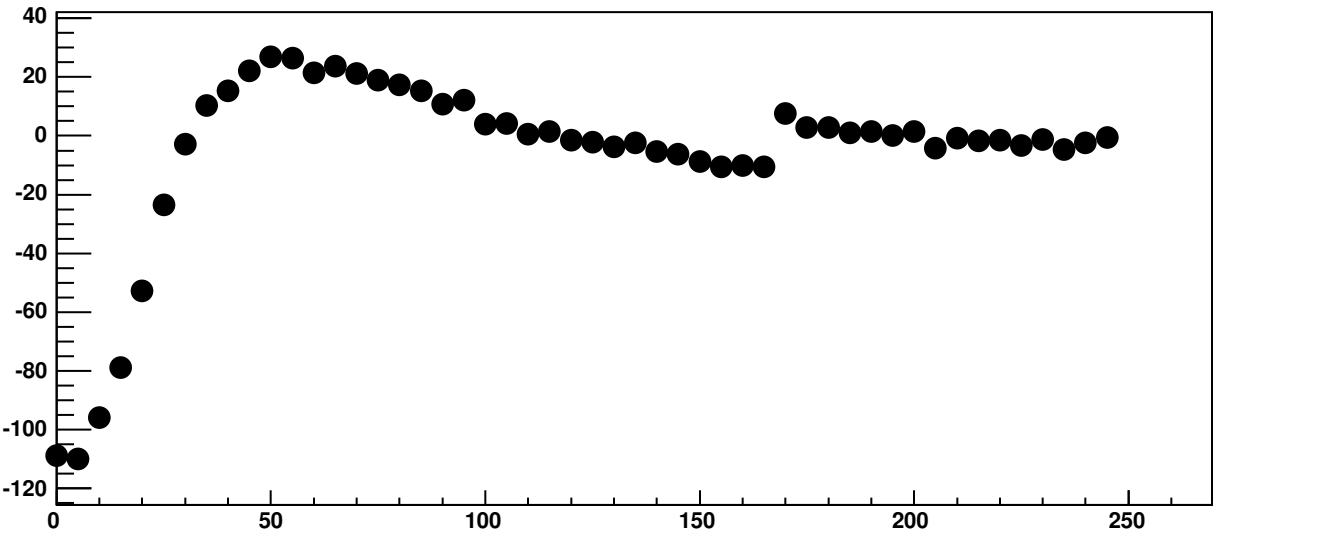
Chip 5, Channel 17, Enable 3, DAC=4000, ADC Residuals vs Hold



Chip 5, Channel 17, Enable 4, DAC=4000, ADC Mean vs Hold



Chip 5, Channel 17, Enable 4, DAC=4000, ADC Noise vs Hold



Chip 5, Channel 17, Enable 4, DAC=4000, ADC Residuals vs Hold